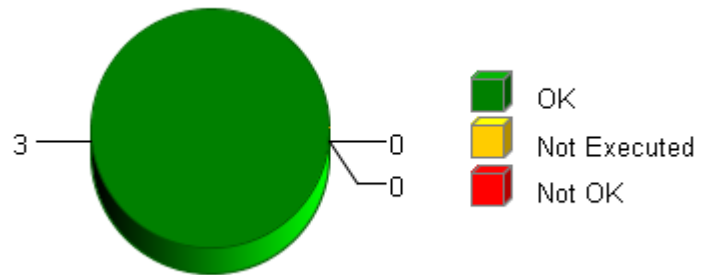


## Summary

**Total Test Objects:** 3  
**Successful:** 3  
**Failed:** 0  
**Not Executed:** 0  
**Date:** 2015-01-29  
**Time:** 14:40:43+0530

## Overall Test Object Results (including Coverage)



## Selected Project Items

Test Object "CBD\_UnitTest/StabilityComp2/ApplyStabilityComp2"  
 Test Object "CBD\_UnitTest/StabilityComp2/StabilityComp2\_Init1"  
 Test Object "CBD\_UnitTest/StabilityComp2/StabilityComp2\_Per1"

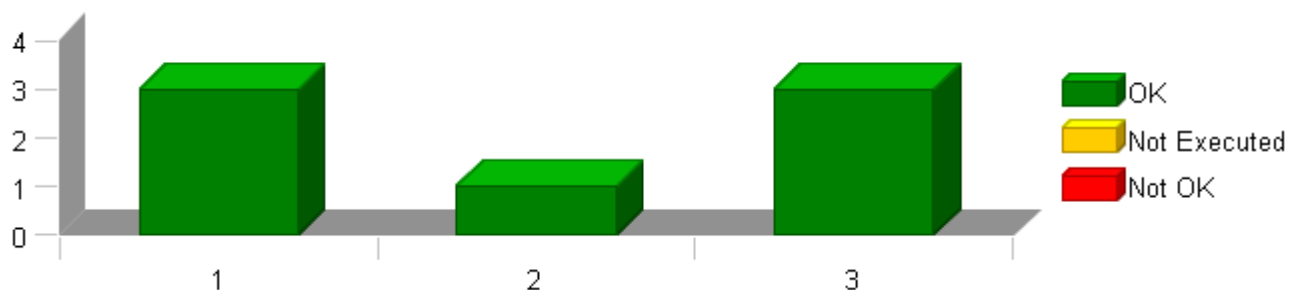
## Used Test Environments

TI TMS 570 PLS UDE (Default)

## Batch Operation Settings

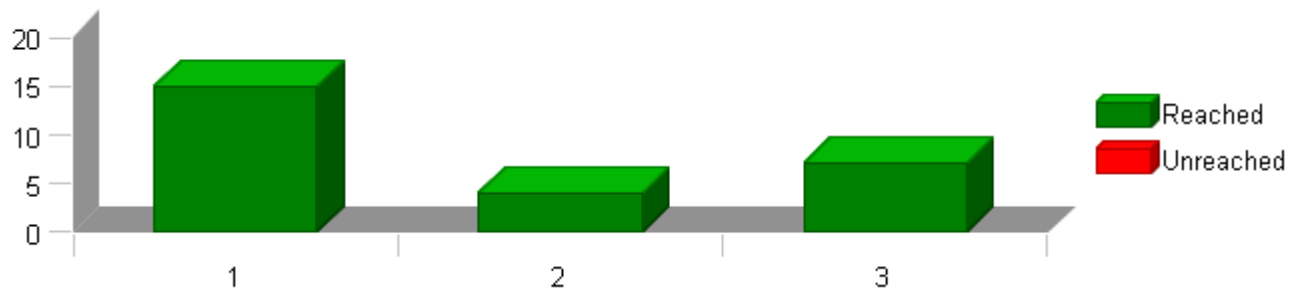
**Check Interface:** No  
**Generate Driver:** Yes  
**Execute Test:** Yes  
**Create New Test Run:** No  
**Instrumentation:** Test Object Only  
**Coverage:** Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

## Test Case Results for Each Test Object (without Coverage)



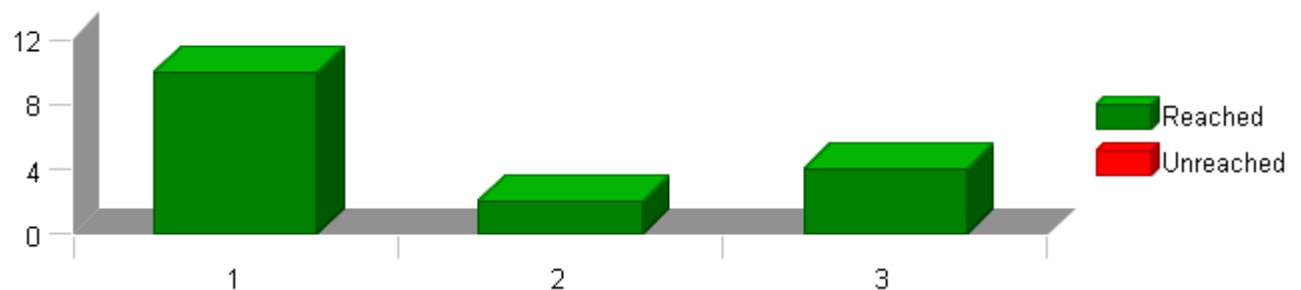
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

### Statement (C0) Coverage: Total Statements for Each Test Object



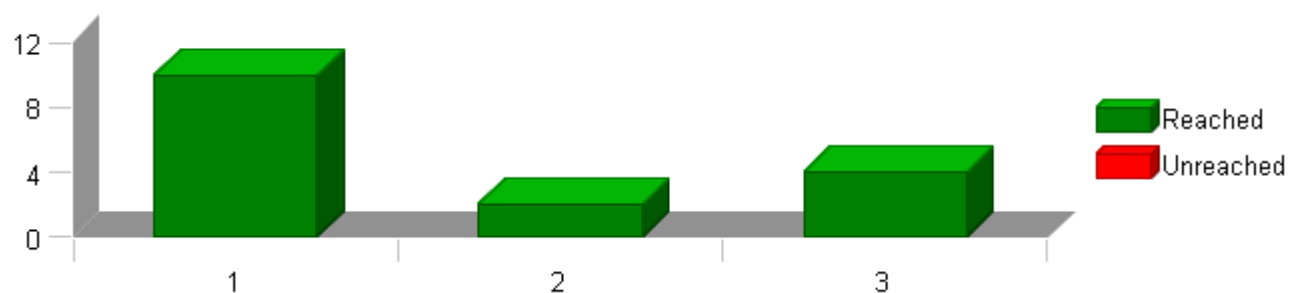
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

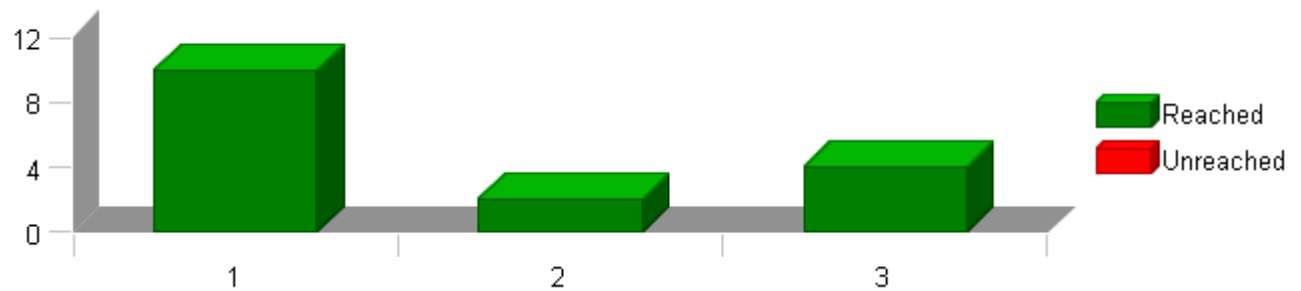
### Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

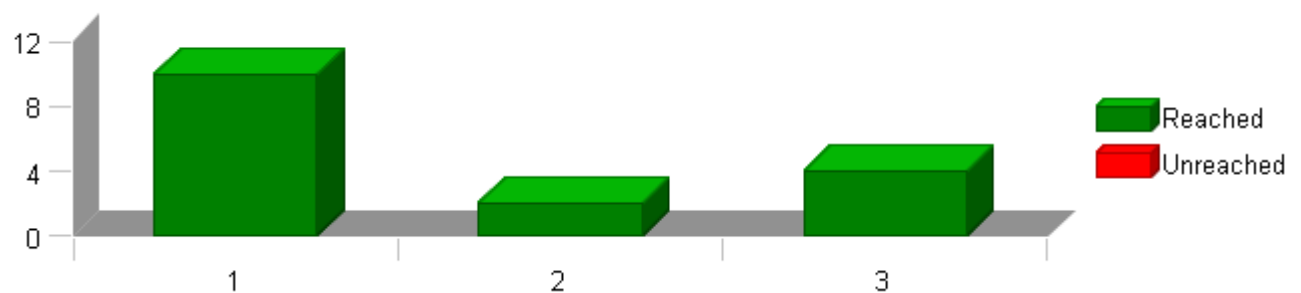
## MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## TEST OVERVIEW REPORT

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Project CBD\_StabilityComp2



### Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	CBD_StabilityComp2	100 %	100 %	100 %	100 %	100 %	7 of 7 passed	✓
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	7 of 7 passed	✓
	StabilityComp2	100 %	100 %	100 %	100 %	100 %	7 of 7 passed	✓
1	<a href="#">ApplyStabilityComp2</a>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
2	<a href="#">StabilityComp2_Init1</a>	100 %	100 %	100 %	100 %	100 %	1 of 1 passed	✓
3	<a href="#">StabilityComp2_Per1</a>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓

# TEST DETAILS REPORT

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ApplyStabilityComp2



Project	CBD_StabilityComp2
Module	StabilityComp2
Test Object	ApplyStabilityComp2

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

## Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_StabilityComp2
Configuration File	D:\Synergy_Work_Area\CBD_StabilityComp2\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\StabilityComp\src\Ap_StabilityComp2.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxtLib\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include
File	\$(SOURCEROOT)\NxtLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxtLib\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include
File	\$(SOURCEROOT)\StabilityComp\src\NF_Init_f32_UnitTest.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxtLib\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'StabilityComp2'	*****Unit Test Description***** Name of Tester: Spoorti Mali Code File(s) Under Test: Ap_StabilityComp2.c Code File(s) Version: 6 Module Design Document: StabilityCompensation2_MDD Module Design Document Version: 6 Data Dictionary Version: 17 Unit Test Plan Version: 5 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): 1238 Total RAM Used (Bytes): 100 Total CALS Used (Bytes): 380 Special Test Requirements: Place ""NF_Init_f32_UnitTest.c"" at ""...\\CBD_StabilityComp\\StabilityComp\\src"" location and add it in the Source files list of TESSy for Testing purpose which is embedded with this UTP. Test Date: 01-29-15 Comments: ""Note1: Inline function defined in globalmacro.h is not unit tested.\\n\\nNote2: In ""StabilityComp2_Per1"" function, global variable ""CombinedAssist_MtrNm_f32"" has given out of range value to cover 100% path coverage.\\n\\nNote3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."" *****

## Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_StabilityComp2\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

# TEST DETAILS REPORT

ApplyStabilityComp2

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## Test Case 1: Metrics Test

**Specification** Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 8410 Cycles  
TS1.2 8547 Cycles

**Description** Vector description:

TS1.1 Shortest Execution Path:(WriteDisplayVars\_Cnt\_T\_Igc = True)==>TRUE  
TS1.2 Longest Execution Path: (WriteDisplayVars\_Cnt\_T\_Igc = True)==>False

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	10.275
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_Igc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[1].B0_Uls_f32	32.62970983
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.307507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[2].B0_Uls_f32	4.629709826
t_StCmpNFK_Str[2].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	2.407507151
t_StCmpNFK_Str[3].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[3].B2_Uls_f32	2.507507151
tgt_CompBlnd_Uls_T_u2p14[0]	492
tgt_CompBlnd_Uls_T_u2p14[1]	983
tgt_CompBlnd_Uls_T_u2p14[2]	1475
tgt_CompBlnd_Uls_T_u2p14[3]	1966
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	30.9881782
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	22.1110891
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	176.86
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	35.777
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	25.172
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	177.67
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	40.5658218
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	39.312
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	96.5125243
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	45.3546436
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	31.2938218
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	177.67
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777

# TEST DETAILS REPORT

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ApplyStabilityComp2

Name	Input Value
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-13.0217123	-13.02171478 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	62.1063004	62.10630329 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	58.0084457	58.00844666 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	12.7788887	12.77888592 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	128.545456	128.5454457 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	154.254684	154.2546841 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	6.44206715	6.442065301 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	70.6705399	70.67053851 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	67.5860901	67.58609026 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	21.1635799	21.1635769 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	137.109695	137.1096809 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	163.832336	163.8323277 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	-3.08
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412
tgt_CompBlnd_Uls_T_u2p14[0]	3441
tgt_CompBlnd_Uls_T_u2p14[1]	3932
tgt_CompBlnd_Uls_T_u2p14[2]	4424
tgt_CompBlnd_Uls_T_u2p14[3]	4915
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1567.617957
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1099.795592



# TEST DETAILS REPORT

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ApplyStabilityComp2

Name	Input Value
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2311.793127
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1567.617957
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1099.795592
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2311.793127
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1567.617957
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1099.795592
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2311.793127
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1567.617957
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1099.795592
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-2311.793127
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

**Test Case 2: Boundary Test**

# TEST DETAILS REPORT

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ApplyStabilityComp2

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment)
	CPU Cycles:
	TS2.1 8557 Cycles
	TS2.2 8440 Cycles
	TS2.3 8378 Cycles
	TS2.4 8359 Cycles
	TS2.5 8379 Cycles
	TS2.6 8379 Cycles
	TS2.7 8378 Cycles
	TS2.8 8359 Cycles
	TS2.9 8379 Cycles
	TS2.10 8359 Cycles
	TS2.11 8359 Cycles
	TS2.12 8379 Cycles
	TS2.13 8379 Cycles
	TS2.14 8379 Cycles
	TS2.15 8379 Cycles
	TS2.16 8398 Cycles
	TS2.17 8378 Cycles
	TS2.18 8378 Cycles
	TS2.19 8398 Cycles
	TS2.20 8398 Cycles
	TS2.21 8359 Cycles
	TS2.22 8378 Cycles
	TS2.23 8379 Cycles
	TS2.24 8398 Cycles
	TS2.25 8379 Cycles
	TS2.26 8378 Cycles
	TS2.27 8379 Cycles
	TS2.28 8378 Cycles
	TS2.29 8359 Cycles
	TS2.30 8398 Cycles
	TS2.31 8398 Cycles
	TS2.32 8398 Cycles
	TS2.33 8378 Cycles
	TS2.34 8378 Cycles
	TS2.35 8398 Cycles
	TS2.36 8398 Cycles
	TS2.37 8359 Cycles
	TS2.38 8378 Cycles
	TS2.39 8379 Cycles
	TS2.40 8398 Cycles
	TS2.41 8379 Cycles
	TS2.42 8378 Cycles
	TS2.43 8379 Cycles
	TS2.44 8378 Cycles
	TS2.45 8359 Cycles
	TS2.46 8398 Cycles
	TS2.47 8398 Cycles
	TS2.48 8379 Cycles
Description	Vector Description:
	TS2.1 All Min
	TS2.2 All Max
	TS2.3 Assist_MtrNm_T_f32==>Min
	TS2.4 Assist_MtrNm_T_f32==>Max
	TS2.5 Assist_MtrNm_T_f32==>Pos
	TS2.6 Assist_MtrNm_T_f32==>Zero
	TS2.7 Assist_MtrNm_T_f32==>Neg
	TS2.8 ADDFBlend_Uls_T_u2p14==>Min
	TS2.9 ADDFBlend_Uls_T_u2p14==>Max
	TS2.10 ADDFBlend_Uls_T_u2p14==>Pos
	TS2.11 WriteDisplayVars_Cnt_T_lgc==>False
	TS2.12 WriteDisplayVars_Cnt_T_lgc==>True
	TS2.13 CompBlnd_Uls_T_u2p14[4]==>Min
	TS2.14 CompBlnd_Uls_T_u2p14[4]==>Max
	TS2.15 CompBlnd_Uls_T_u2p14[4]==>Pos
	TS2.16 Min output, SV1, SV2 using test case 3 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.17 Max output, SV1, SV2 using test case 3 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.18 Min output, SV1, SV2 using test case 4 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.19 Max output, SV1, SV2 using test case 4 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.20 Min output, SV1, SV2 using test case 5 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.21 Max output, SV1, SV2 using test case 5 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.22 Min output, SV1, SV2 using test case 6 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.23 Max output, SV1, SV2 using test case 6 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.24 Min output, SV1, SV2 using test case 7 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.25 Max output, SV1, SV2 using test case 7 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.26 Min output, SV1, SV2 using test case 8 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.27 Max output, SV1, SV2 using test case 8 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.28 Min output, SV1, SV2 using test case 9 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.29 Max output, SV1, SV2 using test case 9 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.30 Min output, SV1, SV2 using test case 10 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.31 Max output, SV1, SV2 using test case 10 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
	TS2.32 t_StCmpNFK_Str[4].A1_Uls_f32==>Min
	TS2.33 t_StCmpNFK_Str[4].A1_Uls_f32==>Max
	TS2.34 t_StCmpNFK_Str[4].A1_Uls_f32==>Zero
	TS2.35 t_StCmpNFK_Str[4].A2_Uls_f32==>Min
	TS2.36 t_StCmpNFK_Str[4].A2_Uls_f32==>Max
	TS2.37 t_StCmpNFK_Str[4].A2_Uls_f32==>Zero
	TS2.38 t_StCmpNFK_Str[4].A2_Uls_f32==>Pos
	TS2.39 t_StCmpNFK_Str[4].A2_Uls_f32==>Neg
	TS2.40 t_StCmpNFK_Str[4].B0_Uls_f32==>Min
	TS2.41 t_StCmpNFK_Str[4].B0_Uls_f32==>Max
	TS2.42 t_StCmpNFK_Str[4].B0_Uls_f32==>Pos
	TS2.43 t_StCmpNFK_Str[4].B1_Uls_f32==>Min
	TS2.44 t_StCmpNFK_Str[4].B1_Uls_f32==>Max
	TS2.45 t_StCmpNFK_Str[4].B1_Uls_f32==>Neg
	TS2.46 t_StCmpNFK_Str[4].B2_Uls_f32==>Min
	TS2.47 t_StCmpNFK_Str[4].B2_Uls_f32==>Max
	TS2.48 t_StCmpNFK_Str[4].B2_Uls_f32==>Neg

# TEST DETAILS REPORT

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ApplyStabilityComp2



Test Step 2.1 (Repeat Count = 1)				✓
Name		Input Value		
ADDFBlend_Uls_T_u2p14		0		
Assist_MtrNm_T_f32		-10.275		
CompBlnd_Uls_T_u2p14		tgt_CompBlnd_Uls_T_u2p14		
FiltSVPtr_Uls_T_Str		tgt_FiltSVPtr_Uls_T_Str		
WriteDisplayVars_Cnt_T_lgc		0		
t_StCmpNFK_Str[0].A1_Uls_f32		-2		
t_StCmpNFK_Str[0].A2_Uls_f32		-1		
t_StCmpNFK_Str[0].B0_Uls_f32		0		
t_StCmpNFK_Str[0].B1_Uls_f32		-4500		
t_StCmpNFK_Str[0].B2_Uls_f32		-4		
t_StCmpNFK_Str[1].A1_Uls_f32		-2		
t_StCmpNFK_Str[1].A2_Uls_f32		-1		
t_StCmpNFK_Str[1].B0_Uls_f32		0		
t_StCmpNFK_Str[1].B1_Uls_f32		-4500		
t_StCmpNFK_Str[1].B2_Uls_f32		-4		
t_StCmpNFK_Str[2].A1_Uls_f32		-2		
t_StCmpNFK_Str[2].A2_Uls_f32		-1		
t_StCmpNFK_Str[2].B0_Uls_f32		0		
t_StCmpNFK_Str[2].B1_Uls_f32		-4500		
t_StCmpNFK_Str[2].B2_Uls_f32		-4		
t_StCmpNFK_Str[3].A1_Uls_f32		-2		
t_StCmpNFK_Str[3].A2_Uls_f32		-1		
t_StCmpNFK_Str[3].B0_Uls_f32		0		
t_StCmpNFK_Str[3].B1_Uls_f32		-4500		
t_StCmpNFK_Str[3].B2_Uls_f32		-4		
tgt_CompBlnd_Uls_T_u2p14[0]		0		
tgt_CompBlnd_Uls_T_u2p14[1]		0		
tgt_CompBlnd_Uls_T_u2p14[2]		0		
tgt_CompBlnd_Uls_T_u2p14[3]		0		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32		-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32		-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32		-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32		-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32		-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32		-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32		-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32		-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32		-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32		-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32		-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32		-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32		-0.718797066		
tgt_KPtr_Cnt_Str.A2_Uls_f32		0.129167306		
tgt_KPtr_Cnt_Str.B0_Uls_f32		104.703713		
tgt_KPtr_Cnt_Str.B1_Uls_f32		-207.6908067		
tgt_KPtr_Cnt_Str.B2_Uls_f32		103.397464		
tgt_KPtr_Cnt_Str1.A1_Uls_f32		-0.718797066		
tgt_KPtr_Cnt_Str1.A2_Uls_f32		0.129167306		
tgt_KPtr_Cnt_Str1.B0_Uls_f32		104.703713		
tgt_KPtr_Cnt_Str1.B1_Uls_f32		-207.6908067		
tgt_KPtr_Cnt_Str1.B2_Uls_f32		103.397464		
tgt_KPtr_Cnt_Str2.A1_Uls_f32		-0.718797066		
tgt_KPtr_Cnt_Str2.A2_Uls_f32		0.129167306		
tgt_KPtr_Cnt_Str2.B0_Uls_f32		104.703713		
tgt_KPtr_Cnt_Str2.B1_Uls_f32		-207.6908067		
tgt_KPtr_Cnt_Str2.B2_Uls_f32		103.397464		
tgt_KPtr_Cnt_Str3.A1_Uls_f32		-0.718797066		
tgt_KPtr_Cnt_Str3.A2_Uls_f32		0.129167306		
tgt_KPtr_Cnt_Str3.B0_Uls_f32		104.703713		
tgt_KPtr_Cnt_Str3.B1_Uls_f32		-207.6908067		
tgt_KPtr_Cnt_Str3.B2_Uls_f32		103.397464		
Name		Actual Value	Expected Value	Result
ApplyStabilityComp2()		-18022	-18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32		39850.8047	39850.80719 ± 0.0625	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32		-2602.34839	-2602.348608 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32		-2643.44849	-2643.448608 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32		39850.8047	39850.80719 ± 0.0625	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-2602.34839	-2602.348608 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2643.44849	-2643.448608 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	39850.8047	39850.80719 ± 0.0625	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-2602.34839	-2602.348608 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2643.44849	-2643.448608 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	39850.8047	39850.80719 ± 0.0625	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-2602.34839	-2602.348608 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-2643.44849	-2643.448608 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.2 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	16384
Assist_MtrNm_T_f32	10.275
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	0
t_StCmpNFK_Str[0].A2_Uls_f32	1
t_StCmpNFK_Str[0].B0_Uls_f32	2300
t_StCmpNFK_Str[0].B1_Uls_f32	0
t_StCmpNFK_Str[0].B2_Uls_f32	2300
t_StCmpNFK_Str[1].A1_Uls_f32	0
t_StCmpNFK_Str[1].A2_Uls_f32	1
t_StCmpNFK_Str[1].B0_Uls_f32	2300
t_StCmpNFK_Str[1].B1_Uls_f32	0
t_StCmpNFK_Str[1].B2_Uls_f32	2300
t_StCmpNFK_Str[2].A1_Uls_f32	0
t_StCmpNFK_Str[2].A2_Uls_f32	1
t_StCmpNFK_Str[2].B0_Uls_f32	2300
t_StCmpNFK_Str[2].B1_Uls_f32	0
t_StCmpNFK_Str[2].B2_Uls_f32	2300
t_StCmpNFK_Str[3].A1_Uls_f32	0
t_StCmpNFK_Str[3].A2_Uls_f32	1
t_StCmpNFK_Str[3].B0_Uls_f32	2300
t_StCmpNFK_Str[3].B1_Uls_f32	0
t_StCmpNFK_Str[3].B2_Uls_f32	2300
tgt_CompBlnd_Uls_T_u2p14[0]	16384
tgt_CompBlnd_Uls_T_u2p14[1]	16384
tgt_CompBlnd_Uls_T_u2p14[2]	16384
tgt_CompBlnd_Uls_T_u2p14[3]	16384
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1099.795592

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ApplyStabilityComp2

Name	Input Value		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2311.793127		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1099.79553	1099.795592 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	20989.0508	20989.05139 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2643.44849	2643.448608 ± 0.00489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1099.79553	1099.795592 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	20989.0508	20989.05139 ± 0.00049	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2643.44849	2643.448608 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1099.79553	1099.795592 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	20989.0508	20989.05139 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2643.44849	2643.448608 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1099.79553	1099.795592 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	20989.0508	20989.05139 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2643.44849	2643.448608 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	3277
Assist_MtrNm_T_f32	-10.275
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.256265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.796280471
t_StCmpNFK_Str[1].B0_Uls_f32	3.629709826
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.207507151

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ApplyStabilityComp2

Name	Input Value		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.246265753		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.879628047		
t_StCmpNFK_Str[2].B0_Uls_f32	4.6297		
t_StCmpNFK_Str[2].B1_Uls_f32	-6.753110777		
t_StCmpNFK_Str[2].B2_Uls_f32	3.207507151		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.336265753		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047		
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826		
t_StCmpNFK_Str[3].B1_Uls_f32	-7.753110777		
t_StCmpNFK_Str[3].B2_Uls_f32	3.207507151		
tgt_CompBlnd_Uls_T_u2p14[0]	328		
tgt_CompBlnd_Uls_T_u2p14[1]	819		
tgt_CompBlnd_Uls_T_u2p14[2]	1311		
tgt_CompBlnd_Uls_T_u2p14[3]	1802		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	28.0291485		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	19.1520594		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	76.17866968		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	32.242		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	21.637		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	97.43		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	36.4548515		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	35.777		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	19.795592		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	40.667703		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	26.6068812		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	97.43		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	4392	4392	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	68.2286377	68.22863644 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-21.9964695	-21.9964728 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	1.00888252	1.008880038 ± 0.00489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	48.3759575	48.37596068 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-91.3499298	-91.34992711 ± 0.00049	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-86.2356873	-86.23568408 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	107.488632	107.4886279 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-24.6582069	-24.65821212 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	9.43458748	9.434583038 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	80.1052551	80.10526252 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-85.8389816	-85.83898146 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-77.8099823	-77.80998108 ± 0.000489	✔

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ApplyStabilityComp2



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.4 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	10.275
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[1].B0_Uls_f32	32.62970983
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.307507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[2].B0_Uls_f32	4.629709826
t_StCmpNFK_Str[2].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	2.407507151
t_StCmpNFK_Str[3].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[3].B2_Uls_f32	2.507507151
tgt_CompBlnd_Uls_T_u2p14[0]	492
tgt_CompBlnd_Uls_T_u2p14[1]	983
tgt_CompBlnd_Uls_T_u2p14[2]	1475
tgt_CompBlnd_Uls_T_u2p14[3]	1966
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	30.9881782
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	22.1110891
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	176.86
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	35.777
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	25.172
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	177.67
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	40.5658218
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	39.312
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	96.5125243
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	45.3546436
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	31.2938218
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	177.67
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709



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ApplyStabilityComp2

Name	Input Value
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.23626573
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-13.0217123	-13.02171478 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	62.1063004	62.10630329 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	58.0084457	58.00844666 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	12.7788887	12.77888592 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	128.545456	128.5454457 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	154.254684	154.2546841 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	6.44206715	6.442065301 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	70.6705399	70.67053851 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	67.5860901	67.58609026 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	21.1635799	21.1635769 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	137.109695	137.1096809 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	163.832336	163.8323277 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	11469
Assist_MtrNm_T_f32	10
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[0].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[0].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[0].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[0].B2_Uls_f32	8.95890172
t_StCmpNFK_Str[1].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[1].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[1].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[1].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[1].B2_Uls_f32	8.95890172
t_StCmpNFK_Str[2].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[2].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[2].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[2].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[2].B2_Uls_f32	8.95890172
t_StCmpNFK_Str[3].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[3].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[3].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[3].B1_Uls_f32	-20.35078362

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Name	Input Value
t_StCmpNFK_Str[3].B2_Uls_f32	8.95890172
tgt_CompBlnd_Uls_T_u2p14[0]	655
tgt_CompBlnd_Uls_T_u2p14[1]	1147
tgt_CompBlnd_Uls_T_u2p14[2]	1638
tgt_CompBlnd_Uls_T_u2p14[3]	2130
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	33.9472079
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	25.0701188
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	67.2521
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	39.312
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	28.707
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	86.24
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	44.6767921
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	21.637
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	80.330346
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	50.0415842
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	35.9807624
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	86.24
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-168.061234	-168.0612174 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	131.095337	131.0953386 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	60.2443047	60.24430616 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-148.16925	-148.1692499 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	196.11618	196.1161875 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	154.618744	154.6187485 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-169.646286	-169.646269 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	138.487656	138.4876649 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	70.9738922	70.97389036 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-139.047424	-139.0474202 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	203.508499	203.5085139 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	165.348328	165.3483327 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

# TEST DETAILS REPORT

ApplyStabilityComp2

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## Test Step 2.6 (Repeat Count = 1)



Name	Input Value
ADDFBlend_Uls_T_u2p14	14746
Assist_MtrNm_T_f32	0
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[1].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.507507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[2].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[2].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	2.607507151
t_StCmpNFK_Str[3].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[3].B2_Uls_f32	2.707507151
tgt_CompBlnd_Uls_T_u2p14[0]	819
tgt_CompBlnd_Uls_T_u2p14[1]	1311
tgt_CompBlnd_Uls_T_u2p14[2]	1802
tgt_CompBlnd_Uls_T_u2p14[3]	2294
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	19.1520594
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	37.80256412
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	19.795592
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	21.637
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	430.88
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	100
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	24.1219406
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	25.172
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	76.17866968
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	26.6068812
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1217.034872
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	110
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	42.3275375	42.32753986 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	13.0162773	13.01627673 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	19.1520596	19.1520594 ± 0.00489	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	435.992096	435.9920821 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	14.7051125	14.70511206 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	21.6369991	21.637 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	30.8711891	30.87118847 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	16.3939476	16.39394738 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	24.1219406	24.1219406 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	1223.32117	1223.321167 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	18.0827847	18.08278271 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	26.6068821	26.6068812 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	-10.02
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[0].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[0].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[0].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[0].B2_Uls_f32	8.55890172
t_StCmpNFK_Str[1].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[1].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[1].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[1].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[1].B2_Uls_f32	8.45890172
t_StCmpNFK_Str[2].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[2].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[2].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[2].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[2].B2_Uls_f32	8.65890172
t_StCmpNFK_Str[3].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[3].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[3].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[3].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[3].B2_Uls_f32	8.35890172
tgt_CompBlnd_Uls_T_u2p14[0]	983
tgt_CompBlnd_Uls_T_u2p14[1]	1475
tgt_CompBlnd_Uls_T_u2p14[2]	1966
tgt_CompBlnd_Uls_T_u2p14[3]	2458
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	22.1110891
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	40.0332778
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	96.5125243
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	25.172
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	462.99
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	76
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	28.2329109
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	28.707
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	176.86
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	31.2938218

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Name	Input Value
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	1308.903444
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	96
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-5483	-5483	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	243.218094	243.218071172708 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-88.6804581	-88.6804518405268 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	-4.23860359	-4.23860335652 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	651.340271	651.340290892344 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-147.016922	-147.016921319767 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-90.3653717	-90.365361997 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	232.946243	232.946218061504 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-85.4647369	-85.4647204203417 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	1.88321686	1.88321844348 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	1498.30823	1498.30815958114 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-141.797195	-141.797189899582 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-84.2435455	-84.243540197 ± 0.000489	✓

## Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.8 (Repeat Count = 1) ✓

Name	Input Value
ADDFBlend_Uls_T_u2p14	0
Assist_MtrNm_T_f32	1
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106

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Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].B2_Uls_f32	0.939541087		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	0.949541087		
tgt_CompBlnd_Uls_T_u2p14[0]	1147		
tgt_CompBlnd_Uls_T_u2p14[1]	1638		
tgt_CompBlnd_Uls_T_u2p14[2]	2130		
tgt_CompBlnd_Uls_T_u2p14[3]	2621		
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	25.0701188		
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	42.26399148		
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	80.330346		
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	28.707		
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	495.1		
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	181		
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	32.3438812		
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	32.242		
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	87.2521		
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	35.9807624		
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	1400.772017		
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	191		
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	92.1162033	92.11620553 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-23.1921711	-23.19217121 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	27.6998272	27.69982863 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	568.362122	568.3620794 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-34.1104431	-34.11044524 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	40.2376747	40.23767485 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	95.6753159	95.67531846 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-29.5163479	-29.51634772 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	34.9735909	34.97359103 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	1487.61511	1487.615201 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-40.4246216	-40.42462175 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	47.5114365	47.51143725 ± 0.000489	✓

# TEST DETAILS REPORT

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ApplyStabilityComp2



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.9 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	16384
Assist_MtrNm_T_f32	10.275
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[0].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[0].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[1].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[1].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[1].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].B2_Uls_f32	0.842693873
t_StCmpNFK_Str[2].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[2].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[2].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].B2_Uls_f32	0.852693873
t_StCmpNFK_Str[3].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[3].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[3].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].B2_Uls_f32	0.836693873
tgt_CompBlnd_Uls_T_u2p14[0]	1311
tgt_CompBlnd_Uls_T_u2p14[1]	1802
tgt_CompBlnd_Uls_T_u2p14[2]	2294
tgt_CompBlnd_Uls_T_u2p14[3]	2785
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	28.0291485
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	19.1520594
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	76.17866968
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-748.9277
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	21.637
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	97.43
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	36.4548515
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	35.777
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	99.795592
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	40.667703
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	26.6068812
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	97.43
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709

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ApplyStabilityComp2

Name	Input Value
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.23626573
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	98.5559616	98.55595576 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-34.2133484	-34.21334777 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	55.0494156	55.04941696 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-1114.63831	-1114.638271 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	498.471161	498.4711618 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-630.450012	-630.4500159 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	130.12323	130.123216 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-40.5539894	-40.55398846 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	63.4751205	63.47511996 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	290.61676	290.6167688 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-115.04702	-115.0470265 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	159.145386	159.1453871 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	4
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[1].B2_Uls_f32	45.83360735
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[2].B2_Uls_f32	46.83360735
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179



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ApplyStabilityComp2

Name	Input Value
t_StCmpNFK_Str[3].B2_Uls_f32	47.83360735
tgt_CompBlnd_Uls_T_u2p14[0]	1475
tgt_CompBlnd_Uls_T_u2p14[1]	1966
tgt_CompBlnd_Uls_T_u2p14[2]	2458
tgt_CompBlnd_Uls_T_u2p14[3]	2949
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	30.9881782
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	22.1110891
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	176.86
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	35.777
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	25.172
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	177.67
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	25.0701188
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	39.312
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	9.5125243
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	45.3546436
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	31.2938218
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	177.67
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-325.174957	-325.1749542 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	200.519104	200.5191112 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	41.507019	41.5070175 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-309.524719	-309.5247086 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	225.13504	225.1350496 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	81.899704	81.8996994 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-309.818542	-309.8185465 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	205.498596	205.498605 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	35.5889587	35.5889581 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-300.417786	-300.4177876 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	238.023361	238.0233636 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	91.4773407	91.477343 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

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ApplyStabilityComp2



## Test Step 2.11 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	16384
Assist_MtrNm_T_f32	6
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[1].B2_Uls_f32	104.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[2].B2_Uls_f32	105.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[3].B2_Uls_f32	106.397464
tgt_CompBlnd_Uls_T_u2p14[0]	1638
tgt_CompBlnd_Uls_T_u2p14[1]	2130
tgt_CompBlnd_Uls_T_u2p14[2]	2621
tgt_CompBlnd_Uls_T_u2p14[3]	3113
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	33.9472079
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	25.0701188
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	176.86
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	39.312
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	28.707
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	867.24
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	28.0291485
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	21.637
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	10.330346
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	50.0415842
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	35.9807624
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	867.24
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1185.33228	-1185.332202 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	613.961853	613.9618792 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	49.7254677	49.72546686 ± 0.00489	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1139.45117	-1139.451199 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	612.370605	612.3706415 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	108.496048	108.4960491 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1193.01929	-1193.019204 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	626.726257	626.726299 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	43.8074074	43.80740746 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1124.46509	-1124.465043 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	622.984741	622.98473 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	119.225632	119.2256333 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	0
Assist_MtrNm_T_f32	5
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[0].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[0].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[1].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[1].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[1].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[2].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[2].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[2].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[2].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[3].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[3].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[3].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[3].B2_Uls_f32	89.70733524
tgt_CompBlnd_Uls_T_u2p14[0]	1802
tgt_CompBlnd_Uls_T_u2p14[1]	2294
tgt_CompBlnd_Uls_T_u2p14[2]	2785
tgt_CompBlnd_Uls_T_u2p14[3]	3277
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	19.1520594
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	37.80256412
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	99.795592
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	21.637
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	430.88
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	100
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	30.9881782
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	25.172
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	76.17866968
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	26.6068812

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Name	Input Value
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	1217.034872
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	100
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-896.10614	-896.1060697 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	447.286224	447.2862292 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	32.3006058	32.30060853 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-471.958282	-471.9582396 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	445.467133	445.4671239 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	79.2903748	79.29037425 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-900.910461	-900.9104004 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	446.828033	446.8280199 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	44.1367264	44.13672733 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	317.482788	317.4827983 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	445.274719	445.2747258 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	84.2602539	84.26025545 ± 0.000489	✓

## Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.13 (Repeat Count = 1) ✓

Name	Input Value
ADDFBlend_Uls_T_u2p14	11469
Assist_MtrNm_T_f32	1
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101

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Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
tgt_CompBlnd_Uls_T_u2p14[0]	0		
tgt_CompBlnd_Uls_T_u2p14[1]	0		
tgt_CompBlnd_Uls_T_u2p14[2]	0		
tgt_CompBlnd_Uls_T_u2p14[3]	0		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	149.8747		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-142.8569		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-176.86		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	25.172		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	495.1		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	96		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	33.9472079		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	32.242		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-176.86		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-748.9277		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1400.772017		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	96		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-162.795135	-162.7951245 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	137.320938	137.3209397 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	152.50441	152.5044098 ± 0.00489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	458.239197	458.2391842 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	48.1031151	48.10311512 ± 0.00049	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	36.7026749	36.70267485 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-4.63719225	-4.637193216 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	48.0062294	48.00622731 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	36.5769196	36.57691773 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1250.7887	1250.788778 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-548.291138	-548.2911549 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-737.396973	-737.3970252 ± 0.000489	✔

# TEST DETAILS REPORT

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ApplyStabilityComp2



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	14746
Assist_MtrNm_T_f32	4
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412
tgt_CompBlnd_Uls_T_u2p14[0]	16384
tgt_CompBlnd_Uls_T_u2p14[1]	16384
tgt_CompBlnd_Uls_T_u2p14[2]	16384
tgt_CompBlnd_Uls_T_u2p14[3]	16384
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	28.0291485
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	37.80256412
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	10.330346
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	32.242
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	430.88
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	191
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	22.1110891
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	25.172
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	176.86
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	40.667703
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	430.88
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	191
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709

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ApplyStabilityComp2

Name	Input Value
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.23626573
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	37.2584343	37.25843446 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	31.575779	31.57577902 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	38.5479889	38.5479878 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	438.20459	438.2045841 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	60.9165955	60.91659931 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	78.3647003	78.3646994 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	23.4583244	23.45832334 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	27.2147789	27.21477807 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	32.6299286	32.6299284 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	439.86972	439.8697002 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	67.1254807	67.12547563 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	86.7904053	86.7904024 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.15 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	11469
Assist_MtrNm_T_f32	10
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523

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Name	Input Value
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914
tgt_CompBlnd_Uls_T_u2p14[0]	2294
tgt_CompBlnd_Uls_T_u2p14[1]	2785
tgt_CompBlnd_Uls_T_u2p14[2]	3277
tgt_CompBlnd_Uls_T_u2p14[3]	3768
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	19.1520594
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	40.0332778
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	76.17866968
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	21.637
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	462.99
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	97.43
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	30.9881782
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	32.242
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	19.795592
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	26.6068812
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	28.707
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	191
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	78.7039871	78.70398637 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	5.58321667	5.583216874 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	45.4491577	45.44915766 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	582.288818	582.2888544 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	15.810524	15.8105243 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	136.943741	136.9437485 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	81.3430939	81.34309909 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	6.90626335	6.906263675 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	57.2852745	57.28527646 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	152.385483	152.3854828 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	16.3660603	16.3660599 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	141.913635	141.9136297 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓



# TEST DETAILS REPORT

ApplyStabilityComp2

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## Test Step 2.16 (Repeat Count = 1)



Name	Input Value
ADDFBlend_Uls_T_u2p14	4915
Assist_MtrNm_T_f32	-2.3
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	0.929541087
tgt_CompBlnd_Uls_T_u2p14[0]	2458
tgt_CompBlnd_Uls_T_u2p14[1]	2949
tgt_CompBlnd_Uls_T_u2p14[2]	3441
tgt_CompBlnd_Uls_T_u2p14[3]	3932
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-8.803286323
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-8.796749586
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-10.2749703
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-8.803286323
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-8.796749586
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-10.2749703
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-8.803286323
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-8.796749586
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-10.2749703
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-8.803286323
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-8.796749586
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-10.2749703
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541087
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.929541087
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.929541087
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.929541087

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-24.981535	-24.9815336 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	7.4134779	7.413476737 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-10.9682407	-10.96824007 ± 0.00489	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-24.981535	-24.9815336 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	7.4134779	7.413476737 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-10.9682407	-10.96824007 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-24.981535	-24.9815336 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	7.4134779	7.413476737 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-10.9682407	-10.96824007 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-24.981535	-24.9815336 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	7.4134779	7.413476737 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-10.9682407	-10.96824007 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	11469
Assist_MtrNm_T_f32	-2.43
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[0].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[0].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[1].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[1].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[1].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[2].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[2].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[2].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[3].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[3].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[3].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].B2_Uls_f32	0.832693873
tgt_CompBlnd_Uls_T_u2p14[0]	2621
tgt_CompBlnd_Uls_T_u2p14[1]	3113
tgt_CompBlnd_Uls_T_u2p14[2]	3604
tgt_CompBlnd_Uls_T_u2p14[3]	4096
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	8.803286323
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	8.796749586
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	10.2749703
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	8.803286323
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	8.796749586
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	10.2749703
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	8.803286323
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	8.796749586
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	10.2749703
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	8.803286323

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Name	Input Value
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	8.796749586
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	10.2749703
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541087
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.929541087
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.929541087
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.929541087

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	10831	10831	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	24.6617146	24.6617139 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-7.08586359	-7.085863771 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	6.51596546	6.515965622 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	24.6617146	24.6617139 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-7.08586359	-7.085863771 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	6.51596546	6.515965622 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	24.6617146	24.6617139 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-7.08586359	-7.085863771 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	6.51596546	6.515965622 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	24.6617146	24.6617139 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-7.08586359	-7.085863771 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	6.51596546	6.515965622 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.18 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	1637
Assist_MtrNm_T_f32	-2.56
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179

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Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735		
tgt_CompBlnd_Uls_T_u2p14[0]	2785		
tgt_CompBlnd_Uls_T_u2p14[1]	3277		
tgt_CompBlnd_Uls_T_u2p14[2]	3768		
tgt_CompBlnd_Uls_T_u2p14[3]	4260		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-4.586334043		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-4.579654921		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-10.2749613		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-4.586334043		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-4.579654921		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-10.2749613		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-4.586334043		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-4.579654921		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-10.2749613		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-4.586334043		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-4.579654921		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-10.2749613		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.832693873		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-13136	-13136	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	223.780029	223.7800378 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-118.348572	-118.3485706 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-7.00356627	-7.003566148 ± 0.00489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	223.780029	223.7800378 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-118.348572	-118.3485706 ± 0.00049	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-7.00356627	-7.003566148 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	223.780029	223.7800378 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-118.348572	-118.3485706 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-7.00356627	-7.003566148 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	223.780029	223.7800378 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-118.348572	-118.3485706 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-7.00356627	-7.003566148 ± 0.000489	✔

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ApplyStabilityComp2



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	-2.69
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464
tgt_CompBlnd_Uls_T_u2p14[0]	2949
tgt_CompBlnd_Uls_T_u2p14[1]	3441
tgt_CompBlnd_Uls_T_u2p14[2]	3932
tgt_CompBlnd_Uls_T_u2p14[3]	4424
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	4.586334043
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	4.579654921
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	10.2749613
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	4.586334043
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	4.579654921
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	10.2749613
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	4.586334043
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	4.579654921
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	10.2749613
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	4.586334043
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	4.579654921
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	10.2749613
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925164
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.944231291
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.832693873
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.776925164

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ApplyStabilityComp2

Name	Input Value
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.944231291
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.832693873
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.776925164
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.944231291
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.832693873
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.776925164
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.944231291
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.832693873

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	2783	2783	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	564.738831	564.7388367 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-278.403503	-278.4034999 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2.04635215	2.04635187 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	564.738831	564.7388367 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-278.403503	-278.4034999 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2.04635215	2.04635187 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	564.738831	564.7388367 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-278.403503	-278.4034999 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2.04635215	2.04635187 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	564.738831	564.7388367 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-278.403503	-278.4034999 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2.04635215	2.04635187 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	31130
Assist_MtrNm_T_f32	-2.82
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[0].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[0].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[1].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[1].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[1].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[2].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[2].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[2].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[2].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[3].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[3].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[3].B1_Uls_f32	-191.0532639

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ApplyStabilityComp2

Name	Input Value		
t_StCmpNFK_Str[3].B2_Uls_f32	89.70733524		
tgt_CompBlnd_Uls_T_u2p14[0]	3113		
tgt_CompBlnd_Uls_T_u2p14[1]	3604		
tgt_CompBlnd_Uls_T_u2p14[2]	4096		
tgt_CompBlnd_Uls_T_u2p14[3]	4588		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1844.703127		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1640.081281		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2311.06356		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1844.703127		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1640.081281		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2311.06356		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1844.703127		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1640.081281		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2311.06356		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1844.703127		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1640.081281		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-2311.06356		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.83360735		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-2405.71289	-2405.712858 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-176.604736	-176.6047415 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-1972.73108	-1972.731135 ± 0.00489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-2405.71289	-2405.712858 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-176.604736	-176.6047415 ± 0.00049	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-1972.73108	-1972.731135 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-2405.71289	-2405.712858 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-176.604736	-176.6047415 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-1972.73108	-1972.731135 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-2405.71289	-2405.712858 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-176.604736	-176.6047415 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-1972.73108	-1972.731135 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

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ApplyStabilityComp2



## Test Step 2.21 (Repeat Count = 1)



Name	Input Value
ADDFBlend_Uls_T_u2p14	3277
Assist_MtrNm_T_f32	-2.95
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343
tgt_CompBlnd_Uls_T_u2p14[0]	3277
tgt_CompBlnd_Uls_T_u2p14[1]	3768
tgt_CompBlnd_Uls_T_u2p14[2]	4260
tgt_CompBlnd_Uls_T_u2p14[3]	4751
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1844.703127
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1640.081281
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2311.06356
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1844.703127
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1640.081281
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2311.06356
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1844.703127
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1640.081281
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2311.06356
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1844.703127
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1640.081281
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2311.06356
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673654
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.40000283
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.05567179
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.83360735
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673654
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.40000283
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.05567179
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.83360735
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673654
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.40000283
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.05567179
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.83360735
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673654
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.40000283
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.05567179
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.83360735

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	2014.64551	2014.645481 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1259.55432	1259.554327 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	1710.77307	1710.773119 ± 0.00489	✓



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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	2014.64551	2014.645481 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1259.55432	1259.554327 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	1710.77307	1710.773119 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	2014.64551	2014.645481 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1259.55432	1259.554327 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	1710.77307	1710.773119 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	2014.64551	2014.645481 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1259.55432	1259.554327 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	1710.77307	1710.773119 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	-3.08
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412
tgt_CompBlnd_Uls_T_u2p14[0]	3441
tgt_CompBlnd_Uls_T_u2p14[1]	3932
tgt_CompBlnd_Uls_T_u2p14[2]	4424
tgt_CompBlnd_Uls_T_u2p14[3]	4915
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1567.617957
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1099.795592
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2311.793127
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1567.617957
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1099.795592
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2311.793127
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1567.617957
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1099.795592
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2311.793127
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1567.617957

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Name	Input Value
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-1099.795592
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-2311.793127
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✓

## Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.23 (Repeat Count = 1) ✓

Name	Input Value
ADDFBlend_Uls_T_u2p14	13107
Assist_MtrNm_T_f32	-3.21
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523

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Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914		
tgt_CompBlnd_Uls_T_u2p14[0]	3604		
tgt_CompBlnd_Uls_T_u2p14[1]	4096		
tgt_CompBlnd_Uls_T_u2p14[2]	4588		
tgt_CompBlnd_Uls_T_u2p14[3]	5079		
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	1567.617957		
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	1099.795592		
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	2311.793127		
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	1567.617957		
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	1099.795592		
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	2311.793127		
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	1567.617957		
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	1099.795592		
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	2311.793127		
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	1567.617957		
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	1099.795592		
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	2311.793127		
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	2185.49512	2185.495246 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	137.498337	137.4983372 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	1231.51904	1231.519038 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	2185.49512	2185.495246 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	137.498337	137.4983372 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	1231.51904	1231.519038 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	2185.49512	2185.495246 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	137.498337	137.4983372 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	1231.51904	1231.519038 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	2185.49512	2185.495246 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	137.498337	137.4983372 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	1231.51904	1231.519038 ± 0.000489	✓

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ApplyStabilityComp2



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	1638
Assist_MtrNm_T_f32	-3.34
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	0.929541087
tgt_CompBlnd_Uls_T_u2p14[0]	3768
tgt_CompBlnd_Uls_T_u2p14[1]	4260
tgt_CompBlnd_Uls_T_u2p14[2]	4751
tgt_CompBlnd_Uls_T_u2p14[3]	5243
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1337.910389
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-926.5125243
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2311.715352
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1337.910389
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-926.5125243
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2311.715352
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1337.910389
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-926.5125243
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2311.715352
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1337.910389
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-926.5125243
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-2311.715352
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.038712799

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ApplyStabilityComp2

Name	Input Value
tgt_KPTr_Cnt_Str1.B0_Uls_f32	101.7234252
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-191.0532639
tgt_KPTr_Cnt_Str1.B2_Uls_f32	89.70733524
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.661216198
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.038712799
tgt_KPTr_Cnt_Str2.B0_Uls_f32	101.7234252
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-191.0532639
tgt_KPTr_Cnt_Str2.B2_Uls_f32	89.70733524
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.661216198
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.038712799
tgt_KPTr_Cnt_Str3.B0_Uls_f32	101.7234252
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-191.0532639
tgt_KPTr_Cnt_Str3.B2_Uls_f32	89.70733524

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-4052.70825	-4052.708227 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1457.84998	1457.849922 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-1677.66663	-1677.666629 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-4052.70825	-4052.708227 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1457.84998	1457.849922 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-1677.66663	-1677.666629 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-4052.70825	-4052.708227 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1457.84998	1457.849922 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-1677.66663	-1677.666629 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-4052.70825	-4052.708227 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1457.84998	1457.849922 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-1677.66663	-1677.666629 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.25 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	65534
Assist_MtrNm_T_f32	-3.47
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[0].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[0].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[1].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[1].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[1].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[2].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[2].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[2].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[3].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[3].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[3].B1_Uls_f32	-1.773421113

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ApplyStabilityComp2

Name	Input Value		
t_StCmpNFK_Str[3].B2_Uls_f32	0.832693873		
tgt_CompBlnd_Uls_T_u2p14[0]	3932		
tgt_CompBlnd_Uls_T_u2p14[1]	4424		
tgt_CompBlnd_Uls_T_u2p14[2]	4915		
tgt_CompBlnd_Uls_T_u2p14[3]	5407		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1337.910389		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	926.5125243		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2311.715352		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1337.910389		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	926.5125243		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2311.715352		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1337.910389		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	926.5125243		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2311.715352		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1337.910389		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	926.5125243		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2311.715352		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	89.70733524		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	2679.3623	2679.362136 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-768.106445	-768.10643 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	984.930115	984.9301036 ± 0.00489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	2679.3623	2679.362136 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-768.106445	-768.10643 ± 0.00049	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	984.930115	984.9301036 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	2679.3623	2679.362136 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-768.106445	-768.10643 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	984.930115	984.9301036 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	2679.3623	2679.362136 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-768.106445	-768.10643 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	984.930115	984.9301036 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

# TEST DETAILS REPORT

ApplyStabilityComp2

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Test Step 2.26 (Repeat Count = 1)			
Name	Input Value		
ADDFBlend_Uls_T_u2p14	16419		
Assist_MtrNm_T_f32	-3.6		
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14		
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str		
WriteDisplayVars_Cnt_T_lgc	0		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735		
tgt_CompBlnd_Uls_T_u2p14[0]	4096		
tgt_CompBlnd_Uls_T_u2p14[1]	4588		
tgt_CompBlnd_Uls_T_u2p14[2]	5079		
tgt_CompBlnd_Uls_T_u2p14[3]	5571		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-2076.199938		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1980.330346		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2306.490226		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-2076.199938		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1980.330346		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2306.490226		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-2076.199938		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1980.330346		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2306.490226		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-2076.199938		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1980.330346		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-2306.490226		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	19.82605343		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-2328.45166	-2328.45179 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1262.37622	-1262.376245 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2157.13403	-2157.134085 ± 0.00489	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-2328.45166	-2328.45179 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-1262.37622	-1262.376245 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-2157.13403	-2157.134085 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-2328.45166	-2328.45179 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-1262.37622	-1262.376245 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-2157.13403	-2157.134085 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-2328.45166	-2328.45179 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-1262.37622	-1262.376245 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-2157.13403	-2157.134085 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.27 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	1638
Assist_MtrNm_T_f32	-3.73
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464
tgt_CompBlnd_Uls_T_u2p14[0]	4260
tgt_CompBlnd_Uls_T_u2p14[1]	4751
tgt_CompBlnd_Uls_T_u2p14[2]	5243
tgt_CompBlnd_Uls_T_u2p14[3]	5734
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	2076.199938
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	1980.330346
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	2306.490226
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	2076.199938
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	1980.330346
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	2306.490226
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	2076.199938
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	1980.330346
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	2306.490226
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	2076.199938



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Name	Input Value
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1980.330346
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2306.490226
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134178
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.770435992
tgt_KPtr_Cnt_Str.B0_Uls_f32	22.48170741
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.22433101
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.146134178
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.770435992
tgt_KPtr_Cnt_Str1.B0_Uls_f32	22.48170741
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-42.22433101
tgt_KPtr_Cnt_Str1.B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134178
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.770435992
tgt_KPtr_Cnt_Str2.B0_Uls_f32	22.48170741
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-42.22433101
tgt_KPtr_Cnt_Str2.B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134178
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.770435992
tgt_KPtr_Cnt_Str3.B0_Uls_f32	22.48170741
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-42.22433101
tgt_KPtr_Cnt_Str3.B2_Uls_f32	19.82605343

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	4187.10742	4187.10748 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-643.018127	-643.0181405 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	1992.34314	1992.343169 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	4187.10742	4187.10748 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-643.018127	-643.0181405 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	1992.34314	1992.343169 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	4187.10742	4187.10748 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-643.018127	-643.0181405 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	1992.34314	1992.343169 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	4187.10742	4187.10748 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-643.018127	-643.0181405 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	1992.34314	1992.343169 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.28 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	14746
Assist_MtrNm_T_f32	-3.86
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[0].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[0].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[1].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[1].B1_Uls_f32	-191.0532639

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ApplyStabilityComp2

Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[2].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[2].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[2].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[3].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[3].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[3].B2_Uls_f32	89.70733524		
tgt_CompBlnd_Uls_T_u2p14[0]	4424		
tgt_CompBlnd_Uls_T_u2p14[1]	4915		
tgt_CompBlnd_Uls_T_u2p14[2]	5407		
tgt_CompBlnd_Uls_T_u2p14[3]	5898		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-78.86288978		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-76.17866968		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-92.34203664		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-78.86288978		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-76.17866968		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-92.34203664		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-78.86288978		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-76.17866968		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-92.34203664		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-78.86288978		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-76.17866968		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-92.34203664		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623396		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897125		
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.040528483		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469412		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623396		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897125		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.040528483		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.792469412		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623396		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897125		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.040528483		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.792469412		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623396		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897125		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.040528483		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.792469412		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	605.78894	605.7889716 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-343.020996	-343.0210269 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-83.9331512	-83.93314859 ± 0.00489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	605.78894	605.7889716 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-343.020996	-343.0210269 ± 0.00049	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-83.9331512	-83.93314859 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	605.78894	605.7889716 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-343.020996	-343.0210269 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-83.9331512	-83.93314859 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	605.78894	605.7889716 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-343.020996	-343.0210269 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-83.9331512	-83.93314859 ± 0.000489	✔

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ApplyStabilityComp2



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.29 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	7373
Assist_MtrNm_T_f32	-3.99
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343
tgt_CompBlnd_Uls_T_u2p14[0]	4588
tgt_CompBlnd_Uls_T_u2p14[1]	5079
tgt_CompBlnd_Uls_T_u2p14[2]	5571
tgt_CompBlnd_Uls_T_u2p14[3]	6062
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	78.86288978
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	76.17866968
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	92.34203664
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	78.86288978
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	76.17866968
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	92.34203664
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	78.86288978
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	76.17866968
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	92.34203664
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	78.86288978
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	76.17866968
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	92.34203664
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897125
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.040528483
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469412
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897125

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ApplyStabilityComp2

Name	Input Value
tgt_KPTr_Cnt_Str1.B0_Uls_f32	1.31353855
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-2.040528483
tgt_KPTr_Cnt_Str1.B2_Uls_f32	0.792469412
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.197623396
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.736897125
tgt_KPTr_Cnt_Str2.B0_Uls_f32	1.31353855
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-2.040528483
tgt_KPTr_Cnt_Str2.B2_Uls_f32	0.792469412
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.197623396
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.736897125
tgt_KPTr_Cnt_Str3.B0_Uls_f32	1.31353855
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-2.040528483
tgt_KPTr_Cnt_Str3.B2_Uls_f32	0.792469412

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	255.412415	255.412422 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-22.3850174	-22.385014 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	73.6218719	73.62187097 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	255.412415	255.412422 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-22.3850174	-22.385014 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	73.6218719	73.62187097 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	255.412415	255.412422 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-22.3850174	-22.385014 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	73.6218719	73.62187097 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	255.412415	255.412422 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-22.3850174	-22.385014 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	73.6218719	73.62187097 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	7586
Assist_MtrNm_T_f32	-4.12
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483

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ApplyStabilityComp2

Name	Input Value
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412
tgt_CompBlnd_Uls_T_u2p14[0]	4751
tgt_CompBlnd_Uls_T_u2p14[1]	5243
tgt_CompBlnd_Uls_T_u2p14[2]	5734
tgt_CompBlnd_Uls_T_u2p14[3]	6226
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-5.277343599
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1.086842875
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-5.797149125
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-5.277343599
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1.086842875
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-5.797149125
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-5.277343599
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1.086842875
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-5.797149125
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-5.277343599
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1.086842875
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-5.797149125
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.881234018
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.11178046
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.094760132
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.138063523
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.050288914
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.881234018
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.11178046
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.094760132
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.138063523
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.050288914
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.881234018
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.11178046
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.094760132
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.138063523
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.050288914
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.881234018
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.11178046
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.094760132
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.138063523
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.050288914

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-10995	-10995	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	6.20005322	6.200053417 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-7.44152641	-7.441526595 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-5.6677556	-5.667755343 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	6.20005322	6.200053417 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-7.44152641	-7.441526595 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-5.6677556	-5.667755343 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	6.20005322	6.200053417 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-7.44152641	-7.441526595 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-5.6677556	-5.667755343 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	6.20005322	6.200053417 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-7.44152641	-7.441526595 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-5.6677556	-5.667755343 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

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ApplyStabilityComp2



## Test Step 2.31 (Repeat Count = 1)



Name	Input Value
ADDFBlend_Uls_T_u2p14	7799
Assist_MtrNm_T_f32	-4.25
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914
tgt_CompBlnd_Uls_T_u2p14[0]	4915
tgt_CompBlnd_Uls_T_u2p14[1]	5407
tgt_CompBlnd_Uls_T_u2p14[2]	5898
tgt_CompBlnd_Uls_T_u2p14[3]	6390
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	5.277343599
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1.086842875
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	5.797149125
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	5.277343599
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1.086842875
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	5.797149125
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	5.277343599
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1.086842875
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	5.797149125
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	5.277343599
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1.086842875
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	5.797149125
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.0184
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.0072
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.094760132
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.22433101
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.0194
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.0059
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.2
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-46
tgt_KPtr_Cnt_Str1.B2_Uls_f32	21
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.0054
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.0102
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.305239868
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-49.77566899
tgt_KPtr_Cnt_Str2.B2_Uls_f32	22.17394657
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.0214
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.0075
tgt_KPtr_Cnt_Str3.B0_Uls_f32	3.410479736
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-53.55133798
tgt_KPtr_Cnt_Str3.B2_Uls_f32	23.34789315

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-2136	-2136	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	5.96928787	5.969287681 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	0.331158638	0.331158603 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	4.87461329	4.874613038 ± 0.00489	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	1.82989383	1.82989406 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-0.19390437	-0.193904335 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	0.177343369	0.177343599 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-2.30949974	-2.309499562 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-0.718967319	-0.718967274 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-4.51992607	-4.51992584 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-6.44889355	-6.448893183 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-1.24403024	-1.244030213 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-9.21719551	-9.217195279 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.32 (Repeat Count = 1)	
Name	Input Value
ADDBlend_Uls_T_u2p14	4915
Assist_MtrNm_T_f32	-2.3
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-2
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-2
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	90.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-2
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	91.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-2
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483
t_StCmpNFK_Str[3].B2_Uls_f32	92.70733524
tgt_CompBlnd_Uls_T_u2p14[0]	2458
tgt_CompBlnd_Uls_T_u2p14[1]	2949
tgt_CompBlnd_Uls_T_u2p14[2]	3441
tgt_CompBlnd_Uls_T_u2p14[3]	3932
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-8.803286323
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-8.796749586
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	-10.2749703
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-8.803286323
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-8.796749586
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-10.2749703
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-8.803286323
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-8.796749586
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-10.2749703
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-8.803286323

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ApplyStabilityComp2



Name	Input Value		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-8.796749586		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-10.2749703		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.929541087		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-26.0400162	-26.04001422 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-214.409332	-214.4093356 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-10.9682407	-10.96824007 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-25.8100147	-25.81001422 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-216.709335	-216.7093356 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-10.9682407	-10.96824007 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-25.5800152	-25.58001422 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-219.009323	-219.0093356 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-10.9682407	-10.96824007 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-25.3500156	-25.35001422 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-221.309326	-221.3093356 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-10.9682407	-10.96824007 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.33 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	11469
Assist_MtrNm_T_f32	-2.43
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	0
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	0
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523



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ApplyStabilityComp2

Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	20.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	0		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	21.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	0		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	22.82605343		
tgt_CompBlnd_Uls_T_u2p14[0]	2621		
tgt_CompBlnd_Uls_T_u2p14[1]	3113		
tgt_CompBlnd_Uls_T_u2p14[2]	3604		
tgt_CompBlnd_Uls_T_u2p14[3]	4096		
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	8.803286323		
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	8.796749586		
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	10.2749703		
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	8.803286323		
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	8.796749586		
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	10.2749703		
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	8.803286323		
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	8.796749586		
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	10.2749703		
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	8.803286323		
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	8.796749586		
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	10.2749703		
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.929541087		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	10831	10831	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	9.13224316	9.132243947 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-43.3757133	-43.3757135 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	6.51596546	6.515965622 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	9.15654373	9.156543947 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-45.8057137	-45.8057135 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	6.51596546	6.515965622 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	9.20514393	9.205143947 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-48.235714	-48.2357135 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	6.51596546	6.515965622 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	9.13467312	9.134673947 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-50.6657143	-50.6657135 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	6.51596546	6.515965622 ± 0.000489	✓

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ApplyStabilityComp2



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.34 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	1638
Assist_MtrNm_T_f32	-2.56
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-1.1
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-1.2
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	1.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-1.3
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	2.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-1.4
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	3.792469412
tgt_CompBlnd_Uls_T_u2p14[0]	2785
tgt_CompBlnd_Uls_T_u2p14[1]	3277
tgt_CompBlnd_Uls_T_u2p14[2]	3768
tgt_CompBlnd_Uls_T_u2p14[3]	4260
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-4.586334043
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-4.579654921
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-10.2749613
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-4.586334043
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-4.579654921
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-10.2749613
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-4.586334043
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-4.579654921
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-10.2749613
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-4.586334043
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-4.579654921
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-10.2749613
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925164
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.944231291
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.832693873
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.776925164

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ApplyStabilityComp2

Name	Input Value
tgt_KPTr_Cnt_Str1.B0_Uls_f32	0.944231291
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-1.773421113
tgt_KPTr_Cnt_Str1.B2_Uls_f32	0.832693873
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-1.773421113
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.776925164
tgt_KPTr_Cnt_Str2.B0_Uls_f32	0.944231291
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-1.773421113
tgt_KPTr_Cnt_Str2.B2_Uls_f32	0.832693873
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-1.773421113
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.776925164
tgt_KPTr_Cnt_Str3.B0_Uls_f32	0.944231291
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-1.773421113
tgt_KPTr_Cnt_Str3.B2_Uls_f32	0.832693873

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-13136	-13136	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-7.50370979	-7.50370925255157 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-2.81158352	-2.81158354 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	-7.00356627	-7.003566148 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-7.94806576	-7.94806586739143 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-5.37158346	-5.37158354 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-7.00356627	-7.003566148 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-9.16042328	-9.1604224822313 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-7.9315834	-7.93158354 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-7.00356627	-7.003566148 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-9.60477924	-9.60477909707117 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-10.4915838	-10.49158354 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-7.00356627	-7.003566148 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.35 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	-2.69
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	-1
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	-1
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	1.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[2].A2_Uls_f32	-1
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	1.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[3].A2_Uls_f32	-1
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483

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Name	Input Value		
t_StCmpNFK_Str[3].B2_Uls_f32	1.050288914		
tgt_CompBlnd_Uls_T_u2p14[0]	2949		
tgt_CompBlnd_Uls_T_u2p14[1]	3441		
tgt_CompBlnd_Uls_T_u2p14[2]	3932		
tgt_CompBlnd_Uls_T_u2p14[3]	4424		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	4.586334043		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	4.579654921		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	10.2749613		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	4.586334043		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	4.579654921		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	10.2749613		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	4.586334043		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	4.579654921		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	10.2749613		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	4.586334043		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	4.579654921		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	10.2749613		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.832693873		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	2783	2783	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	11.4217567	11.42175754 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1.911075	1.911074692 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2.04635215	2.04635187 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	11.6907578	11.69075754 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-0.77892518	-0.778925308 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2.04635215	2.04635187 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	11.9597569	11.95975754 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-0.77892518	-0.778925308 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2.04635215	2.04635187 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	12.2287579	12.22875754 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-0.77892518	-0.778925308 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2.04635215	2.04635187 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

# TEST DETAILS REPORT

ApplyStabilityComp2

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Test Step 2.36 (Repeat Count = 1)			
Name	Input Value		
ADDFBlend_Uls_T_u2p14	14746		
Assist_MtrNm_T_f32	-2.82		
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14		
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str		
WriteDisplayVars_Cnt_T_lgc	1		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[0].A2_Uls_f32	1		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[1].A2_Uls_f32	1		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523		
t_StCmpNFK_Str[1].B2_Uls_f32	2.929541087		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	1		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	3.929541087		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	1		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	4.929541087		
tgt_CompBlnd_Uls_T_u2p14[0]	3113		
tgt_CompBlnd_Uls_T_u2p14[1]	3604		
tgt_CompBlnd_Uls_T_u2p14[2]	4096		
tgt_CompBlnd_Uls_T_u2p14[3]	4588		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1844.703127		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1640.081281		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2311.06356		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1844.703127		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1640.081281		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2311.06356		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1844.703127		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1640.081281		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2311.06356		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1844.703127		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1640.081281		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-2311.06356		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.83360735		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1927.97546	-1927.975385 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1970.10974	1970.109829 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-1972.73108	-1972.731135 ± 0.000489	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1927.94727	-1927.947185 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1964.46973	1964.469829 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-1972.73108	-1972.731135 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1927.89075	-1927.890785 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1961.64978	1961.649829 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-1972.73108	-1972.731135 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1927.97253	-1927.972565 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1958.82983	1958.829829 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-1972.73108	-1972.731135 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.37 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	3277
Assist_MtrNm_T_f32	-2.95
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	0
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	0
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	90.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	0
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	91.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	0
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	92.70733524
tgt_CompBlnd_Uls_T_u2p14[0]	3277
tgt_CompBlnd_Uls_T_u2p14[1]	3768
tgt_CompBlnd_Uls_T_u2p14[2]	4260
tgt_CompBlnd_Uls_T_u2p14[3]	4751
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1844.703127
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1640.081281
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2311.06356
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1844.703127
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1640.081281
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2311.06356
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1844.703127
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1640.081281
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2311.06356
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1844.703127

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Name	Input Value
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	1640.081281
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	2311.06356
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673654
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.40000283
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.05567179
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.83360735
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673654
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.40000283
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.05567179
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.83360735
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673654
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.40000283
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.05567179
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.83360735
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673654
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.40000283
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.05567179
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.83360735

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	1983.67822	1983.678127 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-264.636658	-264.636639 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	1710.77307	1710.773119 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	1983.97314	1983.973127 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-267.586639	-267.586639 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	1710.77307	1710.773119 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	1983.38306	1983.383127 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-270.536652	-270.536639 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	1710.77307	1710.773119 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	1983.67822	1983.678127 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-273.486633	-273.486639 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	1710.77307	1710.773119 ± 0.000489	✓

## Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.38 (Repeat Count = 1) ✓

Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	-3.08
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	0.4
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	0.4
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483

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Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	20.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	0.4		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	21.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	0.4		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	22.82605343		
tgt_CompBlnd_Uls_T_u2p14[0]	3441		
tgt_CompBlnd_Uls_T_u2p14[1]	3932		
tgt_CompBlnd_Uls_T_u2p14[2]	4424		
tgt_CompBlnd_Uls_T_u2p14[3]	4915		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-2759.13574	-2759.135934 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	694.977905	694.9779127 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-2758.82788	-2758.827934 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	691.897949	691.8979127 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-2758.51978	-2758.519934 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	688.817932	688.8179127 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-2758.21191	-2758.211934 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	685.737915	685.7379127 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✔



# TEST DETAILS REPORT

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ApplyStabilityComp2



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.39 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	13107
Assist_MtrNm_T_f32	-3.21
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	-0.4
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	-0.4
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523
t_StCmpNFK_Str[1].B2_Uls_f32	1.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	-0.4
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523
t_StCmpNFK_Str[2].B2_Uls_f32	2.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	-0.4
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523
t_StCmpNFK_Str[3].B2_Uls_f32	3.792469412
tgt_CompBlnd_Uls_T_u2p14[0]	3604
tgt_CompBlnd_Uls_T_u2p14[1]	4096
tgt_CompBlnd_Uls_T_u2p14[2]	4588
tgt_CompBlnd_Uls_T_u2p14[3]	5079
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306

# TEST DETAILS REPORT

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ApplyStabilityComp2

Name	Input Value
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	3399.65234	3399.652438 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	490.063812	490.0637885 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	1231.51904	1231.519038 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	3399.68457	3399.684538 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	486.853821	486.8537885 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	1231.51904	1231.519038 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	3399.74854	3399.748738 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	483.643799	483.6437885 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	1231.51904	1231.519038 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	3399.65552	3399.655648 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	480.433807	480.4337885 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	1231.51904	1231.519038 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.40 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	1638
Assist_MtrNm_T_f32	-3.34
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	1.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106

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Name	Input Value
t_StCmpNFK_Str[3].B2_Uls_f32	1.050288914
tgt_CompBlnd_Uls_T_u2p14[0]	3768
tgt_CompBlnd_Uls_T_u2p14[1]	4260
tgt_CompBlnd_Uls_T_u2p14[2]	4751
tgt_CompBlnd_Uls_T_u2p14[3]	5243
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-1337.910389
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-926.5125243
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	-2311.715352
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-1337.910389
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-926.5125243
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-2311.715352
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-1337.910389
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-926.5125243
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-2311.715352
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-1337.910389
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-926.5125243
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-2311.715352
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str1.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str1.B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str2.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str2.B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str3.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str3.B2_Uls_f32	89.70733524

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-1251.82251	-1251.822466 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-187.698318	-187.6983125 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	-1677.66663	-1677.666629 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-1251.4884	-1251.488466 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-191.038315	-191.0383125 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-1677.66663	-1677.666629 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-1252.15649	-1252.156466 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-191.038315	-191.0383125 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-1677.66663	-1677.666629 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-1251.82251	-1251.822466 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-191.038315	-191.0383125 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-1677.66663	-1677.666629 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

# TEST DETAILS REPORT

ApplyStabilityComp2

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## Test Step 2.41 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	6554
Assist_MtrNm_T_f32	-3.47
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	2300
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	2300
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	2.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	2300
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	3.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	2300
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483
t_StCmpNFK_Str[3].B2_Uls_f32	4.929541087
tgt_CompBlnd_Uls_T_u2p14[0]	3932
tgt_CompBlnd_Uls_T_u2p14[1]	4424
tgt_CompBlnd_Uls_T_u2p14[2]	4915
tgt_CompBlnd_Uls_T_u2p14[3]	5407
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1337.910389
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	926.5125243
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2311.715352
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1337.910389
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	926.5125243
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2311.715352
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1337.910389
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	926.5125243
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2311.715352
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1337.910389
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	926.5125243
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2311.715352
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str1.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str1.B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str2.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str2.B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str3.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str3.B2_Uls_f32	89.70733524

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1801.54712	1801.547071 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-860.927612	-860.9275861 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	984.930115	984.9301036 ± 0.000489	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1801.89404	1801.894071 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-867.867615	-867.8675861 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	984.930115	984.9301036 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1802.24109	1802.241071 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-871.337585	-871.3375861 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	984.930115	984.9301036 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1802.58813	1802.588071 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-874.807617	-874.8075861 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	984.930115	984.9301036 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.42 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	11469
Assist_MtrNm_T_f32	-3.6
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	100.1
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	101.2
t_StCmpNFK_Str[1].B1_Uls_f32	-0.263523
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	102.3
t_StCmpNFK_Str[2].B1_Uls_f32	-0.38063523
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	103.4
t_StCmpNFK_Str[3].B1_Uls_f32	-1.138063523
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343
tgt_CompBlnd_Uls_T_u2p14[0]	4096
tgt_CompBlnd_Uls_T_u2p14[1]	4588
tgt_CompBlnd_Uls_T_u2p14[2]	5079
tgt_CompBlnd_Uls_T_u2p14[3]	5571
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-2076.199938
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1980.330346
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2306.490226
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-2076.199938
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1980.330346
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2306.490226
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-2076.199938
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1980.330346
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2306.490226
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-2076.199938

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Name	Input Value		
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-1980.330346		
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-2306.490226		
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	19.82605343		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✔
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	-3406.16504	-3406.165316 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-1660.95959	-1660.959698 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	-2157.13403	-2157.134085 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-3405.71338	-3405.713662 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-1660.95959	-1660.959698 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-2157.13403	-2157.134085 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-3405.29199	-3405.292058 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-1660.95959	-1660.959698 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-2157.13403	-2157.134085 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-3402.56519	-3402.565316 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-1660.95959	-1660.959698 ± 0.000489	✔
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-2157.13403	-2157.134085 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.43 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	1638
Assist_MtrNm_T_f32	-3.73
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-4500
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-4500

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Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-4500		
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-4500		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
tgt_CompBlnd_Uls_T_u2p14[0]	4260		
tgt_CompBlnd_Uls_T_u2p14[1]	4751		
tgt_CompBlnd_Uls_T_u2p14[2]	5243		
tgt_CompBlnd_Uls_T_u2p14[3]	5734		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	2076.199938		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1980.330346		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2306.490226		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	2076.199938		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1980.330346		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2306.490226		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	2076.199938		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1980.330346		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2306.490226		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	2076.199938		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1980.330346		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2306.490226		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134178		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.770435992		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	22.48170741		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-42.22433101		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	19.82605343		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	19056.4805	19056.47978 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	219.749115	219.749125 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	1992.34314	1992.343169 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	19056.4805	19056.47978 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	219.749115	219.749125 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	1992.34314	1992.343169 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	19056.4805	19056.47978 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	219.749115	219.749125 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	1992.34314	1992.343169 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	19056.4805	19056.47978 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	219.749115	219.749125 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	1992.34314	1992.343169 ± 0.000489	✓

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ApplyStabilityComp2



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.44 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	14746
Assist_MtrNm_T_f32	-3.86
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	0
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	0
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	0
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	0
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735
tgt_CompBlnd_Uls_T_u2p14[0]	4424
tgt_CompBlnd_Uls_T_u2p14[1]	4915
tgt_CompBlnd_Uls_T_u2p14[2]	5407
tgt_CompBlnd_Uls_T_u2p14[3]	5898
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-78.86288978
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-76.17866968
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-92.34203664
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-78.86288978
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-76.17866968
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-92.34203664
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-78.86288978
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-76.17866968
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-92.34203664
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-78.86288978
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-76.17866968
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-92.34203664
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897125
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.040528483
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469412
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897125



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ApplyStabilityComp2

Name	Input Value
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.040528483
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.792469412
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897125
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.040528483
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.792469412
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897125
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.040528483
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.792469412

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-92.7658234	-92.76582354 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-182.439804	-182.4398103 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-83.9331512	-83.93314859 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-92.7658234	-92.76582354 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-182.439804	-182.4398103 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-83.9331512	-83.93314859 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-92.7658234	-92.76582354 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-182.439804	-182.4398103 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-83.9331512	-83.93314859 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-92.7658234	-92.76582354 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-182.439804	-182.4398103 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-83.9331512	-83.93314859 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.45 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	7373
Assist_MtrNm_T_f32	-3.99
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-2300.1
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-2301.2
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-2302.3
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-2303.4

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ApplyStabilityComp2

Name	Input Value		
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464		
tgt_CompBlnd_Uls_T_u2p14[0]	4588		
tgt_CompBlnd_Uls_T_u2p14[1]	5079		
tgt_CompBlnd_Uls_T_u2p14[2]	5571		
tgt_CompBlnd_Uls_T_u2p14[3]	6062		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	78.86288978		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	76.17866968		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	92.34203664		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	78.86288978		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	76.17866968		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	92.34203664		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	78.86288978		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	76.17866968		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	92.34203664		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	78.86288978		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	76.17866968		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	92.34203664		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623396		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897125		
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.040528483		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469412		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623396		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897125		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.040528483		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.792469412		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623396		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897125		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.040528483		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.792469412		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623396		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897125		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.040528483		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.792469412		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	9318.45605	9318.4557668436 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-404.326385	-404.3263948 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	73.6218719	73.62187097 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	9322.84473	9322.84476684361 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-404.326385	-404.3263948 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	73.6218719	73.62187097 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	9327.2334	9327.23376684361 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-404.326385	-404.3263948 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	73.6218719	73.62187097 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	9331.62207	9331.62276684361 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-404.326385	-404.3263948 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	73.6218719	73.62187097 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

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ApplyStabilityComp2



Test Step 2.46 (Repeat Count = 1)			
Name	Input Value		
ADDFBlend_Uls_T_u2p14	7586		
Assist_MtrNm_T_f32	-4.12		
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14		
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str		
WriteDisplayVars_Cnt_T_lgc	1		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	-4		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523		
t_StCmpNFK_Str[1].B2_Uls_f32	-4		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	-4		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	-4		
tgt_CompBlnd_Uls_T_u2p14[0]	4751		
tgt_CompBlnd_Uls_T_u2p14[1]	5243		
tgt_CompBlnd_Uls_T_u2p14[2]	5734		
tgt_CompBlnd_Uls_T_u2p14[3]	6226		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-5.277343599		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1.086842875		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-5.797149125		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-5.277343599		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1.086842875		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-5.797149125		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-5.277343599		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1.086842875		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-5.797149125		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-5.277343599		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1.086842875		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-5.797149125		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.881234018		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.11178046		
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.094760132		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.138063523		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.050288914		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.881234018		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.11178046		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.094760132		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.138063523		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.050288914		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.881234018		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.11178046		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.094760132		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.138063523		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.050288914		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.881234018		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.11178046		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.094760132		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.138063523		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.050288914		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-10995	-10995	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-11.1004925	-11.1004918 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	21.4156246	21.41562489 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-5.6677556	-5.667755343 ± 0.000489	✓

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ApplyStabilityComp2

Name	Actual Value	Expected Value	Result
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	-11.0592918	-11.0592918 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	21.4156246	21.41562489 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-5.6677556	-5.667755343 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	-10.9768925	-10.9768918 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	21.4156246	21.41562489 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-5.6677556	-5.667755343 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	-11.0963717	-11.0963718 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	21.4156246	21.41562489 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-5.6677556	-5.667755343 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 2.47 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	7799
Assist_MtrNm_T_f32	-4.25
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	2300
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	2300
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	2300
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	2300
tgt_CompBlnd_Uls_T_u2p14[0]	4915
tgt_CompBlnd_Uls_T_u2p14[1]	5407
tgt_CompBlnd_Uls_T_u2p14[2]	5898
tgt_CompBlnd_Uls_T_u2p14[3]	6390
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	5.277343599
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	1.086842875
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	5.797149125
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	5.277343599
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	1.086842875
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	5.797149125
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	5.277343599
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	1.086842875
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	5.797149125
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	5.277343599

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Name	Input Value
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	1.086842875
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	5.797149125
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.0184
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.0072
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.094760132
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.22433101
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.0194
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.0059
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.2
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-46
tgt_KPtr_Cnt_Str1.B2_Uls_f32	21
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.0054
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.0102
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.305239868
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-49.77566899
tgt_KPtr_Cnt_Str2.B2_Uls_f32	22.17394657
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.0214
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.0075
tgt_KPtr_Cnt_Str3.B0_Uls_f32	3.410479736
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-53.55133798
tgt_KPtr_Cnt_Str3.B2_Uls_f32	23.34789315

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-2136	-2136	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	9.98550892	9.985508908 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-9771.4082	-9771.407912 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	4.87461329	4.874613038 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	9.48221874	9.48221857 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-9774.86914	-9774.869316 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	0.177343369	0.177343599 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	7.70392752	7.703928231 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-9778.33105	-9778.33072 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	-4.51992607	-4.51992584 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	7.20063734	7.200637893 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-9781.79199	-9781.792125 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-9.21719551	-9.217195279 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 2.48 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	11469
Assist_MtrNm_T_f32	10
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPTr_Uls_T_Str	tgt_FiltSVPTr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	1000.1
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483

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Name	Input Value		
t_StCmpNFK_Str[1].B2_Uls_f32	1100.2		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	1200.3		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	1300.4		
tgt_CompBlnd_Uls_T_u2p14[0]	2294		
tgt_CompBlnd_Uls_T_u2p14[1]	2785		
tgt_CompBlnd_Uls_T_u2p14[2]	3277		
tgt_CompBlnd_Uls_T_u2p14[3]	3768		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	19.1520594		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	40.0332778		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	76.17866968		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	21.637		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	462.99		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	97.43		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	30.9881782		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	32.242		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	19.795592		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	26.6068812		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	28.707		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	191		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	59.6793365	59.67933679 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	10006.0801	10006.0803277498 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	45.4491577	45.44915766 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	562.264221	562.2642049 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	11017.3076	11017.3076352015 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	136.943741	136.9437485 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	60.3184471	60.31844952 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	12009.4033	12009.4033745539 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	57.2852745	57.28527646 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	130.36084	130.3608333 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	13019.8633	13019.8631708081 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	141.913635	141.9136297 ± 0.000489	✔

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## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Case 3: Path Test

### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 8547 Cycles  
TS3.2 8410 Cycles  
TS3.3 8371 Cycles  
TS3.4 8370 Cycles

### Description

Vector Description:

TS3.1 (WriteDisplayVars\_Cnt\_T\_lgc = True)==>TRUE  
TS3.2 (WriteDisplayVars\_Cnt\_T\_lgc = True)==>False  
TS3.3 ( WriteDisplayVars\_Cnt\_T\_lgc == TRUE )=>True  
TS3.4 (FilteredOutput\_MtrNm\_T\_f32<=(8.8)) =>False

## Test Step 3.1 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	10.275
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiItSVPtr_Uls_T_Str	tgt_FiItSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[1].B0_Uls_f32	32.62970983
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.307507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[2].B0_Uls_f32	4.629709826
t_StCmpNFK_Str[2].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	2.407507151
t_StCmpNFK_Str[3].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[3].B2_Uls_f32	2.507507151
tgt_CompBlnd_Uls_T_u2p14[0]	492
tgt_CompBlnd_Uls_T_u2p14[1]	983
tgt_CompBlnd_Uls_T_u2p14[2]	1475
tgt_CompBlnd_Uls_T_u2p14[3]	1966
tgt_FiItSVPtr_Uls_T_Str[0].SV1_Uls_f32	30.9881782
tgt_FiItSVPtr_Uls_T_Str[0].SV2_Uls_f32	22.1110891
tgt_FiItSVPtr_Uls_T_Str[0].Out_Uls_f32	176.86
tgt_FiItSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiItSVPtr_Uls_T_Str[1].SV1_Uls_f32	35.777
tgt_FiItSVPtr_Uls_T_Str[1].SV2_Uls_f32	25.172
tgt_FiItSVPtr_Uls_T_Str[1].Out_Uls_f32	177.67
tgt_FiItSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1

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ApplyStabilityComp2



Name	Input Value
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	40.5658218
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	39.312
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	96.5125243
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	45.3546436
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	31.2938218
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	177.67
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-13.0217123	-13.02171478 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	62.1063004	62.10630329 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	58.0084457	58.00844666 ± 0.00489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	12.7788887	12.77888592 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	128.545456	128.5454457 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	154.254684	154.2546841 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	6.44206715	6.442065301 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	70.6705399	70.67053851 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	67.5860901	67.58609026 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	21.1635799	21.1635769 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	137.109695	137.1096809 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	163.832336	163.8323277 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 3.2 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	8192
Assist_MtrNm_T_f32	-3.08
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483



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ApplyStabilityComp2

Name	Input Value		
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
tgt_CompBlnd_Uls_T_u2p14[0]	3441		
tgt_CompBlnd_Uls_T_u2p14[1]	3932		
tgt_CompBlnd_Uls_T_u2p14[2]	4424		
tgt_CompBlnd_Uls_T_u2p14[3]	4915		
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1567.617957		
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1099.795592		
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-2311.793127		
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464		
Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	-18022	-18022	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.00489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.00049	✔
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	-1467.03979	-1467.039811 ± 0.000489	✔
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	-1395.25391	-1395.254036 ± 0.000489	✔
tot_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	-1890.10535	-1890.105393 ± 0.000489	✔

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## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

## Test Step 3.3 (Repeat Count = 1)

Name	Input Value
ADDFBlend_Uls_T_u2p14	16384
Assist_MtrNm_T_f32	10.275
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	1
t_StCmpNFK_Str[0].A1_Uls_f32	0
t_StCmpNFK_Str[0].A2_Uls_f32	1
t_StCmpNFK_Str[0].B0_Uls_f32	2300
t_StCmpNFK_Str[0].B1_Uls_f32	0
t_StCmpNFK_Str[0].B2_Uls_f32	2300
t_StCmpNFK_Str[1].A1_Uls_f32	0
t_StCmpNFK_Str[1].A2_Uls_f32	1
t_StCmpNFK_Str[1].B0_Uls_f32	2300
t_StCmpNFK_Str[1].B1_Uls_f32	0
t_StCmpNFK_Str[1].B2_Uls_f32	2300
t_StCmpNFK_Str[2].A1_Uls_f32	0
t_StCmpNFK_Str[2].A2_Uls_f32	1
t_StCmpNFK_Str[2].B0_Uls_f32	2300
t_StCmpNFK_Str[2].B1_Uls_f32	0
t_StCmpNFK_Str[2].B2_Uls_f32	2300
t_StCmpNFK_Str[3].A1_Uls_f32	0
t_StCmpNFK_Str[3].A2_Uls_f32	1
t_StCmpNFK_Str[3].B0_Uls_f32	2300
t_StCmpNFK_Str[3].B1_Uls_f32	0
t_StCmpNFK_Str[3].B2_Uls_f32	2300
tgt_CompBlnd_Uls_T_u2p14[0]	16384
tgt_CompBlnd_Uls_T_u2p14[1]	16384
tgt_CompBlnd_Uls_T_u2p14[2]	16384
tgt_CompBlnd_Uls_T_u2p14[3]	16384
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1567.617957
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	1099.795592
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2311.793127
tgt_FiltSVPtr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306

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Name	Input Value
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	18022	18022	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV1_Uls_f32	1099.79553	1099.795592 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].SV2_Uls_f32	20989.0508	20989.05139 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[0].Out_Uls_f32	2643.44849	2643.448608 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV1_Uls_f32	1099.79553	1099.795592 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[1].SV2_Uls_f32	20989.0508	20989.05139 ± 0.00049	✓
tgt_FiltSVPtr_Uls_T_Str[1].Out_Uls_f32	2643.44849	2643.448608 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV1_Uls_f32	1099.79553	1099.795592 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].SV2_Uls_f32	20989.0508	20989.05139 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[2].Out_Uls_f32	2643.44849	2643.448608 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV1_Uls_f32	1099.79553	1099.795592 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].SV2_Uls_f32	20989.0508	20989.05139 ± 0.000489	✓
tgt_FiltSVPtr_Uls_T_Str[3].Out_Uls_f32	2643.44849	2643.448608 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
ADDFBlend_Uls_T_u2p14	3277
Assist_MtrNm_T_f32	-10.275
CompBlnd_Uls_T_u2p14	tgt_CompBlnd_Uls_T_u2p14
FiltSVPtr_Uls_T_Str	tgt_FiltSVPtr_Uls_T_Str
WriteDisplayVars_Cnt_T_lgc	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.256265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.796280471
t_StCmpNFK_Str[1].B0_Uls_f32	3.629709826
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.246265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.879628047
t_StCmpNFK_Str[2].B0_Uls_f32	4.6297
t_StCmpNFK_Str[2].B1_Uls_f32	-6.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	3.207507151
t_StCmpNFK_Str[3].A1_Uls_f32	-0.336265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-7.753110777

# TEST DETAILS REPORT

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ApplyStabilityComp2

Name	Input Value
t_StCmpNFK_Str[3].B2_Uls_f32	3.207507151
tgt_CompBlnd_Uls_T_u2p14[0]	328
tgt_CompBlnd_Uls_T_u2p14[1]	819
tgt_CompBlnd_Uls_T_u2p14[2]	1311
tgt_CompBlnd_Uls_T_u2p14[3]	1802
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	28.0291485
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	19.1520594
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	76.17866968
tgt_FiltSVPTr_Uls_T_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	32.242
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	21.637
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	97.43
tgt_FiltSVPTr_Uls_T_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	36.4548515
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	35.777
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	19.795592
tgt_FiltSVPTr_Uls_T_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	40.667703
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	26.6068812
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	97.43
tgt_FiltSVPTr_Uls_T_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172

Name	Actual Value	Expected Value	Result
ApplyStabilityComp2()	4392	4392	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV1_Uls_f32	68.2286377	68.22863644 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].SV2_Uls_f32	-21.9964695	-21.9964728 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[0].Out_Uls_f32	1.00888252	1.008880038 ± 0.00489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV1_Uls_f32	48.3759575	48.37596068 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[1].SV2_Uls_f32	-91.3499298	-91.34992711 ± 0.00049	✓
tgt_FiltSVPTr_Uls_T_Str[1].Out_Uls_f32	-86.2356873	-86.23568408 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV1_Uls_f32	107.488632	107.4886279 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].SV2_Uls_f32	-24.6582069	-24.65821212 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[2].Out_Uls_f32	9.43458748	9.434583038 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV1_Uls_f32	80.1052551	80.10526252 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].SV2_Uls_f32	-85.8389816	-85.83898146 ± 0.000489	✓
tgt_FiltSVPTr_Uls_T_Str[3].Out_Uls_f32	-77.8099823	-77.80998108 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓



# TEST DETAILS REPORT

StabilityComp2\_Init1

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Project	CBD_StabilityComp2
Module	StabilityComp2
Test Object	StabilityComp2_Init1

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

## Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_StabilityComp2
Configuration File	D:\Synergy_Work_Area\CBD_StabilityComp2\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\StabilityComp\src\Ap_StabilityComp2.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxrLib\include -I\$(SOURCEROOT)\StabilityComp\utp\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utp\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include
File	\$(SOURCEROOT)\NxrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxrLib\include -I\$(SOURCEROOT)\StabilityComp\utp\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utp\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include
File	\$(SOURCEROOT)\StabilityComp\src\NF_Init_f32_UnitTest.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxrLib\include -I\$(SOURCEROOT)\StabilityComp\utp\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utp\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'StabilityComp2'	*****Unit Test Description***** Name of Tester: Spoorti Mali Code File(s) Under Test: Ap_StabilityComp2.c Code File(s) Version: 6 Module Design Document: StabilityCompensation2_MDD Module Design Document Version: 6 Data Dictionary Version: 17 Unit Test Plan Version: 5 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): 1238 Total RAM Used (Bytes): 100 Total CALS Used (Bytes): 380 Special Test Requirements: Place ""NF_Init_f32_UnitTest.c"" at ""...\\CBD_StabilityComp\\StabilityComp\\src"" location and add it in the Source files list of TESSY for Testing purpose which is embedded with this UTP. Test Date: 01-29-15 Comments: ""Note1: Inline function defined in globalmacro.h is not unit tested.\\n\\nNote2: In ""StabilityComp2_Per1"" function, global variable ""CombinedAssist_MtrNm_f32"" has given out of range value to cover 100% path coverage.\\n\\nNote3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."" *****

## Attributes

Name	Value
CTE File	\$(PROJECTROOT)\tessy\persist\tessy\project\00000013\00000015\database\tdb\00000024\StabilityComp2_Init1.cte
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5

# TEST DETAILS REPORT

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StabilityComp2\_Init1



Attributes	
Name	Value
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_StabilityComp2\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP





# TEST DETAILS REPORT

StabilityComp2\_Init1

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## Test Case 1: Boundary Test





Specification	Performance Metrics (With "None"		
	Instrumentation and WithPS Environment)		
	CPU Cycles:		
	TS1.1	2792	Cycles
	TS1.2	2723	Cycles
	TS1.3	2711	Cycles
	TS1.4	2676	Cycles
	TS1.5	2697	Cycles
	TS1.6	2697	Cycles
	TS1.7	2697	Cycles
	TS1.8	2697	Cycles
	TS1.9	2697	Cycles
	TS1.10	2660	Cycles
	TS1.11	2660	Cycles
	TS1.12	2660	Cycles
	TS1.13	2697	Cycles
	TS1.14	2697	Cycles
	TS1.15	2697	Cycles
	TS1.16	2660	Cycles
	TS1.17	2660	Cycles
	TS1.18	2697	Cycles
	TS1.19	2697	Cycles
	TS1.20	2697	Cycles
	TS1.21	2697	Cycles
	TS1.22	2697	Cycles
	TS1.23	2697	Cycles
	TS1.24	2697	Cycles
	TS1.25	2697	Cycles
	TS1.26	2697	Cycles
	TS1.27	2660	Cycles
	TS1.28	2660	Cycles
	TS1.29	2660	Cycles
	TS1.30	2660	Cycles
	TS1.31	2660	Cycles
	TS1.32	2660	Cycles
	TS1.33	2660	Cycles
	TS1.34	2697	Cycles
	TS1.35	2697	Cycles
	TS1.36	2697	Cycles
	TS1.37	2660	Cycles
	TS1.38	2660	Cycles
	TS1.39	2660	Cycles
	TS1.40	2660	Cycles
	TS1.41	2660	Cycles
	TS1.42	2697	Cycles
	TS1.43	2697	Cycles
	TS1.44	2697	Cycles
	TS1.45	2660	Cycles
	TS1.46	2660	Cycles
	TS1.47	2660	Cycles
	TS1.48	2660	Cycles

# TEST DETAILS REPORT

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StabilityComp2\_Init1

Description	Test Vector Description:
TS1.1	All Min
TS1.2	All Max
TS1.3	k_STSmphwTrqLPFKn_Hz_f32==>Min
TS1.4	k_STSmphwTrqLPFKn_Hz_f32==>Max
TS1.5	k_STSmphwTrqLPFKn_Hz_f32==>Pos
TS1.6	suggested vectors in MDD
TS1.7	suggested vectors in MDD
TS1.8	A1 = min, A2 = max
TS1.9	suggested vectors in MDD
TS1.10	A1, A2 = mid
TS1.11	"B0, B2 = max B1 = min"
TS1.12	suggested vectors in MDD
TS1.13	"A1 = max, A2 = min B0, B1, B2 = mid"
TS1.14	suggested vectors in MDD
TS1.15	"B0, B2 = min B1 = max"
TS1.16	"suggested vectors in MDD && HwTrqSV2_HwNm_M_Str.K==>Min"
TS1.17	"suggested vectors in MDD && HwTrqSV2_HwNm_M_Str.K==>Max"
TS1.18	"suggested vectors in MDD && HwTrqSV2_HwNm_M_Str.K==>Pos"
TS1.19	"suggested vectors in MDD && HwTrqSV2_HwNm_M_Str.SV==>Min"
TS1.20	"suggested vectors in MDD && HwTrqSV2_HwNm_M_Str.SV==>Max"
TS1.21	"suggested vectors in MDD && HwTrqSV2_HwNm_M_Str.SV==>Pos"
TS1.22	"suggested vectors in MDD && HwTrqSV2_HwNm_M_Str.SV==>Zero"
TS1.23	"suggested vectors in MDD && HwTrqSV2_HwNm_M_Str.SV==>Neg"
TS1.24	suggested vectors in MDD
TS1.25	suggested vectors in MDD
TS1.26	suggested vectors in MDD
TS1.27	suggested vectors in MDD
TS1.28	suggested vectors in MDD
TS1.29	suggested vectors in MDD
TS1.30	suggested vectors in MDD
TS1.31	suggested vectors in MDD
TS1.32	t_STSmphwTrqLPFKn_Hz_f32==>Min
TS1.33	t_STSmphwTrqLPFKn_Hz_f32==>Max
TS1.34	t_STSmphwTrqLPFKn_Hz_f32==>Zero
TS1.35	t_STSmphwTrqLPFKn_Hz_f32==>Min
TS1.36	t_STSmphwTrqLPFKn_Hz_f32==>Max
TS1.37	t_STSmphwTrqLPFKn_Hz_f32==>Zero
TS1.38	t_STSmphwTrqLPFKn_Hz_f32==>Pos
TS1.39	t_STSmphwTrqLPFKn_Hz_f32==>Neg
TS1.40	t_STSmphwTrqLPFKn_Hz_f32==>Min
TS1.41	t_STSmphwTrqLPFKn_Hz_f32==>Max
TS1.42	t_STSmphwTrqLPFKn_Hz_f32==>Pos
TS1.43	t_STSmphwTrqLPFKn_Hz_f32==>Min
TS1.44	t_STSmphwTrqLPFKn_Hz_f32==>Max
TS1.45	t_STSmphwTrqLPFKn_Hz_f32==>Neg
TS1.46	t_STSmphwTrqLPFKn_Hz_f32==>Min
TS1.47	t_STSmphwTrqLPFKn_Hz_f32==>Max
TS1.48	t_STSmphwTrqLPFKn_Hz_f32==>Neg

Test Step 1.1 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-2077
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1981
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2312
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-2077
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1981
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2312
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-2077
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1981
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2312
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-2077
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1981
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2312
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-10

# TEST DETAILS REPORT

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StabilityComp2\_Init1

Name	Input Value
HwTrqSV2_HwNm_M_Str_K_Uls_f32	0.000125
k_StCmpHwTrqLPFKn_Hz_f32	0.01
t_StCmpNFK_Str[0].A1_Uls_f32	-2
t_StCmpNFK_Str[0].A2_Uls_f32	-1
t_StCmpNFK_Str[0].B0_Uls_f32	0
t_StCmpNFK_Str[0].B1_Uls_f32	-4500
t_StCmpNFK_Str[0].B2_Uls_f32	-4
t_StCmpNFK_Str[1].A1_Uls_f32	-2
t_StCmpNFK_Str[1].A2_Uls_f32	-1
t_StCmpNFK_Str[1].B0_Uls_f32	0
t_StCmpNFK_Str[1].B1_Uls_f32	-4500
t_StCmpNFK_Str[1].B2_Uls_f32	-4
t_StCmpNFK_Str[2].A1_Uls_f32	-2
t_StCmpNFK_Str[2].A2_Uls_f32	-1
t_StCmpNFK_Str[2].B0_Uls_f32	0
t_StCmpNFK_Str[2].B1_Uls_f32	-4500
t_StCmpNFK_Str[2].B2_Uls_f32	-4
t_StCmpNFK_Str[3].A1_Uls_f32	-2
t_StCmpNFK_Str[3].A2_Uls_f32	-1
t_StCmpNFK_Str[3].B0_Uls_f32	0
t_StCmpNFK_Str[3].B1_Uls_f32	-4500
t_StCmpNFK_Str[3].B2_Uls_f32	-4

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str_SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str_K_Uls_f32	0.000125646591	0.000125656 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-2	-2 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-1	-1 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4500	-4500 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	-4	-4 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-2	-2 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-1	-1 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-4500	-4500 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	-4	-4 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-2	-2 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-1	-1 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4500	-4500 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	-4	-4 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-2	-2 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-1	-1 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-4500	-4500 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	-4	-4 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.2 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	2077
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1981
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2312
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	2077
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1981

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Name	Input Value		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2312		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	2077		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1981		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2312		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	2077		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1981		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2312		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	10		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.4666		
k_StCmpHwTrqLPFKn_Hz_f32	50		
t_StCmpNFK_Str[0].A1_Uls_f32	0		
t_StCmpNFK_Str[0].A2_Uls_f32	1		
t_StCmpNFK_Str[0].B0_Uls_f32	2300		
t_StCmpNFK_Str[0].B1_Uls_f32	0		
t_StCmpNFK_Str[0].B2_Uls_f32	2300		
t_StCmpNFK_Str[1].A1_Uls_f32	0		
t_StCmpNFK_Str[1].A2_Uls_f32	1		
t_StCmpNFK_Str[1].B0_Uls_f32	2300		
t_StCmpNFK_Str[1].B1_Uls_f32	0		
t_StCmpNFK_Str[1].B2_Uls_f32	2300		
t_StCmpNFK_Str[2].A1_Uls_f32	0		
t_StCmpNFK_Str[2].A2_Uls_f32	1		
t_StCmpNFK_Str[2].B0_Uls_f32	2300		
t_StCmpNFK_Str[2].B1_Uls_f32	0		
t_StCmpNFK_Str[2].B2_Uls_f32	2300		
t_StCmpNFK_Str[3].A1_Uls_f32	0		
t_StCmpNFK_Str[3].A2_Uls_f32	1		
t_StCmpNFK_Str[3].B0_Uls_f32	2300		
t_StCmpNFK_Str[3].B1_Uls_f32	0		
t_StCmpNFK_Str[3].B2_Uls_f32	2300		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.466511905	0.466511909 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	1	1 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	1	1 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	1	1 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	1	1 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B0_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B2_Uls_f32	2300	2300 ± 0.00005	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

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## Test Step 1.3 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	8.796749586
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	9.796749586
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	11.2749703
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	10.79674959
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	12.2749703
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	11.79674959
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	13.2749703
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0112
k_StCmpHwTrqLPFKn_Hz_f32	0.01
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.256265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.796280471
t_StCmpNFK_Str[1].B0_Uls_f32	3.629709826
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.246265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.879628047
t_StCmpNFK_Str[2].B0_Uls_f32	4.6297
t_StCmpNFK_Str[2].B1_Uls_f32	-6.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	3.207507151
t_StCmpNFK_Str[3].A1_Uls_f32	-0.336265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-7.753110777
t_StCmpNFK_Str[3].B2_Uls_f32	3.207507151

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.000125646591	0.000125656 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.62970972	2.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.20750713	2.207507151 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.256265759	-0.256265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.796280444	-0.796280471 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	3.62970972	3.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	2.20750713	2.207507151 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.246265754	-0.246265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.879628062	-0.879628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	4.62970018	4.6297 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-6.75311089	-6.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	3.20750713	3.207507151 ± 0.00005	✓

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.336265743	-0.336265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	5.62970972	5.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-7.75311089	-7.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	3.20750713	3.207507151 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.4 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	4.579654921
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	5.579654921
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	11.2749613
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	6.579654921
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	12.2749613
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	7.579654921
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	13.2749613
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0134
k_StCmpHwTrqLPPKHz_Hz_f32	50
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[1].B0_Uls_f32	32.62970983
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.307507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[2].B0_Uls_f32	4.629709826
t_StCmpNFK_Str[2].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	2.4075071507673
t_StCmpNFK_Str[3].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[3].B2_Uls_f32	2.507507151

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.466511905	0.466511909 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.62970972	2.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.20750713	2.207507151 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	32.6297112	32.62970983 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	2.30750704	2.307507151 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	4.62970972	4.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.40750718	2.4075071507673 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	5.62970972	5.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	2.50750709	2.507507151 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.5 (Repeat Count = 1)				
Name	Input Value			
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	10.2749703			
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1640.081281			
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.06356			
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str			
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	10.2749703			
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1641.081281			
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2311.06356			
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1			
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.2749703			
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1642.081281			
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2311.06356			
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2			
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	10.2749703			
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1643.081281			
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2311.06356			
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3			
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6			
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0156			
k_StCmpHwTrqLPFKn_Hz_f32	10			
t_StCmpNFK_Str[0].A1_Uls_f32	-0.172240343			
t_StCmpNFK_Str[0].A2_Uls_f32	-0.688966709			
t_StCmpNFK_Str[0].B0_Uls_f32	11.53067485			
t_StCmpNFK_Str[0].B1_Uls_f32	-20.35078362			
t_StCmpNFK_Str[0].B2_Uls_f32	8.95890172			
t_StCmpNFK_Str[1].A1_Uls_f32	-0.172240343			
t_StCmpNFK_Str[1].A2_Uls_f32	-0.688966709			
t_StCmpNFK_Str[1].B0_Uls_f32	11.53067485			
t_StCmpNFK_Str[1].B1_Uls_f32	-20.35078362			
t_StCmpNFK_Str[1].B2_Uls_f32	8.95890172			
t_StCmpNFK_Str[2].A1_Uls_f32	-0.172240343			
t_StCmpNFK_Str[2].A2_Uls_f32	-0.688966709			
t_StCmpNFK_Str[2].B0_Uls_f32	11.53067485			
t_StCmpNFK_Str[2].B1_Uls_f32	-20.35078362			
t_StCmpNFK_Str[2].B2_Uls_f32	8.95890172			
t_StCmpNFK_Str[3].A1_Uls_f32	-0.172240343			
t_StCmpNFK_Str[3].A2_Uls_f32	-0.688966709			
t_StCmpNFK_Str[3].B0_Uls_f32	11.53067485			
t_StCmpNFK_Str[3].B1_Uls_f32	-20.35078362			
t_StCmpNFK_Str[3].B2_Uls_f32	8.95890172			
Name	Actual Value	Expected Value	Result	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓	
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓	
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓	
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓	
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓	
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓	
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓	



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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.118088603	0.118088622 ± 0.0000152587890625	✓
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.172240347	-0.172240343 ± 0.00005	✓
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.688966691	-0.688966709 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B0_Uls_f32	11.5306749	11.53067485 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B1_Uls_f32	-20.3507843	-20.35078362 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B2_Uls_f32	8.95890141	8.95890172 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240347	-0.172240343 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966691	-0.688966709 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.5306749	11.53067485 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.3507843	-20.35078362 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890141	8.95890172 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.172240347	-0.172240343 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.688966691	-0.688966709 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B0_Uls_f32	11.5306749	11.53067485 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-20.3507843	-20.35078362 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B2_Uls_f32	8.95890141	8.95890172 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240347	-0.172240343 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966691	-0.688966709 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.5306749	11.53067485 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.3507843	-20.35078362 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890141	8.95890172 ± 0.00005	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
NF_Init_f32	4	NF_Init_f32	4	✓	

Test Step 1.6 (Repeat Count = 1)		✓
Name	Input Value	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	149.8747	
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	142.8569	
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86	
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str	
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	150.8747	
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	142.8569	
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	176.86	
CombAstNFSV2_Cnt_M_Str[1].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str1	
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	151.8747	
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	142.8569	
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86	
CombAstNFSV2_Cnt_M_Str[2].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str2	
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	152.8747	
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	142.8569	
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	176.86	
CombAstNFSV2_Cnt_M_Str[3].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str3	
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-1	
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0178	
k_StCmpHwTrqLPFKn_Hz_f32	12	
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753	
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047	
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826	
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777	
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151	
t_StCmpNFK_Str[1].A1_Uls_f32	-0.236265753	
t_StCmpNFK_Str[1].A2_Uls_f32	-0.679628047	
t_StCmpNFK_Str[1].B0_Uls_f32	2.629709826	
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777	
t_StCmpNFK_Str[1].B2_Uls_f32	2.507507151	
t_StCmpNFK_Str[2].A1_Uls_f32	-0.236265753	
t_StCmpNFK_Str[2].A2_Uls_f32	-0.679628047	
t_StCmpNFK_Str[2].B0_Uls_f32	2.629709826	
t_StCmpNFK_Str[2].B1_Uls_f32	-4.753110777	
t_StCmpNFK_Str[2].B2_Uls_f32	2.607507151	
t_StCmpNFK_Str[3].A1_Uls_f32	-0.236265753	
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047	

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Name	Input Value	Expected Value	Result
t_StCmpNFK_Str[3].B0_Uls_f32	2.629709826		
t_StCmpNFK_Str[3].B1_Uls_f32	-4.753110777		
t_StCmpNFK_Str[3].B2_Uls_f32	2.707507151		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.139977276	0.139977259 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.62970972	2.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.20750713	2.207507151 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	2.62970972	2.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	2.50750709	2.507507151 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.62970972	2.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.60750723	2.607507151 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.236265749	-0.236265753 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.679628074	-0.679628047 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	2.62970972	2.629709826 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-4.75311089	-4.753110777 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	2.70750713	2.707507151 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.7 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	748.9277
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	689.4414
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	749.9277
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	689.4414
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	750.9277
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	689.4414
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	751.9277
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	689.4414
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-3
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.02
k_StCmpHwTrqLPFKn_Hz_f32	14
t_StCmpNFK_Str[0].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[0].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[0].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[0].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[0].B2_Uls_f32	8.55890172
t_StCmpNFK_Str[1].A1_Uls_f32	-0.172240343

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Name	Input Value
t_StCmpNFK_Str[1].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[1].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[1].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[1].B2_Uls_f32	8.45890172
t_StCmpNFK_Str[2].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[2].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[2].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[2].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[2].B2_Uls_f32	8.65890172
t_StCmpNFK_Str[3].A1_Uls_f32	-0.172240343
t_StCmpNFK_Str[3].A2_Uls_f32	-0.688966709
t_StCmpNFK_Str[3].B0_Uls_f32	11.53067485
t_StCmpNFK_Str[3].B1_Uls_f32	-20.35078362
t_StCmpNFK_Str[3].B2_Uls_f32	8.35890172

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.161322653	0.161322631 ± 0.0000152587890625	✓
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.172240347	-0.172240343 ± 0.00005	✓
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.688966691	-0.688966709 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B0_Uls_f32	11.5306749	11.53067485 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B1_Uls_f32	-20.3507843	-20.35078362 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B2_Uls_f32	8.55890179	8.55890172 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240347	-0.172240343 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966691	-0.688966709 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.5306749	11.53067485 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.3507843	-20.35078362 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.45890141	8.45890172 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.172240347	-0.172240343 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.688966691	-0.688966709 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B0_Uls_f32	11.5306749	11.53067485 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-20.3507843	-20.35078362 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B2_Uls_f32	8.65890217	8.65890172 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240347	-0.172240343 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966691	-0.688966709 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.5306749	11.53067485 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.3507843	-20.35078362 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.35890198	8.35890172 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.8 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	8.803286323
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	8.796749586
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	8.903286323
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	8.796749586
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	9.803286323
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	8.796749586
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	10.80328632
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	8.796749586

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-4
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0222
k_StCmpHwTrqLPFKn_Hz_f32	13
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].B2_Uls_f32	0.939541087
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	0.949541087

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.15071702	0.150717003 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.939541101	0.939541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.949541092	0.949541087 ± 0.00005	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
NF_Init_f32	4	NF_Init_f32	4		✓

Test Step 1.9 (Repeat Count = 1)		✓
Name	Input Value	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	4.586334043	
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	4.579654921	
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.2749613	

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Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	5.586334043		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	4.579654921		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	10.2749613		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	6.586334043		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	4.579654921		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.2749613		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	7.586334043		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	4.579654921		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	10.2749613		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-2		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0244		
k_StCmpHwTrqLPFKn_Hz_f32	22		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[0].A2_Uls_f32	0.776925164		
t_StCmpNFK_Str[0].B0_Uls_f32	0.944231291		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[0].B2_Uls_f32	0.832693873		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[1].A2_Uls_f32	0.776925164		
t_StCmpNFK_Str[1].B0_Uls_f32	0.944231291		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[1].B2_Uls_f32	0.842693873		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[2].A2_Uls_f32	0.776925164		
t_StCmpNFK_Str[2].B0_Uls_f32	0.944231291		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[2].B2_Uls_f32	0.852693873		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[3].A2_Uls_f32	0.776925164		
t_StCmpNFK_Str[3].B0_Uls_f32	0.944231291		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[3].B2_Uls_f32	0.836693873		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.2415362	0.241536159 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.842693865	0.842693873 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.852693856	0.852693873 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.836693883	0.836693873 ± 0.00005	✔

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## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.10 (Repeat Count = 1)

Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1844.703127		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1640.081281		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.06356		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1845.703127		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1640.081281		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2311.06356		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1846.703127		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1640.081281		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2311.06356		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1847.703127		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1640.081281		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2311.06356		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-1		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0266		
k_StCmpHwTrqLPFKn_Hz_f32	31		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[1].B2_Uls_f32	45.83360735		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[2].B2_Uls_f32	46.83360735		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[3].B2_Uls_f32	47.83360735		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.322643459	0.322643454 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	45.8336067	45.83360735 ± 0.00005	✔

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	46.8336067	46.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	47.8336067	47.83360735 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.11 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1568.617957
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1569.617957
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1570.617957
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-5
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0288
k_StCmpHwTrqLPFKn_Hz_f32	40
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[1].B2_Uls_f32	104.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[2].B2_Uls_f32	105.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[3].B2_Uls_f32	106.397464

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓



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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str_K_Uls_f32	0.395077467	0.395077437 ± 0.0000152587890625	✓
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPTr_Cnt_Str.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B2_Uls_f32	104.397461	104.397464 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B2_Uls_f32	105.397461	105.397464 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B2_Uls_f32	106.397461	106.397464 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.12 (Repeat Count = 1)				
Name		Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32		1337.910389		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32		926.5125243		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32		2311.715352		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32		1338.910389		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32		926.5125243		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32		2311.715352		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32		1339.910389		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32		926.5125243		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32		2311.715352		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32		1340.910389		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32		926.5125243		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32		2311.715352		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32		-7		
HwTrqSV2_HwNm_M_Str.K_Uls_f32		0.031		
k_StCmpHwTrqLPFKn_Hz_f32		49		
t_StCmpNFK_Str[0].A1_Uls_f32		-0.661216198		
t_StCmpNFK_Str[0].A2_Uls_f32		0.038712799		
t_StCmpNFK_Str[0].B0_Uls_f32		101.7234252		
t_StCmpNFK_Str[0].B1_Uls_f32		-191.0532639		
t_StCmpNFK_Str[0].B2_Uls_f32		89.70733524		
t_StCmpNFK_Str[1].A1_Uls_f32		-0.661216198		
t_StCmpNFK_Str[1].A2_Uls_f32		0.038712799		
t_StCmpNFK_Str[1].B0_Uls_f32		101.7234252		
t_StCmpNFK_Str[1].B1_Uls_f32		-191.0532639		
t_StCmpNFK_Str[1].B2_Uls_f32		89.70733524		
t_StCmpNFK_Str[2].A1_Uls_f32		-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32		0.038712799		
t_StCmpNFK_Str[2].B0_Uls_f32		101.7234252		
t_StCmpNFK_Str[2].B1_Uls_f32		-191.0532639		
t_StCmpNFK_Str[2].B2_Uls_f32		89.70733524		
t_StCmpNFK_Str[3].A1_Uls_f32		-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32		0.038712799		
t_StCmpNFK_Str[3].B0_Uls_f32		101.7234252		
t_StCmpNFK_Str[3].B1_Uls_f32		-191.0532639		
t_StCmpNFK_Str[3].B2_Uls_f32		89.70733524		
Name	Actual Value	Expected Value	Result	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489		



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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.459765613	0.4597656 ± 0.0000152587890625	✓
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPTr_Cnt_Str.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.13 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	2075.199938
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	2074.199938
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	2073.199938
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-8
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0332
k_StCmpHwTrqLPFKn_Hz_f32	2
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992

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Name	Input Value		
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0248195529	0.024819543 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
NF_Init_f32	4	NF_Init_f32	4		✓

Test Step 1.14 (Repeat Count = 1)		
Name	Input Value	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	5.277343599	
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	76.17866968	
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	92.34203664	
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str	
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	5.277343599	
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	77.17866968	
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	92.34203664	
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1	
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	5.277343599	
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	78.17866968	
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	92.34203664	
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2	
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	5.277343599	
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	79.17866968	
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	92.34203664	
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3	
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1	
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0354	
k_StCmpHwTrqLPFKn_Hz_f32	5	
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396	

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Name	Input Value		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0608986616	0.060898633 ± 0.0000152587890625	✓
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
NF_Init f32	4	NF_Init f32	4	✓	

Test Step 1.15 (Repeat Count = 1)		✓
Name	Input Value	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	78.86288978	
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1.086842875	
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	5.797149125	
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str	
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	79.86288978	
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	2.086842875	
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	5.797149125	
CombAstNFSV2_Cnt_M_Str[1].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str1	
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	80.86288978	

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Name	Input Value		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	3.086842875		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	5.797149125		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	81.86288978		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	4.086842875		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	5.797149125		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0376		
k_StCmpHwTrqLPFKn_Hz_f32	8		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0956429243	0.095642891 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

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## Test Step 1.16 (Repeat Count = 1)

Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	8.803286323		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	8.796749586		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.2749703		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	9.803286323		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	9.796749586		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	11.2749703		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.80328632		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	10.79674959		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	12.2749703		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	11.80328632		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	11.79674959		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	13.2749703		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-8		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.000125		
k_StCmpHwTrqLPFKn_Hz_f32	40		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	0.929541087		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.395077467	0.395077437 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✔

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.17 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	4.586334043
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	4.579654921
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	5.586334043
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	5.579654921
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	11.2749613
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	6.586334043
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	6.579654921
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	12.2749613
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	7.586334043
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	7.579654921
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	13.2749613
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.4666
k_StCmpHwTrqLPFKn_Hz_f32	49
t_StCmpNFK_Str[0].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[0].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[0].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[1].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[1].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[1].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[2].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[2].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[2].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[3].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[3].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[3].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].B2_Uls_f32	0.832693873

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.459765613	0.4597656 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.18 (Repeat Count = 1)

Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1844.703127		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1640.081281		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.06356		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1845.703127		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1641.081281		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2310.06356		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1846.703127		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1642.081281		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2309.06356		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1847.703127		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1643.081281		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2308.06356		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0794		
k_StCmpHwTrqLPFKn_Hz_f32	2		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔



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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0248195529	0.024819543 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.19 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.06356
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1568.617957
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1100.795592
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2310.06356
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1569.617957
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1101.795592
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2309.06356
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1570.617957
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1102.795592
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2308.06356
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-10
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0816
k_StCmpHwTrqLPFKn_Hz_f32	5
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067



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Name	Input Value		
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0608986616	0.060898633 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.20 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1337.910389
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	926.5125243
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.06356
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1338.910389
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	927.5125243
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2310.06356
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1339.910389
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	928.5125243
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2309.06356
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1340.910389
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	929.5125243
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2308.06356
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	10
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0838
k_StCmpHwTrqLPFKn_Hz_f32	8
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[0].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[0].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[1].B0_Uls_f32	101.7234252

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Name	Input Value
t_StCmpNFK_Str[1].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[1].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[2].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[2].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[2].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[2].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[3].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[3].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[3].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[3].B2_Uls_f32	89.70733524

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0956429243	0.095642891 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.21 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	2075.199938
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1981.330346
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2307.490226
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	2075.199938
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1982.330346
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2308.490226
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	2075.199938
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1983.330346
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2309.490226
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3

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Name	Input Value		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.086		
k_StCmpHwTrqLPFKn_Hz_f32	11		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.129101694	0.129101705 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.22 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	92.34203664
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	865.2521

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Name	Input Value		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	77.17866968		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	93.34203664		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	864.2521		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	78.17866968		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	94.34203664		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	863.2521		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	79.17866968		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	95.34203664		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0882		
k_StCmpHwTrqLPFKn_Hz_f32	14		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.161322653	0.161322631 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓

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## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.23 (Repeat Count = 1)

Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	10.2749703		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1.086842875		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	5.797149125		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	11.2749703		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	2.086842875		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	6.797149125		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	12.2749703		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	3.086842875		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	7.797149125		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	13.2749703		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	4.086842875		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	8.797149125		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-7		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0904		
k_StCmpHwTrqLPFKn_Hz_f32	17		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.19235146	0.192351468 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✔

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.24 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-8.796749586
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-10.2749703
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	863.2521
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-9.796749586
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-11.2749703
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	861.2521
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-10.79674959
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-12.2749703
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	864.2521
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-11.79674959
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-13.2749703
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	9
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0926
k_StCmpHwTrqLPFKn_Hz_f32	20
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	0.929541087

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str_K_Uls_f32	0.222232342	0.222232321 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.25 (Repeat Count = 1)				
Name		Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32		10.2749703		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32		-4.579654921		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32		-10.2749613		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32		11.2749703		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32		-5.579654921		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32		-11.2749613		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32		12.2749703		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32		-6.579654921		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32		-12.2749613		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32		13.2749703		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32		-7.579654921		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32		-13.2749613		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str		tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32		-2		
HwTrqSV2_HwNm_M_Str.K_Uls_f32		0.0948		
k_StCmpHwTrqLPFKn_Hz_f32		23		
t_StCmpNFK_Str[0].A1_Uls_f32		-1.773421113		
t_StCmpNFK_Str[0].A2_Uls_f32		0.776925164		
t_StCmpNFK_Str[0].B0_Uls_f32		0.944231291		
t_StCmpNFK_Str[0].B1_Uls_f32		-1.773421113		
t_StCmpNFK_Str[0].B2_Uls_f32		0.832693873		
t_StCmpNFK_Str[1].A1_Uls_f32		-1.773421113		
t_StCmpNFK_Str[1].A2_Uls_f32		0.776925164		
t_StCmpNFK_Str[1].B0_Uls_f32		0.944231291		
t_StCmpNFK_Str[1].B1_Uls_f32		-1.773421113		
t_StCmpNFK_Str[1].B2_Uls_f32		0.832693873		
t_StCmpNFK_Str[2].A1_Uls_f32		-1.773421113		
t_StCmpNFK_Str[2].A2_Uls_f32		0.776925164		
t_StCmpNFK_Str[2].B0_Uls_f32		0.944231291		
t_StCmpNFK_Str[2].B1_Uls_f32		-1.773421113		
t_StCmpNFK_Str[2].B2_Uls_f32		0.832693873		
t_StCmpNFK_Str[3].A1_Uls_f32		-1.773421113		
t_StCmpNFK_Str[3].A2_Uls_f32		0.776925164		
t_StCmpNFK_Str[3].B0_Uls_f32		0.944231291		
t_StCmpNFK_Str[3].B1_Uls_f32		-1.773421113		
t_StCmpNFK_Str[3].B2_Uls_f32		0.832693873		
Name	Actual Value	Expected Value	Result	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489		



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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.251007676	0.251007661 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.776925147	0.776925164 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.944231272	0.944231291 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.77342117	-1.773421113 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.832693875	0.832693873 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.26 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	8.803286323
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1640.081281
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	9.803286323
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1641.081281
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.80328632
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1642.081281
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	11.80328632
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1643.081281
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-1
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.097
k_StCmpHwTrqLPFKn_Hz_f32	26
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957



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Name	Input Value		
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.278718412	0.27871839 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673641	-0.311673654 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957	-0.510387957 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.4000015	45.40000283 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.0556717	-90.05567179 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.27 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	4.586334043
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	5.586334043
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1100.795592
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	6.586334043
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1101.795592
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	7.586334043
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1102.795592
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-5
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0992
k_StCmpHwTrqLPFKn_Hz_f32	29
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066

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Name	Input Value		
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306		
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713		
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067		
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066		
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306		
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713		
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067		
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066		
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306		
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713		
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067		
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066		
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306		
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713		
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067		
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.305403888	0.305403896 ± 0.0000152587890625	✓
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPTr_Cnt_Str.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.718797088	-0.718797066 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.129167303	0.129167306 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B0_Uls_f32	104.703712	104.703713 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-207.690811	-207.6908067 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
NF_Init f32	4	NF_Init f32	4	✓	

Test Step 1.28 (Repeat Count = 1)		✔
Name	Input Value	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1844.703127	
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-926.5125243	
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.715352	
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str	
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1845.703127	
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-927.5125243	
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.715352	
CombAstNFSV2_Cnt_M_Str[1].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str1	
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1846.703127	

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Name	Input Value		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-928.5125243		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.715352		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1847.703127		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-929.5125243		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.715352		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-7		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1014		
k_StCmpHwTrqLPFKn_Hz_f32	32		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[0].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[0].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[0].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[1].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[1].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[1].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[1].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[2].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[2].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[2].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[3].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[3].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[3].B2_Uls_f32	89.70733524		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.331102133	0.331102109 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.0387127995	0.038712799 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B0_Uls_f32	101.723427	101.7234252 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-191.053268	-191.0532639 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

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## Test Step 1.29 (Repeat Count = 1)

Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1567.617957		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1980.330346		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2306.490226		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1568.617957		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1981.330346		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2306.490226		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1569.617957		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1982.330346		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2306.490226		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1570.617957		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1983.330346		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2306.490226		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-8		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1036		
k_StCmpHwTrqLPFKn_Hz_f32	35		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.355849564	0.355849556 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.770435989	-0.770435992 ± 0.00005	✓

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str3.B0_Uls_f32	22.4817066	22.48170741 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-42.2243309	-42.22433101 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.30 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1337.910389
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-92.34203664
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1338.910389
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-77.17866968
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-93.34203664
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1339.910389
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-78.17866968
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-94.34203664
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1340.910389
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-79.17866968
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-95.34203664
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1058
k_StCmpHwTrqLPFKn_Hz_f32	38
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.379681408	0.379681413 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.31 (Repeat Count = 1)

Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	2076.199938		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1.086842875		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-5.797149125		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	2076.199938		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-2.086842875		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-6.797149125		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	2076.199938		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-3.086842875		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-7.797149125		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	2076.199938		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-4.086842875		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-8.797149125		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.108		
k_StCmpHwTrqLPFKn_Hz_f32	41		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.402631581	0.402631555 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.32 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	8.803286323
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	926.5125243
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	5.797149125
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	9.803286323
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	927.5125243
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	6.797149125
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.80328632
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	928.5125243
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	7.797149125
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	11.80328632
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	929.5125243
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	8.797149125
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1058
k_StCmpHwTrqLPFKn_Hz_f32	38
t_StCmpNFK_Str[0].A1_Uls_f32	-2
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-2
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	90.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-2
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	91.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-2
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483



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Name	Input Value		
t_StCmpNFK_Str[3].B2_Uls_f32	92.70733524		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.379681408	0.379681413 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-2	-2 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-2	-2 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.14052844	-2.140528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	90.7073364	90.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-2	-2 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.24052858	-2.240528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	91.7073364	91.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-2	-2 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.34052849	-2.340528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	92.7073364	92.70733524 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.33 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	4.586334043
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-10.2749703
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	5.586334043
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1981.330346
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-11.2749703
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	6.586334043
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1982.330346
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-12.2749703
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	7.586334043
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1983.330346
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-13.2749703
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	9
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.108
k_StCmpHwTrqLPFKn_Hz_f32	41
t_StCmpNFK_Str[0].A1_Uls_f32	0
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	0
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132



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Name	Input Value		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523		
t_StCmpNFK_Str[1].B2_Uls_f32	20.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	0		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	21.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	0		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	22.82605343		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.402631581	0.402631555 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.148063526	-0.148063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	20.8260536	20.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.168063521	-0.168063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	21.8260536	21.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	0	0 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.139063522	-0.139063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B2_Uls_f32	22.8260536	22.82605343 ± 0.00005	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.34 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1337.910389
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-10.2749613
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1338.910389
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	77.17866968
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-11.2749613
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1339.910389
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	78.17866968
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-12.2749613
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1340.910389
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	79.17866968
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-13.2749613
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3

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StabilityComp2\_Init1

Name	Input Value		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0926		
k_StCmpHwTrqLPFKn_Hz_f32	20		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.5		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.6		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106		
t_StCmpNFK_Str[1].B2_Uls_f32	1.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.7		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106		
t_StCmpNFK_Str[2].B2_Uls_f32	2.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.8		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	3.792469412		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.222232342	0.222232321 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.5	-1.5 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.60000002	-1.6 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.96713614	-1.967136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	1.79246938	1.792469412 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.70000005	-1.7 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.7671361	-1.767136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.7924695	2.792469412 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.79999995	-1.8 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B2_Uls_f32	3.7924695	3.792469412 ± 0.00005	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.35 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	926.5125243
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	5.797149125
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	2076.199938

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Name	Input Value		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	927.5125243		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	6.797149125		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	2076.199938		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	928.5125243		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	7.797149125		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	2076.199938		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	929.5125243		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	8.797149125		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	9		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0948		
k_StCmpHwTrqLPFKn_Hz_f32	23		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[0].A2_Uls_f32	-1		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[1].A2_Uls_f32	-1		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483		
t_StCmpNFK_Str[1].B2_Uls_f32	1.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32	-1		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	1.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32	-1		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	1.050288914		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.251007676	0.251007661 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-1	-1 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-1	-1 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.14052844	-2.140528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	1.05028892	1.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-1	-1 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.24052858	-2.240528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	1.05028892	1.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-1	-1 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.34052849	-2.340528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	1.05028892	1.050288914 ± 0.00005	✓

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## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.36 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-10.2749703
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1981.330346
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-11.2749703
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1982.330346
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-12.2749703
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1983.330346
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-13.2749703
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-2
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.097
k_StCmpHwTrqLPFKn_Hz_f32	26
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	1
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	1
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523
t_StCmpNFK_Str[1].B2_Uls_f32	2.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	1
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523
t_StCmpNFK_Str[2].B2_Uls_f32	3.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	1
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523
t_StCmpNFK_Str[3].B2_Uls_f32	4.929541087

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.278718412	0.27871839 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	1	1 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541111	0.929541087 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	1	1 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.148063526	-0.148063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	2.92954111	2.929541087 ± 0.00005	✔

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	1	1 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.168063521	-0.168063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	3.92954111	3.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	1	1 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.139063522	-0.139063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	4.92954111	4.929541087 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.37 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	926.5125243
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	5.797149125
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	927.5125243
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	6.797149125
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	928.5125243
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	7.797149125
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	929.5125243
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	8.797149125
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-1
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0992
k_StCmpHwTrqLPPKHz_f32	29
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	0
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	0
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	90.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	0
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	91.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	0
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	92.70733524

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str_K_Uls_f32	0.305403888	0.305403896 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.7073364	89.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.96713614	-1.967136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	90.7073364	90.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.7671361	-1.767136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	91.7073364	91.70733524 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	92.7073364	92.70733524 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.38 (Repeat Count = 1)				
Name	Input Value			
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	8.803286323			
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1980.330346			
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-10.2749703			
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str			
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	9.803286323			
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1981.330346			
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-11.2749703			
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1			
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.80328632			
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1982.330346			
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-12.2749703			
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2			
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	11.80328632			
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1983.330346			
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-13.2749703			
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3			
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-5			
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1014			
k_StCmpHwTrqLPFKn_Hz_f32	32			
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018			
t_StCmpNFK_Str[0].A2_Uls_f32	0.4			
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132			
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483			
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343			
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018			
t_StCmpNFK_Str[1].A2_Uls_f32	0.4			
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132			
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483			
t_StCmpNFK_Str[1].B2_Uls_f32	20.82605343			
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018			
t_StCmpNFK_Str[2].A2_Uls_f32	0.4			
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132			
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483			
t_StCmpNFK_Str[2].B2_Uls_f32	21.82605343			
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018			
t_StCmpNFK_Str[3].A2_Uls_f32	0.4			
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132			
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483			
t_StCmpNFK_Str[3].B2_Uls_f32	22.82605343			
Name	Actual Value	Expected Value	Result	
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓	
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓	

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.331102133	0.331102109 ± 0.0000152587890625	✓
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPTr_Cnt_Str.A2_Uls_f32	0.400000006	0.4 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A2_Uls_f32	0.400000006	0.4 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-2.14052844	-2.140528483 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.B2_Uls_f32	20.8260536	20.82605343 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.400000006	0.4 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-2.24052858	-2.240528483 ± 0.00005	✓
tgt_KPTr_Cnt_Str2.B2_Uls_f32	21.8260536	21.82605343 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.400000006	0.4 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-2.34052849	-2.340528483 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B2_Uls_f32	22.8260536	22.82605343 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.39 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	4.586334043
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-10.2749613
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	5.586334043
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	77.17866968
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-11.2749613
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	6.586334043
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	78.17866968
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-12.2749613
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	7.586334043
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	79.17866968
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-13.2749613
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-7
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1036
k_StCmpHwTrqLPFKn_Hz_f32	35
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	-0.4
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	-0.4
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523
t_StCmpNFK_Str[1].B2_Uls_f32	1.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	-0.4



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Name	Input Value		
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	2.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.4		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	3.792469412		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.355849564	0.355849556 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.400000006	-0.4 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.400000006	-0.4 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.148063526	-0.148063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	1.79246938	1.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.400000006	-0.4 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.168063521	-0.168063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.7924695	2.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.400000006	-0.4 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.139063522	-0.139063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	3.7924695	3.792469412 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.40 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1337.910389
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1.086842875
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1338.910389
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	2.086842875
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1339.910389
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	3.086842875
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1340.910389
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	4.086842875
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-8
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1058
k_StCmpHwTrqLPFKn_Hz_f32	38
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396



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Name	Input Value		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106		
t_StCmpNFK_Str[1].B2_Uls_f32	1.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106		
t_StCmpNFK_Str[2].B2_Uls_f32	1.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	1.050288914		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.379681408	0.379681413 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.0502889156	0.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.96713614	-1.967136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	1.05028892	1.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.7671361	-1.767136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	1.05028892	1.050288914 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	1.05028892	1.050288914 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.41 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-8.796749586
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-9.796749586
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	2076.199938

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-10.79674959
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-11.79674959
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.108
k_StCmpHwTrqLPFkn_Hz_f32	41
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	2300
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	2300
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	2.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	2300
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	3.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	2300
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483
t_StCmpNFK_Str[3].B2_Uls_f32	4.929541087

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.402631581	0.402631555 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	2300	2300 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.92954111	0.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	2300	2300 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.14052844	-2.140528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	2.92954111	2.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2300	2300 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.24052858	-2.240528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	3.92954111	3.929541087 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	2300	2300 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.34052849	-2.340528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	4.92954111	4.929541087 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

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## Test Step 1.42 (Repeat Count = 1)

Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	867.2521		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-4.579654921		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.715352		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	867.2521		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-5.579654921		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.715352		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	867.2521		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-6.579654921		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.715352		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	867.2521		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-7.579654921		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.715352		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0926		
k_StCmpHwTrqLPFKn_Hz_f32	20		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	100.1		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	101.2		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.263523		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	102.3		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.38063523		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	103.4		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.138063523		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.222232342	0.222232321 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	100.099998	100.1 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	101.199997	101.2 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.263523012	-0.263523 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	102.300003	102.3 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.380635232	-0.38063523 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216199	-0.661216198 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔

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Name	Actual Value	Expected Value	Result
tgt_KPTr_Cnt_Str3.B0_Uls_f32	103.400002	103.4 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-1.13806355	-1.138063523 ± 0.00005	✓
tgt_KPTr_Cnt_Str3.B2_Uls_f32	19.8260536	19.82605343 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.43 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1640.081281
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2306.490226
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1641.081281
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2306.490226
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1642.081281
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2306.490226
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1643.081281
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2306.490226
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPTr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	9
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0948
k_StCmpHwTrqLPFKn_Hz_f32	23
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-4500
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-4500
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-4500
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-4500
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.251007676	0.251007661 ± 0.0000152587890625	✓
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4500	-4500 ± 0.00005	✓
tgt_KPTr_Cnt_Str.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓

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Name	Actual Value	Expected Value	Result
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-4500	-4500 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4500	-4500 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.146134183	-0.146134178 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-4500	-4500 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.792469382	0.792469412 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.44 (Repeat Count = 1)

Name	Input Value		
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	8.803286323		
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1099.795592		
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-92.34203664		
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str		
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	9.803286323		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1100.795592		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-93.34203664		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.80328632		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1101.795592		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-94.34203664		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	11.80328632		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1102.795592		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-95.34203664		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.097		
k_StCmpHwTrqLPFKn_Hz_f32	26		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	0		
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	0		
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	0		
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	0		
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.278718412	0.27871839 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	0	0 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.8336067	44.83360735 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.45 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-926.5125243
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-5.797149125
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-927.5125243
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-6.797149125
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-928.5125243
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-7.797149125
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-929.5125243
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-8.797149125
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0992
k_StCmpHwTrqLPFKn_Hz_f32	29
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-2300.1
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-2301.2
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-2302.3
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-2303.4

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Name	Input Value		
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.305403888	0.305403896 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2300.1001	-2300.1 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2301.19995	-2301.2 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2302.30005	-2302.3 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239	0.941284239 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2303.3999	-2303.4 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397461	103.397464 ± 0.00005	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.46 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1980.330346
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-8.796749586
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1981.330346
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-9.796749586
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1982.330346
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-10.79674959
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1983.330346
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-8.796749586
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	-1
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1014
k_StCmpHwTrqLPFKn_Hz_f32	32
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	-4
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855



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Name	Input Value
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523
t_StCmpNFK_Str[1].B2_Uls_f32	-4
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523
t_StCmpNFK_Str[2].B2_Uls_f32	-4
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523
t_StCmpNFK_Str[3].B2_Uls_f32	-4

Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.331102133	0.331102109 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.13806352	-0.138063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	-4	-4 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.148063526	-0.148063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	-4	-4 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.168063521	-0.168063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	-4	-4 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.87082535	0.870825326 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.139063522	-0.139063523 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	-4	-4 ± 0.00005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

Test Step 1.47 (Repeat Count = 1)	
Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	8.803286323
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	9.803286323
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-77.17866968
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-77.17866968
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	10.80328632
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-78.17866968
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-78.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	11.80328632
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-79.17866968
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3



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Name	Input Value		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1036		
k_StCmpHwTrqLPFKn_Hz_f32	35		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].B2_Uls_f32	2300		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106		
t_StCmpNFK_Str[1].B2_Uls_f32	2300		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106		
t_StCmpNFK_Str[2].B2_Uls_f32	2300		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	2300		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.355849564	0.355849556 ± 0.0000152587890625	✔
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✔
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str.B2_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.96713614	-1.967136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str1.B2_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.7671361	-1.767136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2300	2300 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.197623402	-0.197623396 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.736897111	-0.736897125 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B0_Uls_f32	1.31353855	1.31353855 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.86713612	-1.867136106 ± 0.00005	✔
tgt_KPtr_Cnt_Str3.B2_Uls_f32	2300	2300 ± 0.00005	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

## Test Step 1.48 (Repeat Count = 1)

Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	867.2521
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	867.2521

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Name	Input Value		
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-77.17866968		
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-77.17866968		
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1		
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	867.2521		
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-78.17866968		
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-78.17866968		
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2		
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	867.2521		
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-79.17866968		
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-76.17866968		
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3		
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1		
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.1058		
k_StCmpHwTrqLPFKn_Hz_f32	38		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	1000.1		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483		
t_StCmpNFK_Str[1].B2_Uls_f32	1100.2		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	1200.3		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	1300.4		
Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	0	0 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	0	0 ± 0.000489	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.00391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.379681408	0.379681413 ± 0.0000152587890625	✓
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.04052854	-2.040528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str.B2_Uls_f32	1000.09998	1000.1 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.14052844	-2.140528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str1.B2_Uls_f32	1100.19995	1100.2 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-2.24052858	-2.240528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str2.B2_Uls_f32	1200.30005	1200.3 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.88123399	-0.881234018 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.111780457	-0.11178046 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.0947601348	0.094760132 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-2.34052849	-2.340528483 ± 0.00005	✓
tgt_KPtr_Cnt_Str3.B2_Uls_f32	1300.40002	1300.4 ± 0.00005	✓

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## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
NF_Init_f32	4	NF_Init_f32	4	✓

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StabilityComp2\_Per1



Project	CBD_StabilityComp2
Module	StabilityComp2
Test Object	StabilityComp2_Per1

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

## Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_StabilityComp2
Configuration File	D:\Synergy_Work_Area\CBD_StabilityComp2\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\StabilityComp\src\Ap_StabilityComp2.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxtLib\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include
File	\$(SOURCEROOT)\NxtLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxtLib\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include
File	\$(SOURCEROOT)\StabilityComp\src\NF_Init_f32_UnitTest.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(SOURCEROOT)\NxtLib\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract -I\$(SOURCEROOT)\StdDef\include -I\$(SOURCEROOT)\StabilityComp\utpl\contract\Ap_StabilityComp2 -I\$(Compiler Install Path)\include

## Comments/Description/Specification

Name	Text
Module 'StabilityComp2'	*****Unit Test Description***** Name of Tester:Spoorti Mali Code File(s) Under Test:Ap_StabilityComp2.c Code File(s) Version:6 Module Design Document:StabilityCompensation2_MDD Module Design Document Version:6 Data Dictionary Version:17 Unit Test Plan Version:5 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):1238 Total RAM Used (Bytes):100 Total CALS Used (Bytes):380 Special Test Requirements:Place ""NF_Init_f32_UnitTest.c"" at ""...\CBD_StabilityComp\StabilityComp\src"" location and add it in the Source files list of TESSy for Testing purpose which is embedded with this UTP. Test Date:01-29-15 Comments: ""Note1: Inline function defined in globalmacro.h is not unit tested.\n\nNote2:\n *****StabilityComp2_Per1"" function, global variable ""CombinedAssist_MtrNm_f32"" has given out of range value to cover 100% path coverage.\n\nNote3: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."" *****

## Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

# TEST DETAILS REPORT

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StabilityComp2\_Per1



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_StabilityComp2\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

# TEST DETAILS REPORT

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StabilityComp2\_Per1

## Test Case 1: Metrics Test

**Specification** Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 13587 Cycles  
TS1.2 13971 Cycles

**Description** Vector Description:

TS1.1 Shortest Execution Path : if(CombinedAssist\_MtrNm\_T\_f32 <= - D\_COMPINLIMIT\_MTRNM\_F32)=>True  
TS1.2 Longest Execution Path: if(CombinedAssist\_MtrNm\_T\_f32 >= D\_COMPINLIMIT\_MTRNM\_F32)=>True

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	44.6767921
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	3.325082672
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.625391233
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDBlendX_Uls_u2p14[0]	6226
t_StCmpADDBlendX_Uls_u2p14[1]	6390
t_StCmpADDBlendY_Uls_u2p14[0]	0
t_StCmpADDBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	256
t_StCmpBlend12Trq_HwNm_u8p8[1]	333
t_StCmpBlend12Trq_HwNm_u8p8[2]	358
t_StCmpBlend12Trq_HwNm_u8p8[3]	384
t_StCmpBlend12Trq_HwNm_u8p8[4]	410
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	10368
t_StCmpBlendSpdBS_Kph_u9p7[1]	10496
t_StCmpBlendSpdBS_Kph_u9p7[2]	10624
t_StCmpBlendSpdBS_Kph_u9p7[3]	10752
t_StCmpBlendSpdBS_Kph_u9p7[4]	10880
t_StCmpBlendSpdBS_Kph_u9p7[5]	11008
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32



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Name	Input Value		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.5		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.089867711		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	11		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-2.781880462		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	2.062094629		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.5	1.5 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-399.875427	-399.8754503 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	250.684235	250.6842371 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	60.9674759	60.96747636 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-382.089508	-382.0895353 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	325.279541	325.2795508 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	157.789688	157.7896841 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-401.740601	-401.7406101 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	258.950684	258.950695 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	71.6970596	71.69706056 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-373.247772	-373.247814 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	333.546021	333.5460086 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	168.519272	168.5192683 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-2.78188038	-2.781880462 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.644559026	0.644559042 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	2.06209469	2.062094629 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	44.6767921
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	3.325082672
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.625391233
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071

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StabilityComp2\_Per1

Name	Input Value
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	6226
t_StCmpADDFBlendX_Uls_u2p14[1]	6390
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	256
t_StCmpBlend12Trq_HwNm_u8p8[1]	333
t_StCmpBlend12Trq_HwNm_u8p8[2]	358
t_StCmpBlend12Trq_HwNm_u8p8[3]	384
t_StCmpBlend12Trq_HwNm_u8p8[4]	410
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	10368
t_StCmpBlendSpdBS_Kph_u9p7[1]	10496
t_StCmpBlendSpdBS_Kph_u9p7[2]	10624
t_StCmpBlendSpdBS_Kph_u9p7[3]	10752
t_StCmpBlendSpdBS_Kph_u9p7[4]	10880
t_StCmpBlendSpdBS_Kph_u9p7[5]	11008
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343

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Name	Input Value		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.5		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0.089867711		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-11		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-2.781880462		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	2.062094629		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.5	1.5 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	459.937378	459.9373825 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-198.375931	-198.3759355 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	6.92694283	6.926939438 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	450.993164	450.993189 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-264.704773	-264.7047913 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-79.1656799	-79.16568408 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	458.072205	458.0722227 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-190.109467	-190.1094777 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	17.6565266	17.65652364 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	459.8349	459.834910325712 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-256.438324	-256.4383335 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-68.4360962	-68.43609988 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-2.78188038	-2.781880462 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.644559026	0.644559042 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071000005	0.0071 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	2.06209469	2.062094629 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-1.21826172	-1.218261719 ± 0.000489	✔

# TEST DETAILS REPORT

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StabilityComp2\_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

**Test Case 2: Boundary Test**

# TEST DETAILS REPORT

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StabilityComp2\_Per1

Specification	Performance Metrics (With "None"	
	Instrumentation and WithPS Environment)	
	CPU Cycles:	
TS2.1	13770	Cycles
TS2.2	14146	Cycles
TS2.3	13799	Cycles
TS2.4	13823	Cycles
TS2.5	13542	Cycles
TS2.6	13838	Cycles
TS2.7	13732	Cycles
TS2.8	13765	Cycles
TS2.9	13784	Cycles
TS2.10	13800	Cycles
TS2.11	13732	Cycles
TS2.12	13765	Cycles
TS2.13	13790	Cycles
TS2.14	13762	Cycles
TS2.15	13803	Cycles
TS2.16	13765	Cycles
TS2.17	13567	Cycles
TS2.18	13859	Cycles
TS2.19	13765	Cycles
TS2.20	13717	Cycles
TS2.21	13548	Cycles
TS2.22	13765	Cycles
TS2.23	13732	Cycles
TS2.24	13765	Cycles
TS2.25	13784	Cycles
TS2.26	13717	Cycles
TS2.27	13548	Cycles
TS2.28	13626	Cycles
TS2.29	13720	Cycles
TS2.30	13608	Cycles
TS2.31	13732	Cycles
TS2.32	13548	Cycles
TS2.33	13737	Cycles
TS2.34	13737	Cycles
TS2.35	13608	Cycles
TS2.36	13775	Cycles
TS2.37	13737	Cycles
TS2.38	13548	Cycles
TS2.39	13681	Cycles
TS2.40	13790	Cycles
TS2.41	13775	Cycles
TS2.42	13738	Cycles
TS2.43	13819	Cycles
TS2.44	13790	Cycles
TS2.45	13794	Cycles
TS2.46	13800	Cycles
TS2.47	13725	Cycles
TS2.48	13775	Cycles
TS2.49	13743	Cycles
TS2.50	13719	Cycles
TS2.51	13719	Cycles
TS2.52	13737	Cycles
TS2.53	13794	Cycles
TS2.54	13756	Cycles
TS2.55	13731	Cycles
TS2.56	13781	Cycles
TS2.57	13713	Cycles
TS2.58	13737	Cycles
TS2.59	13771	Cycles
TS2.60	14169	Cycles
TS2.61	13969	Cycles
TS2.62	13759	Cycles
TS2.63	13647	Cycles
TS2.64	13795	Cycles
TS2.65	13770	Cycles
TS2.66	13673	Cycles
TS2.67	13745	Cycles
TS2.68	13550	Cycles

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Description	Vector Description:
TS2.1	All Min Vector
TS2.2	All Max Vector
TS2.3	Min output, SV1, SV2 using test case 3 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.4	Max output, SV1, SV2 using test case 3 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.5	Min output, SV1, SV2 using test case 4 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.6	Max output, SV1, SV2 using test case 4 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.7	Min output, SV1, SV2 using test case 5 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.8	Max output, SV1, SV2 using test case 5 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.9	Min output, SV1, SV2 using test case 6 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.10	Max output, SV1, SV2 using test case 6 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.11	Min output, SV1, SV2 using test case 7 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.12	Max output, SV1, SV2 using test case 7 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.13	Min output, SV1, SV2 using test case 8 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.14	Max output, SV1, SV2 using test case 8 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.15	Min output, SV1, SV2 using test case 9 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.16	Max output, SV1, SV2 using test case 9 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.17	Min output, SV1, SV2 using test case 10 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.18	Max output, SV1, SV2 using test case 10 from Stability Comp NF Guidelines sheet for B0, B1 B2 & A1 , A2
TS2.19	AssistDDFactor_Uls_f32==>Min
TS2.20	AssistDDFactor_Uls_f32==>Max
TS2.21	AssistDDFactor_Uls_f32==>Pos
TS2.22	AsstFWActive_Uls_f32==>Min
TS2.23	AsstFWActive_Uls_f32==>Max
TS2.24	AsstFWActive_Uls_f32==>Mid
TS2.25	CombinedAssist_MtrNm_f32==>Min
TS2.26	CombinedAssist_MtrNm_f32==>Max
TS2.27	CombinedAssist_MtrNm_f32==>Pos
TS2.28	CombinedAssist_MtrNm_f32==>Zero
TS2.29	CombinedAssist_MtrNm_f32==>Neg
TS2.30	HwTorque_HwNm_f32==>Min
TS2.31	HwTorque_HwNm_f32==>Max
TS2.32	HwTorque_HwNm_f32==>Pos
TS2.33	HwTorque_HwNm_f32==>Zero
TS2.34	HwTorque_HwNm_f32==>Neg
TS2.35	VehicleSpeed_Kph_f32==>Min
TS2.36	VehicleSpeed_Kph_f32==>Max
TS2.37	VehicleSpeed_Kph_f32==>Pos
TS2.38	AssistDDFactorSV_Uls_M_f32==>Min
TS2.39	AssistDDFactorSV_Uls_M_f32==>Max
TS2.40	AssistDDFactorSV_Uls_M_f32==>Pos
TS2.41	HwTorqueSV_HwNm_M_f32==>Min
TS2.42	HwTorqueSV_HwNm_M_f32==>Max
TS2.43	HwTorqueSV_HwNm_M_f32==>Pos
TS2.44	HwTorqueSV_HwNm_M_f32==>Zero
TS2.45	HwTorqueSV_HwNm_M_f32==>Neg
TS2.46	HwTrqSV2_HwNm_M_Str.K==>Min
TS2.47	HwTrqSV2_HwNm_M_Str.K==>Max
TS2.48	HwTrqSV2_HwNm_M_Str.K==>Pos
TS2.49	HwTrqSV2_HwNm_M_Str.SV==>Min
TS2.50	HwTrqSV2_HwNm_M_Str.SV==>Max
TS2.51	HwTrqSV2_HwNm_M_Str.SV==>Pos
TS2.52	HwTrqSV2_HwNm_M_Str.SV==>Zero
TS2.53	HwTrqSV2_HwNm_M_Str.SV==>Neg
TS2.54	t_StCmpADDFBlendX_Uls_u2p14[2]==>Min
TS2.55	t_StCmpADDFBlendX_Uls_u2p14[2]==>Max
TS2.56	t_StCmpADDFBlendX_Uls_u2p14[2]==>Pos
TS2.57	t_StCmpADDFBlendY_Uls_u2p14[2]==>Min
TS2.58	t_StCmpADDFBlendY_Uls_u2p14[2]==>Max
TS2.59	t_StCmpADDFBlendY_Uls_u2p14[2]==>Pos
TS2.60	t_StCmpBlendSpdBS_Kph_u9p7[6]==>Min
TS2.61	t_StCmpBlendSpdBS_Kph_u9p7[6]==>Max
TS2.62	t_StCmpBlendSpdBS_Kph_u9p7[6]==>Pos
TS2.63	t_StCmpBlend12Trq_HwNm_u8p8[5]==>Min
TS2.64	t_StCmpBlend12Trq_HwNm_u8p8[5]==>Max
TS2.65	t_StCmpBlend12Trq_HwNm_u8p8[5]==>Pos
TS2.66	t_StCmpBlend34Trq_HwNm_u8p8[5]==>Min
TS2.67	t_StCmpBlend34Trq_HwNm_u8p8[5]==>Max
TS2.68	t_StCmpBlend34Trq_HwNm_u8p8[5]==>Pos

Test Step 2.1 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3



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Name	Input Value
HwTorqueSV_HwNm_M_f32	-10
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.000125
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	0

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	0
t_StCmpADDFBlendX_Uls_u2p14[0]	0
t_StCmpADDFBlendX_Uls_u2p14[1]	0
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	0
t_StCmpBlend12Trq_HwNm_u8p8[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[2]	0
t_StCmpBlend12Trq_HwNm_u8p8[3]	0
t_StCmpBlend12Trq_HwNm_u8p8[4]	0
t_StCmpBlend34Trq_HwNm_u8p8[0]	0
t_StCmpBlend34Trq_HwNm_u8p8[1]	0
t_StCmpBlend34Trq_HwNm_u8p8[2]	0
t_StCmpBlend34Trq_HwNm_u8p8[3]	0
t_StCmpBlend34Trq_HwNm_u8p8[4]	0
t_StCmpBlendSpdBS_Kph_u9p7[0]	0
t_StCmpBlendSpdBS_Kph_u9p7[1]	0
t_StCmpBlendSpdBS_Kph_u9p7[2]	0
t_StCmpBlendSpdBS_Kph_u9p7[3]	0
t_StCmpBlendSpdBS_Kph_u9p7[4]	0
t_StCmpBlendSpdBS_Kph_u9p7[5]	0
t_StCmpNFK_Str[0].A1_Uls_f32	-2
t_StCmpNFK_Str[0].A2_Uls_f32	-1

# TEST DETAILS REPORT

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StabilityComp2\_Per1

Name	Input Value		
t_StCmpNFK_Str[0].B0_Uls_f32	0		
t_StCmpNFK_Str[0].B1_Uls_f32	-4500		
t_StCmpNFK_Str[0].B2_Uls_f32	-4		
t_StCmpNFK_Str[1].A1_Uls_f32	-2		
t_StCmpNFK_Str[1].A2_Uls_f32	-1		
t_StCmpNFK_Str[1].B0_Uls_f32	0		
t_StCmpNFK_Str[1].B1_Uls_f32	-4500		
t_StCmpNFK_Str[1].B2_Uls_f32	-4		
t_StCmpNFK_Str[2].A1_Uls_f32	-2		
t_StCmpNFK_Str[2].A2_Uls_f32	-1		
t_StCmpNFK_Str[2].B0_Uls_f32	0		
t_StCmpNFK_Str[2].B1_Uls_f32	-4500		
t_StCmpNFK_Str[2].B2_Uls_f32	-4		
t_StCmpNFK_Str[3].A1_Uls_f32	-2		
t_StCmpNFK_Str[3].A2_Uls_f32	-1		
t_StCmpNFK_Str[3].B0_Uls_f32	0		
t_StCmpNFK_Str[3].B1_Uls_f32	-4500		
t_StCmpNFK_Str[3].B2_Uls_f32	-4		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	103.397464		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-8.8		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-10		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	0		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1	1 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33522.1797	33522.18314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-2453.81079	-2453.810632 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2489.01074	-2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	33522.1797	33522.18314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-2453.81079	-2453.810632 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2489.01074	-2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	33522.1797	33522.18314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-2453.81079	-2453.810632 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2489.01074	-2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	33522.1797	33522.18314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-2453.81079	-2453.810632 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2489.01074	-2489.010632 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-10	-10 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.00125000009	0.00125 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.000125000006	0.000125 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	0	0 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✔

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StabilityComp2\_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.2 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	2
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	10
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	10
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.4666
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	16384

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	16384
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	16384
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	16384
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	16384

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Name	Input Value
t2_StCmpBlend34TbLY_Uls_u2p14[1][4]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[2][0]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[2][1]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[2][2]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[2][3]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[2][4]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[3][0]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[3][1]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[3][2]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[3][3]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[3][4]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[4][0]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[4][1]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[4][2]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[4][3]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[4][4]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[5][0]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[5][1]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[5][2]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[5][3]	16384
t2_StCmpBlend34TbLY_Uls_u2p14[5][4]	16384
t_StCmpADDBlendX_Uls_u2p14[0]	32768
t_StCmpADDBlendX_Uls_u2p14[1]	32768
t_StCmpADDBlendY_Uls_u2p14[0]	16384
t_StCmpADDBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2560
t_StCmpBlend12Trq_HwNm_u8p8[1]	2560
t_StCmpBlend12Trq_HwNm_u8p8[2]	2560
t_StCmpBlend12Trq_HwNm_u8p8[3]	2560
t_StCmpBlend12Trq_HwNm_u8p8[4]	2560
t_StCmpBlend34Trq_HwNm_u8p8[0]	2560
t_StCmpBlend34Trq_HwNm_u8p8[1]	2560
t_StCmpBlend34Trq_HwNm_u8p8[2]	2560
t_StCmpBlend34Trq_HwNm_u8p8[3]	2560
t_StCmpBlend34Trq_HwNm_u8p8[4]	2560
t_StCmpBlendSpdBS_Kph_u9p7[0]	32640
t_StCmpBlendSpdBS_Kph_u9p7[1]	32640
t_StCmpBlendSpdBS_Kph_u9p7[2]	32640
t_StCmpBlendSpdBS_Kph_u9p7[3]	32640
t_StCmpBlendSpdBS_Kph_u9p7[4]	32640
t_StCmpBlendSpdBS_Kph_u9p7[5]	32640
t_StCmpNFK_Str[0].A1_Uls_f32	0
t_StCmpNFK_Str[0].A2_Uls_f32	1
t_StCmpNFK_Str[0].B0_Uls_f32	2300
t_StCmpNFK_Str[0].B1_Uls_f32	0
t_StCmpNFK_Str[0].B2_Uls_f32	2300
t_StCmpNFK_Str[1].A1_Uls_f32	0
t_StCmpNFK_Str[1].A2_Uls_f32	1
t_StCmpNFK_Str[1].B0_Uls_f32	2300
t_StCmpNFK_Str[1].B1_Uls_f32	0
t_StCmpNFK_Str[1].B2_Uls_f32	2300
t_StCmpNFK_Str[2].A1_Uls_f32	0
t_StCmpNFK_Str[2].A2_Uls_f32	1
t_StCmpNFK_Str[2].B0_Uls_f32	2300
t_StCmpNFK_Str[2].B1_Uls_f32	0
t_StCmpNFK_Str[2].B2_Uls_f32	2300
t_StCmpNFK_Str[3].A1_Uls_f32	0
t_StCmpNFK_Str[3].A2_Uls_f32	1
t_StCmpNFK_Str[3].B0_Uls_f32	2300
t_StCmpNFK_Str[3].B1_Uls_f32	0
t_StCmpNFK_Str[3].B2_Uls_f32	2300
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306

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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	103.397464		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	2		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.8		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	10		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	511.9921875		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	2	2 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1099.79553	1099.795592 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	17750.9883	17750.98937 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2489.01074	2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1099.79553	1099.795592 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	17750.9883	17750.98937 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2489.01074	2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1099.79553	1099.795592 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	17750.9883	17750.98937 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2489.01074	2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1099.79553	1099.795592 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	17750.9883	17750.98937 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2489.01074	2489.010632 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	10	10 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	10	10 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.466600001	0.4666 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	511.992188	511.9921875 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.3 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-8.803286323
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-8.796749586
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-10.2749703
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-8.803286323
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-8.796749586
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-10.2749703



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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-8.803286323
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-8.796749586
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-10.2749703
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-8.803286323
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-8.796749586
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-10.2749703
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-2.139261341
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.589816654
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0059
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238



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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	13763
t_StCmpADDFBlendX_Uls_u2p14[1]	13926
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1536
t_StCmpBlend12Trq_HwNm_u8p8[1]	1613
t_StCmpBlend12Trq_HwNm_u8p8[2]	1638
t_StCmpBlend12Trq_HwNm_u8p8[3]	1664
t_StCmpBlend12Trq_HwNm_u8p8[4]	1690
t_StCmpBlend34Trq_HwNm_u8p8[0]	1792
t_StCmpBlend34Trq_HwNm_u8p8[1]	1869
t_StCmpBlend34Trq_HwNm_u8p8[2]	1894
t_StCmpBlend34Trq_HwNm_u8p8[3]	1920

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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1946		
t_StCmpBlendSpdBS_Kph_u9p7[0]	22144		
t_StCmpBlendSpdBS_Kph_u9p7[1]	22272		
t_StCmpBlendSpdBS_Kph_u9p7[2]	22400		
t_StCmpBlendSpdBS_Kph_u9p7[3]	22528		
t_StCmpBlendSpdBS_Kph_u9p7[4]	22656		
t_StCmpBlendSpdBS_Kph_u9p7[5]	22784		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047		
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826		
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777		
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.256265753		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.796280471		
t_StCmpNFK_Str[1].B0_Uls_f32	3.629709826		
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777		
t_StCmpNFK_Str[1].B2_Uls_f32	2.207507151		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.246265753		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.879628047		
t_StCmpNFK_Str[2].B0_Uls_f32	4.6297		
t_StCmpNFK_Str[2].B1_Uls_f32	-6.753110777		
t_StCmpNFK_Str[2].B2_Uls_f32	3.207507151		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.336265753		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047		
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826		
t_StCmpNFK_Str[3].B1_Uls_f32	-7.753110777		
t_StCmpNFK_Str[3].B2_Uls_f32	3.207507151		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-1.867136106		
tgt_KPTr_Cnt_Str.A2_Uls_f32	0.870825326		
tgt_KPTr_Cnt_Str.B0_Uls_f32	0.941284239		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-1.867136106		
tgt_KPTr_Cnt_Str.B2_Uls_f32	0.929541087		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-1.867136106		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	0.870825326		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	0.941284239		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-1.867136106		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	0.929541087		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-1.867136106		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.870825326		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	0.941284239		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-1.867136106		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	0.929541087		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-1.867136106		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.870825326		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	0.941284239		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-1.867136106		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	0.929541087		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	5.280749321		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	2.009881043		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	286.7783825		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-34.8022499	-34.80224827 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	9.05254841	9.052549287 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-3.83260059	-3.832600216 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-34.8788986	-34.87890028 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	8.60546684	8.605467182 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-3.83260059	-3.832600216 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-45.4020767	-45.40207292 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	13.5667782	13.56677857 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-3.83260059	-3.832600216 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-51.0277557	-51.02775626 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	14.3332987	14.33329861 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-3.83260059	-3.832600216 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	2.00988102	2.009881043 ± 0.000391	✔

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.56945848	5.569458378 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0059000022	0.0059 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	286.778381	286.7783825 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-3.63134766	-3.631347656 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.4 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	8.803286323
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	8.796749586
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	8.803286323
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	8.796749586
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	8.803286323
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	8.796749586
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	8.803286323
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	8.796749586
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-1.136864293
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.50077064
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	14090
t_StCmpADDFBlendX_Uls_u2p14[1]	14254
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1792
t_StCmpBlend12Trq_HwNm_u8p8[1]	1869
t_StCmpBlend12Trq_HwNm_u8p8[2]	1894
t_StCmpBlend12Trq_HwNm_u8p8[3]	1920
t_StCmpBlend12Trq_HwNm_u8p8[4]	1946
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	22656
t_StCmpBlendSpdBS_Kph_u9p7[1]	22784
t_StCmpBlendSpdBS_Kph_u9p7[2]	22912
t_StCmpBlendSpdBS_Kph_u9p7[3]	23040
t_StCmpBlendSpdBS_Kph_u9p7[4]	23168
t_StCmpBlendSpdBS_Kph_u9p7[5]	23296
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[1].B0_Uls_f32	32.62970983
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.307507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[2].B0_Uls_f32	4.629709826
t_StCmpNFK_Str[2].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	2.407507151
t_StCmpNFK_Str[3].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	5.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[3].B2_Uls_f32	2.507507151
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.870825326
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.941284239
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.929541087
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.867136106
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.870825326

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Name	Input Value		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.870825326		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.941284239		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.867136106		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.929541087		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.800544724		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.726917386		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	6.09245919		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.899599063		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	159.5181965		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.80054474	1.800544724 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-16.7265472	-16.7265471 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	23.3295956	23.32959482 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	14.538023	14.53802214 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-16.7265472	-16.7265471 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	23.9388409	23.93884074 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	14.538023	14.53802214 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-16.7265472	-16.7265471 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	24.5480881	24.54808666 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	14.538023	14.53802214 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-16.7265472	-16.7265471 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	25.1573334	25.15733258 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	14.538023	14.53802214 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.89959908	-4.899599063 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.45558691	7.455586905 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	159.518204	159.5181965 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-4.586334043
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-4.579654921

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-10.2749613
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-4.586334043
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-4.579654921
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-10.2749613
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-4.586334043
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-4.579654921
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-10.2749613
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-4.586334043
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-4.579654921
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-10.2749613
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	5.104858136
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.185905731
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.083
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484



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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	14418
t_StCmpADDFBlendX_Uls_u2p14[1]	14582
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	2048
t_StCmpBlend12Trq_HwNm_u8p8[1]	2125
t_StCmpBlend12Trq_HwNm_u8p8[2]	2150
t_StCmpBlend12Trq_HwNm_u8p8[3]	2176



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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	2202		
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304		
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381		
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406		
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432		
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458		
t_StCmpBlendSpdBS_Kph_u9p7[0]	23168		
t_StCmpBlendSpdBS_Kph_u9p7[1]	23296		
t_StCmpBlendSpdBS_Kph_u9p7[2]	23424		
t_StCmpBlendSpdBS_Kph_u9p7[3]	23552		
t_StCmpBlendSpdBS_Kph_u9p7[4]	23680		
t_StCmpBlendSpdBS_Kph_u9p7[5]	23808		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.172240343		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.688966709		
t_StCmpNFK_Str[0].B0_Uls_f32	11.53067485		
t_StCmpNFK_Str[0].B1_Uls_f32	-20.35078362		
t_StCmpNFK_Str[0].B2_Uls_f32	8.95890172		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.172240343		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.688966709		
t_StCmpNFK_Str[1].B0_Uls_f32	11.53067485		
t_StCmpNFK_Str[1].B1_Uls_f32	-20.35078362		
t_StCmpNFK_Str[1].B2_Uls_f32	8.95890172		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.172240343		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.688966709		
t_StCmpNFK_Str[2].B0_Uls_f32	11.53067485		
t_StCmpNFK_Str[2].B1_Uls_f32	-20.35078362		
t_StCmpNFK_Str[2].B2_Uls_f32	8.95890172		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.172240343		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.688966709		
t_StCmpNFK_Str[3].B0_Uls_f32	11.53067485		
t_StCmpNFK_Str[3].B1_Uls_f32	-20.35078362		
t_StCmpNFK_Str[3].B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.776925164		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.944231291		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-1.773421113		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.832693873		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.920140338		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-2.751178718		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-7.3360147		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	151.6329221		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.92014039	1.920140338 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	50.1716003	50.17159893 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-29.5971336	-29.59713382 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-7.18408298	-7.184083076 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	50.1716003	50.17159893 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-29.5971336	-29.59713382 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-7.18408298	-7.184083076 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	50.1716003	50.17159893 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-29.5971336	-29.59713382 ± 0.0625	✓

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-7.18408298	-7.184083076 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.1716003	50.17159893 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-29.5971336	-29.59713382 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-7.18408298	-7.184083076 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-7.33601475	-7.3360147 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.2621789	4.262178781 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0829999968	0.083 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	151.632919	151.6329221 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-6.74121094	-6.741210938 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	4.586334043
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	4.579654921
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	4.586334043
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	4.579654921
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	4.586334043
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	4.579654921
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	4.586334043
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	4.579654921
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	10.2749613
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	0.625391233
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	8.8
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	14746
t_StCmpADDFBlendX_Uls_u2p14[1]	14909
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2304
t_StCmpBlend12Trq_HwNm_u8p8[1]	2381
t_StCmpBlend12Trq_HwNm_u8p8[2]	2406
t_StCmpBlend12Trq_HwNm_u8p8[3]	2432
t_StCmpBlend12Trq_HwNm_u8p8[4]	2458
t_StCmpBlend34Trq_HwNm_u8p8[0]	256
t_StCmpBlend34Trq_HwNm_u8p8[1]	333
t_StCmpBlend34Trq_HwNm_u8p8[2]	358
t_StCmpBlend34Trq_HwNm_u8p8[3]	384
t_StCmpBlend34Trq_HwNm_u8p8[4]	410
t_StCmpBlendSpdBS_Kph_u9p7[0]	23680
t_StCmpBlendSpdBS_Kph_u9p7[1]	23808
t_StCmpBlendSpdBS_Kph_u9p7[2]	23936
t_StCmpBlendSpdBS_Kph_u9p7[3]	24064
t_StCmpBlendSpdBS_Kph_u9p7[4]	24192
t_StCmpBlendSpdBS_Kph_u9p7[5]	24320
t_StCmpNFK_Str[0].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[0].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[0].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[0].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[0].B2_Uls_f32	2.207507151
t_StCmpNFK_Str[1].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[1].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[1].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[1].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[1].B2_Uls_f32	2.507507151
t_StCmpNFK_Str[2].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[2].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[2].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[2].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[2].B2_Uls_f32	2.607507151
t_StCmpNFK_Str[3].A1_Uls_f32	-0.236265753
t_StCmpNFK_Str[3].A2_Uls_f32	-0.679628047
t_StCmpNFK_Str[3].B0_Uls_f32	2.629709826
t_StCmpNFK_Str[3].B1_Uls_f32	-4.753110777
t_StCmpNFK_Str[3].B2_Uls_f32	2.707507151
tgt_KPtr_Cnt_Str.A1_Uls_f32	-1.773421113
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.776925164

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Name	Input Value		
tgt_KPTr_Cnt_Str.B0_Uls_f32	0.944231291		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-1.773421113		
tgt_KPTr_Cnt_Str.B2_Uls_f32	0.832693873		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-1.773421113		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	0.776925164		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	0.944231291		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-1.773421113		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	0.832693873		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-1.773421113		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.776925164		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	0.944231291		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-1.773421113		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	0.832693873		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-1.773421113		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.776925164		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	0.944231291		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-1.773421113		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	0.832693873		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.656895807		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	3.751765192		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	3.334206831		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	192.7936322		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.65689576	1.656895807 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-11.3323278	-11.33232749 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	13.8066559	13.80665526 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	8.1288681	8.128868134 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-11.3323278	-11.33232749 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	14.9321842	14.93218482 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	8.1288681	8.128868134 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-11.3323278	-11.33232749 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	15.3073616	15.30736134 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	8.1288681	8.128868134 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-11.3323278	-11.33232749 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	15.682539	15.68253786 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	8.1288681	8.128868134 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	3.33420682	3.334206831 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	8.74196053	8.741960278 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	192.793625	192.7936322 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	4.48681641	4.486816406 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

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## Test Step 2.7 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-1844.703127
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1640.081281
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-1844.703127
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1640.081281
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-1844.703127
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1640.081281
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1844.703127
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1640.081281
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.06356
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-5.572093987
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6.449829137
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0011
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	15073
t_StCmpADDFBlendX_Uls_u2p14[1]	15237
t_StCmpADDFBlendY_Uls_u2p14[0]	0



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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	0		
t_StCmpBlend12Trq_HwNm_u8p8[0]	256		
t_StCmpBlend12Trq_HwNm_u8p8[1]	333		
t_StCmpBlend12Trq_HwNm_u8p8[2]	358		
t_StCmpBlend12Trq_HwNm_u8p8[3]	384		
t_StCmpBlend12Trq_HwNm_u8p8[4]	410		
t_StCmpBlend34Trq_HwNm_u8p8[0]	512		
t_StCmpBlend34Trq_HwNm_u8p8[1]	589		
t_StCmpBlend34Trq_HwNm_u8p8[2]	614		
t_StCmpBlend34Trq_HwNm_u8p8[3]	640		
t_StCmpBlend34Trq_HwNm_u8p8[4]	666		
t_StCmpBlendSpdBS_Kph_u9p7[0]	24192		
t_StCmpBlendSpdBS_Kph_u9p7[1]	24320		
t_StCmpBlendSpdBS_Kph_u9p7[2]	24448		
t_StCmpBlendSpdBS_Kph_u9p7[3]	24576		
t_StCmpBlendSpdBS_Kph_u9p7[4]	24704		
t_StCmpBlendSpdBS_Kph_u9p7[5]	24832		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.172240343		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.688966709		
t_StCmpNFK_Str[0].B0_Uls_f32	11.53067485		
t_StCmpNFK_Str[0].B1_Uls_f32	-20.35078362		
t_StCmpNFK_Str[0].B2_Uls_f32	8.55890172		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.172240343		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.688966709		
t_StCmpNFK_Str[1].B0_Uls_f32	11.53067485		
t_StCmpNFK_Str[1].B1_Uls_f32	-20.35078362		
t_StCmpNFK_Str[1].B2_Uls_f32	8.45890172		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.172240343		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.688966709		
t_StCmpNFK_Str[2].B0_Uls_f32	11.53067485		
t_StCmpNFK_Str[2].B1_Uls_f32	-20.35078362		
t_StCmpNFK_Str[2].B2_Uls_f32	8.65890172		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.172240343		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.688966709		
t_StCmpNFK_Str[3].B0_Uls_f32	11.53067485		
t_StCmpNFK_Str[3].B1_Uls_f32	-20.35078362		
t_StCmpNFK_Str[3].B2_Uls_f32	8.35890172		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.83360735		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.190112586		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.035405231		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	7.946210194		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	205.7316638		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.19011259	1.190112586 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-2058.50586	-2058.505819 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-950.824646	-950.8246316 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-1479.89575	-1479.895707 ± 0.000489	✓



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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-2058.50586	-2058.505819 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-951.628174	-951.6281722 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-1479.89575	-1479.895707 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-2058.50586	-2058.505819 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-950.021118	-950.0210911 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-1479.89575	-1479.895707 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-2058.50586	-2058.505819 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-952.431702	-952.4317127 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-1479.89575	-1479.895707 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	7.94621038	7.946210194 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6.44886351	6.448863628 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00109999999	0.0011 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	205.731659	205.7316638 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.8 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.018763274
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1844.703127
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1640.081281
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.06356
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1844.703127
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1640.081281
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2311.06356
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1844.703127
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1640.081281
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2311.06356
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1844.703127
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1640.081281
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2311.06356
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-3.628136861
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.717868638
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0023
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	15401
t_StCmpADDFBlendX_Uls_u2p14[1]	15565
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	512
t_StCmpBlend12Trq_HwNm_u8p8[1]	589
t_StCmpBlend12Trq_HwNm_u8p8[2]	614
t_StCmpBlend12Trq_HwNm_u8p8[3]	640
t_StCmpBlend12Trq_HwNm_u8p8[4]	666
t_StCmpBlend34Trq_HwNm_u8p8[0]	1792
t_StCmpBlend34Trq_HwNm_u8p8[1]	1869
t_StCmpBlend34Trq_HwNm_u8p8[2]	1894
t_StCmpBlend34Trq_HwNm_u8p8[3]	1920
t_StCmpBlend34Trq_HwNm_u8p8[4]	1946
t_StCmpBlendSpdBS_Kph_u9p7[0]	24704
t_StCmpBlendSpdBS_Kph_u9p7[1]	24832
t_StCmpBlendSpdBS_Kph_u9p7[2]	24960
t_StCmpBlendSpdBS_Kph_u9p7[3]	25088
t_StCmpBlendSpdBS_Kph_u9p7[4]	25216
t_StCmpBlendSpdBS_Kph_u9p7[5]	25344
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].B2_Uls_f32	0.939541087
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326

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Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	0.949541087		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.311673654		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.510387957		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	45.40000283		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-90.05567179		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	44.83360735		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	7.489502203		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.492302334		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	431.6523712		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	5705.2793	5705.279182 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1895.55347	-1895.553409 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2184.72656	2184.726548 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	5705.2793	5705.279182 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1895.55347	-1895.553409 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2184.72656	2184.726548 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	5705.2793	5705.279182 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1895.47852	-1895.478514 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2184.72656	2184.726548 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	5705.2793	5705.279182 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1895.40369	-1895.403619 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2184.72656	2184.726548 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.49230242	-4.492302334 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.71536255	4.715362255 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00230000005	0.0023 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	431.652374	431.6523712 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.584307946
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-3.52533319
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.373430097
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0035
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800

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Name	Input Value
t2_StCmpBlend34TbLY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbLY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbLY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbLY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbLY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbLY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbLY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbLY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbLY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbLY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbLY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbLY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbLY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbLY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbLY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbLY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbLY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbLY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbLY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbLY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	15729
t_StCmpADDFBlendX_Uls_u2p14[1]	15892
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	768
t_StCmpBlend12Trq_HwNm_u8p8[1]	845
t_StCmpBlend12Trq_HwNm_u8p8[2]	870
t_StCmpBlend12Trq_HwNm_u8p8[3]	896
t_StCmpBlend12Trq_HwNm_u8p8[4]	922
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	25216
t_StCmpBlendSpdBS_Kph_u9p7[1]	25344
t_StCmpBlendSpdBS_Kph_u9p7[2]	25472
t_StCmpBlendSpdBS_Kph_u9p7[3]	25600
t_StCmpBlendSpdBS_Kph_u9p7[4]	25728
t_StCmpBlendSpdBS_Kph_u9p7[5]	25856
t_StCmpNFK_Str[0].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[0].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[0].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[1].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[1].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[1].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].B2_Uls_f32	0.842693873
t_StCmpNFK_Str[2].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[2].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[2].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].B2_Uls_f32	0.852693873
t_StCmpNFK_Str[3].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[3].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[3].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].B2_Uls_f32	0.836693873
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.129167306
tgt_KPtr_Cnt_Str1.B0_Uls_f32	104.703713
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-207.6908067
tgt_KPtr_Cnt_Str1.B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.718797066
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.129167306



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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	103.397464		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.195094594		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.8		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-0.593378115		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	337.9455675		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.19509459	1.195094594 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-2261.43115	-2261.431258 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	509.396332	509.3963899 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-646.22522	-646.2252825 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-2261.43115	-2261.431258 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	509.484344	509.4843899 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-646.22522	-646.2252825 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-2261.43115	-2261.431258 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	509.572327	509.5723899 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-646.22522	-646.2252825 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-2261.43115	-2261.431258 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	509.431549	509.4315899 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-646.22522	-646.2252825 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-0.593378127	-0.593378115 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.36696196	5.366961758 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0035000011	0.0035 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	337.945557	337.9455675 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2311.793127



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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1567.617957
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1099.795592
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2311.793127
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	2.696668637
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.393281364
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0047
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	16056
t_StCmpADDFBlendX_Uls_u2p14[1]	16220
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432

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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458		
t_StCmpBlendSpdBS_Kph_u9p7[0]	25728		
t_StCmpBlendSpdBS_Kph_u9p7[1]	25856		
t_StCmpBlendSpdBS_Kph_u9p7[2]	25984		
t_StCmpBlendSpdBS_Kph_u9p7[3]	26112		
t_StCmpBlendSpdBS_Kph_u9p7[4]	26240		
t_StCmpBlendSpdBS_Kph_u9p7[5]	26368		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[1].B2_Uls_f32	45.83360735		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[2].B2_Uls_f32	46.83360735		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[3].B2_Uls_f32	47.83360735		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str.B2_Uls_f32	103.397464		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	103.397464		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	103.397464		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.88425732		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0.405236602		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	6.02972976		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	0.351316106		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	208.473795		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.88425732	1.88425732 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1242.13989	1242.139961 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1392.65381	1392.653691 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2198.95313	2198.953051 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1242.13989	1242.139961 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1398.68347	1398.683421 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2198.95313	2198.953051 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1242.13989	1242.139961 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1404.71313	1404.713151 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2198.95313	2198.953051 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1242.13989	1242.139961 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1410.74292	1410.74288 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2198.95313	2198.953051 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	0.351316094	0.351316106 ± 0.000391	✔

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.38530731	4.385307284 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00469999993	0.0047 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	208.473801	208.473795 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-1337.910389
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-926.5125243
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.715352
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-1337.910389
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-926.5125243
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.715352
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-1337.910389
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-926.5125243
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.715352
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1337.910389
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-926.5125243
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.715352
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-3.096692801
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	8.413379371
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0059
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	16384
t_StCmpADDFBlendX_Uls_u2p14[1]	16548
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1280
t_StCmpBlend12Trq_HwNm_u8p8[1]	1357
t_StCmpBlend12Trq_HwNm_u8p8[2]	1382
t_StCmpBlend12Trq_HwNm_u8p8[3]	1408
t_StCmpBlend12Trq_HwNm_u8p8[4]	1434
t_StCmpBlend34Trq_HwNm_u8p8[0]	256
t_StCmpBlend34Trq_HwNm_u8p8[1]	333
t_StCmpBlend34Trq_HwNm_u8p8[2]	358
t_StCmpBlend34Trq_HwNm_u8p8[3]	384
t_StCmpBlend34Trq_HwNm_u8p8[4]	410
t_StCmpBlendSpdBS_Kph_u9p7[0]	26240
t_StCmpBlendSpdBS_Kph_u9p7[1]	26368
t_StCmpBlendSpdBS_Kph_u9p7[2]	26496
t_StCmpBlendSpdBS_Kph_u9p7[3]	26624
t_StCmpBlendSpdBS_Kph_u9p7[4]	26752
t_StCmpBlendSpdBS_Kph_u9p7[5]	26880
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[1].B2_Uls_f32	104.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[2].B2_Uls_f32	105.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[3].B2_Uls_f32	106.397464
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.038712799
tgt_KPtr_Cnt_Str.B0_Uls_f32	101.7234252
tgt_KPtr_Cnt_Str.B1_Uls_f32	-191.0532639
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216198
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.038712799

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Name	Input Value		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	89.70733524		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.543761272		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	4.150901175		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	5.512826705		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	487.5196667		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.54376125	1.543761272 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-2446.79492	-2446.794935 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	547.466797	547.4668299 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-915.666504	-915.6665041 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-2446.79492	-2446.794935 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	551.617737	551.6177311 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-915.666504	-915.6665041 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-2446.79492	-2446.794935 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	555.768616	555.7686323 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-915.666504	-915.6665041 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-2446.79492	-2446.794935 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	559.919556	559.9195334 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-915.666504	-915.6665041 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	5.51282692	5.512826705 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	8.38201141	8.38201092 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0059000022	0.0059 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	487.519653	487.5196667 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.080305485
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1337.910389
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	926.5125243



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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2311.715352
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1337.910389
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	926.5125243
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2311.715352
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1337.910389
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	926.5125243
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2311.715352
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1337.910389
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	926.5125243
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2311.715352
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	7.052959383
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.010423636
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484



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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	16712
t_StCmpADDFBlendX_Uls_u2p14[1]	16876
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1536
t_StCmpBlend12Trq_HwNm_u8p8[1]	1613
t_StCmpBlend12Trq_HwNm_u8p8[2]	1638
t_StCmpBlend12Trq_HwNm_u8p8[3]	1664

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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	1690		
t_StCmpBlend34Trq_HwNm_u8p8[0]	512		
t_StCmpBlend34Trq_HwNm_u8p8[1]	589		
t_StCmpBlend34Trq_HwNm_u8p8[2]	614		
t_StCmpBlend34Trq_HwNm_u8p8[3]	640		
t_StCmpBlend34Trq_HwNm_u8p8[4]	666		
t_StCmpBlendSpdBS_Kph_u9p7[0]	26752		
t_StCmpBlendSpdBS_Kph_u9p7[1]	26880		
t_StCmpBlendSpdBS_Kph_u9p7[2]	27008		
t_StCmpBlendSpdBS_Kph_u9p7[3]	27136		
t_StCmpBlendSpdBS_Kph_u9p7[4]	27264		
t_StCmpBlendSpdBS_Kph_u9p7[5]	27392		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[0].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[0].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[0].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[1].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[1].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[1].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[1].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[2].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[2].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[2].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[3].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[3].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[3].B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str.B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	89.70733524		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.661216198		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.038712799		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	101.7234252		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-191.0532639		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	89.70733524		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.131702065		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-1.330154574		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	6.729015124		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	331.177889		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1975.82312	1975.823156 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-165.880737	-165.8807317 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	1202.60254	1202.60251 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	1975.82312	1975.823156 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-165.880737	-165.8807317 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	1202.60254	1202.60251 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1975.82312	1975.823156 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-165.880737	-165.8807317 ± 0.0625	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	1202.60254	1202.60251 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1975.82312	1975.823156 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-165.880737	-165.8807317 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	1202.60254	1202.60251 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	6.72901535	6.729015124 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.03202581	4.03202564 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	331.177887	331.177889 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.108870333
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-2076.199938
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1980.330346
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2306.490226
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-2076.199938
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1980.330346
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2306.490226
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-2076.199938
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1980.330346
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2306.490226
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-2076.199938
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1980.330346
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2306.490226
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-2.86720078
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.040052211
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.083
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	17039
t_StCmpADDFBlendX_Uls_u2p14[1]	17203
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1792
t_StCmpBlend12Trq_HwNm_u8p8[1]	1869
t_StCmpBlend12Trq_HwNm_u8p8[2]	1894
t_StCmpBlend12Trq_HwNm_u8p8[3]	1920
t_StCmpBlend12Trq_HwNm_u8p8[4]	1946
t_StCmpBlend34Trq_HwNm_u8p8[0]	768
t_StCmpBlend34Trq_HwNm_u8p8[1]	845
t_StCmpBlend34Trq_HwNm_u8p8[2]	870
t_StCmpBlend34Trq_HwNm_u8p8[3]	896
t_StCmpBlend34Trq_HwNm_u8p8[4]	922
t_StCmpBlendSpdBS_Kph_u9p7[0]	27264
t_StCmpBlendSpdBS_Kph_u9p7[1]	27392
t_StCmpBlendSpdBS_Kph_u9p7[2]	27520
t_StCmpBlendSpdBS_Kph_u9p7[3]	27648
t_StCmpBlendSpdBS_Kph_u9p7[4]	27776
t_StCmpBlendSpdBS_Kph_u9p7[5]	27904
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.146134178
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.770435992

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Name	Input Value		
tgt_KPTr_Cnt_Str.B0_Uls_f32	22.48170741		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-42.22433101		
tgt_KPTr_Cnt_Str.B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.146134178		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.770435992		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	22.48170741		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-42.22433101		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.146134178		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.770435992		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	22.48170741		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-42.22433101		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.146134178		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.770435992		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	22.48170741		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-42.22433101		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	19.82605343		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.123044491		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-8.496862817		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-1.684210777		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	264.7532097		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-1952.875	-1952.874902 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1915.21008	-1915.210167 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2267.22388	-2267.223922 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-1952.875	-1952.874902 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1915.21008	-1915.210167 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2267.22388	-2267.223922 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-1952.875	-1952.874902 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1915.21008	-1915.210167 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2267.22388	-2267.223922 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1952.875	-1952.874902 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1915.21008	-1915.210167 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2267.22388	-2267.223922 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-1.68421078	-1.684210777 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.94270563	3.942705542 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0829999968	0.083 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	264.753204	264.7532097 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

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## Test Step 2.14 (Repeat Count = 1)



Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	2076.199938
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1980.330346
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	2306.490226
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	5.994390547
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.489446306
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0095
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637



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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	17367
t_StCmpADDFBlendX_Uls_u2p14[1]	17531
t_StCmpADDFBlendY_Uls_u2p14[0]	16384



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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	16384		
t_StCmpBlend12Trq_HwNm_u8p8[0]	2048		
t_StCmpBlend12Trq_HwNm_u8p8[1]	2125		
t_StCmpBlend12Trq_HwNm_u8p8[2]	2150		
t_StCmpBlend12Trq_HwNm_u8p8[3]	2176		
t_StCmpBlend12Trq_HwNm_u8p8[4]	2202		
t_StCmpBlend34Trq_HwNm_u8p8[0]	1024		
t_StCmpBlend34Trq_HwNm_u8p8[1]	1101		
t_StCmpBlend34Trq_HwNm_u8p8[2]	1126		
t_StCmpBlend34Trq_HwNm_u8p8[3]	1152		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1178		
t_StCmpBlendSpdBS_Kph_u9p7[0]	27776		
t_StCmpBlendSpdBS_Kph_u9p7[1]	27904		
t_StCmpBlendSpdBS_Kph_u9p7[2]	28032		
t_StCmpBlendSpdBS_Kph_u9p7[3]	28160		
t_StCmpBlendSpdBS_Kph_u9p7[4]	28288		
t_StCmpBlendSpdBS_Kph_u9p7[5]	28416		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.146134178		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.770435992		
tgt_KPTr_Cnt_Str.B0_Uls_f32	22.48170741		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-42.22433101		
tgt_KPTr_Cnt_Str.B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.146134178		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.770435992		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	22.48170741		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-42.22433101		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.146134178		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.770435992		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	22.48170741		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-42.22433101		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.146134178		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.770435992		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	22.48170741		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-42.22433101		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	19.82605343		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.543752366		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-5.679611623		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-5.16305114		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	114.5647843		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.54375231	1.543752366 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	2376.99146	2376.991428 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1431.35229	1431.352393 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	1948.51257	1948.512571 ± 0.000489	✓

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	2376.99146	2376.991428 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1431.35229	1431.352393 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	1948.51257	1948.512571 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	2376.99146	2376.991428 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1431.35229	1431.352393 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	1948.51257	1948.512571 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	2376.99146	2376.991428 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1431.35229	1431.352393 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	1948.51257	1948.512571 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-5.16305113	-5.16305114 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.50374317	4.503743276 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00949999969	0.0095 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	114.564781	114.5647843 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.15 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-78.86288978
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-92.34203664
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-78.86288978
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-92.34203664
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-78.86288978
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-92.34203664
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-78.86288978
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-76.17866968
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-92.34203664
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-7.447342753
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.399615061
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	17695
t_StCmpADDFBlendX_Uls_u2p14[1]	17859
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2304
t_StCmpBlend12Trq_HwNm_u8p8[1]	2381
t_StCmpBlend12Trq_HwNm_u8p8[2]	2406
t_StCmpBlend12Trq_HwNm_u8p8[3]	2432
t_StCmpBlend12Trq_HwNm_u8p8[4]	2458
t_StCmpBlend34Trq_HwNm_u8p8[0]	1280
t_StCmpBlend34Trq_HwNm_u8p8[1]	1357
t_StCmpBlend34Trq_HwNm_u8p8[2]	1382
t_StCmpBlend34Trq_HwNm_u8p8[3]	1408
t_StCmpBlend34Trq_HwNm_u8p8[4]	1434
t_StCmpBlendSpdBS_Kph_u9p7[0]	28288
t_StCmpBlendSpdBS_Kph_u9p7[1]	28416
t_StCmpBlendSpdBS_Kph_u9p7[2]	28544
t_StCmpBlendSpdBS_Kph_u9p7[3]	28672
t_StCmpBlendSpdBS_Kph_u9p7[4]	28800
t_StCmpBlendSpdBS_Kph_u9p7[5]	28928
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046

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Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.197623396		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.736897125		
tgt_KPTr_Cnt_Str.B0_Uls_f32	1.31353855		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-2.040528483		
tgt_KPTr_Cnt_Str.B2_Uls_f32	0.792469412		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.197623396		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.736897125		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	1.31353855		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-2.040528483		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	0.792469412		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.197623396		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.736897125		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	1.31353855		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-2.040528483		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	0.792469412		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.197623396		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.736897125		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	1.31353855		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-2.040528483		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	0.792469412		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.179090803		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.239088058		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-1.247233367		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-5.960211134		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	234.1693		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.17909086	1.179090803 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-146.946854	-146.9468495 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-9.06118107	-9.061180795 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-80.5011826	-80.50117889 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-146.946854	-146.9468495 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-9.06118107	-9.061180795 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-80.5011826	-80.50117889 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-146.946854	-146.9468495 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-9.06118107	-9.061180795 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-80.5011826	-80.50117889 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-146.946854	-146.9468495 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-9.06118107	-9.061180795 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-80.5011826	-80.50117889 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-5.96021128	-5.960211134 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.61872196	5.618721924 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107000001	0.107 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	234.169296	234.1693 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✔

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.16 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	78.86288978
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	92.34203664
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	78.86288978
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	92.34203664
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	78.86288978
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	92.34203664
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	78.86288978
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	92.34203664
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	0.376466858
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.399615061
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800



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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	18022
t_StCmpADDFBlendX_Uls_u2p14[1]	18186
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	256
t_StCmpBlend12Trq_HwNm_u8p8[1]	333
t_StCmpBlend12Trq_HwNm_u8p8[2]	358
t_StCmpBlend12Trq_HwNm_u8p8[3]	384
t_StCmpBlend12Trq_HwNm_u8p8[4]	410
t_StCmpBlend34Trq_HwNm_u8p8[0]	1536
t_StCmpBlend34Trq_HwNm_u8p8[1]	1613
t_StCmpBlend34Trq_HwNm_u8p8[2]	1638
t_StCmpBlend34Trq_HwNm_u8p8[3]	1664
t_StCmpBlend34Trq_HwNm_u8p8[4]	1690
t_StCmpBlendSpdBS_Kph_u9p7[0]	28800
t_StCmpBlendSpdBS_Kph_u9p7[1]	28928
t_StCmpBlendSpdBS_Kph_u9p7[2]	29056
t_StCmpBlendSpdBS_Kph_u9p7[3]	29184
t_StCmpBlendSpdBS_Kph_u9p7[4]	29312
t_StCmpBlendSpdBS_Kph_u9p7[5]	29440
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	0.929541087
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.736897125
tgt_KPtr_Cnt_Str.B0_Uls_f32	1.31353855
tgt_KPtr_Cnt_Str.B1_Uls_f32	-2.040528483
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.792469412
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.736897125
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.31353855
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-2.040528483
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.792469412
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.197623396
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.736897125



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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	1.31353855		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-2.040528483		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	0.792469412		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.197623396		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.736897125		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	1.31353855		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-2.040528483		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	0.792469412		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.06165941		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.230940223		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-6.825912082		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	2.728865945		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	463.9437109		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.06165946	1.06165941 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	219.430389	219.430399 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-67.2128601	-67.21286165 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	69.8967896	69.89679112 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	219.430389	219.430399 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-67.2128601	-67.21286165 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	69.8967896	69.89679112 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	219.430389	219.430399 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-67.2128601	-67.21286165 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	69.8967896	69.89679112 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	219.430389	219.430399 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-67.2128601	-67.21286165 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	69.8967896	69.89679112 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	2.72886586	2.728865945 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.80186081	4.801860425 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119000003	0.119 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	463.943726	463.9437109 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.898475326
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-5.277343599
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1.086842875
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-5.797149125
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-5.277343599
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1.086842875
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-5.797149125

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-5.277343599
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1.086842875
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-5.797149125
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-5.277343599
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1.086842875
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-5.797149125
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-0.377396059
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.995200348
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	18350
t_StCmpADDFBlendX_Uls_u2p14[1]	18514
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	512
t_StCmpBlend12Trq_HwNm_u8p8[1]	589
t_StCmpBlend12Trq_HwNm_u8p8[2]	614
t_StCmpBlend12Trq_HwNm_u8p8[3]	640
t_StCmpBlend12Trq_HwNm_u8p8[4]	666
t_StCmpBlend34Trq_HwNm_u8p8[0]	1792
t_StCmpBlend34Trq_HwNm_u8p8[1]	1869
t_StCmpBlend34Trq_HwNm_u8p8[2]	1894
t_StCmpBlend34Trq_HwNm_u8p8[3]	1920

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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1946		
t_StCmpBlendSpdBS_Kph_u9p7[0]	29312		
t_StCmpBlendSpdBS_Kph_u9p7[1]	29440		
t_StCmpBlendSpdBS_Kph_u9p7[2]	29568		
t_StCmpBlendSpdBS_Kph_u9p7[3]	29696		
t_StCmpBlendSpdBS_Kph_u9p7[4]	29824		
t_StCmpBlendSpdBS_Kph_u9p7[5]	29952		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[0].A2_Uls_f32	0.776925164		
t_StCmpNFK_Str[0].B0_Uls_f32	0.944231291		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[0].B2_Uls_f32	0.832693873		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[1].A2_Uls_f32	0.776925164		
t_StCmpNFK_Str[1].B0_Uls_f32	0.944231291		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[1].B2_Uls_f32	0.832693873		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[2].A2_Uls_f32	0.776925164		
t_StCmpNFK_Str[2].B0_Uls_f32	0.944231291		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[2].B2_Uls_f32	0.832693873		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[3].A2_Uls_f32	0.776925164		
t_StCmpNFK_Str[3].B0_Uls_f32	0.944231291		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.773421113		
t_StCmpNFK_Str[3].B2_Uls_f32	0.832693873		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.881234018		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.11178046		
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.094760132		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-0.138063523		
tgt_KPtr_Cnt_Str.B2_Uls_f32	0.050288914		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.881234018		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.11178046		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	0.094760132		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-0.138063523		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	0.050288914		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.881234018		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.11178046		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	0.094760132		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-0.138063523		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	0.050288914		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.881234018		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.11178046		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	0.094760132		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-0.138063523		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	0.050288914		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.642914101		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0.347546816		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-3.664106894		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-6.950975156		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	145.1865792		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.64291406	1.642914101 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-4.56354332	-4.563542674 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1.31877899	1.31877884 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-5.62455511	-5.624554852 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-4.56354332	-4.563542674 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1.31877899	1.31877884 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-5.62455511	-5.624554852 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-4.56354332	-4.563542674 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1.31877899	1.31877884 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-5.62455511	-5.624554852 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-4.56354332	-4.563542674 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1.31877899	1.31877884 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-5.62455511	-5.624554852 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-6.95097494	-6.950975156 ± 0.000391	✔

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.97400713	1.974007112 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131000001	0.0131 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	145.186584	145.1865792 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-5.42871094	-5.428710938 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.18 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.642277279
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	5.277343599
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1.086842875
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	5.797149125
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	5.277343599
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	1.086842875
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	5.797149125
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	5.277343599
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	1.086842875
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	5.797149125
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	5.277343599
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1.086842875
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	5.797149125
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	2.318994939
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.325082672
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595455
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	18678
t_StCmpADDFBlendX_Uls_u2p14[1]	18842
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	768
t_StCmpBlend12Trq_HwNm_u8p8[1]	845
t_StCmpBlend12Trq_HwNm_u8p8[2]	870
t_StCmpBlend12Trq_HwNm_u8p8[3]	896
t_StCmpBlend12Trq_HwNm_u8p8[4]	922
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	29824
t_StCmpBlendSpdBS_Kph_u9p7[1]	29952
t_StCmpBlendSpdBS_Kph_u9p7[2]	30080
t_StCmpBlendSpdBS_Kph_u9p7[3]	30208
t_StCmpBlendSpdBS_Kph_u9p7[4]	30336
t_StCmpBlendSpdBS_Kph_u9p7[5]	30464
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.0184
tgt_KPtr_Cnt_Str.A2_Uls_f32	0.0072
tgt_KPtr_Cnt_Str.B0_Uls_f32	0.094760132
tgt_KPtr_Cnt_Str.B1_Uls_f32	-42.22433101
tgt_KPtr_Cnt_Str.B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.0194
tgt_KPtr_Cnt_Str1.A2_Uls_f32	0.0059



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Name	Input Value		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	1.2		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-46		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	21		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.0054		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	0.0102		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.305239868		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-49.77566899		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	22.17394657		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.0214		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	0.0075		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	3.410479736		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-53.55133798		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	23.34789315		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.096237779		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	3.160670412		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.986122262		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	342.4471709		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-281.811279	-281.8112974 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	144.550613	144.5506129 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	5.57684946	5.576849144 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-280.722504	-280.7225282 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	146.333542	146.3335506 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	9.07014847	9.070148093 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-279.633728	-279.6337589 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	148.116486	148.1164883 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	12.563448	12.56344704 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-278.544983	-278.5449897 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	149.899429	149.899426 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	16.0567474	16.05674599 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.98612213	-4.986122262 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.24600887	3.246008749 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595452	0.078595455 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	342.447174	342.4471709 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.028331161
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	19.1520594



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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	36.4548515
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	5.399615061
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.399615061
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.086029371
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	19005
t_StCmpADDFBlendX_Uls_u2p14[1]	19169
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152

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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178		
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304		
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381		
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406		
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432		
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458		
t_StCmpBlendSpdBS_Kph_u9p7[0]	30336		
t_StCmpBlendSpdBS_Kph_u9p7[1]	30464		
t_StCmpBlendSpdBS_Kph_u9p7[2]	30592		
t_StCmpBlendSpdBS_Kph_u9p7[3]	30720		
t_StCmpBlendSpdBS_Kph_u9p7[4]	30848		
t_StCmpBlendSpdBS_Kph_u9p7[5]	30976		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066		
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306		
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713		
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067		
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066		
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306		
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713		
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067		
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066		
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306		
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713		
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067		
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066		
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306		
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713		
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067		
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.509203792		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-4.768756616		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	7.877069461		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	508.5894554		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1	1 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	1020.71216	1020.712202 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-495.077942	-495.0779745 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	15.4887028	15.48870237 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	995.714905	995.7148822 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-490.139404	-490.1394325 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-22.7449799	-22.74498198 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	1043.39343	1043.393513 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-496.16626	-496.1662998 ± 0.0625	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	23.9144077	23.91440537 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1006.74109	1006.741134 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-491.227722	-491.2277578 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-14.3192787	-14.31927898 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	7.87706947	7.877069461 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.39961529	5.399615061 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0860293731	0.086029371 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	508.589447	508.5894554 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.587016013
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	40.5658218
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	45.3546436
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	1.995200348
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.104858136
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0059
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277

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Name	Input Value
t2_StCmpBlend12Tbly_Uls_u2p14[5][4]	1638
t2_StCmpBlend34Tbly_Uls_u2p14[0][0]	4915
t2_StCmpBlend34Tbly_Uls_u2p14[0][1]	3276
t2_StCmpBlend34Tbly_Uls_u2p14[0][2]	655
t2_StCmpBlend34Tbly_Uls_u2p14[0][3]	328
t2_StCmpBlend34Tbly_Uls_u2p14[0][4]	164
t2_StCmpBlend34Tbly_Uls_u2p14[1][0]	163
t2_StCmpBlend34Tbly_Uls_u2p14[1][1]	328
t2_StCmpBlend34Tbly_Uls_u2p14[1][2]	4915
t2_StCmpBlend34Tbly_Uls_u2p14[1][3]	5800
t2_StCmpBlend34Tbly_Uls_u2p14[1][4]	7291
t2_StCmpBlend34Tbly_Uls_u2p14[2][0]	12288
t2_StCmpBlend34Tbly_Uls_u2p14[2][1]	13920
t2_StCmpBlend34Tbly_Uls_u2p14[2][2]	14744
t2_StCmpBlend34Tbly_Uls_u2p14[2][3]	15129
t2_StCmpBlend34Tbly_Uls_u2p14[2][4]	16178
t2_StCmpBlend34Tbly_Uls_u2p14[3][0]	655
t2_StCmpBlend34Tbly_Uls_u2p14[3][1]	983
t2_StCmpBlend34Tbly_Uls_u2p14[3][2]	1311
t2_StCmpBlend34Tbly_Uls_u2p14[3][3]	2022
t2_StCmpBlend34Tbly_Uls_u2p14[3][4]	16269
t2_StCmpBlend34Tbly_Uls_u2p14[4][0]	369
t2_StCmpBlend34Tbly_Uls_u2p14[4][1]	1360
t2_StCmpBlend34Tbly_Uls_u2p14[4][2]	590
t2_StCmpBlend34Tbly_Uls_u2p14[4][3]	1180
t2_StCmpBlend34Tbly_Uls_u2p14[4][4]	604
t2_StCmpBlend34Tbly_Uls_u2p14[5][0]	1638
t2_StCmpBlend34Tbly_Uls_u2p14[5][1]	4915
t2_StCmpBlend34Tbly_Uls_u2p14[5][2]	8190
t2_StCmpBlend34Tbly_Uls_u2p14[5][3]	11469
t2_StCmpBlend34Tbly_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	5898
t_StCmpADDFBlendX_Uls_u2p14[1]	6062
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2304
t_StCmpBlend12Trq_HwNm_u8p8[1]	2381
t_StCmpBlend12Trq_HwNm_u8p8[2]	2406
t_StCmpBlend12Trq_HwNm_u8p8[3]	2432
t_StCmpBlend12Trq_HwNm_u8p8[4]	2458
t_StCmpBlend34Trq_HwNm_u8p8[0]	1792
t_StCmpBlend34Trq_HwNm_u8p8[1]	1869
t_StCmpBlend34Trq_HwNm_u8p8[2]	1894
t_StCmpBlend34Trq_HwNm_u8p8[3]	1920
t_StCmpBlend34Trq_HwNm_u8p8[4]	1946
t_StCmpBlendSpdBS_Kph_u9p7[0]	9856
t_StCmpBlendSpdBS_Kph_u9p7[1]	9984
t_StCmpBlendSpdBS_Kph_u9p7[2]	10112
t_StCmpBlendSpdBS_Kph_u9p7[3]	10240
t_StCmpBlendSpdBS_Kph_u9p7[4]	10368
t_StCmpBlendSpdBS_Kph_u9p7[5]	10496
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[0].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[0].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[1].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[1].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[1].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[2].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[2].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[2].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[2].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[3].A2_Uls_f32	0.038712799
t_StCmpNFK_Str[3].B0_Uls_f32	101.7234252
t_StCmpNFK_Str[3].B1_Uls_f32	-191.0532639
t_StCmpNFK_Str[3].B2_Uls_f32	89.70733524
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	2		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.68121314		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-0.095094776		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-1.865654063		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	241.8238084		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	2	2 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	60.6037903	60.60379037 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-9.72065735	-9.72065709 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	30.7381058	30.73810653 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	66.2714767	66.2714711 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-9.87327766	-9.873277907 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	34.6804924	34.68049306 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	84.1375961	84.13759436 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-10.0914345	-10.09143448 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	40.3157501	40.31575013 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	78.7261887	78.72618599 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-10.2440548	-10.2440553 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	44.2581367	44.25813666 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-1.86565411	-1.865654063 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.08651114	5.086511155 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0059000022	0.0059 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	241.823807	241.8238084 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓



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Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	44.6767921
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	3.325082672
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.625391233
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637



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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	6226
t_StCmpADDFBlendX_Uls_u2p14[1]	6390
t_StCmpADDFBlendY_Uls_u2p14[0]	0

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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	16384		
t_StCmpBlend12Trq_HwNm_u8p8[0]	256		
t_StCmpBlend12Trq_HwNm_u8p8[1]	333		
t_StCmpBlend12Trq_HwNm_u8p8[2]	358		
t_StCmpBlend12Trq_HwNm_u8p8[3]	384		
t_StCmpBlend12Trq_HwNm_u8p8[4]	410		
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048		
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125		
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150		
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176		
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202		
t_StCmpBlendSpdBS_Kph_u9p7[0]	10368		
t_StCmpBlendSpdBS_Kph_u9p7[1]	10496		
t_StCmpBlendSpdBS_Kph_u9p7[2]	10624		
t_StCmpBlendSpdBS_Kph_u9p7[3]	10752		
t_StCmpBlendSpdBS_Kph_u9p7[4]	10880		
t_StCmpBlendSpdBS_Kph_u9p7[5]	11008		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.5		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.089867711		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	5.082198513		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-2.781880462		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	2.062094629		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.5	1.5 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-182.608429	-182.6084256 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	137.210739	137.2107416 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	47.3119164	47.31191527 ± 0.000489	✓

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-171.576981	-171.5769704 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	176.19577	176.1957761 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.9131775	97.91317858 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-184.473602	-184.4735855 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	145.477203	145.4771995 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	58.0415001	58.04149947 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-162.73526	-162.735249 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	184.462234	184.462234 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	108.642761	108.6427628 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-2.78188038	-2.781880462 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.644559026	0.644559042 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	2.06209469	2.062094629 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.22 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	100
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	24.1219406
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1217.034872
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	6.22302115
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.572093987
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.083
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	6554
t_StCmpADDFBlendX_Uls_u2p14[1]	6717
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	512
t_StCmpBlend12Trq_HwNm_u8p8[1]	589
t_StCmpBlend12Trq_HwNm_u8p8[2]	614
t_StCmpBlend12Trq_HwNm_u8p8[3]	640
t_StCmpBlend12Trq_HwNm_u8p8[4]	666
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458
t_StCmpBlendSpdBS_Kph_u9p7[0]	10880
t_StCmpBlendSpdBS_Kph_u9p7[1]	11008
t_StCmpBlendSpdBS_Kph_u9p7[2]	11136
t_StCmpBlendSpdBS_Kph_u9p7[3]	11264
t_StCmpBlendSpdBS_Kph_u9p7[4]	11392
t_StCmpBlendSpdBS_Kph_u9p7[5]	11520
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125

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Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	5.192982221		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	5.537489653		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	218.4607004		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.6897812	33.68978329 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	28.2914696	28.29147102 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	32.808094	32.80809577 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	436.392944	436.3929595 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	64.183876	64.18388311 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	81.5155869	81.51558949 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	22.0413857	22.04138397 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	31.9537621	31.95376219 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	37.777977	37.77797697 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1223.53003	1223.529996 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	67.8461761	67.84617427 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.4854736	86.48547069 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	5.53748989	5.537489653 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.62612104	5.626120942 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0829999968	0.083 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	218.460693	218.4607004 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

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StabilityComp2\_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	76
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	28.2329109
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1308.903444
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-1.046050632
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.628136861
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0095
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830



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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800



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Name	Input Value
t2_StCmpBlend34TbLY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbLY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbLY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbLY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbLY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbLY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbLY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbLY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbLY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbLY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbLY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbLY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbLY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbLY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbLY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbLY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbLY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbLY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbLY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbLY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	6881
t_StCmpADDFBlendX_Uls_u2p14[1]	7045
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	768
t_StCmpBlend12Trq_HwNm_u8p8[1]	845
t_StCmpBlend12Trq_HwNm_u8p8[2]	870
t_StCmpBlend12Trq_HwNm_u8p8[3]	896
t_StCmpBlend12Trq_HwNm_u8p8[4]	922
t_StCmpBlend34Trq_HwNm_u8p8[0]	256
t_StCmpBlend34Trq_HwNm_u8p8[1]	333
t_StCmpBlend34Trq_HwNm_u8p8[2]	358
t_StCmpBlend34Trq_HwNm_u8p8[3]	384
t_StCmpBlend34Trq_HwNm_u8p8[4]	410
t_StCmpBlendSpdBS_Kph_u9p7[0]	11392
t_StCmpBlendSpdBS_Kph_u9p7[1]	11520
t_StCmpBlendSpdBS_Kph_u9p7[2]	11648
t_StCmpBlendSpdBS_Kph_u9p7[3]	11776
t_StCmpBlendSpdBS_Kph_u9p7[4]	11904
t_StCmpBlendSpdBS_Kph_u9p7[5]	12032
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-1.379859626		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.745193446		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	148.11199		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	56.5111618	56.51115741 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1.9965862	1.996586091 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	18.48246	18.48245868 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	471.341888	471.3418697 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0.965839505	0.965839314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	9.26128864	9.261287314 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	50.5796356	50.57963723 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	2.68088603	2.680886148 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	24.6042805	24.60428048 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1322.65015	1322.650071 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1.65013933	1.650139371 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	15.3831091	15.38310911 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.74519348	-4.745193446 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.60360694	3.603607042 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00949999969	0.0095 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	148.111984	148.11199 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	2.35791016	2.357910156 ± 0.000489	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.53399354
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	42.26399148
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	181

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	32.3438812
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	87.2521
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1400.772017
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	4.610673463
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.52533319
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	7209
t_StCmpADDFBlendX_Uls_u2p14[1]	7373
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178
t_StCmpBlend34Trq_HwNm_u8p8[0]	512
t_StCmpBlend34Trq_HwNm_u8p8[1]	589
t_StCmpBlend34Trq_HwNm_u8p8[2]	614
t_StCmpBlend34Trq_HwNm_u8p8[3]	640

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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	666		
t_StCmpBlendSpdBS_Kph_u9p7[0]	11904		
t_StCmpBlendSpdBS_Kph_u9p7[1]	12032		
t_StCmpBlendSpdBS_Kph_u9p7[2]	12160		
t_StCmpBlendSpdBS_Kph_u9p7[3]	12288		
t_StCmpBlendSpdBS_Kph_u9p7[4]	12416		
t_StCmpBlendSpdBS_Kph_u9p7[5]	12544		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[1].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	0.929541087		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0.5		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	5.192982221		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.409883344		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	457.2263319		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	104.874985	104.8749894 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-28.8966255	-28.89662636 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	38.7261543	38.72615517 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	650.805359	650.805348 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-72.3154907	-72.31548451 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	88.5855865	88.58558949 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	108.434105	108.4341024 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-35.2308006	-35.23080288 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	45.9999161	45.99991757 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1570.05835	1570.058469 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-78.6496658	-78.64966102 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	95.8593521	95.85935189 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.4098835	-4.409883344 ± 0.000391	✔

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.64146447	3.641464599 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107000001	0.107 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	457.226318	457.2263319 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.25 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.53399354
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-748.9277
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	36.4548515
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	99.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	4.610673463
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.52533319
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328



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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	7537
t_StCmpADDFBlendX_Uls_u2p14[1]	7700
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1280
t_StCmpBlend12Trq_HwNm_u8p8[1]	1357
t_StCmpBlend12Trq_HwNm_u8p8[2]	1382
t_StCmpBlend12Trq_HwNm_u8p8[3]	1408
t_StCmpBlend12Trq_HwNm_u8p8[4]	1434
t_StCmpBlend34Trq_HwNm_u8p8[0]	768
t_StCmpBlend34Trq_HwNm_u8p8[1]	845
t_StCmpBlend34Trq_HwNm_u8p8[2]	870
t_StCmpBlend34Trq_HwNm_u8p8[3]	896
t_StCmpBlend34Trq_HwNm_u8p8[4]	922
t_StCmpBlendSpdBS_Kph_u9p7[0]	12416
t_StCmpBlendSpdBS_Kph_u9p7[1]	12544
t_StCmpBlendSpdBS_Kph_u9p7[2]	12672
t_StCmpBlendSpdBS_Kph_u9p7[3]	12800
t_StCmpBlendSpdBS_Kph_u9p7[4]	12928
t_StCmpBlendSpdBS_Kph_u9p7[5]	13056
t_StCmpNFK_Str[0].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[0].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[0].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[0].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[1].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[1].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[1].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[1].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[2].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[2].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[2].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[2].B2_Uls_f32	0.832693873
t_StCmpNFK_Str[3].A1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].A2_Uls_f32	0.776925164
t_StCmpNFK_Str[3].B0_Uls_f32	0.944231291
t_StCmpNFK_Str[3].B1_Uls_f32	-1.773421113
t_StCmpNFK_Str[3].B2_Uls_f32	0.832693873
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709



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Name	Input Value		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-8.8		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.409883344		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	457.2263319		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	43.4261246	43.42611917 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-11.1250858	-11.12508478 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	4.88770294	4.887702031 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-1470.86987	-1470.870021 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	653.367554	653.3676188 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-850.397583	-850.3976387 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	74.9933853	74.99337936 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-17.6712265	-17.67122547 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	13.3134079	13.31340503 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-65.6149902	-65.61498148 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	39.9110832	39.91108084 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-60.8022385	-60.80223568 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.4098835	-4.409883344 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.64146447	3.641464599 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107000001	0.107 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	457.226318	457.2263319 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.26 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	22.1110891

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	9.5125243
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	45.3546436
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	4.096375728
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.696668637
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	7864
t_StCmpADDFBlendX_Uls_u2p14[1]	8028
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1536
t_StCmpBlend12Trq_HwNm_u8p8[1]	1613
t_StCmpBlend12Trq_HwNm_u8p8[2]	1638
t_StCmpBlend12Trq_HwNm_u8p8[3]	1664

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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	1690		
t_StCmpBlend34Trq_HwNm_u8p8[0]	1024		
t_StCmpBlend34Trq_HwNm_u8p8[1]	1101		
t_StCmpBlend34Trq_HwNm_u8p8[2]	1126		
t_StCmpBlend34Trq_HwNm_u8p8[3]	1152		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1178		
t_StCmpBlendSpdBS_Kph_u9p7[0]	12928		
t_StCmpBlendSpdBS_Kph_u9p7[1]	13056		
t_StCmpBlendSpdBS_Kph_u9p7[2]	13184		
t_StCmpBlendSpdBS_Kph_u9p7[3]	13312		
t_StCmpBlendSpdBS_Kph_u9p7[4]	13440		
t_StCmpBlendSpdBS_Kph_u9p7[5]	13568		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[0].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[0].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[1].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[1].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[2].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[2].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.311673654		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.510387957		
t_StCmpNFK_Str[3].B0_Uls_f32	45.40000283		
t_StCmpNFK_Str[3].B1_Uls_f32	-90.05567179		
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.8		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	7.537953377		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	421.6508499		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-753.508057	-753.5080447 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	422.162842	422.1628532 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	54.1296234	54.12962467 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-724.541687	-724.5416569 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	464.58493	464.5849293 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	137.246948	137.2469387 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-738.151672	-738.151637 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	419.142334	419.142347 ± 0.0625	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	48.2115631	48.21156527 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-715.434753	-715.4347359 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	469.473236	469.4732433 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	146.824585	146.8245823 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	7.53795338	7.537953377 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.8632338	2.863233781 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119000003	0.119 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	421.650848	421.6508499 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	867.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	867.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	4.793504775
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.096692801
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	8192
t_StCmpADDFBlendX_Uls_u2p14[1]	8356
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1792
t_StCmpBlend12Trq_HwNm_u8p8[1]	1869
t_StCmpBlend12Trq_HwNm_u8p8[2]	1894
t_StCmpBlend12Trq_HwNm_u8p8[3]	1920
t_StCmpBlend12Trq_HwNm_u8p8[4]	1946
t_StCmpBlend34Trq_HwNm_u8p8[0]	1280
t_StCmpBlend34Trq_HwNm_u8p8[1]	1357
t_StCmpBlend34Trq_HwNm_u8p8[2]	1382
t_StCmpBlend34Trq_HwNm_u8p8[3]	1408
t_StCmpBlend34Trq_HwNm_u8p8[4]	1434
t_StCmpBlendSpdBS_Kph_u9p7[0]	13440
t_StCmpBlendSpdBS_Kph_u9p7[1]	13568
t_StCmpBlendSpdBS_Kph_u9p7[2]	13696
t_StCmpBlendSpdBS_Kph_u9p7[3]	13824
t_StCmpBlendSpdBS_Kph_u9p7[4]	13952
t_StCmpBlendSpdBS_Kph_u9p7[5]	14080
t_StCmpNFK_Str[0].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[0].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[0].B0_Uls_f32	104.703713
t_StCmpNFK_Str[0].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[1].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[1].B0_Uls_f32	104.703713
t_StCmpNFK_Str[1].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[2].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[2].B0_Uls_f32	104.703713
t_StCmpNFK_Str[2].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.718797066
t_StCmpNFK_Str[3].A2_Uls_f32	0.129167306
t_StCmpNFK_Str[3].B0_Uls_f32	104.703713
t_StCmpNFK_Str[3].B1_Uls_f32	-207.6908067
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047



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Name	Input Value		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.922343032		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.662033796		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.01		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	2.554050148		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	90.74295741		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.92234302	1.922343032 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-1598.99146	-1598.99136549363 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	821.108032	821.108040253708 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	55.0111847	55.01118360626 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-1540.25037	-1540.25040724646 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	811.205872	811.205877990569 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	131.672714	131.6727055485 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-1606.67847	-1606.67836802677 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	821.872437	821.872460043154 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	49.0931244	49.09312420626 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1525.26428	-1525.2642512041 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	809.819946	809.819966504955 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	142.402298	142.4022897485 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	2.55405021	2.554050148 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.11892104	3.118921038 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131000001	0.0131 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	90.7429581	90.74295741 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓



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## Test Step 2.28 (Repeat Count = 1)



Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	99.795592
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	100
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1217.034872
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	100
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-5.223089886
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.052959383
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	8520
t_StCmpADDFBlendX_Uls_u2p14[1]	8684
t_StCmpADDFBlendY_Uls_u2p14[0]	16384

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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	0		
t_StCmpBlend12Trq_HwNm_u8p8[0]	2048		
t_StCmpBlend12Trq_HwNm_u8p8[1]	2125		
t_StCmpBlend12Trq_HwNm_u8p8[2]	2150		
t_StCmpBlend12Trq_HwNm_u8p8[3]	2176		
t_StCmpBlend12Trq_HwNm_u8p8[4]	2202		
t_StCmpBlend34Trq_HwNm_u8p8[0]	1536		
t_StCmpBlend34Trq_HwNm_u8p8[1]	1613		
t_StCmpBlend34Trq_HwNm_u8p8[2]	1638		
t_StCmpBlend34Trq_HwNm_u8p8[3]	1664		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1690		
t_StCmpBlendSpdBS_Kph_u9p7[0]	13952		
t_StCmpBlendSpdBS_Kph_u9p7[1]	14080		
t_StCmpBlendSpdBS_Kph_u9p7[2]	14208		
t_StCmpBlendSpdBS_Kph_u9p7[3]	14336		
t_StCmpBlendSpdBS_Kph_u9p7[4]	14464		
t_StCmpBlendSpdBS_Kph_u9p7[5]	14592		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[0].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[0].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[0].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[1].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[1].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[1].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[1].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[2].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[2].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[2].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32	0.038712799		
t_StCmpNFK_Str[3].B0_Uls_f32	101.7234252		
t_StCmpNFK_Str[3].B1_Uls_f32	-191.0532639		
t_StCmpNFK_Str[3].B2_Uls_f32	89.70733524		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.704183795		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	0		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-1.835560966		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	55.80715013		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.70418382	1.704183795 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	50.466217	50.46621602 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-0.741429865	-0.741429826 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	19.1520596	19.1520594 ± 0.000489	✓

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	445.186737	445.1867349 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-0.837628782	-0.837628832 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	21.6369991	21.637 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	45.6618881	45.66188537 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1.19963908	-1.199639114 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	30.9881783	30.9881782 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1234.62781	1234.627773 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1.03002691	-1.030026844 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	26.6068821	26.6068812 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-1.83556092	-1.835560966 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.03996754	7.03996731 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	55.8071518	55.80715013 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.29 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.185577411
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	149.8747
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-142.8569
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-748.9277
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1400.772017
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	5.589816654
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.86720078
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0011
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180

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StabilityComp2\_Per1

Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDBlendX_Uls_u2p14[0]	21955
t_StCmpADDBlendX_Uls_u2p14[1]	22118
t_StCmpADDBlendY_Uls_u2p14[0]	0
t_StCmpADDBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458
t_StCmpBlendSpdBS_Kph_u9p7[0]	24192
t_StCmpBlendSpdBS_Kph_u9p7[1]	24320
t_StCmpBlendSpdBS_Kph_u9p7[2]	24448
t_StCmpBlendSpdBS_Kph_u9p7[3]	24576
t_StCmpBlendSpdBS_Kph_u9p7[4]	24704
t_StCmpBlendSpdBS_Kph_u9p7[5]	24832
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992

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StabilityComp2\_Per1

Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.806505536		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.161382675		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-3.01		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-1.555265224		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	5.280526578		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.80650556	1.806505536 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	4.98343658	4.98343807135549 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	49.6941147	49.6941128195184 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	141.959274	141.95927342374 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	620.801819	620.801798538836 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-67.0227814	-67.0227832523085 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-9.53533173	-9.5353312985 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	163.141373	163.14136930572 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-39.6205978	-39.6205995566173 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	26.0317822	26.03178132374 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1413.35132	1413.35139218929 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-663.416992	-663.417053528711 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-783.63501	-783.6350312985 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-1.55526519	-1.555265224 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.87019563	2.870195657 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0010999999	0.0011 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	5.28052664	5.280526578 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✔



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StabilityComp2\_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.330346
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-7.50077064
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.994390547
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0023
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830



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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800

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Name	Input Value
t2_StCmpBlend34TbLY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbLY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbLY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbLY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbLY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbLY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbLY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbLY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbLY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbLY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbLY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbLY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbLY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbLY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbLY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbLY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbLY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbLY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbLY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbLY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	22282
t_StCmpADDFBlendX_Uls_u2p14[1]	22446
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1280
t_StCmpBlend12Trq_HwNm_u8p8[1]	1357
t_StCmpBlend12Trq_HwNm_u8p8[2]	1382
t_StCmpBlend12Trq_HwNm_u8p8[3]	1408
t_StCmpBlend12Trq_HwNm_u8p8[4]	1434
t_StCmpBlend34Trq_HwNm_u8p8[0]	256
t_StCmpBlend34Trq_HwNm_u8p8[1]	333
t_StCmpBlend34Trq_HwNm_u8p8[2]	358
t_StCmpBlend34Trq_HwNm_u8p8[3]	384
t_StCmpBlend34Trq_HwNm_u8p8[4]	410
t_StCmpBlendSpdBS_Kph_u9p7[0]	20096
t_StCmpBlendSpdBS_Kph_u9p7[1]	20224
t_StCmpBlendSpdBS_Kph_u9p7[2]	20352
t_StCmpBlendSpdBS_Kph_u9p7[3]	20480
t_StCmpBlendSpdBS_Kph_u9p7[4]	20608
t_StCmpBlendSpdBS_Kph_u9p7[5]	20736
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.537355884		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-0.780714965		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-10		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	77.80003345		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.5373559	1.537355884 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	44.5291176	44.52911929 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	18.523016	18.52301676 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	25.9760933	25.97609469 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	437.065826	437.0658052 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	16.5066719	16.50667088 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	23.23983	23.23982959 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.7290096	30.72900817 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	14.1620169	14.16201581 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	20.0580368	20.05803529 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	438.730927	438.7309212 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	22.7155457	22.7155472 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	31.6655312	31.66553259 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-10	-10 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.99785519	5.997855221 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00230000005	0.0023 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	77.8000336	77.80003345 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.31 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.827507603
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.43

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-4.185905731
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.447342753
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0035
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	22610
t_StCmpADDFBlendX_Uls_u2p14[1]	22774
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1536
t_StCmpBlend12Trq_HwNm_u8p8[1]	1613
t_StCmpBlend12Trq_HwNm_u8p8[2]	1638
t_StCmpBlend12Trq_HwNm_u8p8[3]	1664
t_StCmpBlend12Trq_HwNm_u8p8[4]	1690
t_StCmpBlend34Trq_HwNm_u8p8[0]	512
t_StCmpBlend34Trq_HwNm_u8p8[1]	589
t_StCmpBlend34Trq_HwNm_u8p8[2]	614
t_StCmpBlend34Trq_HwNm_u8p8[3]	640

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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	666		
t_StCmpBlendSpdBS_Kph_u9p7[0]	20608		
t_StCmpBlendSpdBS_Kph_u9p7[1]	20736		
t_StCmpBlendSpdBS_Kph_u9p7[2]	20864		
t_StCmpBlendSpdBS_Kph_u9p7[3]	20992		
t_StCmpBlendSpdBS_Kph_u9p7[4]	21120		
t_StCmpBlendSpdBS_Kph_u9p7[5]	21248		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.110752657		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0.820073366		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-1.323123538		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	10		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	193.6101041		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.1107527	1.110752657 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	54.0272064	54.02720622 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	1.68535507	1.68535517 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	15.6726284	15.67262843 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	468.79538	468.7953823 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	0.646675766	0.646675963 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	6.38049126	6.380492697 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	56.6663208	56.66631895 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	3.00840187	3.008401974 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	27.5087471	27.50874723 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	38.8920097	38.89201069 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1.20221162	1.20221157 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	11.3503742	11.3503739 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	10	10 ± 0.000391	

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.43592787	7.435927723 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00350000011	0.0035 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	193.610107	193.6101041 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	2.42089844	2.420898438 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.32 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.146168894
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-149.8747
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	8.8
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.376466858
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0047
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408



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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328



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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	22938
t_StCmpADDFBlendX_Uls_u2p14[1]	23101
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1792
t_StCmpBlend12Trq_HwNm_u8p8[1]	1869
t_StCmpBlend12Trq_HwNm_u8p8[2]	1894
t_StCmpBlend12Trq_HwNm_u8p8[3]	1920
t_StCmpBlend12Trq_HwNm_u8p8[4]	1946
t_StCmpBlend34Trq_HwNm_u8p8[0]	768
t_StCmpBlend34Trq_HwNm_u8p8[1]	845
t_StCmpBlend34Trq_HwNm_u8p8[2]	870
t_StCmpBlend34Trq_HwNm_u8p8[3]	896
t_StCmpBlend34Trq_HwNm_u8p8[4]	922
t_StCmpBlendSpdBS_Kph_u9p7[0]	21120
t_StCmpBlendSpdBS_Kph_u9p7[1]	21248
t_StCmpBlendSpdBS_Kph_u9p7[2]	21376
t_StCmpBlendSpdBS_Kph_u9p7[3]	21504
t_StCmpBlendSpdBS_Kph_u9p7[4]	21632
t_StCmpBlendSpdBS_Kph_u9p7[5]	21760
t_StCmpNFK_Str[0].A1_Uls_f32	-2
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-2
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	90.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-2
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	91.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-2
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483
t_StCmpNFK_Str[3].B2_Uls_f32	92.70733524
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709

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Name	Input Value		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.485785289		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-3.679503775		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	3.02		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	96.51161814		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.48578525	1.485785289 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-292.441254	-292.4412628 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-447.65094	-447.6509508 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-159.55072	-159.5507272 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-12.0672455	-12.06724064 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-349.078186	-349.0781928 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-20.790163	-20.79016164 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	86.64534	86.64533495 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-321.732605	-321.7326234 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	21.3121529	21.31215097 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	3.57830429	3.578303711 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-352.774902	-352.7749092 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-15.8202801	-15.82028044 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	3.01999998	3.02 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.416057467	0.416057464 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00469999993	0.0047 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	96.5116196	96.51161814 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.33 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.899227092
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	22.1110891

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	6.449829137
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.377396059
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0059
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	23265
t_StCmpADDFBlendX_Uls_u2p14[1]	23429
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	2048
t_StCmpBlend12Trq_HwNm_u8p8[1]	2125
t_StCmpBlend12Trq_HwNm_u8p8[2]	2150
t_StCmpBlend12Trq_HwNm_u8p8[3]	2176

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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	2202		
t_StCmpBlend34Trq_HwNm_u8p8[0]	1024		
t_StCmpBlend34Trq_HwNm_u8p8[1]	1101		
t_StCmpBlend34Trq_HwNm_u8p8[2]	1126		
t_StCmpBlend34Trq_HwNm_u8p8[3]	1152		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1178		
t_StCmpBlendSpdBS_Kph_u9p7[0]	21632		
t_StCmpBlendSpdBS_Kph_u9p7[1]	21760		
t_StCmpBlendSpdBS_Kph_u9p7[2]	21888		
t_StCmpBlendSpdBS_Kph_u9p7[3]	22016		
t_StCmpBlendSpdBS_Kph_u9p7[4]	22144		
t_StCmpBlendSpdBS_Kph_u9p7[5]	22272		
t_StCmpNFK_Str[0].A1_Uls_f32	0		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[1].A1_Uls_f32	0		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523		
t_StCmpNFK_Str[1].B2_Uls_f32	20.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	0		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	21.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	0		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	22.82605343		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.229246616		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.8		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	0		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	502.4993979		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	20.8961315	20.8961301 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	221.022537	221.0225429 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	63.1747246	63.17472427 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	23.8690414	23.869041 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	599.218201	599.2181762 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	564.459961	564.4599387 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	37.8330421	37.833041 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	231.957245	231.957235 ± 0.0625	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	54.1296234	54.12962467 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	30.0700626	30.0700628 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	406.566711	406.5666885 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	279.139954	279.1399387 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	0	0 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.413223416	0.413223414 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00590000022	0.0059 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	502.49939	502.4993979 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	36.4548515
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-4.717868638
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.318994939
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277



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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	23593
t_StCmpADDFBlendX_Uls_u2p14[1]	23757
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2304
t_StCmpBlend12Trq_HwNm_u8p8[1]	2381
t_StCmpBlend12Trq_HwNm_u8p8[2]	2406
t_StCmpBlend12Trq_HwNm_u8p8[3]	2432
t_StCmpBlend12Trq_HwNm_u8p8[4]	2458
t_StCmpBlend34Trq_HwNm_u8p8[0]	1280
t_StCmpBlend34Trq_HwNm_u8p8[1]	1357
t_StCmpBlend34Trq_HwNm_u8p8[2]	1382
t_StCmpBlend34Trq_HwNm_u8p8[3]	1408
t_StCmpBlend34Trq_HwNm_u8p8[4]	1434
t_StCmpBlendSpdBS_Kph_u9p7[0]	20096
t_StCmpBlendSpdBS_Kph_u9p7[1]	20224
t_StCmpBlendSpdBS_Kph_u9p7[2]	20352
t_StCmpBlendSpdBS_Kph_u9p7[3]	20480
t_StCmpBlendSpdBS_Kph_u9p7[4]	20608
t_StCmpBlendSpdBS_Kph_u9p7[5]	20736
t_StCmpNFK_Str[0].A1_Uls_f32	-1
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-1
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	1.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-1
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	2.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-1
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	3.792469412
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047



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Name	Input Value		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.121356582		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	6.128125453		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.02		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	447.3965932		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.12135661	1.121356582 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	57.7724152	57.77241472 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	9.7908268	9.79082663 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	44.1443405	44.14434022 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	119.555573	119.5555652 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	22.4870701	22.48706928 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	102.903427	102.903422 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	63.3778114	63.37781147 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	22.9889069	22.98890649 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	52.5700455	52.57004322 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	135.867844	135.8678431 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.6851501	35.68514914 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	111.329124	111.329125 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.01999998	-4.02 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.33602691	2.336026942 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	447.396606	447.3965932 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

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## Test Step 2.35 (Repeat Count = 1)



Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	40.5658218
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	45.3546436
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1217.034872
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-5.373430097
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	8.237084794
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.083
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	23921
t_StCmpADDFBlendX_Uls_u2p14[1]	24084
t_StCmpADDFBlendY_Uls_u2p14[0]	0

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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	0		
t_StCmpBlend12Trq_HwNm_u8p8[0]	256		
t_StCmpBlend12Trq_HwNm_u8p8[1]	333		
t_StCmpBlend12Trq_HwNm_u8p8[2]	358		
t_StCmpBlend12Trq_HwNm_u8p8[3]	384		
t_StCmpBlend12Trq_HwNm_u8p8[4]	410		
t_StCmpBlend34Trq_HwNm_u8p8[0]	1536		
t_StCmpBlend34Trq_HwNm_u8p8[1]	1613		
t_StCmpBlend34Trq_HwNm_u8p8[2]	1638		
t_StCmpBlend34Trq_HwNm_u8p8[3]	1664		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1690		
t_StCmpBlendSpdBS_Kph_u9p7[0]	20608		
t_StCmpBlendSpdBS_Kph_u9p7[1]	20736		
t_StCmpBlendSpdBS_Kph_u9p7[2]	20864		
t_StCmpBlendSpdBS_Kph_u9p7[3]	20992		
t_StCmpBlendSpdBS_Kph_u9p7[4]	21120		
t_StCmpBlendSpdBS_Kph_u9p7[5]	21248		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[0].A2_Uls_f32	-1		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[1].A2_Uls_f32	-1		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483		
t_StCmpNFK_Str[1].B2_Uls_f32	1.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32	-1		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	1.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32	-1		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	1.050288914		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.777533753		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	4.147474349		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	7.353238475		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	0		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.77753377	1.777533753 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	57.0410652	57.04106627 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	42.103405	42.10340423 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	41.8948326	41.89483225 ± 0.000489	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	477.280029	477.280005 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	87.9562225	87.9562245 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	83.600174	83.60017817 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	49.9139023	49.91390037 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	55.8285217	55.82852218 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	51.472477	51.47247585 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1268.93835	1268.938275 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	97.5338745	97.5338681 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	93.1778259	93.17782177 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	7.35323858	7.353238475 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.99940109	7.999401454 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0829999968	0.083 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	0	0 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.36 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.810689111
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	88.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	44.6767921
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1308.903444
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-4.393281364
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.399615061
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0095
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180

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StabilityComp2\_Per1

Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	24248
t_StCmpADDFBlendX_Uls_u2p14[1]	24412
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	512
t_StCmpBlend12Trq_HwNm_u8p8[1]	589
t_StCmpBlend12Trq_HwNm_u8p8[2]	614
t_StCmpBlend12Trq_HwNm_u8p8[3]	640
t_StCmpBlend12Trq_HwNm_u8p8[4]	666
t_StCmpBlend34Trq_HwNm_u8p8[0]	1792
t_StCmpBlend34Trq_HwNm_u8p8[1]	1869
t_StCmpBlend34Trq_HwNm_u8p8[2]	1894
t_StCmpBlend34Trq_HwNm_u8p8[3]	1920
t_StCmpBlend34Trq_HwNm_u8p8[4]	1946
t_StCmpBlendSpdBS_Kph_u9p7[0]	21120
t_StCmpBlendSpdBS_Kph_u9p7[1]	21248
t_StCmpBlendSpdBS_Kph_u9p7[2]	21376
t_StCmpBlendSpdBS_Kph_u9p7[3]	21504
t_StCmpBlendSpdBS_Kph_u9p7[4]	21632
t_StCmpBlendSpdBS_Kph_u9p7[5]	21760
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	1
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	1
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523
t_StCmpNFK_Str[1].B2_Uls_f32	2.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	1
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523
t_StCmpNFK_Str[2].B2_Uls_f32	3.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	1



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StabilityComp2\_Per1

Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	4.929541087		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.727139313		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.695452809		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-1.032654858		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-3.981465793		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	511.9921875		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.72713935	1.727139313 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	44.7398567	44.73985765 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-32.1915207	-32.19152039 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	31.2316265	31.23162527 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	467.147675	467.1476754 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-30.4299984	-30.42999744 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	27.4047928	27.4047926 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	35.0125198	35.01251847 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-46.0190697	-46.01906917 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	41.9612083	41.96120947 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1314.61975	1314.619784 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-43.2248917	-43.22489135 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	38.1343765	38.1343768 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-3.98146582	-3.981465793 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.39005518	5.390054891 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00949999969	0.0095 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	511.992188	511.9921875 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔



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StabilityComp2\_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.37 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	42.26399148
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	26.5125243
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	676
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	24.1219406
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1400.772017
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	8.413379371
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.995200348
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830

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StabilityComp2\_Per1

Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800

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Name	Input Value
t2_StCmpBlend34TbLY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbLY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbLY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbLY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbLY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbLY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbLY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbLY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbLY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbLY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbLY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbLY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbLY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbLY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbLY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbLY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbLY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbLY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbLY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbLY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	24576
t_StCmpADDFBlendX_Uls_u2p14[1]	24740
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	768
t_StCmpBlend12Trq_HwNm_u8p8[1]	845
t_StCmpBlend12Trq_HwNm_u8p8[2]	870
t_StCmpBlend12Trq_HwNm_u8p8[3]	896
t_StCmpBlend12Trq_HwNm_u8p8[4]	922
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	21632
t_StCmpBlendSpdBS_Kph_u9p7[1]	21760
t_StCmpBlendSpdBS_Kph_u9p7[2]	21888
t_StCmpBlendSpdBS_Kph_u9p7[3]	22016
t_StCmpBlendSpdBS_Kph_u9p7[4]	22144
t_StCmpBlendSpdBS_Kph_u9p7[5]	22272
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	0
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	0
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	90.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	0
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	91.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	0
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	92.70733524
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.975962193		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.298491478		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	4.861480319		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-3.972531211		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	427.07		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.97596216	1.975962193 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	39.4983139	39.4983144 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	436.110443	436.1104447 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	31.9363403	31.93634196 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	500.890778	500.8907905 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	440.971924	440.9719251 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	77.6931458	77.69314885 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9446373	30.94463575 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	445.833405	445.8334054 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	36.9062233	36.90622316 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1408.03113	1408.03112 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	450.694885	450.6948857 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	82.6630325	82.66303005 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-3.97253132	-3.972531211 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.68194556	2.681945503 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107000001	0.107 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	427.070007	427.07 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.43

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	28.2329109
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-4.010423636
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.325082672
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	24904
t_StCmpADDFBlendX_Uls_u2p14[1]	25068
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432

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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458		
t_StCmpBlendSpdBS_Kph_u9p7[0]	22144		
t_StCmpBlendSpdBS_Kph_u9p7[1]	22272		
t_StCmpBlendSpdBS_Kph_u9p7[2]	22400		
t_StCmpBlendSpdBS_Kph_u9p7[3]	22528		
t_StCmpBlendSpdBS_Kph_u9p7[4]	22656		
t_StCmpBlendSpdBS_Kph_u9p7[5]	22784		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	0.4		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	0.4		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483		
t_StCmpNFK_Str[1].B2_Uls_f32	20.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	0.4		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	21.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	0.4		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	22.82605343		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.310110028		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0.959956527		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-3.644667888		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-5.476300287		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	132.2462543		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.31010997	1.310110028 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	37.6280365	37.6280359 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-77.27005	-77.27004834 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	12.5266714	12.52667014 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	14.5866537	14.58665522 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-69.1626511	-69.16265603 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-16.8534813	-16.85348035 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	60.3766632	60.3766677 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-87.00811	-87.00811284 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	18.6484909	18.64849194 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	25.6802254	25.68022762 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-78.9007187	-78.90072052 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-10.7316608	-10.73165855 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-5.47630024	-5.476300287 ± 0.000391	✔



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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.40663838	3.406638247 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119000003	0.119 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	132.246262	132.2462543 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.39 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	2
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	32.3438812
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-4.040052211
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6.22302115
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408



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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	21955
t_StCmpADDFBlendX_Uls_u2p14[1]	22118
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458
t_StCmpBlendSpdBS_Kph_u9p7[0]	24192
t_StCmpBlendSpdBS_Kph_u9p7[1]	24320
t_StCmpBlendSpdBS_Kph_u9p7[2]	24448
t_StCmpBlendSpdBS_Kph_u9p7[3]	24576
t_StCmpBlendSpdBS_Kph_u9p7[4]	24704
t_StCmpBlendSpdBS_Kph_u9p7[5]	24832
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	-0.4
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	-0.4
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523
t_StCmpNFK_Str[1].B2_Uls_f32	1.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	-0.4
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523
t_StCmpNFK_Str[2].B2_Uls_f32	2.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	-0.4
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523
t_StCmpNFK_Str[3].B2_Uls_f32	3.792469412
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709

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Name	Input Value		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.94521163		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.438818216		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-2.167495239		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-2.346597064		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	14.4906798		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.94521165	1.94521163 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	58.5772095	58.57720679 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	6.0304203	6.030420431 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	19.3702354	19.37023527 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	32.4280624	32.42806266 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-2.39944172	-2.399442052 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	3.71431732	3.71431716 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	89.4242477	89.42424695 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	4.60493517	4.604934913 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	26.6439972	26.64399767 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	52.1114807	52.11148141 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-3.82492781	-3.82492757 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	10.9880791	10.98807956 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-2.34659696	-2.346597064 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6.19442415	6.194424257 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131000001	0.0131 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	14.4906797	14.4906798 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.40 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.5
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-748.9277
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	36.4548515
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	4.489446306
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.046050632
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	22282
t_StCmpADDFBlendX_Uls_u2p14[1]	22446
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1280
t_StCmpBlend12Trq_HwNm_u8p8[1]	1357
t_StCmpBlend12Trq_HwNm_u8p8[2]	1382
t_StCmpBlend12Trq_HwNm_u8p8[3]	1408

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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	1434		
t_StCmpBlend34Trq_HwNm_u8p8[0]	256		
t_StCmpBlend34Trq_HwNm_u8p8[1]	333		
t_StCmpBlend34Trq_HwNm_u8p8[2]	358		
t_StCmpBlend34Trq_HwNm_u8p8[3]	384		
t_StCmpBlend34Trq_HwNm_u8p8[4]	410		
t_StCmpBlendSpdBS_Kph_u9p7[0]	20096		
t_StCmpBlendSpdBS_Kph_u9p7[1]	20224		
t_StCmpBlendSpdBS_Kph_u9p7[2]	20352		
t_StCmpBlendSpdBS_Kph_u9p7[3]	20480		
t_StCmpBlendSpdBS_Kph_u9p7[4]	20608		
t_StCmpBlendSpdBS_Kph_u9p7[5]	20736		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106		
t_StCmpNFK_Str[1].B2_Uls_f32	1.050288914		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106		
t_StCmpNFK_Str[2].B2_Uls_f32	1.050288914		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	1.050288914		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.512525404		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-0.737697268		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-8.753394723		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	424.7121763		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.51252544	1.512525404 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	31.6033382	31.60334001 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	2.87916684	2.879166878 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	26.0892181	26.08921875 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-119.528496	-119.5284982 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-85.4410934	-85.44109914 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-757.433838	-757.4338473 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	29.7615681	29.76156753 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	3.08329868	3.083298567 ± 0.0625	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	34.5149231	34.51492175 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	43.7140198	43.71401946 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	2.82023764	2.820238224 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	32.1615524	32.16155566 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-8.75339508	-8.753394723 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.07049882	1.070498741 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	424.712189	424.7121763 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.41 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	100
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	45.3546436
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1217.034872
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-10
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.610673463
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0011
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745



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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277



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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	22610
t_StCmpADDFBlendX_Uls_u2p14[1]	22774
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1536
t_StCmpBlend12Trq_HwNm_u8p8[1]	1613
t_StCmpBlend12Trq_HwNm_u8p8[2]	1638
t_StCmpBlend12Trq_HwNm_u8p8[3]	1664
t_StCmpBlend12Trq_HwNm_u8p8[4]	1690
t_StCmpBlend34Trq_HwNm_u8p8[0]	512
t_StCmpBlend34Trq_HwNm_u8p8[1]	589
t_StCmpBlend34Trq_HwNm_u8p8[2]	614
t_StCmpBlend34Trq_HwNm_u8p8[3]	640
t_StCmpBlend34Trq_HwNm_u8p8[4]	666
t_StCmpBlendSpdBS_Kph_u9p7[0]	20608
t_StCmpBlendSpdBS_Kph_u9p7[1]	20736
t_StCmpBlendSpdBS_Kph_u9p7[2]	20864
t_StCmpBlendSpdBS_Kph_u9p7[3]	20992
t_StCmpBlendSpdBS_Kph_u9p7[4]	21120
t_StCmpBlendSpdBS_Kph_u9p7[5]	21248
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	2300
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	2300
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	2.929541087
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	2300
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	3.929541087
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	2300
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483
t_StCmpNFK_Str[3].B2_Uls_f32	4.929541087
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.260840893		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.776461065		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	5.00756222		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	401.8708679		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	67.5402603	67.54026309 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-38.9254608	-38.92546231 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	54.0677223	54.0677241 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	532.80127	532.801222 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-93.570755	-93.57074759 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	136.975525	136.9755189 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	47.939209	47.93921149 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-7.44248581	-7.442483106 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	48.149662	48.1496647 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1325.64099	1325.640947 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-84.358284	-84.35828007 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	146.553162	146.5531625 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	5.00756216	5.00756222 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.61660147	4.616601722 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00109999999	0.0011 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	401.87088	401.8708679 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

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## Test Step 2.42 (Repeat Count = 1)



Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.177610544
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-142.8569
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	76
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1400.772017
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	10
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.096375728
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0023
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	22938
t_StCmpADDFBlendX_Uls_u2p14[1]	23101
t_StCmpADDFBlendY_Uls_u2p14[0]	0

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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	16384		
t_StCmpBlend12Trq_HwNm_u8p8[0]	1792		
t_StCmpBlend12Trq_HwNm_u8p8[1]	1869		
t_StCmpBlend12Trq_HwNm_u8p8[2]	1894		
t_StCmpBlend12Trq_HwNm_u8p8[3]	1920		
t_StCmpBlend12Trq_HwNm_u8p8[4]	1946		
t_StCmpBlend34Trq_HwNm_u8p8[0]	768		
t_StCmpBlend34Trq_HwNm_u8p8[1]	845		
t_StCmpBlend34Trq_HwNm_u8p8[2]	870		
t_StCmpBlend34Trq_HwNm_u8p8[3]	896		
t_StCmpBlend34Trq_HwNm_u8p8[4]	922		
t_StCmpBlendSpdBS_Kph_u9p7[0]	21120		
t_StCmpBlendSpdBS_Kph_u9p7[1]	21248		
t_StCmpBlendSpdBS_Kph_u9p7[2]	21376		
t_StCmpBlendSpdBS_Kph_u9p7[3]	21504		
t_StCmpBlendSpdBS_Kph_u9p7[4]	21632		
t_StCmpBlendSpdBS_Kph_u9p7[5]	21760		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	100		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	101		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.263523		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	102		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.38063523		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	103		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.138063523		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.057771564		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-6.539170671		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	6.840499496		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	405.8627497		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-130.877991	-130.8779893 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-117.302124	-117.3021196 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	16.7510872	16.75108653 ± 0.000489	✓

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	472.960571	472.9605569 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-156.239868	-156.2398649 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-36.0890541	-36.0890508 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	41.8940125	41.89401175 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-121.663124	-121.6631206 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.8330269	10.83302713 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1391.44592	1391.445919 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-148.333267	-148.3332651 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-25.3594704	-25.3594666 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	6.8404994	6.840499496 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.10995436	4.109954064 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00230000005	0.0023 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	405.862762	405.8627497 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.43 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.367408816
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	181
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	87.2521
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	8.03
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.793504775
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0035
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180



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StabilityComp2\_Per1

Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDBlendX_Uls_u2p14[0]	23265
t_StCmpADDBlendX_Uls_u2p14[1]	23429
t_StCmpADDBlendY_Uls_u2p14[0]	16384
t_StCmpADDBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	2048
t_StCmpBlend12Trq_HwNm_u8p8[1]	2125
t_StCmpBlend12Trq_HwNm_u8p8[2]	2150
t_StCmpBlend12Trq_HwNm_u8p8[3]	2176
t_StCmpBlend12Trq_HwNm_u8p8[4]	2202
t_StCmpBlend34Trq_HwNm_u8p8[0]	1024
t_StCmpBlend34Trq_HwNm_u8p8[1]	1101
t_StCmpBlend34Trq_HwNm_u8p8[2]	1126
t_StCmpBlend34Trq_HwNm_u8p8[3]	1152
t_StCmpBlend34Trq_HwNm_u8p8[4]	1178
t_StCmpBlendSpdBS_Kph_u9p7[0]	21632
t_StCmpBlendSpdBS_Kph_u9p7[1]	21760
t_StCmpBlendSpdBS_Kph_u9p7[2]	21888
t_StCmpBlendSpdBS_Kph_u9p7[3]	22016
t_StCmpBlendSpdBS_Kph_u9p7[4]	22144
t_StCmpBlendSpdBS_Kph_u9p7[5]	22272
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-4500
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-4500
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-4500
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046



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Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-4500		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.311383316		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.690783262		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	3.21469475		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-3.618236649		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	170.6707347		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.31138337	1.311383316 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-14424.2891	-14424.28966 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	5.63333321	5.633333349 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	27.6057739	27.60577377 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-14026.667	-14026.66763 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	9.10957432	9.10957444 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	58.7045975	58.7045999 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-14435.1904	-14435.19057 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	6.95637989	6.956380153 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	39.4418945	39.44189257 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-14025.9414	-14025.94136 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	9.66510963	9.665110046 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	63.6744843	63.6744811 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-3.61823654	-3.618236649 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.80483246	4.804727508 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00350000011	0.0035 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	170.670731	170.6707347 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

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StabilityComp2\_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.44 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.56490405
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	149.8747
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	99.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-748.9277
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	0
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.223089886
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0047
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDBlendX_Uls_u2p14[0]	23593
t_StCmpADDBlendX_Uls_u2p14[1]	23757
t_StCmpADDBlendY_Uls_u2p14[0]	16384
t_StCmpADDBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2304
t_StCmpBlend12Trq_HwNm_u8p8[1]	2381
t_StCmpBlend12Trq_HwNm_u8p8[2]	2406
t_StCmpBlend12Trq_HwNm_u8p8[3]	2432
t_StCmpBlend12Trq_HwNm_u8p8[4]	2458
t_StCmpBlend34Trq_HwNm_u8p8[0]	1280
t_StCmpBlend34Trq_HwNm_u8p8[1]	1357
t_StCmpBlend34Trq_HwNm_u8p8[2]	1382
t_StCmpBlend34Trq_HwNm_u8p8[3]	1408
t_StCmpBlend34Trq_HwNm_u8p8[4]	1434
t_StCmpBlendSpdBS_Kph_u9p7[0]	20096
t_StCmpBlendSpdBS_Kph_u9p7[1]	20224
t_StCmpBlendSpdBS_Kph_u9p7[2]	20352
t_StCmpBlendSpdBS_Kph_u9p7[3]	20480
t_StCmpBlendSpdBS_Kph_u9p7[4]	20608
t_StCmpBlendSpdBS_Kph_u9p7[5]	20736
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	0
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	0
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	0
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	0
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-2.330312157		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	4.275244737		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	231.3333886		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	68.440979	68.44097997 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-88.4082336	-88.40823301 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	143.746643	143.7466552 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	462.654419	462.6544213 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-104.666115	-104.6661115 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-1.69807243	-1.698071781 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	37.7397194	37.73971749 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-101.366661	-101.3666614 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	27.8191643	27.81916312 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-124.60878	-124.6087903 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-191.195328	-191.195332 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-775.797729	-775.7977718 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	4.27524471	4.275244737 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.19854116	5.198541364 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00469999993	0.0047 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	231.333389	231.3333886 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.45 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.037470858
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	177.67

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	9.5125243
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-3.03
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.589816654
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0059
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	23921
t_StCmpADDFBlendX_Uls_u2p14[1]	24084
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	256
t_StCmpBlend12Trq_HwNm_u8p8[1]	333
t_StCmpBlend12Trq_HwNm_u8p8[2]	358
t_StCmpBlend12Trq_HwNm_u8p8[3]	384
t_StCmpBlend12Trq_HwNm_u8p8[4]	410
t_StCmpBlend34Trq_HwNm_u8p8[0]	1536
t_StCmpBlend34Trq_HwNm_u8p8[1]	1613
t_StCmpBlend34Trq_HwNm_u8p8[2]	1638
t_StCmpBlend34Trq_HwNm_u8p8[3]	1664



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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1690		
t_StCmpBlendSpdBS_Kph_u9p7[0]	20608		
t_StCmpBlendSpdBS_Kph_u9p7[1]	20736		
t_StCmpBlendSpdBS_Kph_u9p7[2]	20864		
t_StCmpBlendSpdBS_Kph_u9p7[3]	20992		
t_StCmpBlendSpdBS_Kph_u9p7[4]	21120		
t_StCmpBlendSpdBS_Kph_u9p7[5]	21248		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[0].B1_Uls_f32	-2300		
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[1].B1_Uls_f32	-2301		
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[2].B1_Uls_f32	-2302		
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-2303		
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.800544724		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0.726917386		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-6.857559502		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-1.269518268		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	284.7731003		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.80054474	1.800544724 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	15800.3486	15800.34752 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-707.936951	-707.9369314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	9.99575615	9.995756895 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	15759.6133	15759.61297 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-714.289001	-714.288973 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-46.8302879	-46.83028888 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	15825.4736	15825.47238 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-708.59845	-708.5984548 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	4.07769775	4.077697495 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	15785.7227	15785.72299 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-713.347168	-713.347144 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-38.4045868	-38.40458588 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-1.26951826	-1.269518268 ± 0.000391	✔



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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.57471371	5.574536736 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00590000022	0.0059 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	284.773102	284.7731003 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	4.51416016	4.514160156 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.46 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	867.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	867.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-1.136864293
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.50077064
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.000125
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	24248
t_StCmpADDFBlendX_Uls_u2p14[1]	24412
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	512
t_StCmpBlend12Trq_HwNm_u8p8[1]	589
t_StCmpBlend12Trq_HwNm_u8p8[2]	614
t_StCmpBlend12Trq_HwNm_u8p8[3]	640
t_StCmpBlend12Trq_HwNm_u8p8[4]	666
t_StCmpBlend34Trq_HwNm_u8p8[0]	1792
t_StCmpBlend34Trq_HwNm_u8p8[1]	1869
t_StCmpBlend34Trq_HwNm_u8p8[2]	1894
t_StCmpBlend34Trq_HwNm_u8p8[3]	1920
t_StCmpBlend34Trq_HwNm_u8p8[4]	1946
t_StCmpBlendSpdBS_Kph_u9p7[0]	21120
t_StCmpBlendSpdBS_Kph_u9p7[1]	21248
t_StCmpBlendSpdBS_Kph_u9p7[2]	21376
t_StCmpBlendSpdBS_Kph_u9p7[3]	21504
t_StCmpBlendSpdBS_Kph_u9p7[4]	21632
t_StCmpBlendSpdBS_Kph_u9p7[5]	21760
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	-4
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523
t_StCmpNFK_Str[1].B2_Uls_f32	-4
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523
t_StCmpNFK_Str[2].B2_Uls_f32	-4
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523
t_StCmpNFK_Str[3].B2_Uls_f32	-4
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709

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Name	Input Value		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.920140338		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	6.09245919		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.899599063		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	159.5181965		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.92014039	1.920140338 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	86.9435806	86.94357836 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-54.9997749	-54.99977583 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	35.1734581	35.1734592 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	195.835785	195.8357743 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-104.387512	-104.387508 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	91.8871689	91.88716596 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	126.061356	126.0613602 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-65.3069687	-65.30696784 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	47.0095749	47.009578 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	211.29187	211.2918728 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-108.715408	-108.7154064 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	96.857048	96.85704716 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.89959908	-4.899599063 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.4999752	7.499975152 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.000125000006	0.000125 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	159.518204	159.5181965 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.47 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-149.8747
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	99.795592
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	100
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	100
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	5.104858136
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.185905731
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.4666
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	24576
t_StCmpADDFBlendX_Uls_u2p14[1]	24740
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	768
t_StCmpBlend12Trq_HwNm_u8p8[1]	845
t_StCmpBlend12Trq_HwNm_u8p8[2]	870
t_StCmpBlend12Trq_HwNm_u8p8[3]	896

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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	922		
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048		
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125		
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150		
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176		
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202		
t_StCmpBlendSpdBS_Kph_u9p7[0]	21632		
t_StCmpBlendSpdBS_Kph_u9p7[1]	21760		
t_StCmpBlendSpdBS_Kph_u9p7[2]	21888		
t_StCmpBlendSpdBS_Kph_u9p7[3]	22016		
t_StCmpBlendSpdBS_Kph_u9p7[4]	22144		
t_StCmpBlendSpdBS_Kph_u9p7[5]	22272		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].B2_Uls_f32	2300		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106		
t_StCmpNFK_Str[1].B2_Uls_f32	2300		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106		
t_StCmpNFK_Str[2].B2_Uls_f32	2300		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	2300		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.656895807		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-2.751178718		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-7.3360147		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	151.6329221		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.65689576	1.656895807 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-0.841569901	-0.841569353 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-6443.48438	-6443.484592 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-157.109497	-157.1095017 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	32.125721	32.12572384 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-6335.14307	-6335.143357 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-10.0859489	-10.08594725 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	31.1929302	31.19293018 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-6310.20703	-6310.207257 ± 0.0625	✓



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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	23.753376	23.75337649 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.1065331	40.10653317 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-6331.48096	-6331.481066 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-5.11606598	-5.116066051 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-7.33601475	-7.3360147 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.61468887	4.614688923 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.466600001	0.4666 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	151.632919	151.6329221 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.48 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1217.034872
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	0.625391233
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	8.8
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.032
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745



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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	24904
t_StCmpADDFBlendX_Uls_u2p14[1]	25068
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458
t_StCmpBlendSpdBS_Kph_u9p7[0]	22144
t_StCmpBlendSpdBS_Kph_u9p7[1]	22272
t_StCmpBlendSpdBS_Kph_u9p7[2]	22400
t_StCmpBlendSpdBS_Kph_u9p7[3]	22528
t_StCmpBlendSpdBS_Kph_u9p7[4]	22656
t_StCmpBlendSpdBS_Kph_u9p7[5]	22784
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	1000
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	1100
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	1200
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483
t_StCmpNFK_Str[3].B2_Uls_f32	1300
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.190112586		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	3.751765192		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	3.334206831		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	192.7936322		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.19011259	1.190112586 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	74.1199646	74.11996886 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	3757.34302	3757.342962 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	49.899334	49.89933159 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	868.974243	868.9743002 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	4183.53076	4183.530612 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	506.250366	506.2503845 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	52.7682037	52.76820223 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	4506.68506	4506.684935 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	40.8542328	40.85423199 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1402.94519	1402.945129 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	4901.99072	4901.99045 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	220.930389	220.9303845 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	3.33420682	3.334206831 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	8.53841305	8.538412519 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0320000015	0.032 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	192.793625	192.7936322 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

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## Test Step 2.49 (Repeat Count = 1)



Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.018763274
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.330346
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	36.4548515
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1308.903444
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-3.628136861
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDBlendX_Uls_u2p14[0]	25231
t_StCmpADDBlendX_Uls_u2p14[1]	25395
t_StCmpADDBlendY_Uls_u2p14[0]	16384

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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	0		
t_StCmpBlend12Trq_HwNm_u8p8[0]	1280		
t_StCmpBlend12Trq_HwNm_u8p8[1]	1357		
t_StCmpBlend12Trq_HwNm_u8p8[2]	1382		
t_StCmpBlend12Trq_HwNm_u8p8[3]	1408		
t_StCmpBlend12Trq_HwNm_u8p8[4]	1434		
t_StCmpBlend34Trq_HwNm_u8p8[0]	256		
t_StCmpBlend34Trq_HwNm_u8p8[1]	333		
t_StCmpBlend34Trq_HwNm_u8p8[2]	358		
t_StCmpBlend34Trq_HwNm_u8p8[3]	384		
t_StCmpBlend34Trq_HwNm_u8p8[4]	410		
t_StCmpBlendSpdBS_Kph_u9p7[0]	22656		
t_StCmpBlendSpdBS_Kph_u9p7[1]	22784		
t_StCmpBlendSpdBS_Kph_u9p7[2]	22912		
t_StCmpBlendSpdBS_Kph_u9p7[3]	23040		
t_StCmpBlendSpdBS_Kph_u9p7[4]	23168		
t_StCmpBlendSpdBS_Kph_u9p7[5]	23296		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	7.489502203		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.492302334		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	431.6523712		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-269.231781	-269.2317813 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	185.255844	185.2558401 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	47.7243652	47.72436604 ± 0.000489	✓

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	164.082428	164.0824417 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	239.861755	239.8617612 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	118.601013	118.6010147 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-279.326782	-279.3267759 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	191.747314	191.747305 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	56.1500702	56.15006904 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1011.22711	1011.227169 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	246.353226	246.3532261 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	127.026718	127.0267177 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-4.49230242	-4.492302334 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.388210654	0.388210644 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107000001	0.107 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	431.652374	431.6523712 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.50 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.584307946
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	42.26399148
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	40.5658218
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	45.3546436
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1400.772017
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-3.52533319
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	10
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602



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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180



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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	25559
t_StCmpADDFBlendX_Uls_u2p14[1]	25723
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1536
t_StCmpBlend12Trq_HwNm_u8p8[1]	1613
t_StCmpBlend12Trq_HwNm_u8p8[2]	1638
t_StCmpBlend12Trq_HwNm_u8p8[3]	1664
t_StCmpBlend12Trq_HwNm_u8p8[4]	1690
t_StCmpBlend34Trq_HwNm_u8p8[0]	512
t_StCmpBlend34Trq_HwNm_u8p8[1]	589
t_StCmpBlend34Trq_HwNm_u8p8[2]	614
t_StCmpBlend34Trq_HwNm_u8p8[3]	640
t_StCmpBlend34Trq_HwNm_u8p8[4]	666
t_StCmpBlendSpdBS_Kph_u9p7[0]	23168
t_StCmpBlendSpdBS_Kph_u9p7[1]	23296
t_StCmpBlendSpdBS_Kph_u9p7[2]	23424
t_StCmpBlendSpdBS_Kph_u9p7[3]	23552
t_StCmpBlendSpdBS_Kph_u9p7[4]	23680
t_StCmpBlendSpdBS_Kph_u9p7[5]	23808
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125

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Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.195094594		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.8		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-0.593378115		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	337.9455675		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.19509459	1.195094594 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	35.0046196	35.00462108 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	46.8616943	46.86169562 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	54.1296234	54.12962467 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	504.266541	504.2665555 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	108.110611	108.1106054 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	137.246948	137.2469387 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	26.8753967	26.87539605 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	53.9194336	53.91943365 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	63.7072678	63.70726827 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1411.8313	1411.831339 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	115.168343	115.1683434 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	146.824585	146.8245823 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-0.593378127	-0.593378115 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	9.22951508	9.22951465 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119000003	0.119 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	337.945557	337.9455675 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.51 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	44.6767921
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	2.696668637
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	8.04
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800

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Name	Input Value
t2_StCmpBlend34TbLY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbLY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbLY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbLY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbLY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbLY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbLY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbLY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbLY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbLY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbLY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbLY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbLY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbLY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbLY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbLY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbLY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbLY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbLY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbLY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	25887
t_StCmpADDFBlendX_Uls_u2p14[1]	26051
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1792
t_StCmpBlend12Trq_HwNm_u8p8[1]	1869
t_StCmpBlend12Trq_HwNm_u8p8[2]	1894
t_StCmpBlend12Trq_HwNm_u8p8[3]	1920
t_StCmpBlend12Trq_HwNm_u8p8[4]	1946
t_StCmpBlend34Trq_HwNm_u8p8[0]	768
t_StCmpBlend34Trq_HwNm_u8p8[1]	845
t_StCmpBlend34Trq_HwNm_u8p8[2]	870
t_StCmpBlend34Trq_HwNm_u8p8[3]	896
t_StCmpBlend34Trq_HwNm_u8p8[4]	922
t_StCmpBlendSpdBS_Kph_u9p7[0]	23680
t_StCmpBlendSpdBS_Kph_u9p7[1]	23808
t_StCmpBlendSpdBS_Kph_u9p7[2]	23936
t_StCmpBlendSpdBS_Kph_u9p7[3]	24064
t_StCmpBlendSpdBS_Kph_u9p7[4]	24192
t_StCmpBlendSpdBS_Kph_u9p7[5]	24320
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[1].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[2].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[3].B2_Uls_f32	0.050288914
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.88425732		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.405236602		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	6.02972976		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	0.351316106		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	208.473795		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.88425732	1.88425732 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	62.2082443	62.20824206 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	5.87030315	5.870303188 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	49.8036499	49.8036475 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	116.71701	116.7170143 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	12.469285	12.46928565 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	108.838852	108.8388533 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	88.2884598	88.28845726 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	7.06966114	7.069661046 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	60.5332336	60.5332317 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	131.142166	131.1421701 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	13.668643	13.66864351 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	119.568436	119.5684375 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	0.351316094	0.351316106 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	7.97000217	7.9700023591447 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131000001	0.0131 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	208.473801	208.473795 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.52 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	24.1219406
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-3.096692801
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595455
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238



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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	16384
t_StCmpADDFBlendX_Uls_u2p14[1]	16548
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1280
t_StCmpBlend12Trq_HwNm_u8p8[1]	1357
t_StCmpBlend12Trq_HwNm_u8p8[2]	1382
t_StCmpBlend12Trq_HwNm_u8p8[3]	1408
t_StCmpBlend12Trq_HwNm_u8p8[4]	1434
t_StCmpBlend34Trq_HwNm_u8p8[0]	256
t_StCmpBlend34Trq_HwNm_u8p8[1]	333
t_StCmpBlend34Trq_HwNm_u8p8[2]	358
t_StCmpBlend34Trq_HwNm_u8p8[3]	384



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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	410		
t_StCmpBlendSpdBS_Kph_u9p7[0]	26240		
t_StCmpBlendSpdBS_Kph_u9p7[1]	26368		
t_StCmpBlendSpdBS_Kph_u9p7[2]	26496		
t_StCmpBlendSpdBS_Kph_u9p7[3]	26624		
t_StCmpBlendSpdBS_Kph_u9p7[4]	26752		
t_StCmpBlendSpdBS_Kph_u9p7[5]	26880		
t_StCmpNFK_Str[0].A1_Uls_f32	-2		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524		
t_StCmpNFK_Str[1].A1_Uls_f32	-2		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483		
t_StCmpNFK_Str[1].B2_Uls_f32	90.70733524		
t_StCmpNFK_Str[2].A1_Uls_f32	-2		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	91.70733524		
t_StCmpNFK_Str[3].A1_Uls_f32	-2		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	92.70733524		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.543761272		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	4.150901175		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	5.512826705		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	487.5196667		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.54376125	1.543761272 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	73.7765121	73.77650704 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	394.523132	394.5231034 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	30.0677261	30.06772501 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	155.28627	155.2862614 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	427.731323	427.7313075 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	69.4996948	69.49969178 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	100.086998	100.0870001 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	406.487213	406.4871969 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	35.0376053	35.03760621 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	170.51767	170.5176653 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	439.695404	439.695401 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	74.469574	74.46957298 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	5.51282692	5.512826705 ± 0.000391	✔

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.243385971	0.243385978 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595452	0.078595455 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	487.519653	487.5196667 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.53 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.080305485
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	28.2329109
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	7.052959383
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0095
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDBlendX_Uls_u2p14[0]	16712
t_StCmpADDBlendX_Uls_u2p14[1]	16876
t_StCmpADDBlendY_Uls_u2p14[0]	16384
t_StCmpADDBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1536
t_StCmpBlend12Trq_HwNm_u8p8[1]	1613
t_StCmpBlend12Trq_HwNm_u8p8[2]	1638
t_StCmpBlend12Trq_HwNm_u8p8[3]	1664
t_StCmpBlend12Trq_HwNm_u8p8[4]	1690
t_StCmpBlend34Trq_HwNm_u8p8[0]	512
t_StCmpBlend34Trq_HwNm_u8p8[1]	589
t_StCmpBlend34Trq_HwNm_u8p8[2]	614
t_StCmpBlend34Trq_HwNm_u8p8[3]	640
t_StCmpBlend34Trq_HwNm_u8p8[4]	666
t_StCmpBlendSpdBS_Kph_u9p7[0]	26752
t_StCmpBlendSpdBS_Kph_u9p7[1]	26880
t_StCmpBlendSpdBS_Kph_u9p7[2]	27008
t_StCmpBlendSpdBS_Kph_u9p7[3]	27136
t_StCmpBlendSpdBS_Kph_u9p7[4]	27264
t_StCmpBlendSpdBS_Kph_u9p7[5]	27392
t_StCmpNFK_Str[0].A1_Uls_f32	0
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	0
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523
t_StCmpNFK_Str[1].B2_Uls_f32	20.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	0
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523
t_StCmpNFK_Str[2].B2_Uls_f32	21.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	0
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523
t_StCmpNFK_Str[3].B2_Uls_f32	22.82605343
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709

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Name	Input Value		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.131702065		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-1.330154574		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	6.729015124		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	331.177889		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	25.2537632	25.25376463 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-12.6557255	-12.65572527 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	18.6131687	18.61316855 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	28.9039478	28.90394737 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-20.4549141	-20.45491433 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	9.83442116	9.834420107 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	21.8605499	21.86055046 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-10.804882	-10.80488153 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	24.7349892	24.73499035 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	36.1657372	36.16573838 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-18.6040707	-18.60407059 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	15.9562416	15.95624191 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	6.72901535	6.729015124 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.03850317	3.038503114 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00949999969	0.0095 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	331.177887	331.177889 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	2.45117188	2.451171875 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.54 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.108870333
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	32.3438812
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1217.034872
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-2.86720078
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.040052211
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	0
t_StCmpADDFBlendX_Uls_u2p14[1]	0
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2304
t_StCmpBlend12Trq_HwNm_u8p8[1]	2381
t_StCmpBlend12Trq_HwNm_u8p8[2]	2406
t_StCmpBlend12Trq_HwNm_u8p8[3]	2432



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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	2458		
t_StCmpBlend34Trq_HwNm_u8p8[0]	1792		
t_StCmpBlend34Trq_HwNm_u8p8[1]	1869		
t_StCmpBlend34Trq_HwNm_u8p8[2]	1894		
t_StCmpBlend34Trq_HwNm_u8p8[3]	1920		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1946		
t_StCmpBlendSpdBS_Kph_u9p7[0]	14464		
t_StCmpBlendSpdBS_Kph_u9p7[1]	14592		
t_StCmpBlendSpdBS_Kph_u9p7[2]	14720		
t_StCmpBlendSpdBS_Kph_u9p7[3]	14848		
t_StCmpBlendSpdBS_Kph_u9p7[4]	14976		
t_StCmpBlendSpdBS_Kph_u9p7[5]	15104		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.1		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.2		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106		
t_StCmpNFK_Str[1].B2_Uls_f32	1.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.3		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106		
t_StCmpNFK_Str[2].B2_Uls_f32	2.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.4		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	3.792469412		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.123044491		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-8.496862817		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-1.684210777		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	264.7532097		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	56.6657829	56.6657821493067 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-6.42880821	-6.428808772 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	2.7258358	2.72583516 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	364.473419	364.473419 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-22.9731255	-22.97312668 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-69.2675552	-69.26756239 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	53.1865883	53.186589899599 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-22.6094704	-22.6094699 ± 0.0625	✔



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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	9.99959755	9.99959756 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1146.1084	1146.10835137031 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-39.1537857	-39.15378781 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-61.9937935	-61.99379999 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-1.68421078	-1.684210777 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.91455722	3.914557108 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107000001	0.107 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	264.753204	264.7532097 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	3.98242188	3.981933594 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.55 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-142.8569
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-748.9277
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	88.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	36.4548515
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1400.772017
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	5.994390547
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.489446306
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277

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StabilityComp2\_Per1

Name	Input Value
t2_StCmpBlend12TbLY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbLY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbLY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbLY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbLY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbLY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbLY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbLY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbLY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbLY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbLY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbLY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbLY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbLY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbLY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbLY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbLY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbLY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbLY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbLY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbLY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbLY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbLY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbLY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbLY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbLY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbLY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbLY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	32768
t_StCmpADDFBlendX_Uls_u2p14[1]	32768
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	256
t_StCmpBlend12Trq_HwNm_u8p8[1]	333
t_StCmpBlend12Trq_HwNm_u8p8[2]	358
t_StCmpBlend12Trq_HwNm_u8p8[3]	384
t_StCmpBlend12Trq_HwNm_u8p8[4]	410
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	14976
t_StCmpBlendSpdBS_Kph_u9p7[1]	15104
t_StCmpBlendSpdBS_Kph_u9p7[2]	15232
t_StCmpBlendSpdBS_Kph_u9p7[3]	15360
t_StCmpBlendSpdBS_Kph_u9p7[4]	15488
t_StCmpBlendSpdBS_Kph_u9p7[5]	15616
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	-1
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	-1
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	1.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[2].A2_Uls_f32	-1
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	1.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[3].A2_Uls_f32	-1
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483
t_StCmpNFK_Str[3].B2_Uls_f32	1.050288914
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.543752366		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-5.679611623		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-5.16305114		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	114.5647843		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.54375231	1.543752366 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-122.609909	-122.6099106 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	12.8077965	12.80779651 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	13.0934181	13.09341801 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-31.2485962	-31.24864266 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-820.382629	-820.382688 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-814.417419	-814.4174549 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	59.196125	59.19612299 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	15.5538902	15.55388788 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	21.5191231	21.51912101 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1397.65259	1397.652567 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-30.7872849	-30.78728502 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-24.822052	-24.8220519 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-5.16305113	-5.16305114 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	4.66853476	4.668534671 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119000003	0.119 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	114.564781	114.5647843 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	0.0786132813	0.078613281 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

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Test Step 2.56 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	26.5125243
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	676
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	45.3546436
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-7.447342753
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.399615061
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	9503
t_StCmpADDFBlendX_Uls_u2p14[1]	9667
t_StCmpADDFBlendY_Uls_u2p14[0]	16384

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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	16384		
t_StCmpBlend12Trq_HwNm_u8p8[0]	512		
t_StCmpBlend12Trq_HwNm_u8p8[1]	589		
t_StCmpBlend12Trq_HwNm_u8p8[2]	614		
t_StCmpBlend12Trq_HwNm_u8p8[3]	640		
t_StCmpBlend12Trq_HwNm_u8p8[4]	666		
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304		
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381		
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406		
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432		
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458		
t_StCmpBlendSpdBS_Kph_u9p7[0]	15488		
t_StCmpBlendSpdBS_Kph_u9p7[1]	15616		
t_StCmpBlendSpdBS_Kph_u9p7[2]	15744		
t_StCmpBlendSpdBS_Kph_u9p7[3]	15872		
t_StCmpBlendSpdBS_Kph_u9p7[4]	16000		
t_StCmpBlendSpdBS_Kph_u9p7[5]	16128		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[0].A2_Uls_f32	1		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[1].A2_Uls_f32	1		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523		
t_StCmpNFK_Str[1].B2_Uls_f32	2.929541087		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	1		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	3.929541087		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	1		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	4.929541087		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.179090803		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.239088058		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-1.247233367		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-5.960211134		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	234.1693		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.17909086	1.179090803 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	42.0238953	42.02389359 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-28.867672	-28.86767102 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	27.7083168	27.70831636 ± 0.000489	✓



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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	434.191284	434.191292 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-25.0493793	-25.04937898 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	21.3955574	21.39555758 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	28.5659161	28.56591572 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-26.6913109	-26.69131172 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	21.7902565	21.79025696 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	435.579681	435.579688 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-37.1214905	-37.12148931 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	30.9732018	30.97320118 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-5.96021128	-5.96021134 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.42644072	5.426440294 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131000001	0.0131 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	234.169296	234.1693 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.57 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	0.376466858
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.399615061
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595455
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	9830
t_StCmpADDFBlendX_Uls_u2p14[1]	9994
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	768
t_StCmpBlend12Trq_HwNm_u8p8[1]	845
t_StCmpBlend12Trq_HwNm_u8p8[2]	870
t_StCmpBlend12Trq_HwNm_u8p8[3]	896
t_StCmpBlend12Trq_HwNm_u8p8[4]	922
t_StCmpBlend34Trq_HwNm_u8p8[0]	256
t_StCmpBlend34Trq_HwNm_u8p8[1]	333
t_StCmpBlend34Trq_HwNm_u8p8[2]	358
t_StCmpBlend34Trq_HwNm_u8p8[3]	384
t_StCmpBlend34Trq_HwNm_u8p8[4]	410
t_StCmpBlendSpdBS_Kph_u9p7[0]	16000
t_StCmpBlendSpdBS_Kph_u9p7[1]	16128
t_StCmpBlendSpdBS_Kph_u9p7[2]	16256
t_StCmpBlendSpdBS_Kph_u9p7[3]	16384
t_StCmpBlendSpdBS_Kph_u9p7[4]	16512
t_StCmpBlendSpdBS_Kph_u9p7[5]	16640
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	0
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	89.70733524
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	0
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	90.70733524
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	0
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	91.70733524
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	0

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Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].B2_Uls_f32	92.70733524		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.06165941		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.230940223		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-6.825912082		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	2.728865945		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	463.9437109		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.06165946	1.06165941 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	55.9395752	55.93957404 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-612.334412	-612.3343835 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	15.9970417	15.99703983 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	468.632019	468.6320508 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-619.160278	-619.1602955 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-39.3953743	-39.39537277 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	46.2961617	46.29615804 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-625.986206	-625.9862076 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.0789814	10.07898043 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	35.7868767	35.78687642 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-632.812134	-632.8121197 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-28.6657906	-28.66578857 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	2.72886586	2.728865945 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.00481844	5.004818445 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595452	0.078595455 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	463.943726	463.9437109 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

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StabilityComp2\_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.58 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.898475326
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-0.377396059
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.995200348
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0095
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	10158
t_StCmpADDFBlendX_Uls_u2p14[1]	10322
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178
t_StCmpBlend34Trq_HwNm_u8p8[0]	512
t_StCmpBlend34Trq_HwNm_u8p8[1]	589
t_StCmpBlend34Trq_HwNm_u8p8[2]	614
t_StCmpBlend34Trq_HwNm_u8p8[3]	640
t_StCmpBlend34Trq_HwNm_u8p8[4]	666
t_StCmpBlendSpdBS_Kph_u9p7[0]	16512
t_StCmpBlendSpdBS_Kph_u9p7[1]	16640
t_StCmpBlendSpdBS_Kph_u9p7[2]	16768
t_StCmpBlendSpdBS_Kph_u9p7[3]	16896
t_StCmpBlendSpdBS_Kph_u9p7[4]	17024
t_StCmpBlendSpdBS_Kph_u9p7[5]	17152
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	0.4
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	0.4
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483
t_StCmpNFK_Str[1].B2_Uls_f32	20.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	0.4
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483
t_StCmpNFK_Str[2].B2_Uls_f32	21.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	0.4
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483
t_StCmpNFK_Str[3].B2_Uls_f32	22.82605343
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047



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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.642914101		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.347546816		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-3.664106894		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-6.950975156		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	145.1865792		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.64291406	1.642914101 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	35.0150566	35.01505636 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-76.4513855	-76.45138765 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	9.51652241	9.516521497 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	11.3155766	11.31557864 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-68.0638351	-68.06383586 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-20.612627	-20.61262521 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	62.8032112	62.80320886 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-88.5140457	-88.51404896 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	21.3526421	21.3526403 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	21.397913	21.39790959 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-77.3800049	-77.38000213 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-15.6427441	-15.64274401 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-6.95097494	-6.950975156 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.97983122	1.979831207 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0094999969	0.0095 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	145.186584	145.1865792 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.59 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.642277279
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	149.8747
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-748.9277
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	2.318994939
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.325082672
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	10486
t_StCmpADDFBlendX_Uls_u2p14[1]	10650
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1280
t_StCmpBlend12Trq_HwNm_u8p8[1]	1357
t_StCmpBlend12Trq_HwNm_u8p8[2]	1382
t_StCmpBlend12Trq_HwNm_u8p8[3]	1408
t_StCmpBlend12Trq_HwNm_u8p8[4]	1434
t_StCmpBlend34Trq_HwNm_u8p8[0]	768
t_StCmpBlend34Trq_HwNm_u8p8[1]	845
t_StCmpBlend34Trq_HwNm_u8p8[2]	870
t_StCmpBlend34Trq_HwNm_u8p8[3]	896

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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	922		
t_StCmpBlendSpdBS_Kph_u9p7[0]	17024		
t_StCmpBlendSpdBS_Kph_u9p7[1]	17152		
t_StCmpBlendSpdBS_Kph_u9p7[2]	17280		
t_StCmpBlendSpdBS_Kph_u9p7[3]	17408		
t_StCmpBlendSpdBS_Kph_u9p7[4]	17536		
t_StCmpBlendSpdBS_Kph_u9p7[5]	17664		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.4		
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.4		
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523		
t_StCmpNFK_Str[1].B2_Uls_f32	1.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.4		
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	2.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.4		
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	3.792469412		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0.096237779		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	3.160670412		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.986122262		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	342.4471709		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	317.030151	317.030154 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	65.779274	65.77927304 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	158.18634	158.186346 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	139.750717	139.7507159 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	30.3120708	30.31207017 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	61.6166649	61.61666283 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	117.683838	117.6838386 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.7296181	25.72961702 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	42.2588539	42.25885394 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1299.44861	-1299.448516 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-273.00647	-273.006469 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-712.483032	-712.4830372 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.98612213	-4.986122262 ± 0.000391	✔

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.21743131	3.217431285 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107000001	0.107 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	342.447174	342.4471709 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.60 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.147636803
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	28.0291485
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	19.795592
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	100
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	40.667703
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	110
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-8.237084794
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.399615061
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	10813
t_StCmpADDFBlendX_Uls_u2p14[1]	10977
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1536
t_StCmpBlend12Trq_HwNm_u8p8[1]	1613
t_StCmpBlend12Trq_HwNm_u8p8[2]	1638
t_StCmpBlend12Trq_HwNm_u8p8[3]	1664
t_StCmpBlend12Trq_HwNm_u8p8[4]	1690
t_StCmpBlend34Trq_HwNm_u8p8[0]	1024
t_StCmpBlend34Trq_HwNm_u8p8[1]	1101
t_StCmpBlend34Trq_HwNm_u8p8[2]	1126
t_StCmpBlend34Trq_HwNm_u8p8[3]	1152
t_StCmpBlend34Trq_HwNm_u8p8[4]	1178
t_StCmpBlendSpdBS_Kph_u9p7[0]	0
t_StCmpBlendSpdBS_Kph_u9p7[1]	0
t_StCmpBlendSpdBS_Kph_u9p7[2]	0
t_StCmpBlendSpdBS_Kph_u9p7[3]	0
t_StCmpBlendSpdBS_Kph_u9p7[4]	0
t_StCmpBlendSpdBS_Kph_u9p7[5]	0
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	0.050288914
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	1.050288914
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	1.050288914
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	1.050288914
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709



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Name	Input Value		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.592193711		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	6.993259883		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	4.080830288		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	456.6353055		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.59219372	1.592193711 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	21.1863079	21.18630881 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	5.5404644	5.540464517 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	46.4193916	46.41939273 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	37.257843	37.25783846 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	19.9626122	19.96261053 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	112.879013	112.8790059 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	17.2829704	17.282969 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	11.8722019	11.872201 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	40.5013351	40.50133333 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	46.8960419	46.89604289 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	20.9044399	20.90443948 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	121.30471	121.3047089 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	4.0808301	4.080830288 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.73727417	5.737273959 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119000003	0.119 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	456.635315	456.6353055 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.61 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	96.5125243
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	76
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1217.034872
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-5.319796002
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.995200348
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	11141
t_StCmpADDFBlendX_Uls_u2p14[1]	11305
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1792
t_StCmpBlend12Trq_HwNm_u8p8[1]	1869
t_StCmpBlend12Trq_HwNm_u8p8[2]	1894
t_StCmpBlend12Trq_HwNm_u8p8[3]	1920

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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	1946		
t_StCmpBlend34Trq_HwNm_u8p8[0]	1280		
t_StCmpBlend34Trq_HwNm_u8p8[1]	1357		
t_StCmpBlend34Trq_HwNm_u8p8[2]	1382		
t_StCmpBlend34Trq_HwNm_u8p8[3]	1408		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1434		
t_StCmpBlendSpdBS_Kph_u9p7[0]	32640		
t_StCmpBlendSpdBS_Kph_u9p7[1]	32640		
t_StCmpBlendSpdBS_Kph_u9p7[2]	32640		
t_StCmpBlendSpdBS_Kph_u9p7[3]	32640		
t_StCmpBlendSpdBS_Kph_u9p7[4]	32640		
t_StCmpBlendSpdBS_Kph_u9p7[5]	32640		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[0].B0_Uls_f32	2300		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	0.929541087		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[1].B0_Uls_f32	2300		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483		
t_StCmpNFK_Str[1].B2_Uls_f32	2.929541087		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[2].B0_Uls_f32	2300		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	3.929541087		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[3].B0_Uls_f32	2300		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	4.929541087		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.000480555		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	1.569518936		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	8.8		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	509.1895632		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.00048053	1.000480555 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	55.1145477	55.11454939 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-18.8133926	-18.81339252 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	23.279438	23.27943877 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	462.535919	462.5358923 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-30.0039368	-30.00393669 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	39.7346115	39.73461252 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	52.6004715	52.60047201 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-24.4120293	-24.41202772 ± 0.0625	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	35.1155586	35.11555757 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1252.75659	1252.756489 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-31.1927986	-31.19279724 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	44.7044945	44.70449372 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	8.80000019	8.8 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.03875256	2.038752551 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131000001	0.0131 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	509.189575	509.1895632 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.62 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.859828312
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-149.8747
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	181
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	87.2521
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1308.903444
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	0.415957952
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.325082672
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595455
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	11469
t_StCmpADDFBlendX_Uls_u2p14[1]	11633
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2048
t_StCmpBlend12Trq_HwNm_u8p8[1]	2125
t_StCmpBlend12Trq_HwNm_u8p8[2]	2150
t_StCmpBlend12Trq_HwNm_u8p8[3]	2176
t_StCmpBlend12Trq_HwNm_u8p8[4]	2202
t_StCmpBlend34Trq_HwNm_u8p8[0]	1024
t_StCmpBlend34Trq_HwNm_u8p8[1]	1101
t_StCmpBlend34Trq_HwNm_u8p8[2]	1126
t_StCmpBlend34Trq_HwNm_u8p8[3]	1152
t_StCmpBlend34Trq_HwNm_u8p8[4]	1178
t_StCmpBlendSpdBS_Kph_u9p7[0]	17024
t_StCmpBlendSpdBS_Kph_u9p7[1]	17152
t_StCmpBlendSpdBS_Kph_u9p7[2]	17280
t_StCmpBlendSpdBS_Kph_u9p7[3]	17408
t_StCmpBlendSpdBS_Kph_u9p7[4]	17536
t_StCmpBlendSpdBS_Kph_u9p7[5]	17664
t_StCmpNFK_Str[0].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	100.1
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	101.2
t_StCmpNFK_Str[1].B1_Uls_f32	-0.263523
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	102.3
t_StCmpNFK_Str[2].B1_Uls_f32	-0.38063523
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.661216198
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	103.4
t_StCmpNFK_Str[3].B1_Uls_f32	-1.138063523
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047



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Name	Input Value		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	8.111196733		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	2.973350191		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	430.7076892		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-46.0823555	-46.08235842 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	66.0888748	66.08886903 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-128.544601	-128.5446063 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	537.001221	537.0011937 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	245.67746	245.6774619 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	115.164574	115.1645722 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	60.2132797	60.21328163 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	199.366196	199.366204 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	52.3182678	52.31827195 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1379.10718	1379.107233 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	249.339752	249.3397531 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	120.134453	120.1344534 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	2.97335029	2.973350191 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.09643888	3.096438692 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595452	0.078595455 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	430.707703	430.7076892 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

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## Test Step 2.63 (Repeat Count = 1)



Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	40.0332778
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	42.26399148
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	462.99
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	99.795592
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1400.772017
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	8.8
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6.22302115
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0095
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	11796
t_StCmpADDFBlendX_Uls_u2p14[1]	11960
t_StCmpADDFBlendY_Uls_u2p14[0]	0

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Name	Input Value		
t_StCmpADDFBlendY_Uls_u2p14[1]	16384		
t_StCmpBlend12Trq_HwNm_u8p8[0]	0		
t_StCmpBlend12Trq_HwNm_u8p8[1]	0		
t_StCmpBlend12Trq_HwNm_u8p8[2]	0		
t_StCmpBlend12Trq_HwNm_u8p8[3]	0		
t_StCmpBlend12Trq_HwNm_u8p8[4]	0		
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048		
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125		
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150		
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176		
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202		
t_StCmpBlendSpdBS_Kph_u9p7[0]	19072		
t_StCmpBlendSpdBS_Kph_u9p7[1]	19200		
t_StCmpBlendSpdBS_Kph_u9p7[2]	19328		
t_StCmpBlendSpdBS_Kph_u9p7[3]	19456		
t_StCmpBlendSpdBS_Kph_u9p7[4]	19584		
t_StCmpBlendSpdBS_Kph_u9p7[5]	19712		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-4500		
t_StCmpNFK_Str[0].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-4500		
t_StCmpNFK_Str[1].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-4500		
t_StCmpNFK_Str[2].B2_Uls_f32	0.792469412		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-4500		
t_StCmpNFK_Str[3].B2_Uls_f32	0.792469412		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.855066657		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	3.424969494		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	2.338728845		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	209.9828401		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✓
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-15362.9326	-15362.93232 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	8.19589233	8.195892148 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	49.0399551	49.03995373 ± 0.000489	✓

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-14843.833	-14843.8329 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	58.8818741	58.88187609 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	502.482208	502.4822096 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-15374.2754	-15374.27611 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	7.18482637	7.184826754 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	39.994854	39.99485413 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-13979.8555	-13979.85589 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.9886742	26.98867525 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	217.162201	217.1622096 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	2.3387289	2.338728845 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	6.24750233	6.247502449 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00949999969	0.0095 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	209.982834	209.9828401 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Step 2.64 (Repeat Count = 1)

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.324611119
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	42.26399148
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	35.777
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	9.5125243
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	26.6068812
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	177.67
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	5.510279036
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.995200348
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	12124
t_StCmpADDFBlendX_Uls_u2p14[1]	12288
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	2560
t_StCmpBlend12Trq_HwNm_u8p8[1]	2560
t_StCmpBlend12Trq_HwNm_u8p8[2]	2560
t_StCmpBlend12Trq_HwNm_u8p8[3]	2560
t_StCmpBlend12Trq_HwNm_u8p8[4]	2560
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	19584
t_StCmpBlendSpdBS_Kph_u9p7[1]	19712
t_StCmpBlendSpdBS_Kph_u9p7[2]	19840
t_StCmpBlendSpdBS_Kph_u9p7[3]	19968
t_StCmpBlendSpdBS_Kph_u9p7[4]	20096
t_StCmpBlendSpdBS_Kph_u9p7[5]	20224
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[0].B1_Uls_f32	0
t_StCmpNFK_Str[0].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[1].B1_Uls_f32	0
t_StCmpNFK_Str[1].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132
t_StCmpNFK_Str[2].B1_Uls_f32	0
t_StCmpNFK_Str[2].B2_Uls_f32	44.83360735
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046



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Name	Input Value		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	0		
t_StCmpNFK_Str[3].B2_Uls_f32	44.83360735		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-0.595267248		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	7.98779552		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	413.1063008		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	27.1950569	27.19505719 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-22.1386681	-22.13866857 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	40.6986122	40.69861135 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	118.123894	118.1238894 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	27.8872871	27.88728526 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	488.236176	488.2361669 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	42.9383163	42.93831536 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-22.6373672	-22.63736897 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	36.2371826	36.23718399 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	60.362175	60.36217596 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-7.59518623	-7.595186157 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	170.806168	170.8061669 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	7.98779535	7.98779552 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	2.37131381	2.371313768 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.107000001	0.107 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	413.106293	413.1063008 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.65 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.118767971
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	22.1110891
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	867.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	19.1520594
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	10.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	31.2938218
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	867.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-1.720983469
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.325082672
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	12452
t_StCmpADDFBlendX_Uls_u2p14[1]	12616
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	512
t_StCmpBlend12Trq_HwNm_u8p8[1]	589
t_StCmpBlend12Trq_HwNm_u8p8[2]	614
t_StCmpBlend12Trq_HwNm_u8p8[3]	640
t_StCmpBlend12Trq_HwNm_u8p8[4]	666
t_StCmpBlend34Trq_HwNm_u8p8[0]	2304
t_StCmpBlend34Trq_HwNm_u8p8[1]	2381
t_StCmpBlend34Trq_HwNm_u8p8[2]	2406
t_StCmpBlend34Trq_HwNm_u8p8[3]	2432
t_StCmpBlend34Trq_HwNm_u8p8[4]	2458
t_StCmpBlendSpdBS_Kph_u9p7[0]	20096
t_StCmpBlendSpdBS_Kph_u9p7[1]	20224
t_StCmpBlendSpdBS_Kph_u9p7[2]	20352
t_StCmpBlendSpdBS_Kph_u9p7[3]	20480
t_StCmpBlendSpdBS_Kph_u9p7[4]	20608
t_StCmpBlendSpdBS_Kph_u9p7[5]	20736
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[0].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[0].B1_Uls_f32	-2300.1
t_StCmpNFK_Str[0].B2_Uls_f32	103.397464
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[1].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[1].B1_Uls_f32	-2301.2
t_StCmpNFK_Str[1].B2_Uls_f32	103.397464
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[2].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[2].B1_Uls_f32	-2302.3
t_StCmpNFK_Str[2].B2_Uls_f32	103.397464
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046
t_StCmpNFK_Str[3].B0_Uls_f32	0.941284239
t_StCmpNFK_Str[3].B1_Uls_f32	-2303.4
t_StCmpNFK_Str[3].B2_Uls_f32	103.397464
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.455563188		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	4.08969835		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	1.892487586		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	507.576678		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-9353.03418	-9353.03401915115 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	426.868927	426.8689549 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	35.8248367	35.82483874 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-9319.18848	-9319.18792142135 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	431.344513	431.3445487 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	75.8639832	75.86398191 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-9350.04688	-9350.04563988497 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	426.207397	426.2074315 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	29.9067802	29.90677934 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-9262.76172	-9262.76255521808 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	438.866577	438.8665912 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	143.156982	143.1569819 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	1.89248765	1.892487586 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.13419485	3.134194867 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.119000003	0.119 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	507.576691	507.576678 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.66 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.484720515
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	99.795592
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	100

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	76.17866968
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	67.24
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	100
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	0.213709807
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.399615061
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	12780
t_StCmpADDFBlendX_Uls_u2p14[1]	12943
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	768
t_StCmpBlend12Trq_HwNm_u8p8[1]	845
t_StCmpBlend12Trq_HwNm_u8p8[2]	870
t_StCmpBlend12Trq_HwNm_u8p8[3]	896
t_StCmpBlend12Trq_HwNm_u8p8[4]	922
t_StCmpBlend34Trq_HwNm_u8p8[0]	0
t_StCmpBlend34Trq_HwNm_u8p8[1]	0
t_StCmpBlend34Trq_HwNm_u8p8[2]	0
t_StCmpBlend34Trq_HwNm_u8p8[3]	0



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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	0		
t_StCmpBlendSpdBS_Kph_u9p7[0]	20608		
t_StCmpBlendSpdBS_Kph_u9p7[1]	20736		
t_StCmpBlendSpdBS_Kph_u9p7[2]	20864		
t_StCmpBlendSpdBS_Kph_u9p7[3]	20992		
t_StCmpBlendSpdBS_Kph_u9p7[4]	21120		
t_StCmpBlendSpdBS_Kph_u9p7[5]	21248		
t_StCmpNFK_Str[0].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[0].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[0].B1_Uls_f32	-0.138063523		
t_StCmpNFK_Str[0].B2_Uls_f32	-4		
t_StCmpNFK_Str[1].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[1].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[1].B1_Uls_f32	-0.148063523		
t_StCmpNFK_Str[1].B2_Uls_f32	-4		
t_StCmpNFK_Str[2].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[2].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[2].B1_Uls_f32	-0.168063523		
t_StCmpNFK_Str[2].B2_Uls_f32	-4		
t_StCmpNFK_Str[3].A1_Uls_f32	-1.867136106		
t_StCmpNFK_Str[3].A2_Uls_f32	0.870825326		
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855		
t_StCmpNFK_Str[3].B1_Uls_f32	-0.139063523		
t_StCmpNFK_Str[3].B2_Uls_f32	-4		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.163147333		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-7.600464559		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.239764881		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	132.5807687		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.16314733	1.163147333 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	35.6103096	35.61030986 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.9753628	25.97536387 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	5.08310318	5.083102467 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-80.2007446	-80.20075287 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	81.7208862	81.72088834 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-58.9314804	-58.93148554 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	48.9799423	48.97993857 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	18.2449665	18.24496986 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	13.9601936	13.96019157 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1.04903793	-1.049039079 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	48.1653748	48.16537606 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-20.3984833	-20.39848554 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-4.23976469	-4.239764881 ± 0.000391	✔

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	5.33167982	5.331679702 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131000001	0.0131 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	132.580765	132.5807687 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.67 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	92.34203664
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	37.80256412
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-176.86
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	92.34203664
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	430.88
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	25.172
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1217.034872
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	96
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-2.139261341
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.995200348
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328

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Name	Input Value
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	13107
t_StCmpADDFBlendX_Uls_u2p14[1]	13271
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	1024
t_StCmpBlend12Trq_HwNm_u8p8[1]	1101
t_StCmpBlend12Trq_HwNm_u8p8[2]	1126
t_StCmpBlend12Trq_HwNm_u8p8[3]	1152
t_StCmpBlend12Trq_HwNm_u8p8[4]	1178
t_StCmpBlend34Trq_HwNm_u8p8[0]	2560
t_StCmpBlend34Trq_HwNm_u8p8[1]	2560
t_StCmpBlend34Trq_HwNm_u8p8[2]	2560
t_StCmpBlend34Trq_HwNm_u8p8[3]	2560
t_StCmpBlend34Trq_HwNm_u8p8[4]	2560
t_StCmpBlendSpdBS_Kph_u9p7[0]	21120
t_StCmpBlendSpdBS_Kph_u9p7[1]	21248
t_StCmpBlendSpdBS_Kph_u9p7[2]	21376
t_StCmpBlendSpdBS_Kph_u9p7[3]	21504
t_StCmpBlendSpdBS_Kph_u9p7[4]	21632
t_StCmpBlendSpdBS_Kph_u9p7[5]	21760
t_StCmpNFK_Str[0].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[0].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[0].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[0].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[0].B2_Uls_f32	2300
t_StCmpNFK_Str[1].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[1].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[1].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[1].B1_Uls_f32	-1.967136106
t_StCmpNFK_Str[1].B2_Uls_f32	2300
t_StCmpNFK_Str[2].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[2].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[2].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[2].B1_Uls_f32	-1.767136106
t_StCmpNFK_Str[2].B2_Uls_f32	2300
t_StCmpNFK_Str[3].A1_Uls_f32	-0.197623396
t_StCmpNFK_Str[3].A2_Uls_f32	-0.736897125
t_StCmpNFK_Str[3].B0_Uls_f32	1.31353855
t_StCmpNFK_Str[3].B1_Uls_f32	-1.867136106
t_StCmpNFK_Str[3].B2_Uls_f32	2300
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709

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Name	Input Value		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.9921875		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	1		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	5.280749321		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	2.009881043		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	286.7783825		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.9921875	1.9921875 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	48.935997	48.93599743 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	12224.0029	12224.00319 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	106.228874	106.228875 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	450.774414	450.774402 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	12258.6406	12258.64013 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	153.232635	153.23264 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	23.5390053	23.53900338 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	12174.4307	12174.43071 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	38.956955	38.95695718 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1256.95447	1256.954471 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	12331.3408	12331.3409 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	251.890594	251.8906034 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	2.00988102	2.009881043 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	1.9970876	1.997087547 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0131000001	0.0131 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	286.778381	286.7783825 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 2.68 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-142.8569

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	10.330346
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	10.2749703
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	495.1
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	30.9881782
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	32.242
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	176.86
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	97.43
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	1400.772017
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	191
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-1.136864293
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.325082672
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595455
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484

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Name	Input Value
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	13435
t_StCmpADDFBlendX_Uls_u2p14[1]	13599
t_StCmpADDFBlendY_Uls_u2p14[0]	16384
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	1280
t_StCmpBlend12Trq_HwNm_u8p8[1]	1357
t_StCmpBlend12Trq_HwNm_u8p8[2]	1382
t_StCmpBlend12Trq_HwNm_u8p8[3]	1408



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Name	Input Value		
t_StCmpBlend12Trq_HwNm_u8p8[4]	1434		
t_StCmpBlend34Trq_HwNm_u8p8[0]	1024		
t_StCmpBlend34Trq_HwNm_u8p8[1]	1101		
t_StCmpBlend34Trq_HwNm_u8p8[2]	1126		
t_StCmpBlend34Trq_HwNm_u8p8[3]	1152		
t_StCmpBlend34Trq_HwNm_u8p8[4]	1178		
t_StCmpBlendSpdBS_Kph_u9p7[0]	21632		
t_StCmpBlendSpdBS_Kph_u9p7[1]	21760		
t_StCmpBlendSpdBS_Kph_u9p7[2]	21888		
t_StCmpBlendSpdBS_Kph_u9p7[3]	22016		
t_StCmpBlendSpdBS_Kph_u9p7[4]	22144		
t_StCmpBlendSpdBS_Kph_u9p7[5]	22272		
t_StCmpNFK_Str[0].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[0].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[0].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[0].B1_Uls_f32	-2.040528483		
t_StCmpNFK_Str[0].B2_Uls_f32	1000.1		
t_StCmpNFK_Str[1].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[1].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[1].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[1].B1_Uls_f32	-2.140528483		
t_StCmpNFK_Str[1].B2_Uls_f32	1100.2		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[2].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[2].B1_Uls_f32	-2.240528483		
t_StCmpNFK_Str[2].B2_Uls_f32	1200.3		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.881234018		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.11178046		
t_StCmpNFK_Str[3].B0_Uls_f32	0.094760132		
t_StCmpNFK_Str[3].B1_Uls_f32	-2.340528483		
t_StCmpNFK_Str[3].B2_Uls_f32	1300.4		
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPtr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPtr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPtr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPtr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPtr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPtr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPtr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPtr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.800544724		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.726917386		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	6.09245919		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-4.899599063		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	159.5181965		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.80054474	1.800544724 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-132.115494	-132.1154806 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	6096.00781	6096.00785626471 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	26.2963676	26.2963701 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	553.020386	553.0204069 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	6711.92432	6711.92473761035 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	80.5251389	80.52513626 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	60.0181046	60.01811096 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	7318.03418	7318.03351800985 ± 0.0625	✔

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Name	Actual Value	Expected Value	Result
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	47.0095749	47.009578 ± 0.000489	✓
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	1534.27795	1534.277909 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	7941.37744	7941.37729675953 ± 0.0625	✓
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	167.680176	167.680166 ± 0.000489	✓
HwTorqueSV_HwNm_M_f32	-4.89959908	-4.899599063 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	3.15309882	3.153098654 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.078595452	0.078595455 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	159.518204	159.5181965 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✓

## Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

## Test Case 3: Path Test ✓

<b>Specification</b>	Performance Metrics (With "None" Instrumentation and WithPS Environment)
	CPU Cycles:
	TS3.1 13993 Cycles
	TS3.2 13564 Cycles
	TS3.3 13573 Cycles
<b>Description</b>	Vector Description:
	TS3.1 if(CombinedAssist_MtrNm_T_f32 <= - D_COMPINLIMIT_MTRNM_F32)=>True
	TS3.2 if(CombinedAssist_MtrNm_T_f32 >= D_COMPINLIMIT_MTRNM_F32)=>True
	TS3.3 CombinedAssist_MtrNm_T_f32 = Limit_m(CombinedAssist_MtrNm_T_f32, -D_COMPINLIMIT_MTRNM_F32, D_COMPINLIMIT_MTRNM_F32)=>False

## Test Step 3.1 (Repeat Count = 1) ✓

Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	44.6767921
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	3.325082672
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.625391233
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TblY_Uls_u2p14[0][0]	16

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	6226
t_StCmpADDFBlendX_Uls_u2p14[1]	6390
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	256
t_StCmpBlend12Trq_HwNm_u8p8[1]	333
t_StCmpBlend12Trq_HwNm_u8p8[2]	358
t_StCmpBlend12Trq_HwNm_u8p8[3]	384
t_StCmpBlend12Trq_HwNm_u8p8[4]	410
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	10368
t_StCmpBlendSpdBS_Kph_u9p7[1]	10496
t_StCmpBlendSpdBS_Kph_u9p7[2]	10624
t_StCmpBlendSpdBS_Kph_u9p7[3]	10752
t_StCmpBlendSpdBS_Kph_u9p7[4]	10880
t_StCmpBlendSpdBS_Kph_u9p7[5]	11008
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992

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Name	Input Value		
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343		
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178		
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992		
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741		
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101		
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	8.95890172		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.236265753		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	-0.679628047		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.5		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.089867711		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	11		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-2.781880462		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	2.062094629		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.5	1.5 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-399.875427	-399.8754503 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	250.684235	250.6842371 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	60.9674759	60.96747636 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-382.089508	-382.0895353 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	325.279541	325.2795508 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	157.789688	157.7896841 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-401.740601	-401.7406101 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	258.950684	258.950695 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	71.6970596	71.69706056 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-373.247772	-373.247814 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	333.546021	333.5460086 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	168.519272	168.5192683 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-2.78188038	-2.781880462 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.644559026	0.644559042 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	2.06209469	2.062094629 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	8.79980469	8.799804688 ± 0.000489	✔

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1.9921875
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33.9472079
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	25.0701188
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	67.2521
CombAstNFSV2_Cnt_M_Str[0].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	39.312
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	28.707
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	44.6767921
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	21.637
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	80.330346
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	50.0415842
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	35.9807624
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	86.24
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	3.325082672
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.625391233
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.0071
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	16
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	49
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	82
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	115
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	147
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	492
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	819
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	1147
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	1475
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	8190
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	1602
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	9830
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	13435
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	16176
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	14745
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	8192
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	4915
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	1638
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	1474
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	1408
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	1966
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	4931
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	6554
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	8176
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	9830

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Name	Input Value
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	11469
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	13107
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	14746
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	15565
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	9856
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	11494
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	13133
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	14771
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	16368
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	1966
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	3840
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	5888
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	7974
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	14384
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	11432
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	13599
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	15172
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	15763
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	16384
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	182
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	546
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	728
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	1275
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	1637
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	973
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	1465
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	1638
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	4880
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	7484
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	12768
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	9826
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	1238
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	1638
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	4915
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	8192
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	12384
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	14745
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	14748
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	16382
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	164
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	492
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	819
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	1147
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	1475
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	328
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	655
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	983
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	1311
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	819
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	442
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	1360
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	426
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	1180
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	606
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	13107
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	9830
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	6554
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	3277
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	1638
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	3276
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	655
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	328
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	164
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	163
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	328
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	4915
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	5800



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Name	Input Value
t2_StCmpBlend34TbLY_Uls_u2p14[1][4]	7291
t2_StCmpBlend34TbLY_Uls_u2p14[2][0]	12288
t2_StCmpBlend34TbLY_Uls_u2p14[2][1]	13920
t2_StCmpBlend34TbLY_Uls_u2p14[2][2]	14744
t2_StCmpBlend34TbLY_Uls_u2p14[2][3]	15129
t2_StCmpBlend34TbLY_Uls_u2p14[2][4]	16178
t2_StCmpBlend34TbLY_Uls_u2p14[3][0]	655
t2_StCmpBlend34TbLY_Uls_u2p14[3][1]	983
t2_StCmpBlend34TbLY_Uls_u2p14[3][2]	1311
t2_StCmpBlend34TbLY_Uls_u2p14[3][3]	2022
t2_StCmpBlend34TbLY_Uls_u2p14[3][4]	16269
t2_StCmpBlend34TbLY_Uls_u2p14[4][0]	369
t2_StCmpBlend34TbLY_Uls_u2p14[4][1]	1360
t2_StCmpBlend34TbLY_Uls_u2p14[4][2]	590
t2_StCmpBlend34TbLY_Uls_u2p14[4][3]	1180
t2_StCmpBlend34TbLY_Uls_u2p14[4][4]	604
t2_StCmpBlend34TbLY_Uls_u2p14[5][0]	1638
t2_StCmpBlend34TbLY_Uls_u2p14[5][1]	4915
t2_StCmpBlend34TbLY_Uls_u2p14[5][2]	8190
t2_StCmpBlend34TbLY_Uls_u2p14[5][3]	11469
t2_StCmpBlend34TbLY_Uls_u2p14[5][4]	14746
t_StCmpADDFBlendX_Uls_u2p14[0]	6226
t_StCmpADDFBlendX_Uls_u2p14[1]	6390
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	16384
t_StCmpBlend12Trq_HwNm_u8p8[0]	256
t_StCmpBlend12Trq_HwNm_u8p8[1]	333
t_StCmpBlend12Trq_HwNm_u8p8[2]	358
t_StCmpBlend12Trq_HwNm_u8p8[3]	384
t_StCmpBlend12Trq_HwNm_u8p8[4]	410
t_StCmpBlend34Trq_HwNm_u8p8[0]	2048
t_StCmpBlend34Trq_HwNm_u8p8[1]	2125
t_StCmpBlend34Trq_HwNm_u8p8[2]	2150
t_StCmpBlend34Trq_HwNm_u8p8[3]	2176
t_StCmpBlend34Trq_HwNm_u8p8[4]	2202
t_StCmpBlendSpdBS_Kph_u9p7[0]	10368
t_StCmpBlendSpdBS_Kph_u9p7[1]	10496
t_StCmpBlendSpdBS_Kph_u9p7[2]	10624
t_StCmpBlendSpdBS_Kph_u9p7[3]	10752
t_StCmpBlendSpdBS_Kph_u9p7[4]	10880
t_StCmpBlendSpdBS_Kph_u9p7[5]	11008
t_StCmpNFK_Str[0].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[0].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[0].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[0].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[0].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[1].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[1].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[1].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[1].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[1].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[2].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[2].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[2].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[2].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[2].B2_Uls_f32	19.82605343
t_StCmpNFK_Str[3].A1_Uls_f32	-0.146134178
t_StCmpNFK_Str[3].A2_Uls_f32	-0.770435992
t_StCmpNFK_Str[3].B0_Uls_f32	22.48170741
t_StCmpNFK_Str[3].B1_Uls_f32	-42.22433101
t_StCmpNFK_Str[3].B2_Uls_f32	19.82605343
tgt_KPtr_Cnt_Str.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str.A2_Uls_f32	-0.679628047
tgt_KPtr_Cnt_Str.B0_Uls_f32	2.629709826
tgt_KPtr_Cnt_Str.B1_Uls_f32	-4.753110777
tgt_KPtr_Cnt_Str.B2_Uls_f32	2.207507151
tgt_KPtr_Cnt_Str1.A1_Uls_f32	-0.172240343
tgt_KPtr_Cnt_Str1.A2_Uls_f32	-0.688966709
tgt_KPtr_Cnt_Str1.B0_Uls_f32	11.53067485
tgt_KPtr_Cnt_Str1.B1_Uls_f32	-20.35078362
tgt_KPtr_Cnt_Str1.B2_Uls_f32	8.95890172
tgt_KPtr_Cnt_Str2.A1_Uls_f32	-0.236265753
tgt_KPtr_Cnt_Str2.A2_Uls_f32	-0.679628047

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Name	Input Value		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	2.629709826		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-4.753110777		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	2.207507151		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.172240343		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	-0.688966709		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	11.53067485		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-20.35078362		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	8.95890172		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AsstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1.5		
tgt_StabilityComp2_Per1_AsstFWActive_Uls_f32.value	0.089867711		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-11		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-2.781880462		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	2.062094629		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1.5	1.5 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	459.937378	459.9373825 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-198.375931	-198.3759355 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	6.92694283	6.926939438 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	450.993164	450.993189 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-264.704773	-264.7047913 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-79.1656799	-79.16568408 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	458.072205	458.0722227 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-190.109467	-190.1094777 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	17.6565266	17.65652364 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	459.8349	459.834910325712 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-256.438324	-256.4383335 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-68.4360962	-68.43609988 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-2.78188038	-2.781880462 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.644559026	0.644559042 ± 0.000391	✔
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.00710000005	0.0071 ± 0.00005	✔
VehicleSpeedSV_Kph_M_f32	2.06209469	2.062094629 ± 0.000782	✔
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-1.21826172	-1.218261719 ± 0.000489	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓
Blend_f32	5	Blend_f32	5	✓
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
AssistDDFactorSV_Uls_M_f32	1
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[0].KPTr_Cnt_Str	tgt_KPTr_Cnt_Str
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2311.793127

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Name	Input Value
CombAstNFSV2_Cnt_M_Str[1].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str1
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[2].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str2
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	-1567.617957
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-1099.795592
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2311.793127
CombAstNFSV2_Cnt_M_Str[3].KPtr_Cnt_Str	tgt_KPtr_Cnt_Str3
HwTorqueSV_HwNm_M_f32	-10
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.000125
Rte_Inst_Ap_StabilityComp2	tgt_Rte_Inst_Ap_StabilityComp2
t2_StCmpBlend02TbIY_Uls_u2p14[0][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[0][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[0][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[0][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[0][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[1][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[2][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[3][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[4][4]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][0]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][1]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][2]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][3]	0
t2_StCmpBlend02TbIY_Uls_u2p14[5][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[0][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[1][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[2][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[3][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[4][4]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][0]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][1]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][2]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][3]	0
t2_StCmpBlend04TbIY_Uls_u2p14[5][4]	0

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Name	Input Value
t2_StCmpBlend12TbIY_Uls_u2p14[0][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[0][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[1][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[2][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[3][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[4][4]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][0]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][1]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][2]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][3]	0
t2_StCmpBlend12TbIY_Uls_u2p14[5][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[0][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[1][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[2][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[3][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[4][4]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][0]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][1]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][2]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][3]	0
t2_StCmpBlend34TbIY_Uls_u2p14[5][4]	0
t_StCmpADDFBlendX_Uls_u2p14[0]	0
t_StCmpADDFBlendX_Uls_u2p14[1]	0
t_StCmpADDFBlendY_Uls_u2p14[0]	0
t_StCmpADDFBlendY_Uls_u2p14[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[0]	0
t_StCmpBlend12Trq_HwNm_u8p8[1]	0
t_StCmpBlend12Trq_HwNm_u8p8[2]	0
t_StCmpBlend12Trq_HwNm_u8p8[3]	0
t_StCmpBlend12Trq_HwNm_u8p8[4]	0
t_StCmpBlend34Trq_HwNm_u8p8[0]	0
t_StCmpBlend34Trq_HwNm_u8p8[1]	0
t_StCmpBlend34Trq_HwNm_u8p8[2]	0
t_StCmpBlend34Trq_HwNm_u8p8[3]	0

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Name	Input Value		
t_StCmpBlend34Trq_HwNm_u8p8[4]	0		
t_StCmpBlendSpdBS_Kph_u9p7[0]	0		
t_StCmpBlendSpdBS_Kph_u9p7[1]	0		
t_StCmpBlendSpdBS_Kph_u9p7[2]	0		
t_StCmpBlendSpdBS_Kph_u9p7[3]	0		
t_StCmpBlendSpdBS_Kph_u9p7[4]	0		
t_StCmpBlendSpdBS_Kph_u9p7[5]	0		
t_StCmpNFK_Str[0].A1_Uls_f32	-2		
t_StCmpNFK_Str[0].A2_Uls_f32	-1		
t_StCmpNFK_Str[0].B0_Uls_f32	0		
t_StCmpNFK_Str[0].B1_Uls_f32	-4500		
t_StCmpNFK_Str[0].B2_Uls_f32	-4		
t_StCmpNFK_Str[1].A1_Uls_f32	-2		
t_StCmpNFK_Str[1].A2_Uls_f32	-1		
t_StCmpNFK_Str[1].B0_Uls_f32	0		
t_StCmpNFK_Str[1].B1_Uls_f32	-4500		
t_StCmpNFK_Str[1].B2_Uls_f32	-4		
t_StCmpNFK_Str[2].A1_Uls_f32	-2		
t_StCmpNFK_Str[2].A2_Uls_f32	-1		
t_StCmpNFK_Str[2].B0_Uls_f32	0		
t_StCmpNFK_Str[2].B1_Uls_f32	-4500		
t_StCmpNFK_Str[2].B2_Uls_f32	-4		
t_StCmpNFK_Str[3].A1_Uls_f32	-2		
t_StCmpNFK_Str[3].A2_Uls_f32	-1		
t_StCmpNFK_Str[3].B0_Uls_f32	0		
t_StCmpNFK_Str[3].B1_Uls_f32	-4500		
t_StCmpNFK_Str[3].B2_Uls_f32	-4		
tgt_KPTr_Cnt_Str.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str.B2_Uls_f32	103.397464		
tgt_KPTr_Cnt_Str1.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str1.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str1.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str1.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str1.B2_Uls_f32	103.397464		
tgt_KPTr_Cnt_Str2.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str2.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str2.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str2.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str2.B2_Uls_f32	103.397464		
tgt_KPTr_Cnt_Str3.A1_Uls_f32	-0.718797066		
tgt_KPTr_Cnt_Str3.A2_Uls_f32	0.129167306		
tgt_KPTr_Cnt_Str3.B0_Uls_f32	104.703713		
tgt_KPTr_Cnt_Str3.B1_Uls_f32	-207.6908067		
tgt_KPTr_Cnt_Str3.B2_Uls_f32	103.397464		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssistDDFactor_Uls_f32	tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_AssstFWActive_Uls_f32	tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_CombinedAssist_MtrNm_f32	tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_HwTorque_HwNm_f32	tgt_StabilityComp2_Per1_HwTorque_HwNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_SysAssistCmd_MtrNm_f32	tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_StabilityComp2.StabilityComp2_Per1_VehicleSpeed_Kph_f32	tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32		
tgt_StabilityComp2_Per1_AssistDDFactor_Uls_f32.value	1		
tgt_StabilityComp2_Per1_AssstFWActive_Uls_f32.value	0		
tgt_StabilityComp2_Per1_CombinedAssist_MtrNm_f32.value	-8.8		
tgt_StabilityComp2_Per1_HwTorque_HwNm_f32.value	-10		
tgt_StabilityComp2_Per1_VehicleSpeed_Kph_f32.value	0		
Name	Actual Value	Expected Value	Result
AssistDDFactorSV_Uls_M_f32	1	1 ± 0.000391	✔
CombAstNFSV2_Cnt_M_Str[0].SV1_Uls_f32	33522.1797	33522.18314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].SV2_Uls_f32	-2453.81079	-2453.810632 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[0].Out_Uls_f32	-2489.01074	-2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[1].SV1_Uls_f32	33522.1797	33522.18314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].SV2_Uls_f32	-2453.81079	-2453.810632 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[1].Out_Uls_f32	-2489.01074	-2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[2].SV1_Uls_f32	33522.1797	33522.18314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].SV2_Uls_f32	-2453.81079	-2453.810632 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[2].Out_Uls_f32	-2489.01074	-2489.010632 ± 0.000489	✔
CombAstNFSV2_Cnt_M_Str[3].SV1_Uls_f32	33522.1797	33522.18314 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].SV2_Uls_f32	-2453.81079	-2453.810632 ± 0.0625	✔
CombAstNFSV2_Cnt_M_Str[3].Out_Uls_f32	-2489.01074	-2489.010632 ± 0.000489	✔
HwTorqueSV_HwNm_M_f32	-10	-10 ± 0.000391	✔

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Name	Actual Value	Expected Value	Result
HwTrqSV2_HwNm_M_Str.SV_Uls_f32	0.00125000009	0.00125 ± 0.000391	✓
HwTrqSV2_HwNm_M_Str.K_Uls_f32	0.000125000006	0.000125 ± 0.00005	✓
VehicleSpeedSV_Kph_M_f32	0	0 ± 0.000782	✓
tgt_StabilityComp2_Per1_SysAssistCmd_MtrNm_f32.value	-8.79980469	-8.799804688 ± 0.000489	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓	
BilinearXYM_u16_u16Xu16YM_Cnt	4	BilinearXYM_u16_u16Xu16YM_Cnt	4	✓	
ApplyStabilityComp2	1	ApplyStabilityComp2	1	✓	
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓	
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓	
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓	
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓	
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓	
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓	
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓	
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓	
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓	
NF_FullUpdate_f32	1	NF_FullUpdate_f32	1	✓	
NF_OpUpdate_f32	1	NF_OpUpdate_f32	1	✓	
NF_SvUpdate_f32	1	NF_SvUpdate_f32	1	✓	
Blend_f32	5	Blend_f32	5	✓	
Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	Rte_Call_StabilityComp2_Per1_CP1_CheckpointReached	1	✓	