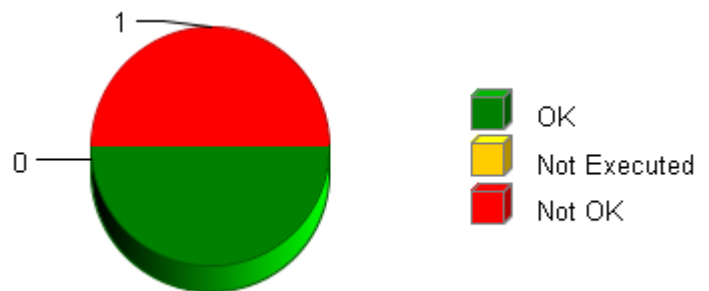


Summary

Total Test Objects: 2
Successful: 1
Failed: 1
Not Executed: 0
Date: 2015-02-04
Time: 12:38:14+0530

Overall Test Object Results (including Coverage)



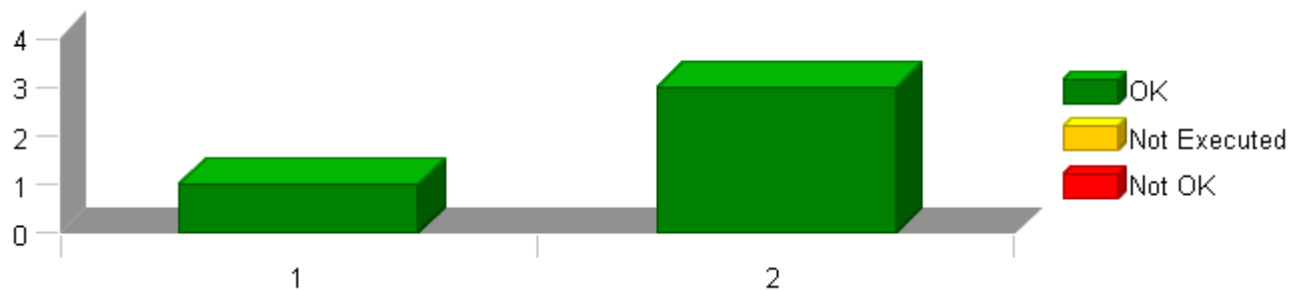
Selected Project Items

Test Object "CBD_UnitTest/TrqOvlSta/TrqOvlSta_Init"
 Test Object "CBD_UnitTest/TrqOvlSta/TrqOvlSta_Per1"

Used Test Environments

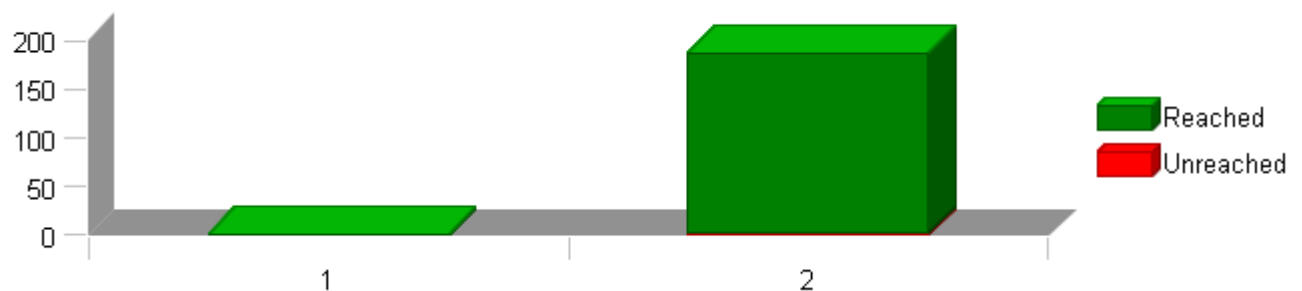
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



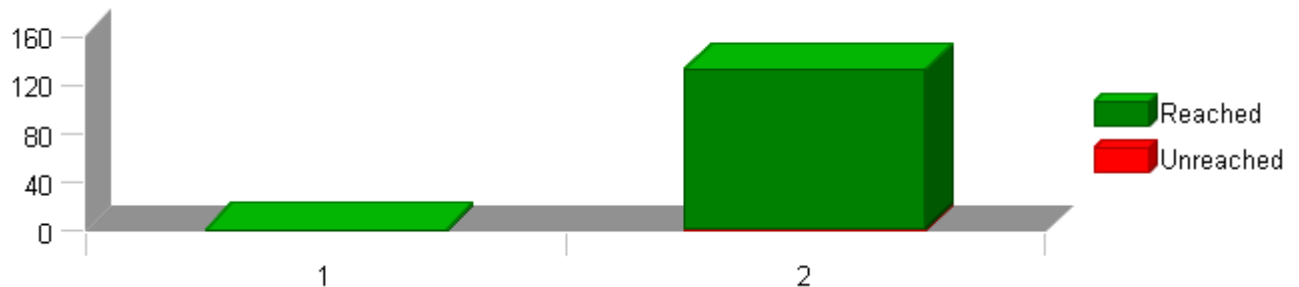
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



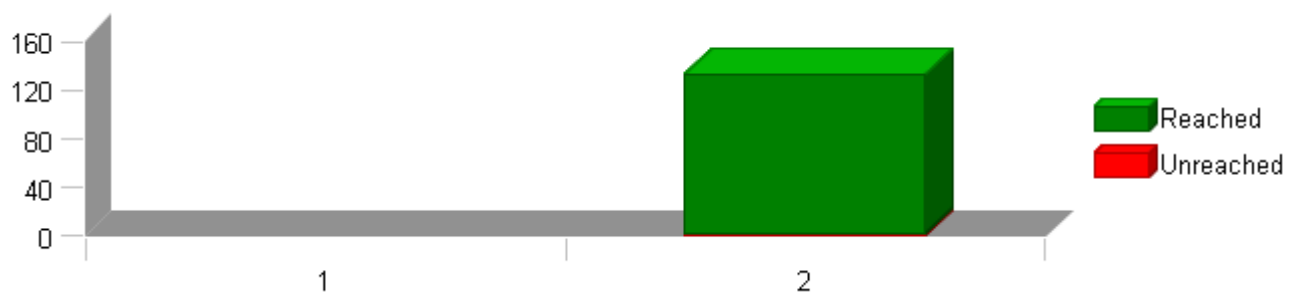
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

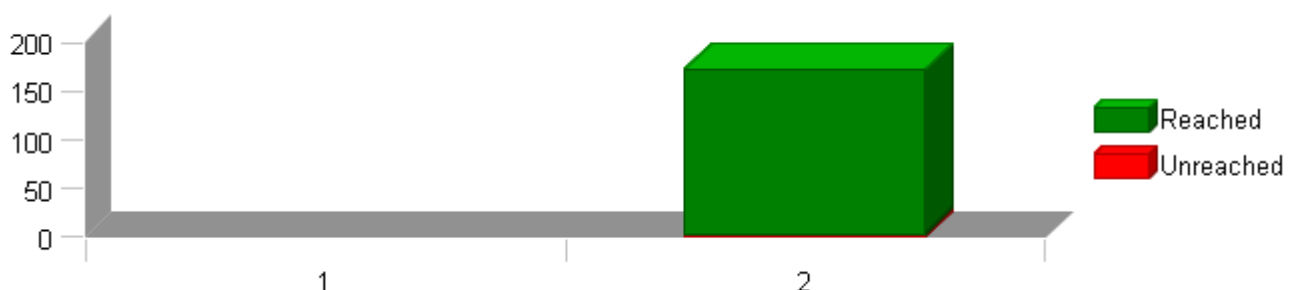
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

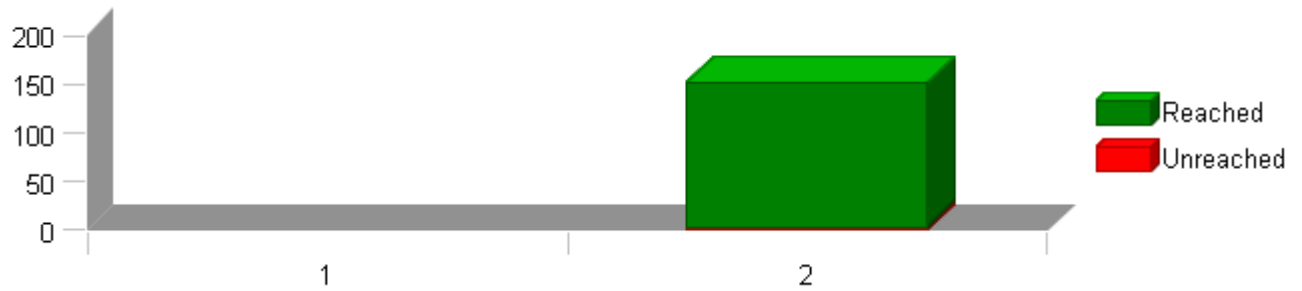
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2015-02-04, 12:38:14+0530

Project CBD_UnitTest



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	CBD_UnitTest	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	✘
	CBD_UnitTest	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	✘
	TrqOvlSta	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	✘
1	TrqOvlSta_Init	100 %	100 %	-	-	-	1 of 1 passed	✔
2	TrqOvlSta_Per1	98.93 %	98.49 %	98.49 %	98.25 %	98.02 %	3 of 3 passed	✘

TEST DETAILS REPORT

2015-02-04, 12:27:15+0530



TrqOvSta_Init

Project	CBD_UnitTest
Module	TrqOvSta
Test Object	TrqOvSta_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\TrqOvSta_CBD
Configuration File	D:\Synergy_Work_Area\TrqOvSta_CBD\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqOvSta\src\Ap_TrqOvSta.c
Compiler Options	-DSKIP_MAGIC_NUMBER=-D_DATA_ACCESS=-Dconst=-I\$(PROJECTROOT)\TrqOvSta\utp\contract-I\$(PROJECTROOT)\TrqOvSta\utp\contract\Ap_TrqOvSta-I\$(PROJECTROOT)\NxtLib\include-I\$(PROJECTROOT)\StdDef\include-I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'TrqOvSta'	<p>*****Unit Test Description*****</p> <p>Name of Tester:Imran desai Code File(s) Under Test:Ap_TrqOvSta.c Code File(s) Version:8 Module Design Document:TrqOvSta_MDD.docx Module Design Document Version:7 Data Dictionary Version:6 Unit Test Plan Version:5 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):2128 Total RAM Used (Bytes):44 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:2/4/2015 Comments:"NOTE 1:- Inline functions defined in ""GlobalMacro.h"" are not unit tested.</p> <p>NOTE 2:- ""CBD_Sandbox_dbg.map"" map file is embedded for reference.</p> <p>NOTE 3:- In function ""TrqOvSta_Per1"", for 100% path coverage, Variable ""TrqOvSta_APASState_State_M_enum"" is given out of range value at Vector Number ""76"" in Path sheet, Hence at this test vector output variable ""TrqOvSta_APASState_State_M_enum"" and ""APASState_State_enum"" is going Out of Range.</p> <p>NOTE 4:-In function ""TrqOvSta_Per1"", for 100% path coverage Variable ""TrqOvSta_HapticState_State_M_enum"" and ""TrqOvSta_ESCState_State_M_enum"" is given out of range value at Vector Number ""78"" in Path sheet, Hence at this test vector, output variable ""TrqOvSta_ESCState_State_M_enum"", ""TrqOvSta_HapticState_State_M_enum"", ""ESCState_State_enum"" and ""GMOSHoscillateState_Stat is going Out of Range.</p> <p>NOTE 5:-In function ""TrqOvSta_Per1"",100% MCDC coverage is not achieved because (TrqOvSta_APASState_State_M_enum == APA_Active)==>True and (PosTrajEnable_Cnt_T_lgc == FALSE) ==>False cannot be covered as control goes to if loop.</p> <p>NOTE 6:- When value of 'TrqOvSta_LKAPermFault_Cnt_M_u08' is greater than three, then the value of 'TrqOvSta_LKAPermFault_Cnt_M_lgc' is taken as 'TRUE' as 'INPUT' to avoid overflow issue.</p> <p>NOTE 7:-In function ""TrqOvSta_Per1"", (SWARTrgtAngRequest_HwDeg_T_f32<=(CCWPosition_HwDeg_T_f32 + k_SWARLimiter_HwDeg_f32)) = True and (SWARTrgtAngRequest_HwDeg_T_f32 >=(CWPosition_HwDeg_T_f32 - k_SWARLimiter_HwDeg_f32)) = True cannot be covered."</p> <p>*****</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5

Attributes	
Name	Value
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\TrqOvlSta_CBD\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS1.1 114.00 Cycles TS1.2 116.00 Cycles TS1.3 115.00 Cycles TS1.4 113.00 Cycles TS1.5 113.00 Cycles TS1.6 113.00 Cycles TS1.7 113.00 Cycles TS1.8 115.00 Cycles TS1.9 115.00 Cycles TS1.10 115.00 Cycles TS1.11 115.00 Cycles TS1.12 113.00 Cycles TS1.13 115.00 Cycles
Description	Vector Description: TS1.1k_APAHwTrqLPFKn_Hz_f32 = MIN TS1.2k_APAHwTrqLPFKn_Hz_f32 = MAX TS1.3k_APAHwTrqLPFKn_Hz_f32 = POS TS1.4TrqOviSta_HwTorqueSV_HwNm_M_Str.SV= MIN TS1.5TrqOviSta_HwTorqueSV_HwNm_M_Str.SV = MAX TS1.6TrqOviSta_HwTorqueSV_HwNm_M_Str.SV= ZERO TS1.7TrqOviSta_HwTorqueSV_HwNm_M_Str.SV= POS TS1.8TrqOviSta_HwTorqueSV_HwNm_M_Str.SV= NEG TS1.9TrqOviSta_HwTorqueSV_HwNm_M_Str.K = MIN TS1.10TrqOviSta_HwTorqueSV_HwNm_M_Str.K = MAX TS1.11TrqOviSta_HwTorqueSV_HwNm_M_Str.K = ZERO TS1.12ALL MIN TS1.13ALL MAX

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.53999996		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.023		
k_APAHwTrqLPFKn_Hz_f32	0.100000001		
Name	Actual Value	Expected Value	Result
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.00125584798 ± 0.00000009	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.32000017		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0869999975		
k_APAHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.715390444 ± 0.00000009	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.38000011		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.150999993		
k_APAHwTrqLPFKn_Hz_f32	50.6500015		
Name	Actual Value	Expected Value	Result
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470851779	0.470851779 ± 0.00000009	✓

Test Step 1.4 (Repeat Count = 1)

Name	Input Value		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.215000004		
k_APAHwTrqLPFKn_Hz_f32	5.409999985		
Name	Actual Value	Expected Value	Result
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0657246709	0.0657246411 ± 0.00000009	✓

TEST DETAILS REPORT

TrqOviSta_Init

2015-02-04, 12:27:15+0530



Test Step 1.5 (Repeat Count = 1)

Name		Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32		10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32		0.279000014		
k_APAHwTrqLPFKn_Hz_f32		11.4200001		
Name		Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32		0	0 ± 0.000009	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32		0.133686125	0.133686095 ± 0.00000009	✔

Test Step 1.6 (Repeat Count = 1)

Test Step No. Input Value			
Name	Input Value		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342999995		
k_APAHwTrqLPFKn_Hz_f32	15.8500004		
Name	Actual Value	Expected Value	Result
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.18059516	0.18059513 ± 0.00000009	✓

Test Step 1.7 (Repeat Count = 1)

Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407000005		
k_APAHwTrqLPFKn_Hz_f32	20		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.222232342	0.222232327 ± 0.00000009	✓

Test Step 1.8 (Repeat Count = 1)

Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470999986		
k_APAHwTrqLPFKn_Hz_f32	28.6100006		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.301991463	0.301991403 ± 0.0000009	✔

Test Step 1.9 (Repeat Count = 1)

Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.56999993		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798		
k_APAHwTrqLPFKn_Hz_f32	36.8600006		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.370730996	0.370730966 ± 0.0000009	✔

Test Step 1.10 (Repeat Count = 1)

Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444		
k_APAHwTrqLPFKn_Hz_f32	42		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.4100914	0.410091341 ± 0.0000009	✓

TEST DETAILS REPORT

2015-02-04, 12:27:15+0530

TrqOviSta_Init



Test Step 1.11 (Repeat Count = 1)

Name	Input Value		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.009999999978		
k_APAHwTrqLPFKn_Hz_f32	48.3400002		
Name	Actual Value	Expected Value	Result
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.455266416	0.455266356 ± 0.0000009	✓

Test Step 1.12 (Repeat Count = 1)

Name	Input Value		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798		
k_APAHwTrqLPFKn_Hz_f32	0.100000001		
Name	Actual Value	Expected Value	Result
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.00125584798 ± 0.0000009	✓

Test Step 1.13 (Repeat Count = 1)

Name	Input Value		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444		
k_APAHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.715390444 ± 0.0000009	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530

TrqOvISta_Per1



Project	CBD_UnitTest
Module	TrqOvISta
Test Object	TrqOvISta_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	98.93 %
Decision Coverage	98.49 %
Branch (C1) Coverage	98.49 %
MCC Coverage	98.02 %
MC/DC Coverage	98.25 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\TrqOvISta_CBD
Configuration File	D:\Synergy_Work_Area\TrqOvISta_CBD\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqOvISta\src\Ap_TrqOvISta.c
Compiler Options	-DSKIP_MAGIC_NUMBER= -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqOvISta\utp\contract -I\$(PROJECTROOT)\TrqOvISta\utp\contract\Ap_TrqOvISta -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
------	------

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Module 'TrqOviSta'

*****Unit Test Description*****

Name of Tester:Imran desai
Code File(s) Under Test:Ap_TrqOviSta.c
Code File(s) Version:8
Module Design Document:TrqOviSta_MDD.docx
Module Design Document Version:7
Data Dictionary Version:6
Unit Test Plan Version:5
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):2128
Total RAM Used (Bytes):44
Total CALS Used (Bytes):65
Special Test Requirements:
Test Date:2/4/2015
Comments:"NOTE 1:- Inline functions defined in ""GlobalMacro.h"" are not unit tested.

NOTE 2:- ""CBD_Sandbox_dbg.map"" map file is embedded for reference.

NOTE 3:- In function ""TrqOviSta_Per1"", for 100% path coverage, Variable ""TrqOviSta_APASState_State_M_enum"" is given out of range value at Vector Number ""76"" in Path sheet, Hence at this test vector output variable ""TrqOviSta_APASState_State_M_enum"" and ""APASState_State_enum"" is going Out of Range.

NOTE 4:-In function ""TrqOviSta_Per1"", for 100% path coverage Variable ""TrqOviSta_HapticState_State_M_enum"" and ""TrqOviSta_ESCState_State_M_enum"" is given out of range value at Vector Number ""78"" in Path sheet, Hence at this test vector, output variable ""TrqOviSta_ESCState_State_M_enum"", ""TrqOviSta_HapticState_State_M_enum"", ""ESCState_State_enum"" and ""GMOSHOScillateState_Stat is going Out of Range.

NOTE 5:-In function ""TrqOviSta_Per1"", 100% MCDC coverage is not achieved because (TrqOviSta_APASState_State_M_enum == APA_Active)=>True and (PosTrajEnable_Cnt_T_lgc == FALSE) =>False cannot be covered as control goes to if loop.

NOTE 6:- When value of 'TrqOviSta_LKAPermFault_Cnt_M_u08' is greater than three, then the value of 'TrqOviSta_LKAPermFault_Cnt_M_lgc' is taken as 'TRUE' as 'INPUT' to avoid overflow issue.

NOTE 7:-In function ""TrqOviSta_Per1"", (SWARTrgtAngRequest_HwDeg_T_f32<=(CCWPosition_HwDeg_T_f32 + k_SWARLimiter_HwDeg_f32)) = True and (SWARTrgtAngRequest_HwDeg_T_f32 >=(CWPosition_HwDeg_T_f32 - k_SWARLimiter_HwDeg_f32)) = True cannot be covered."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\TrqOviSta_CBD\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 874.00 Cycles
TS1.2 1053.00 Cycles

Description Vector Description:

```
TS1.1"Shrotest Excution Path==>
(LKAResquest_Cnt_T_f32 > D_ZERO_ULS_F32)=> TRUE
(HapticRequest_Cnt_T_lgc == TRUE)=>FALSE
(ShiftLeverIsInReverse_Cnt_T_lgc == TRUE)=>FALSE
(SecureVehicleSpeed_Kph_T_f32 >= k_StandstillThresh_Kph_f32)=>FALSE
(TrqOviSta_HapticState_State_M_enum != Haptic_Inactive)=>TRUE
(HwTrqFilt_HwNm_T_f32 >= k_APAMaxHwTrq_HwNm_f32)=>FALSE
((LKAFault_Cnt_T_lgc == TRUE) && (TrqOviSta_LKAPermFault_Cnt_M_lgc == FALSE) && (TrqOviSta_LKAFault_Cnt_M_lgc == FALSE))=>FALSE
(TrqOviSta_LKAMfgEnableTyH_Cnt_G_lgc ==FALSE)=>FALSE
(TrqOviSta_APAMfgEnableTyH_Cnt_G_lgc ==TRUE)=>FALSE
Select (TrqOviSta_APASate_State_M_enum)
Case: Default
Select (TrqOviSta_LKASate_State_M_enum)
Case: Default
(TrqOviSta_HapticState_State_M_enum==Haptic_Active)=>TRUE
(TrqOviSta_APASate_State_M_enum==APA_Active)=>FALSE

"

TS1.2"Longest Excution Path==>
(LKAResquest_Cnt_T_f32 > D_ZERO_ULS_F32)=> FALSE
(HapticRequest_Cnt_T_lgc == TRUE)=>TRUE
(TrqOviSta_HapticTime_Sec_M_f32 >= (k_MaxHapticReqTime_Sec_f32))=>FALSE
(ShiftLeverIsInReverse_Cnt_T_lgc == TRUE)=>TRUE
(TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32 > (k_ShiftLevRevTime_Sec_f32))=>FALSE
(SecureVehicleSpeed_Kph_T_f32 >= k_StandstillThresh_Kph_f32)=>TRUE
(TrqOviSta_ElapsedStandstillTime_Sec_M_f32 >= k_StandstillTime_Sec_f32)=>FALSE
(TrqOviSta_HapticState_State_M_enum != Haptic_Inactive)=>FALSE
(Abs_f32_m(HwTrqFilt_HwNm_T_f32) >= k_APAMaxHwTrq_HwNm_f32)=>TRUE
(TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 >= ((k_APAMaxHwTrqTime_Sec_f32)))=>FALSE
(((LKAFault_Cnt_T_lgc == TRUE) && (TrqOviSta_LKAPermFault_Cnt_M_lgc == FALSE) && (TrqOviSta_LKAFault_Cnt_M_lgc == FALSE))=>TRUE
(TrqOviSta_LKAPermFault_Cnt_M_u08 >= 3U)=>True && (*Rte_Pim_TrqOviStaMfgEnable_Cnt_b08() & D_LKAMFGENABLE_CNT_U08) != D_LKAMFGENABLE_CNT_U08)=>True && ((*Rte_Pim_TrqOviStaMfgEnable_Cnt_b08() & D_APAMFGENABLE_CNT_U08) != D_APAMFGENABLE_CNT_U08)=>False
(TrqOviSta_LKAMfgEnableTyH_Cnt_G_lgc ==FALSE)=>FALSE
(TrqOviSta_APAMfgEnableTyH_Cnt_G_lgc ==TRUE)=>FALSE
Select (TrqOviSta_APASate_State_M_enum)
Case(APA_AvailCntrl):
(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=>FALSE
((SecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) || (APAREcoverableFaults_Cnt_T_lgc==TRUE))
((HapticRequest_Cnt_T_lgc==TRUE)&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)))=>FALSE
(k_APAINcludeHaptic_Cnt_lgc == FALSE)=>FALSE
Select (TrqOviSta_HapticState_State_M_enum)
Case(Haptic_Inactive):
((HapticRequest_Cnt_T_lgc==FALSE) && (TrqOviSta_ReadytoPulse_Cnt_M_lgc==FALSE))=>FALSE
(TrqOviSta_ReadytoPulse_Cnt_M_lgc== TRUE)=>FALSE
((StandstillTime_Cnt_T_lgc==TRUE) && (APAREquest_Cnt_T_lgc ==TRUE)&&(HapticRequest_Cnt_T_lgc==TRUE)&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))=>FALSE
(HapticRequest_Cnt_T_lgc==TRUE)=>TRUE
Select (TrqOviSta_LKASate_State_M_enum):
DEFAULT

(TrqOviSta_HapticState_State_M_enum==Haptic_Active)
=>FALSE
(TrqOviSta_APASate_State_M_enum==APA_Active)=>FALSE

"
```

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASate_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	5270
TrqOviSta_HapticTime_Sec_M_f32	27475.5293
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReactTime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-390.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	360.980011
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0		
target_TrqOviSta_Per1_LKAResponse_Cnt_lgc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOviSta_Per1_ShiftLeversInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
k_HapticFreq_Hz_f32	32.3019981		
k_HapticReacttime_Sec_f32	1.23199999		
k_LKAMaxVehSpd_Kph_f32	146.300003		
k_LKAMinVehSpd_Kph_f32	1		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	0		
k_ShiftLevRevTime_Sec_f32	0.308999985		
k_StandstillThresh_Kph_f32	0		
k_StandstillTime_Sec_f32	0.308999985		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIinhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIinhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-388.230011		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIinhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	2	2	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKAShock_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APAShock_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKAShock_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530

TrqOvlSta_Per1



Test Case 2: Boundary Test



TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment)
	CPU Cycles:
	TS2.1 906.00 Cycles
	TS2.2 891.00 Cycles
	TS2.3 887.00 Cycles
	TS2.4 899.00 Cycles
	TS2.5 887.00 Cycles
	TS2.6 905.00 Cycles
	TS2.7 884.00 Cycles
	TS2.8 940.00 Cycles
	TS2.9 883.00 Cycles
	TS2.10 932.00 Cycles
	TS2.11 883.00 Cycles
	TS2.12 912.00 Cycles
	TS2.13 889.00 Cycles
	TS2.14 890.00 Cycles
	TS2.15 893.00 Cycles
	TS2.16 892.00 Cycles
	TS2.17 903.00 Cycles
	TS2.18 893.00 Cycles
	TS2.19 900.00 Cycles
	TS2.20 906.00 Cycles
	TS2.21 896.00 Cycles
	TS2.22 923.00 Cycles
	TS2.23 879.00 Cycles
	TS2.24 886.00 Cycles
	TS2.25 916.00 Cycles
	TS2.26 904.00 Cycles
	TS2.27 892.00 Cycles
	TS2.28 895.00 Cycles
	TS2.29 904.00 Cycles
	TS2.30 889.00 Cycles
	TS2.31 931.00 Cycles
	TS2.32 887.00 Cycles
	TS2.33 903.00 Cycles
	TS2.34 909.00 Cycles
	TS2.35 915.00 Cycles
	TS2.36 893.00 Cycles
	TS2.37 891.00 Cycles
	TS2.38 913.00 Cycles
	TS2.39 911.00 Cycles
	TS2.40 889.00 Cycles
	TS2.41 900.00 Cycles
	TS2.42 902.00 Cycles
	TS2.43 911.00 Cycles
	TS2.44 921.00 Cycles
	TS2.45 913.00 Cycles
	TS2.46 907.00 Cycles
	TS2.47 907.00 Cycles
	TS2.48 903.00 Cycles
	TS2.49 907.00 Cycles
	TS2.50 896.00 Cycles
	TS2.51 899.00 Cycles
	TS2.52 900.00 Cycles
	TS2.53 915.00 Cycles
	TS2.54 898.00 Cycles
	TS2.55 918.00 Cycles
	TS2.56 894.00 Cycles
	TS2.57 924.00 Cycles
	TS2.58 887.00 Cycles
	TS2.59 934.00 Cycles
	TS2.60 890.00 Cycles
	TS2.61 923.00 Cycles
	TS2.62 940.00 Cycles
	TS2.63 893.00 Cycles
	TS2.64 919.00 Cycles
	TS2.65 888.00 Cycles
	TS2.66 894.00 Cycles
	TS2.67 911.00 Cycles
	TS2.68 942.00 Cycles
	TS2.69 894.00 Cycles
	TS2.70 913.00 Cycles
	TS2.71 893.00 Cycles
	TS2.72 907.00 Cycles
	TS2.73 897.00 Cycles
	TS2.74 906.00 Cycles
	TS2.75 893.00 Cycles
	TS2.76 889.00 Cycles
	TS2.77 896.00 Cycles
	TS2.78 883.00 Cycles
	TS2.79 906.00 Cycles
	TS2.80 899.00 Cycles
	TS2.81 917.00 Cycles
	TS2.82 908.00 Cycles
	TS2.83 913.00 Cycles
	TS2.84 905.00 Cycles
	TS2.85 887.00 Cycles
	TS2.86 890.00 Cycles
	TS2.87 896.00 Cycles
	TS2.88 924.00 Cycles
	TS2.89 888.00 Cycles
	TS2.90 892.00 Cycles
	TS2.91 908.00 Cycles
	TS2.92 900.00 Cycles
	TS2.93 888.00 Cycles
	TS2.94 907.00 Cycles
	TS2.95 902.00 Cycles
	TS2.96 920.00 Cycles
	TS2.97 903.00 Cycles
	TS2.98 933.00 Cycles
	TS2.99 900.00 Cycles
	TS2.100 912.00 Cycles
	TS2.101 897.00 Cycles
	TS2.102 945.00 Cycles
	TS2.103 893.00 Cycles
	TS2.104 953.00 Cycles
	TS2.105 903.00 Cycles
	TS2.106 933.00 Cycles
	TS2.107 900.00 Cycles
	TS2.108 890.00 Cycles
	TS2.109 900.00 Cycles
	TS2.110 883.00 Cycles

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

TS2.111	903.00	Cycles
TS2.112	883.00	Cycles
TS2.113	894.00	Cycles
TS2.114	895.00	Cycles
TS2.115	898.00	Cycles
TS2.116	883.00	Cycles
TS2.117	915.00	Cycles
TS2.118	883.00	Cycles
TS2.119	899.00	Cycles
TS2.120	896.00	Cycles
TS2.121	889.00	Cycles
TS2.122	883.00	Cycles
TS2.123	905.00	Cycles
TS2.124	883.00	Cycles
TS2.125	898.00	Cycles
TS2.126	892.00	Cycles
TS2.127	902.00	Cycles
TS2.128	883.00	Cycles
TS2.129	908.00	Cycles
TS2.130	883.00	Cycles
TS2.131	889.00	Cycles
TS2.132	896.00	Cycles
TS2.133	893.00	Cycles
TS2.134	883.00	Cycles
TS2.135	910.00	Cycles
TS2.136	883.00	Cycles
TS2.137	898.00	Cycles
TS2.138	883.00	Cycles
TS2.139	901.00	Cycles
TS2.140	896.00	Cycles
TS2.141	898.00	Cycles
TS2.142	883.00	Cycles
TS2.143	914.00	Cycles
TS2.144	883.00	Cycles
TS2.145	898.00	Cycles
TS2.146	896.00	Cycles
TS2.147	889.00	Cycles
TS2.148	881.00	Cycles
TS2.149	903.00	Cycles
TS2.150	893.00	Cycles
TS2.151	913.00	Cycles
TS2.152	899.00	Cycles
TS2.153	908.00	Cycles
TS2.154	883.00	Cycles
TS2.155	908.00	Cycles
TS2.156	883.00	Cycles
TS2.157	900.00	Cycles
TS2.158	892.00	Cycles
TS2.159	904.00	Cycles
TS2.160	896.00	Cycles
TS2.161	892.00	Cycles

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Description Vector Description:

TS2.1APANonRecoverableFaults_Cnt_Igc = MIN
TS2.2APANonRecoverableFaults_Cnt_Igc = MAX
TS2.3APARecoverableFaults_Cnt_Igc = MIN
TS2.4APARecoverableFaults_Cnt_Igc = MAX
TS2.5APARequest_Cnt_Igc = MIN
TS2.6APARequest_Cnt_Igc = MAX
TS2.7HapticRequest_Cnt_Igc = MIN
TS2.8HapticRequest_Cnt_Igc = MAX
TS2.9ShiftLeverIsInReverse_Cnt_Igc = MIN
TS2.10ShiftLeverIsInReverse_Cnt_Igc = MAX
TS2.11LKAFault_Cnt_Igc = MIN
TS2.12LKAFault_Cnt_Igc = MAX
TS2.13LKAlnhibit_Cnt_Igc = MIN
TS2.14LKAlnhibit_Cnt_Igc = MAX
TS2.15LKARequest_Cnt_Igc = MIN
TS2.16LKARequest_Cnt_Igc = MAX
TS2.17HwTorque_HwNm_f32 = MIN
TS2.18HwTorque_HwNm_f32 = MAX
TS2.19HwTorque_HwNm_f32 = ZERO
TS2.20HwTorque_HwNm_f32 = POS
TS2.21HwTorque_HwNm_f32 = NEG
TS2.22TrqOvlSta_HapticState_State_M_enum = Haptic_Inactive
TS2.23TrqOvlSta_HapticState_State_M_enum = Haptic_Active
TS2.24TrqOvlSta_HapticState_State_M_enum = Haptic_Waiting
TS2.25TrqOvlSta_LKASState_State_M_enum = LKA_Inactive
TS2.26TrqOvlSta_LKASState_State_M_enum = LKA_Active
TS2.27TrqOvlSta_LKASState_State_M_enum = LKA_TemporarilyLimited
TS2.28TrqOvlSta_LKASState_State_M_enum = LKA_PermanentlyFailed
TS2.29TrqOvlSta_LKASState_State_M_enum = LKA_ManufacturingDisable
TS2.30TrqOvlSta_APASState_State_M_enum = APA_TemplInhibit
TS2.31TrqOvlSta_APASState_State_M_enum = APA_AvailCntrl
TS2.32TrqOvlSta_APASState_State_M_enum = APA_Active
TS2.33TrqOvlSta_APASState_State_M_enum = APA_PermanentlyFailed
TS2.34TrqOvlSta_APASState_State_M_enum = APA_ManufacturingDisable
TS2.35TrqOvlSta_Haptictime_Sec_M_f32 = MIN
TS2.36TrqOvlSta_Haptictime_Sec_M_f32 = MAX
TS2.37TrqOvlSta_Haptictime_Sec_M_f32 = POS
TS2.38TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 = MIN
TS2.39TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 = MAX
TS2.40TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32
TS2.41TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = MIN
TS2.42TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = MAX
TS2.43TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = POS
TS2.44TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = MIN
TS2.45TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = MAX
TS2.46TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = POS
TS2.47TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = MIN
TS2.48TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = MAX
TS2.49TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = ZERO
TS2.50TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = POS
TS2.51TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = NEG
TS2.52TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = MIN
TS2.53TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = MAX
TS2.54TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = POS
TS2.55TrqOvlSta_LKAPermFault_Cnt_M_Igc = MIN
TS2.56TrqOvlSta_LKAPermFault_Cnt_M_Igc = MAX
TS2.57TrqOvlSta_LKAFault_Cnt_M_Igc = MIN
TS2.58TrqOvlSta_LKAFault_Cnt_M_Igc = MAX
TS2.59TrqOvlSta_LKAPermFault_Cnt_M_u08 = MIN
TS2.60TrqOvlSta_LKAPermFault_Cnt_M_u08 = MAX
TS2.61TrqOvlSta_LKAPermFault_Cnt_M_u08 = POS
TS2.62TrqOvlSta_ReadytoPulse_Cnt_M_Igc = MIN
TS2.63TrqOvlSta_ReadytoPulse_Cnt_M_Igc = MAX
TS2.64TrqOvlSta_Hapticdur_Cnt_M_u16 = MIN
TS2.65TrqOvlSta_Hapticdur_Cnt_M_u16 = MAX
TS2.66TrqOvlSta_Hapticdur_Cnt_M_u16 = POS
TS2.67k_MaxHapticReqTime_Sec_f32 = MIN
TS2.68k_MaxHapticReqTime_Sec_f32 = MAX
TS2.69k_MaxHapticReqTime_Sec_f32 = POS
TS2.70k_ShiftLevRevTime_Sec_f32 = MIN
TS2.71k_ShiftLevRevTime_Sec_f32 = MAX
TS2.72k_ShiftLevRevTime_Sec_f32 = POS
TS2.73k_StandstillThresh_Kph_f32 = MIN
TS2.74k_StandstillThresh_Kph_f32 = MAX
TS2.75k_StandstillThresh_Kph_f32 = POS
TS2.76k_StandstillTime_Sec_f32 = MIN
TS2.77k_StandstillTime_Sec_f32 = MAX
TS2.78k_StandstillTime_Sec_f32 = POS
TS2.79k_APAMaxHwTrq_HwNm_f32 = MIN
TS2.80k_APAMaxHwTrq_HwNm_f32 = MAX
TS2.81k_APAMaxHwTrq_HwNm_f32 = POS
TS2.82k_APAMaxHwTrqTime_Sec_f32 = MIN
TS2.83k_APAMaxHwTrqTime_Sec_f32 = MAX
TS2.84k_APAMaxHwTrqTime_Sec_f32 = POS
TS2.85k_APAMaxVehSpd_Kph_f32 = MIN
TS2.86k_APAMaxVehSpd_Kph_f32 = MAX
TS2.87k_APAMaxVehSpd_Kph_f32 = POS
TS2.88k_HapticDuration_Sec_f32 = MIN
TS2.89k_HapticDuration_Sec_f32 = MAX
TS2.90k_HapticDuration_Sec_f32 = POS
TS2.91k_HapticReacttime_Sec_f32 = MIN
TS2.92k_HapticReacttime_Sec_f32 = MAX
TS2.93k_HapticReacttime_Sec_f32 = POS
TS2.94k_LKAMinVehSpd_Kph_f32 = MIN

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

TS2.95k_LKAMinVehSpd_Kph_f32= MAX
 TS2.96k_LKAMinVehSpd_Kph_f32 = POS
 TS2.97k_LKAMaxVehSpd_Kph_f32 = MIN
 TS2.98k_LKAMaxVehSpd_Kph_f32= MAX
 TS2.99k_LKAMaxVehSpd_Kph_f32 = POS
 TS2.100k_HapticAmplitude_MtrNm_f32 = MIN
 TS2.101k_HapticAmplitude_MtrNm_f32= MAX
 TS2.102k_HapticAmplitude_MtrNm_f32 = POS
 TS2.103k_APAINcludeHaptic_Cnt_lgc = MIN
 TS2.104k_APAINcludeHaptic_Cnt_lgc= MAX
 TS2.105k_HapticFreq_Hz_f32=MIN
 TS2.106k_HapticFreq_Hz_f32=MAX
 TS2.107k_HapticFreq_Hz_f32=POS
 TS2.108MaxSecureVehicleSpeed_Kph_f32=Min
 TS2.109MaxSecureVehicleSpeed_Kph_f32=Max
 TS2.110MaxSecureVehicleSpeed_Kph_f32=Pos
 TS2.111MinSecureVehicleSpeed_Kph_f32=Min
 TS2.112MinSecureVehicleSpeed_Kph_f32=Max
 TS2.113MinSecureVehicleSpeed_Kph_f32=Pos
 TS2.114ESCRequest_Cnt_lgc=FALSE
 TS2.115ESCRequest_Cnt_lgc=TRUE
 TS2.116ESCFault_Cnt_lgc=FALSE
 TS2.117ESCFault_Cnt_lgc=TRUE
 TS2.118ESCIsLimited_Cnt_lgc=FALSE
 TS2.119ESCIsLimited_Cnt_lgc=TRUE
 TS2.120CWFound_Cnt_lgc=FALSE
 TS2.121CWFound_Cnt_lgc=TRUE
 TS2.122CCWFound_Cnt_lgc=FALSE
 TS2.123CCWFound_Cnt_lgc=TRUE
 TS2.124PosTrajHwAngle_HwDeg_f32=Min
 TS2.125PosTrajHwAngle_HwDeg_f32=Max
 TS2.126PosTrajHwAngle_HwDeg_f32=Pos
 TS2.127SWARTrgtAngRequest_HwDeg_f32=Min
 TS2.128SWARTrgtAngRequest_HwDeg_f32=Max
 TS2.129SWARTrgtAngRequest_HwDeg_f32=Zero
 TS2.130SWARTrgtAngRequest_HwDeg_f32=Pos
 TS2.131SWARTrgtAngRequest_HwDeg_f32=Neg
 TS2.132PosTrajEnable_Cnt_lgc=FALSE
 TS2.133PosTrajEnable_Cnt_lgc=TRUE
 TS2.134CWPosition_HwDeg_f32=Min
 TS2.135CWPosition_HwDeg_f32=Max
 TS2.136CWPosition_HwDeg_f32=Pos
 TS2.137CCWPosition_HwDeg_f32=Min
 TS2.138CCWPosition_HwDeg_f32=Max
 TS2.139CCWPosition_HwDeg_f32=Neg
 TS2.140GMOSH_APAMfgEnable_Cnt_lgc=FALSE
 TS2.141GMOSH_APAMfgEnable_Cnt_lgc=TRUE
 TS2.142GMOSH_LKAMfgEnable_Cnt_lgc=FALSE
 TS2.143GMOSH_LKAMfgEnable_Cnt_lgc=TRUE
 TS2.144GMOSH_ESCMfgEnable_Cnt_lgc=FALSE
 TS2.145GMOSH_ESCMfgEnable_Cnt_lgc=TRUE
 TS2.146TrqOviSta_ESCState_State_M_enum=ESC_Inactive
 TS2.147TrqOviSta_ESCState_State_M_enum=ESC_Active
 TS2.148TrqOviSta_ESCState_State_M_enum=ESC_TemporarilyLimited
 TS2.149TrqOviSta_ESCState_State_M_enum=ESC_PermanentlyFailed
 TS2.150TrqOviSta_ESCState_State_M_enum=ESC_ManufacturingDisable
 TS2.151k_ESCMaxVehSpd_Kph_f32=Min
 TS2.152k_ESCMaxVehSpd_Kph_f32=Max
 TS2.153k_ESCMaxVehSpd_Kph_f32=Pos
 TS2.154k_SWARLimiter_HwDeg_f32=Min
 TS2.155k_SWARLimiter_HwDeg_f32=Max
 TS2.156k_SWARLimiter_HwDeg_f32=Pos
 TS2.157TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32=Min
 TS2.158TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32=Max
 TS2.159TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32=Pos
 TS2.160All MIN
 TS2.161All MAX

Test Step 2.1 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	45
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0.850000024
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	15
TrqOviSta_HapticTime_Sec_M_f32	75.6800003
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85999966
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.109999999

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value
k_APAMaxHwTrq_HwNm_f32	0.100000001
k_APAMaxVehSpd_Kph_f32	0.200000003
k_ESCMaxVehSpd_Kph_f32	10.10000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	0.50999999
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	0.400000006
k_MaxHapticReqTime_Sec_f32	0.0149999997
k_SWARLimiter_HwDeg_f32	2.20000005
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.00100000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOCscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAINhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAINhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAREquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOvlSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1140.10999
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.209999993
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOvlSta_Per1_LKAINhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	25.2999992
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	35.0999985
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-32.5
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008	0.460000008 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	45	45 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	15	15 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	75.6800003	75.6800003 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.83985996	-9.83985996 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005	1.20000005 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1.20000005	1.20000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.2 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7.11999989
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	132.839996
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	8
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	240
TrqOviSta_HapticTime_Sec_M_f32	150
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	2.29999995
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.119999997
k_APAMaxHwTrq_HwNm_f32	0.0799999982
k_APAMaxVehSpd_Kph_f32	1.79999995
k_ESCMaxVehSpd_Kph_f32	12.5
k_HapticAmplitude_MtrNm_f32	0.00600000005
k_HapticDuration_Sec_f32	0.0149999997
k_HapticFreq_Hz_f32	3.53999996
k_HapticReactTime_Sec_f32	0.0160000008
k_LKAMaxVehSpd_Kph_f32	1.89999998
k_LKAMinVehSpd_Kph_f32	1.75
k_MaxHapticReqTime_Sec_f32	0.0299999993
k_SWARLimiter_HwDeg_f32	3.5999999
k_ShiftLevRevTime_Sec_f32	0.00499999989
k_StandstillThresh_Kph_f32	0.0399999991
k_StandstillTime_Sec_f32	0.00499999989
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARequest_Cnt_lgc	target_TrqOviSta_Per1APARequest_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH OscillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAI nhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAI nhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_Igc	target_TrqOviSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1130.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	364.940002		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.560000002		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAI nhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.2000008		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Haptidur_Cnt_M_u16	240	240 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25	-9.25 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.00600000005	0.00600000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3.53999996	3.53999996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	85
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	220.679993
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	68.3000031
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	530
TrqOviSta_HapticTime_Sec_M_f32	860.26001
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.64000034
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	3.4000001
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAlncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.129999995
k_APAMaxHwTrq_HwNm_f32	0.159999996
k_APAMaxVehSpd_Kph_f32	3.5999999
k_ESCMaxVehSpd_Kph_f32	14.8999996
k_HapticAmplitude_MtrNm_f32	0.0120000001
k_HapticDuration_Sec_f32	0.0299999993
k_HapticFreq_Hz_f32	4
k_HapticReactTime_Sec_f32	0.0320000015
k_LKAMaxVehSpd_Kph_f32	3.79999995
k_LKAMinVehSpd_Kph_f32	3.5
k_MaxHapticReqTime_Sec_f32	0.0450000018
k_SWARLimiter_HwDeg_f32	5
k_ShiftLevRevTime_Sec_f32	0.00899999961
k_StandstillThresh_Kph_f32	0.0799999982
k_StandstillTime_Sec_f32	0.00899999961
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enum	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAlnhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAlnhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1119.91003
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	369.880005
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.910000026
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	35.0999985
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	44.9000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.7000008
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	530	530 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	860.26001	860.26001 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.64000034	-8.64000034 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	3.4000001	3.4000001 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3.4000001	3.4000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0120000001	0.0120000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	4	4 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.4 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	225.639999
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	684
TrqOviSta_HapticTime_Sec_M_f32	1345
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0086000029
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	4.5
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.140000001
k_APAMaxHwTrq_HwNm_f32	0.239999995
k_APAMaxVehSpd_Kph_f32	5.4000001
k_ESCMaxVehSpd_Kph_f32	17.2999992
k_HapticAmplitude_MtrNm_f32	0.017999992
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	4.55999994
k_HapticReactTime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.69999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.0599999987
k_SWARLimiter_HwDeg_f32	6.4000001
k_ShiftLevRevTime_Sec_f32	0.0130000003
k_StandstillThresh_Kph_f32	0.119999997
k_StandstillTime_Sec_f32	0.0130000003
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1109.81006
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	374.820007
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.25999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	40
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	308.519989 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	684	684 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973	-8.02999973 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0179999992	0.0179999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	4.55999994	4.55999994 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	450.359985
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	396.359985
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	540
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	960
TrqOviSta_HapticTime_Sec_M_f32	1830.43005
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.42000008
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	5.59999999

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.150000006
k_APAMaxHwTrq_HwNm_f32	0.319999993
k_APAMaxVehSpd_Kph_f32	7.19999981
k_ESCMaxVehSpd_Kph_f32	19.7000008
k_HapticAmplitude_MtrNm_f32	0.0240000002
k_HapticDuration_Sec_f32	0.0599999987
k_HapticFreq_Hz_f32	5.05999994
k_HapticReacttime_Sec_f32	0.064000003
k_LKAMaxVehSpd_Kph_f32	7.5999999
k_LKAMinVehSpd_Kph_f32	7
k_MaxHapticReqTime_Sec_f32	0.075000003
k_SWARLimiter_HwDeg_f32	7.80000019
k_ShiftLevRevTime_Sec_f32	0.0170000009
k_StandstillThresh_Kph_f32	0.159999996
k_StandstillTime_Sec_f32	0.0170000009
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1099.70996
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	379.76001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.61000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	54.7000008
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-24.8999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	396.359985	396.359985 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	960	960 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.42000008	-7.42000008 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999	5.5999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	5.5999999	5.5999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0240000002	0.0240000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	5.05999994	5.05999994 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	1050
TrqOviSta_HapticTime_Sec_M_f32	2315.65991
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.400000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.0299999993
k_HapticDuration_Sec_f32	0.075000003
k_HapticFreq_Hz_f32	5.57399988
k_HapticReactTime_Sec_f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k_LKAMinVehSpd_Kph_f32	8.75
k_MaxHapticReqTime_Sec_f32	0.0900000036
k_SWARLimiter_HwDeg_f32	9.19999981
k_ShiftLevRevTime_Sec_f32	0.0209999997
k_StandstillThresh_Kph_f32	0.200000003
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1089.60999		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	384.700012		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.96000004		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	2315.65991	2315.65991 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0299999993	0.0299999993 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1750
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	572.039978
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1800.45996
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	3575
TrqOvlSta_HapticTime_Sec_M_f32	2800.93994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0152000003
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.170000002
k_APAMaxHwTrq_HwNm_f32	0.479999989
k_APAMaxVehSpd_Kph_f32	10.8000002
k_ESCMaxVehSpd_Kph_f32	19.7000008
k_HapticAmplitude_MtrNm_f32	0.0359999985
k_HapticDuration_Sec_f32	0.0900000036
k_HapticFreq_Hz_f32	6.08799982
k_HapticReactTime_Sec_f32	0.0960000008
k_LKAMaxVehSpd_Kph_f32	11.3999996
k_LKAMinVehSpd_Kph_f32	10.5
k_MaxHapticReqTime_Sec_f32	0.104999997
k_SWARLimiter_HwDeg_f32	7.80000019
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	0.239999995
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1079.51001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	389.640015
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.30999994
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	44.9000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	54.7000008
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-24.8999996
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	3575	3575 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981	-6.19999981 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999	5.5999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	5.5999999	5.5999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.035999985	0.035999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	6.08799982	6.08799982 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.8 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	6100
TrqOviSta_HapticTime_Sec_M_f32	3285.03003
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReactTime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	9.19999981
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1069.41003
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	394.579987
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.66000009
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	6100	6100 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3529.32007
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	747.719971
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	3539.93994
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	8625
TrqOviSta_HapticTime_Sec_M_f32	3770.09009
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.98000002
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0196000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	10

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.189999998
k_APAMaxHwTrq_HwNm_f32	0.639999986
k_APAMaxVehSpd_Kph_f32	14.3999996
k_ESCMaxVehSpd_Kph_f32	29.2999992
k_HapticAmplitude_MtrNm_f32	0.048000004
k_HapticDuration_Sec_f32	0.119999997
k_HapticFreq_Hz_f32	7.11600018
k_HapticReacttime_Sec_f32	0.128000006
k_LKAMaxVehSpd_Kph_f32	15.1999998
k_LKAMinVehSpd_Kph_f32	14
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	13.3999996
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	0.319999993
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1059.31006
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	399.519989
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.00999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	64.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	74.3000031
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	8625	8625 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.98000002	-4.98000002 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	10	10 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	10	10 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0480000004	0.0480000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.11600018	7.11600018 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	14.8000002
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1049.20996		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	404.459991		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5308.64014
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	923.400024
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	5279.41992
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptidur_Cnt_M_u16	13675
TrqOvlSta_HapticTime_Sec_M_f32	4740.43018
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0240000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	12.1999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.209999993
k_APAMaxHwTrq_HwNm_f32	0.800000012
k_APAMaxVehSpd_Kph_f32	18
k_ESCMaxVehSpd_Kph_f32	34.0999985
k_HapticAmplitude_MtrNm_f32	0.0599999987
k_HapticDuration_Sec_f32	0.150000006
k_HapticFreq_Hz_f32	8.14400005
k_HapticReactTime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	19
k_LKAMinVehSpd_Kph_f32	17.5
k_MaxHapticReqTime_Sec_f32	0.165000007
k_SWARLimiter_HwDeg_f32	16.2000008
k_ShiftLevRevTime_Sec_f32	0.0410000011
k_StandstillThresh_Kph_f32	0.400000006
k_StandstillTime_Sec_f32	0.0410000011
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1039.10999
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	409.399994
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.71000004
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	74.3000031
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	84.0999985
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-13.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	13675	13675 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75999999	-3.75999999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	12.1999998	12.1999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	12.1999998	12.1999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.059999987	0.059999987 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	8.14400005	8.14400005 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.12 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6198.2998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	6149.16016
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	16200
TrqOviSta_HapticTime_Sec_M_f32	5225.33984
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	13.3000002
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.219999999
k_APAMaxHwTrq_HwNm_f32	0.879999995
k_APAMaxVehSpd_Kph_f32	19.7999992
k_ESCMaxVehSpd_Kph_f32	36.5
k_HapticAmplitude_MtrNm_f32	0.0659999996
k_HapticDuration_Sec_f32	0.165000007
k_HapticFreq_Hz_f32	8.65799999
k_HapticReactTime_Sec_f32	0.175999999
k_LKAMaxVehSpd_Kph_f32	20.8999996
k_LKAMinVehSpd_Kph_f32	19.25
k_MaxHapticReqTime_Sec_f32	0.180000007
k_SWARLimiter_HwDeg_f32	17.6000004
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	0.439999998
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	414.339996
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.05999994
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	89
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-11.6000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999	1011.23999 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	16200	16200 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001	-3.1500001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0659999996	0.0659999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	8.65799999	8.65799999 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7087.95996
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1099.07996
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	7018.8999
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	18725
TrqOviSta_HapticTime_Sec_M_f32	5710.16992
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53999996
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0284000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APANcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.230000004
k_APAMaxHwTrq_HwNm_f32	0.959999979
k_APAMaxVehSpd_Kph_f32	21.6000004
k_ESCMaxVehSpd_Kph_f32	38.9000015
k_HapticAmplitude_MtrNm_f32	0.071999969
k_HapticDuration_Sec_f32	0.180000007
k_HapticFreq_Hz_f32	9.17199993
k_HapticReacttime_Sec_f32	0.192000002
k_LKAMaxVehSpd_Kph_f32	22.7999992
k_LKAMinVehSpd_Kph_f32	21
k_MaxHapticReqTime_Sec_f32	0.194999993
k_SWARLimiter_HwDeg_f32	19
k_ShiftLevRevTime_Sec_f32	0.0489999987
k_StandstillThresh_Kph_f32	0.479999989
k_StandstillTime_Sec_f32	0.0489999987
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARequest_Cnt_lgc	target_TrqOviSta_Per1APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc	target_TrqOviSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1018.90997
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	419.279999
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.40999985
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	84.0999985
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	93.9000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.69999981		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	18725	18725 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	5710.16992	5710.16992 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53999996	-2.53999996 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996	14.3999996 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	14.3999996	14.3999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.071999969	0.071999969 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	9.17199993	9.17199993 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.14 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	2	
TrqOviSta_ESCState_State_M_enum	3	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7977.62012	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1186.92004	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	7888.64014	
TrqOviSta_HapticState_State_M_enum	2	
TrqOviSta_Hapticdur_Cnt_M_u16	21250	
TrqOviSta_HapticTime_Sec_M_f32	6195	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.92999995	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002	
TrqOviSta_LKAFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_u08	4	
TrqOviSta_LKASState_State_M_enum	1	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	15.5	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	
k_APAINcludeHaptic_Cnt_lgc	0	
k_APAMaxHwTrqTime_Sec_f32	0.239999995	
k_APAMaxHwTrq_HwNm_f32	1.03999996	
k_APAMaxVehSpd_Kph_f32	23.3999996	
k_ESCMaxVehSpd_Kph_f32	41.2999992	
k_HapticAmplitude_MtrNm_f32	0.0780000016	
k_HapticDuration_Sec_f32	0.194999993	
k_HapticFreq_Hz_f32	9.68599987	
k_HapticReactTime_Sec_f32	0.208000004	
k_LKAMaxVehSpd_Kph_f32	24.7000008	
k_LKAMinVehSpd_Kph_f32	22.75	
k_MaxHapticReqTime_Sec_f32	0.209999993	
k_SWARLimiter_HwDeg_f32	20.3999996	
k_ShiftLevRevTime_Sec_f32	0.0529999994	
k_StandstillThresh_Kph_f32	0.519999981	
k_StandstillTime_Sec_f32	0.0529999994	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1008.81		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	424.220001		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.76000023		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	89		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	98.8000031		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.80000019		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	21250	21250 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.92999995	-1.92999995 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0780000016	0.0780000016 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	9.68599987	9.68599987 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.15 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8867.28027
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1274.76001
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	8758.37988
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	23775
TrqOviSta_HapticTime_Sec_M_f32	6679.83008
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0328000002
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.25
k_APAMaxHwTrq_HwNm_f32	1.12
k_APAMaxVehSpd_Kph_f32	25.2000008
k_ESCMaxVehSpd_Kph_f32	43.7000008
k_HapticAmplitude_MtrNm_f32	0.0839999989
k_HapticDuration_Sec_f32	0.209999993
k_HapticFreq_Hz_f32	10.1999998
k_HapticReactTime_Sec_f32	0.224000007
k_LKAMaxVehSpd_Kph_f32	26.6000004
k_LKAMinVehSpd_Kph_f32	24.5
k_MaxHapticReqTime_Sec_f32	0.224999994
k_SWARLimiter_HwDeg_f32	21.7999992
k_ShiftLevRevTime_Sec_f32	0.057
k_StandstillThresh_Kph_f32	0.560000002
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enum	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-998.710022
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	429.160004
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.11000013
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103.699997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.90000001
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1274.76001	1274.76001 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	8758.37988	8758.37988	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	23775	23775 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	6679.83008	6679.83008 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005	-1.32000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004	16.6000004 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	16.6000004	16.6000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0839999989	0.0839999989 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	10.1999998	10.1999998 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.16 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	9756.94043
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1362.59998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	9628.12012
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	26300
TrqOviSta_HapticTime_Sec_M_f32	7164.66016
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.709999979
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0350000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	17.7000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.259999999
k_APAMaxHwTrq_HwNm_f32	1.20000005
k_APAMaxVehSpd_Kph_f32	27
k_ESCMaxVehSpd_Kph_f32	46.0999985
k_HapticAmplitude_MtrNm_f32	0.0900000036
k_HapticDuration_Sec_f32	0.224999994
k_HapticFreq_Hz_f32	10.7139997
k_HapticReactTime_Sec_f32	0.239999995
k_LKAMaxVehSpd_Kph_f32	28.5
k_LKAMinVehSpd_Kph_f32	26.25
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	23.2000008
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	0.600000024
k_StandstillTime_Sec_f32	0.0610000007
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-988.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	434.100006
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-4.30000019
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	98.8000031
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	108.599998
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	26300	26300 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.709999979	-0.709999979 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0900000036	0.0900000036 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	10.7139997	10.7139997 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13315.5801
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	13107.0801
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	36400
TrqOviSta_HapticTime_Sec_M_f32	9103.98047
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.300000012
k_APAMaxHwTrq_HwNm_f32	1.51999998
k_APAMaxVehSpd_Kph_f32	34.2000008
k_ESCMaxVehSpd_Kph_f32	55.7000008
k_HapticAmplitude_MtrNm_f32	0.114
k_HapticDuration_Sec_f32	0.284999996
k_HapticFreq_Hz_f32	12.7700005
k_HapticReacttime_Sec_f32	0.30399999
k_LKAMaxVehSpd_Kph_f32	36.0999985
k_LKAMinVehSpd_Kph_f32	33.25
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	28.7999992
k_ShiftLevRevTime_Sec_f32	0.0769999996
k_StandstillThresh_Kph_f32	0.75999999
k_StandstillTime_Sec_f32	0.0769999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-978.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	439.040009
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-10
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	118.400002
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.199997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.5999999		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996	1713.95996 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	36400	36400 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002	1.73000002 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.114	0.114 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	12.7700005	12.7700005 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.18 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	1	
TrqOviSta_ESCState_State_M_enum	0	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	14205.2402	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1801.80005	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	13976.8203	
TrqOviSta_HapticState_State_M_enum	1	
TrqOviSta_Hapticdur_Cnt_M_u16	38925	
TrqOviSta_HapticTime_Sec_M_f32	9588.80957	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001	
TrqOviSta_LKAFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_u08	1	
TrqOviSta_LKASState_State_M_enum	0	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	23.2000008	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	
k_APAINcludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.310000002	
k_APAMaxHwTrq_HwNm_f32	1.60000002	
k_APAMaxVehSpd_Kph_f32	36	
k_ESCMaxVehSpd_Kph_f32	58.0999985	
k_HapticAmplitude_MtrNm_f32	0.119999997	
k_HapticDuration_Sec_f32	0.300000012	
k_HapticFreq_Hz_f32	13.2840004	
k_HapticReactTime_Sec_f32	0.319999993	
k_LKAMaxVehSpd_Kph_f32	38	
k_LKAMinVehSpd_Kph_f32	35	
k_MaxHapticReqTime_Sec_f32	0.314999998	
k_SWARLimiter_HwDeg_f32	30.2000008	
k_ShiftLevRevTime_Sec_f32	0.0810000002	
k_StandstillThresh_Kph_f32	0.800000012	
k_StandstillTime_Sec_f32	0.0810000002	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-968.409973		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	443.980011		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	10		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	123.300003		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	133.100006		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1801.80005	1801.80005 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	38925	38925 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	9588.80957	9588.80957 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991	2.33999991 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	23.2000008	23.2000008 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	23.2000008	23.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.119999997	0.119999997 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	13.2840004	13.2840004 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15094.9004
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1889.64001
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	14846.5596
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	41450
TrqOvlSta_HapticTime_Sec_M_f32	10073.6396
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.95000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0482000001
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKASState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.319999993
k_APAMaxHwTrq_HwNm_f32	1.67999995
k_APAMaxVehSpd_Kph_f32	37.7999992
k_ESCMaxVehSpd_Kph_f32	60.5
k_HapticAmplitude_MtrNm_f32	0.126000002
k_HapticDuration_Sec_f32	0.314999998
k_HapticFreq_Hz_f32	13.7980003
k_HapticReactTime_Sec_f32	0.335999995
k_LKAMaxVehSpd_Kph_f32	39.9000015
k_LKAMinVehSpd_Kph_f32	36.75
k_MaxHapticReqTime_Sec_f32	0.330000013
k_SWARLimiter_HwDeg_f32	31.6000004
k_ShiftLevRevTime_Sec_f32	0.0850000009
k_StandstillThresh_Kph_f32	0.839999974
k_StandstillTime_Sec_f32	0.0850000009
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAResponse_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHoscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-958.309998
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	448.920013
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	128.199997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	138
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	7.4000001
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	41450	41450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	10073.6396	10073.6396 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.95000005	2.95000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.126000002	0.126000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	13.7980003	13.7980003 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.20 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15984.5596
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1977.47998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	15716.2998
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	43975
TrqOviSta_HapticTime_Sec_M_f32	10558.4697
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.55999994
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0504000001
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	25.3999996
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.330000013
k_APAMaxHwTrq_HwNm_f32	1.75999999
k_APAMaxVehSpd_Kph_f32	39.5999985
k_ESCMaxVehSpd_Kph_f32	62.9000015
k_HapticAmplitude_MtrNm_f32	0.131999999
k_HapticDuration_Sec_f32	0.330000013
k_HapticFreq_Hz_f32	14.3120003
k_HapticReactTime_Sec_f32	0.351999998
k_LKAMaxVehSpd_Kph_f32	41.7999992
k_LKAMinVehSpd_Kph_f32	38.5
k_MaxHapticReqTime_Sec_f32	0.344999999
k_SWARLimiter_HwDeg_f32	33
k_ShiftLevRevTime_Sec_f32	0.0890000015
k_StandstillThresh_Kph_f32	0.879999995
k_StandstillTime_Sec_f32	0.0890000015
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-948.210022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	453.859985
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.4000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	142.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	9.30000019
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15984.5596	15984.5596 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1977.47998	1977.47998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	15716.2998	15716.2998	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	43975	43975 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.60233593	3.60233593 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	25.3999996	25.3999996 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	25.3999996	25.3999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.131999999	0.131999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	14.3120003	14.3120003 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16874.2207
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	16586.0391
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	46500
TrqOviSta_HapticTime_Sec_M_f32	11043.2998
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	26.5

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.340000004
k_APAMaxHwTrq_HwNm_f32	1.84000003
k_APAMaxVehSpd_Kph_f32	41.4000015
k_ESCMaxVehSpd_Kph_f32	65.3000031
k_HapticAmplitude_MtrNm_f32	0.137999997
k_HapticDuration_Sec_f32	0.344999999
k_HapticFreq_Hz_f32	14.8260002
k_HapticReacttime_Sec_f32	0.368000001
k_LKAMaxVehSpd_Kph_f32	43.7000008
k_LKAMinVehSpd_Kph_f32	40.25
k_MaxHapticReqTime_Sec_f32	0.360000014
k_SWARLimiter_HwDeg_f32	34.4000015
k_ShiftLevRevTime_Sec_f32	0.0930000022
k_StandstillThresh_Kph_f32	0.920000017
k_StandstillTime_Sec_f32	0.0930000022
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-938.109985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	458.799988
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-4.4000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	138
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007	2065.32007 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	46500	46500 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008	4.17000008 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.137999997	0.137999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	14.8260002	14.8260002 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	17763.8809
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2153.15991
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	17455.7793
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	49025
TrqOviSta_HapticTime_Sec_M_f32	11528.1299
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.78000021
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.349999994
k_APAMaxHwTrq_HwNm_f32	1.91999996
k_APAMaxVehSpd_Kph_f32	43.2000008
k_ESCMaxVehSpd_Kph_f32	67.6999969
k_HapticAmplitude_MtrNm_f32	0.143999994
k_HapticDuration_Sec_f32	0.360000014
k_HapticFreq_Hz_f32	15.3400002
k_HapticReactTime_Sec_f32	0.384000003
k_LKAMaxVehSpd_Kph_f32	45.5999985
k_LKAMinVehSpd_Kph_f32	42
k_MaxHapticReqTime_Sec_f32	0.375
k_SWARLimiter_HwDeg_f32	35.7999992
k_ShiftLevRevTime_Sec_f32	0.0970000029
k_StandstillThresh_Kph_f32	0.959999979
k_StandstillTime_Sec_f32	0.0970000029
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResquest_Cnt_Igc	target_TrqOviSta_Per1_APAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_Igc	target_TrqOviSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAResquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	463.73999		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-2.5		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAResquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	152.699997		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	17763.8809	17763.8809 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	49025	49025 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	11528.1299	11528.1299 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.38105631	4.38105583 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004	27.6000004 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	27.6000004	27.6000004 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.143999994	0.143999994 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.3400002	15.3400002 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18653.5391
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2241
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18325.5195
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	51550
TrqOvlSta_HapticTime_Sec_M_f32	12012.96
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.360000014
k_APAMaxHwTrq_HwNm_f32	2
k_APAMaxVehSpd_Kph_f32	45
k_ESCMaxVehSpd_Kph_f32	70.0999985
k_HapticAmplitude_MtrNm_f32	0.150000006
k_HapticDuration_Sec_f32	0.375
k_HapticFreq_Hz_f32	15.8540001
k_HapticReacttime_Sec_f32	0.400000006
k_LKAMaxVehSpd_Kph_f32	47.5
k_LKAMinVehSpd_Kph_f32	43.75
k_MaxHapticReqTime_Sec_f32	0.389999986
k_SWARLimiter_HwDeg_f32	37.2000008
k_ShiftLevRevTime_Sec_f32	0.101000004
k_StandstillThresh_Kph_f32	1
k_StandstillTime_Sec_f32	0.101000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHoscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-917.909973
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	468.679993
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-2.29999995
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	147.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	51550	51550 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987	5.38999987 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	15.8540001	15.8540001 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	19543.1992
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2328.84009
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	19195.2598
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	54075
TrqOviSta_HapticTime_Sec_M_f32	12497.79
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.370000005
k_APAMaxHwTrq_HwNm_f32	2.07999992
k_APAMaxVehSpd_Kph_f32	46.7999992
k_ESCMaxVehSpd_Kph_f32	72.5
k_HapticAmplitude_MtrNm_f32	0.156000003
k_HapticDuration_Sec_f32	0.389999986
k_HapticFreq_Hz_f32	16.368
k_HapticReactTime_Sec_f32	0.416000009
k_LKAMaxVehSpd_Kph_f32	49.4000015
k_LKAMinVehSpd_Kph_f32	45.5
k_MaxHapticReqTime_Sec_f32	0.405000001
k_SWARLimiter_HwDeg_f32	38.5999985
k_ShiftLevRevTime_Sec_f32	0.104999997
k_StandstillThresh_Kph_f32	1.03999996
k_StandstillTime_Sec_f32	0.104999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-907.809998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	473.619995
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-2.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	152.699997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	162.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.8999996
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2328.84009	2328.84009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	54075	54075 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6	6 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992	29.7999992 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	29.7999992	29.7999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.156000003	0.156000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	16.368	16.368 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.25 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	20432.8594
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2416.67993
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	20065
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	56600
TrqOviSta_HapticTime_Sec_M_f32	12982.6201
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.61000013
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0614
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	30.8999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.379999995
k_APAMaxHwTrq_HwNm_f32	2.16000009
k_APAMaxVehSpd_Kph_f32	48.5999985
k_ESCMaxVehSpd_Kph_f32	74.9000015
k_HapticAmplitude_MtrNm_f32	0.162
k_HapticDuration_Sec_f32	0.405000001
k_HapticFreq_Hz_f32	16.882
k_HapticReacttime_Sec_f32	0.432000011
k_LKAMaxVehSpd_Kph_f32	51.2999992
k_LKAMinVehSpd_Kph_f32	47.25
k_MaxHapticReqTime_Sec_f32	0.419999987
k_SWARLimiter_HwDeg_f32	40
k_ShiftLevRevTime_Sec_f32	0.108999997
k_StandstillThresh_Kph_f32	1.08000004
k_StandstillTime_Sec_f32	0.108999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-897.710022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	478.559998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-1.89999998
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.7999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	20432.8594	20432.8594 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	20065	20065	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	56600	56600 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	12982.6201	12982.6201 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.08748627	6.08748579 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.162	0.162 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	16.882	16.882 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.26 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	1	
TrqOviSta_ESCState_State_M_enum	3	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21322.5195	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2504.52002	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	20934.7402	
TrqOviSta_HapticState_State_M_enum	2	
TrqOviSta_Hapticdur_Cnt_M_u16	59125	
TrqOviSta_HapticTime_Sec_M_f32	13467.4502	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.21999979	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0636000037	
TrqOviSta_LKAFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_u08	1	
TrqOviSta_LKASState_State_M_enum	1	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	32	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.389999986	
k_APAMaxHwTrq_HwNm_f32	2.24000001	
k_APAMaxVehSpd_Kph_f32	50.4000015	
k_ESCMaxVehSpd_Kph_f32	77.3000031	
k_HapticAmplitude_MtrNm_f32	0.167999998	
k_HapticDuration_Sec_f32	0.419999987	
k_HapticFreq_Hz_f32	17.3959999	
k_HapticReactTime_Sec_f32	0.448000014	
k_LKAMaxVehSpd_Kph_f32	53.2000008	
k_LKAMinVehSpd_Kph_f32	49	
k_MaxHapticReqTime_Sec_f32	0.435000002	
k_SWARLimiter_HwDeg_f32	41.4000015	
k_ShiftLevRevTime_Sec_f32	0.112999998	
k_StandstillThresh_Kph_f32	1.12	
k_StandstillTime_Sec_f32	0.112999998	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	483.5		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-1.70000005		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	172.300003		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.7000008		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	59125	59125 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.21999979	7.21999979 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	32	32 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	32	32 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.167999998	0.167999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	17.3959999	17.3959999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	22212.1797
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2592.36011
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	21804.4805
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	61650
TrqOvlSta_HapticTime_Sec_M_f32	13952.2803
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.82999992
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0658000037
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKASState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	33.0999985
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.400000006
k_APAMaxHwTrq_HwNm_f32	2.31999993
k_APAMaxVehSpd_Kph_f32	52.2000008
k_ESCMaxVehSpd_Kph_f32	79.6999969
k_HapticAmplitude_MtrNm_f32	0.173999995
k_HapticDuration_Sec_f32	0.435000002
k_HapticFreq_Hz_f32	17.9099998
k_HapticReactTime_Sec_f32	0.463999987
k_LKAMaxVehSpd_Kph_f32	55.0999985
k_LKAMinVehSpd_Kph_f32	50.75
k_MaxHapticReqTime_Sec_f32	0.449999988
k_SWARLimiter_HwDeg_f32	42.7999992
k_ShiftLevRevTime_Sec_f32	0.116999999
k_StandstillThresh_Kph_f32	1.15999997
k_StandstillTime_Sec_f32	0.116999999
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-877.51001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	488.440002
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-1.5
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	167.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	177.199997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.6000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2592.36011	2592.36011 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	61650	61650 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	13952.2803	13952.2803 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.82999992	7.82999992 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.173999995	0.173999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	17.9099998	17.9099998 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.28 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	23101.8398
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2680.19995
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	22674.2207
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	64175
TrqOviSta_HapticTime_Sec_M_f32	14437.1104
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.43999958
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0680000037
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.409999996
k_APAMaxHwTrq_HwNm_f32	2.4000001
k_APAMaxVehSpd_Kph_f32	54
k_ESCMaxVehSpd_Kph_f32	82.0999985
k_HapticAmplitude_MtrNm_f32	0.180000007
k_HapticDuration_Sec_f32	0.449999988
k_HapticFreq_Hz_f32	18.4239998
k_HapticReactTime_Sec_f32	0.479999989
k_LKAMaxVehSpd_Kph_f32	57
k_LKAMinVehSpd_Kph_f32	52.5
k_MaxHapticReqTime_Sec_f32	0.465000004
k_SWARLimiter_HwDeg_f32	44.2000008
k_ShiftLevRevTime_Sec_f32	0.120999999
k_StandstillThresh_Kph_f32	1.20000005
k_StandstillTime_Sec_f32	0.120999999
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-867.409973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	493.380005
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-1.29999995
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	172.300003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	182.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	23101.8398	23101.8398 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	64175	64175 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.77767944	7.77767992 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008	34.2000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	34.2000008	34.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	18.4239998	18.4239998 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.29 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	23991.5
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2768.04004
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	23543.9609
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	2450
TrqOviSta_HapticTime_Sec_M_f32	14921.9404
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0702000037
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	35.2999992

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.419999987
k_APAMaxHwTrq_HwNm_f32	2.48000002
k_APAMaxVehSpd_Kph_f32	55.7999992
k_ESCMaxVehSpd_Kph_f32	84.5
k_HapticAmplitude_MtrNm_f32	0.186000004
k_HapticDuration_Sec_f32	0.465000004
k_HapticFreq_Hz_f32	18.9379997
k_HapticReacttime_Sec_f32	0.495999992
k_LKAMaxVehSpd_Kph_f32	58.9000015
k_LKAMinVehSpd_Kph_f32	54.25
k_MaxHapticReqTime_Sec_f32	0.479999989
k_SWARLimiter_HwDeg_f32	45.5999985
k_ShiftLevRevTime_Sec_f32	0.125
k_StandstillThresh_Kph_f32	1.24000001
k_StandstillTime_Sec_f32	0.125
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-857.309998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	498.320007
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-1.10000002
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	177.199997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	187
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.3999996		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2768.04004	2768.04004 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	2450	2450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019	9.05000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.186000004	0.186000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	18.9379997	18.9379997 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	24881.1602
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2855.87988
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	24413.6992
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	8460
TrqOviSta_HapticTime_Sec_M_f32	15406.7695
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.65999985
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0724000037
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	36.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.430000007
k_APAMaxHwTrq_HwNm_f32	2.55999994
k_APAMaxVehSpd_Kph_f32	57.5999985
k_ESCMaxVehSpd_Kph_f32	86.9000015
k_HapticAmplitude_MtrNm_f32	0.192000002
k_HapticDuration_Sec_f32	0.479999989
k_HapticFreq_Hz_f32	19.4519997
k_HapticReactTime_Sec_f32	0.512000024
k_LKAMaxVehSpd_Kph_f32	60.7999992
k_LKAMinVehSpd_Kph_f32	56
k_MaxHapticReqTime_Sec_f32	0.495000005
k_SWARLimiter_HwDeg_f32	47
k_ShiftLevRevTime_Sec_f32	0.128999993
k_StandstillThresh_Kph_f32	1.27999997
k_StandstillTime_Sec_f32	0.128999993
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-847.210022		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	503.26001		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-0.899999976		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	191.899994		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	28.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	24413.6992	24413.6992	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	8460	8460 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	15406.7695	15406.7695 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.65999985	9.65999985 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	36.4000015	36.4000015 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	36.4000015	36.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.192000002	0.192000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	19.4519997	19.4519997 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.31 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	25770.8203
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2943.71997
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	25283.4395
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	14470
TrqOviSta_HapticTime_Sec_M_f32	15891.5996
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.97999954
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0746000037
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	37.5
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.439999998
k_APAMaxHwTrq_HwNm_f32	2.6400001
k_APAMaxVehSpd_Kph_f32	59.4000015
k_ESCMaxVehSpd_Kph_f32	89.3000031
k_HapticAmplitude_MtrNm_f32	0.197999999
k_HapticDuration_Sec_f32	0.495000005
k_HapticFreq_Hz_f32	19.9659996
k_HapticReactTime_Sec_f32	0.527999997
k_LKAMaxVehSpd_Kph_f32	62.7000008
k_LKAMinVehSpd_Kph_f32	57.75
k_MaxHapticReqTime_Sec_f32	0.50999999
k_SWARLimiter_HwDeg_f32	48.4000015
k_ShiftLevRevTime_Sec_f32	0.133000001
k_StandstillThresh_Kph_f32	1.32000005
k_StandstillTime_Sec_f32	0.133000001
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-837.109985
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	508.200012
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-0.699999988
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	187
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	196.800003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.2000008
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	25770.8203	25770.8203 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	14470	14470 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	15891.5996	15891.5996 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.18327141	9.18327236 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.197999999	0.197999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	19.9659996	19.9659996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.32 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	26660.4805
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3031.56006
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	26153.1797
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	20480
TrqOviSta_HapticTime_Sec_M_f32	16376.4297
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0768000036
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.5999985
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.449999988
k_APAMaxHwTrq_HwNm_f32	2.72000003
k_APAMaxVehSpd_Kph_f32	61.2000008
k_ESCMaxVehSpd_Kph_f32	91.6999969
k_HapticAmplitude_MtrNm_f32	0.203999996
k_HapticDuration_Sec_f32	0.509999999
k_HapticFreq_Hz_f32	20.4799995
k_HapticReactTime_Sec_f32	0.54400003
k_LKAMaxVehSpd_Kph_f32	64.5999985
k_LKAMinVehSpd_Kph_f32	59.5
k_MaxHapticReqTime_Sec_f32	0.524999976
k_SWARLimiter_HwDeg_f32	49.7999992
k_ShiftLevRevTime_Sec_f32	0.136999995
k_StandstillThresh_Kph_f32	1.36000001
k_StandstillTime_Sec_f32	0.136999995
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-827.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	513.140015
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-0.5
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	191.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	201.699997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	32.0999985
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3031.56006	3031.56006 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	20480	20480 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9	-9 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.5999985	38.5999985 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	38.5999985	38.5999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.203999996	0.203999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	20.4799995	20.4799995 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.33 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	27550.1406
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3119.3999
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	27022.9199
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	26490
TrqOviSta_HapticTime_Sec_M_f32	16861.2598
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0790000036
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	39.7000008

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.460000008
k_APAMaxHwTrq_HwNm_f32	2.79999995
k_APAMaxVehSpd_Kph_f32	63
k_ESCMaxVehSpd_Kph_f32	94.0999985
k_HapticAmplitude_MtrNm_f32	0.209999993
k_HapticDuration_Sec_f32	0.524999976
k_HapticFreq_Hz_f32	20.9939995
k_HapticReacttime_Sec_f32	0.560000002
k_LKAMaxVehSpd_Kph_f32	66.5
k_LKAMinVehSpd_Kph_f32	61.25
k_MaxHapticReqTime_Sec_f32	0.540000021
k_SWARLimiter_HwDeg_f32	51.2000008
k_ShiftLevRevTime_Sec_f32	0.141000003
k_StandstillThresh_Kph_f32	1.39999998
k_StandstillTime_Sec_f32	0.141000003
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-816.909973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	518.080017
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-0.300000012
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	196.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	206.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3119.3999	3119.3999 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	26490	26490 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	16861.2598	16861.2598 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.5	0.5 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.209999993	0.209999993 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	20.9939995	20.9939995 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	28439.8008
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3207.23999
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	27892.6602
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	32500
TrqOviSta_HapticTime_Sec_M_f32	17346.0898
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0812000036
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	40.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.469999999
k_APAMaxHwTrq_HwNm_f32	2.88000011
k_APAMaxVehSpd_Kph_f32	64.8000031
k_ESCMaxVehSpd_Kph_f32	96.5
k_HapticAmplitude_MtrNm_f32	0.216000006
k_HapticDuration_Sec_f32	0.540000021
k_HapticFreq_Hz_f32	21.5079994
k_HapticReactTime_Sec_f32	0.575999975
k_LKAMaxVehSpd_Kph_f32	68.4000015
k_LKAMinVehSpd_Kph_f32	63
k_MaxHapticReqTime_Sec_f32	0.555000007
k_SWARLimiter_HwDeg_f32	52.5999985
k_ShiftLevRevTime_Sec_f32	0.144999996
k_StandstillThresh_Kph_f32	1.44000006
k_StandstillTime_Sec_f32	0.144999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-806.809998		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	523.02002		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-0.100000001		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	201.699997		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	211.5		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-33.0999985		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	32500	32500 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	17346.0898	17346.0898 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015	6.34000015 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	40.7999992	40.7999992 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	40.7999992	40.7999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.216000006	0.216000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	21.5079994	21.5079994 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.35 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	29329.4609
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3295.08008
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	28762.4004
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	38510
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.86000013
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0834000036
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	41.9000015
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.479999989
k_APAMaxHwTrq_HwNm_f32	2.960000004
k_APAMaxVehSpd_Kph_f32	66.5999985
k_ESCMaxVehSpd_Kph_f32	98.9000015
k_HapticAmplitude_MtrNm_f32	0.222000003
k_HapticDuration_Sec_f32	0.555000007
k_HapticFreq_Hz_f32	22.0219994
k_HapticReactTime_Sec_f32	0.592000008
k_LKAMaxVehSpd_Kph_f32	70.3000031
k_LKAMinVehSpd_Kph_f32	64.75
k_MaxHapticReqTime_Sec_f32	0.569999993
k_SWARLimiter_HwDeg_f32	54
k_ShiftLevRevTime_Sec_f32	0.149000004
k_StandstillThresh_Kph_f32	1.48000002
k_StandstillTime_Sec_f32	0.149000004
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-796.710022
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	527.960022
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.100000001
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	216.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-31.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3295.08008	3295.08008 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	28762.4004	28762.4004	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	38510	38510 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.86000013	-5.86000013 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.222000003	0.222000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	22.0219994	22.0219994 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.36 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	30219.1191
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3382.91992
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	29632.1406
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	44520
TrqOviSta_HapticTime_Sec_M_f32	4.28999987e+009
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.649999976
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.49000001
k_APAMaxHwTrq_HwNm_f32	3.03999996
k_APAMaxVehSpd_Kph_f32	68.4000015
k_ESCMaxVehSpd_Kph_f32	101.300003
k_HapticAmplitude_MtrNm_f32	0.228
k_HapticDuration_Sec_f32	0.569999993
k_HapticFreq_Hz_f32	22.5359993
k_HapticReactime_Sec_f32	0.60799998
k_LKAMaxVehSpd_Kph_f32	72.1999969
k_LKAMinVehSpd_Kph_f32	66.5
k_MaxHapticReqTime_Sec_f32	0.584999979
k_SWARLimiter_HwDeg_f32	55.4000015
k_ShiftLevRevTime_Sec_f32	0.152999997
k_StandstillThresh_Kph_f32	1.51999998
k_StandstillTime_Sec_f32	0.152999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-786.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	532.900024
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.300000012
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	211.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	221.300003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-29.8999996
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3382.91992	3382.91992 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	44520	44520 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	4.28999987e+009	4.28999987e+009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.649999976	0.649999976 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43	43 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	43	43 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.228	0.228 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	22.5359993	22.5359993 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.37 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31108.7793
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3470.76001
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	30501.8809
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	50530
TrqOviSta_HapticTime_Sec_M_f32	35000.5391
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	44.0999985

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.5
k_APAMaxHwTrq_HwNm_f32	3.11999989
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	103.699997
k_HapticAmplitude_MtrNm_f32	0.233999997
k_HapticDuration_Sec_f32	0.584999979
k_HapticFreq_Hz_f32	23.0499992
k_HapticReacttime_Sec_f32	0.624000013
k_LKAMaxVehSpd_Kph_f32	74.0999985
k_LKAMinVehSpd_Kph_f32	68.25
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	56.7999992
k_ShiftLevRevTime_Sec_f32	0.157000005
k_StandstillThresh_Kph_f32	1.55999994
k_StandstillTime_Sec_f32	0.157000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-776.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	537.840027
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.5
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	216.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	226.199997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	50530	50530 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	1.21000004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.233999997	0.233999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	23.0499992	23.0499992 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31998.4395
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	31371.6191
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	3240
TrqOviSta_HapticTime_Sec_M_f32	18460.6309
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.8900001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0340000018
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.50999999
k_APAMaxHwTrq_HwNm_f32	3.3599999
k_APAMaxVehSpd_Kph_f32	75.5999985
k_ESCMaxVehSpd_Kph_f32	106.099998
k_HapticAmplitude_MtrNm_f32	0.252000004
k_HapticDuration_Sec_f32	0.629999995
k_HapticFreq_Hz_f32	23.5639992
k_HapticReactTime_Sec_f32	0.671999991
k_LKAMaxVehSpd_Kph_f32	79.8000031
k_LKAMinVehSpd_Kph_f32	73.5
k_MaxHapticReqTime_Sec_f32	0.644999981
k_SWARLimiter_HwDeg_f32	58.2000008
k_ShiftLevRevTime_Sec_f32	0.169
k_StandstillThresh_Kph_f32	1.67999995
k_StandstillTime_Sec_f32	0.169
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-766.409973		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	542.780029		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.10000002		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	221.300003		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.100006		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7000008		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	3240	3240 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	18460.6309	18460.6309 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.82914019	2.82913995 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008	45.2000008 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	45.2000008	45.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.252000004	0.252000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	23.5639992	23.5639992 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.39 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	32888.1016
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	32241.3594
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	46
TrqOviSta_HapticTime_Sec_M_f32	21465
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.45000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0450000018
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	46.2999992
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.519999981
k_APAMaxHwTrq_HwNm_f32	3.44000006
k_APAMaxVehSpd_Kph_f32	77.4000015
k_ESCMaxVehSpd_Kph_f32	108.5
k_HapticAmplitude_MtrNm_f32	0.257999986
k_HapticDuration_Sec_f32	0.644999981
k_HapticFreq_Hz_f32	24.0779991
k_HapticReactime_Sec_f32	0.688000023
k_LKAMaxVehSpd_Kph_f32	81.6999969
k_LKAMinVehSpd_Kph_f32	75.25
k_MaxHapticReqTime_Sec_f32	0.660000026
k_SWARLimiter_HwDeg_f32	59.5999985
k_ShiftLevRevTime_Sec_f32	0.172999993
k_StandstillThresh_Kph_f32	1.72000003
k_StandstillTime_Sec_f32	0.172999993
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResponse_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-756.309998
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	547.719971
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.29999995
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	226.199997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	236
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.1000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	46	46 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.45000005	3.45000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.257999986	0.257999986 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	24.0779991	24.0779991 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.40 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	33777.7617
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	40350.6602
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	33111.1016
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	1352
TrqOviSta_HapticTime_Sec_M_f32	24470
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.01000023
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0560000017
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	47.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.529999971
k_APAMaxHwTrq_HwNm_f32	3.51999998
k_APAMaxVehSpd_Kph_f32	79.1999969
k_ESCMaxVehSpd_Kph_f32	110.900002
k_HapticAmplitude_MtrNm_f32	0.263999999
k_HapticDuration_Sec_f32	0.660000026
k_HapticFreq_Hz_f32	24.5919991
k_HapticReactTime_Sec_f32	0.703999996
k_LKAMaxVehSpd_Kph_f32	83.5999985
k_LKAMinVehSpd_Kph_f32	77
k_MaxHapticReqTime_Sec_f32	0.675000012
k_SWARLimiter_HwDeg_f32	1.29999995
k_ShiftLevRevTime_Sec_f32	0.177000001
k_StandstillThresh_Kph_f32	1.75999999
k_StandstillTime_Sec_f32	0.177000001
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-746.210022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	552.659973
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.5
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	240.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	33111.1016	33111.1016	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	1352	1352 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	24470	24470 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.01000023	4.01000023 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	47.4000015	47.4000015 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	47.4000015	47.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.263999999	0.263999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	24.5919991	24.5919991 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.41 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	5270
TrqOviSta_HapticTime_Sec_M_f32	27475.5293
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	48.5

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.540000021
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	113.300003
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	25.1060009
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	1.60000002
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	1.88
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-736.109985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	557.599976
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	236
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	245.800003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-21.8999996		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	27475.5293	27475.5293 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	25.1060009	25.1060009 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.42 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	35557.0781
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	8620
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4.2949673e+009
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	6576
TrqOviSta_HapticTime_Sec_M_f32	30480
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.25
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.100000001
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	49.5999985
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.550000012
k_APAMaxHwTrq_HwNm_f32	3.83999991
k_APAMaxVehSpd_Kph_f32	86.4000015
k_ESCMaxVehSpd_Kph_f32	115.699997
k_HapticAmplitude_MtrNm_f32	0.287999988
k_HapticDuration_Sec_f32	0.720000029
k_HapticFreq_Hz_f32	25.6200008
k_HapticReactTime_Sec_f32	0.768000007
k_LKAMaxVehSpd_Kph_f32	91.1999969
k_LKAMinVehSpd_Kph_f32	84
k_MaxHapticReqTime_Sec_f32	0.735000014
k_SWARLimiter_HwDeg_f32	1.89999998
k_ShiftLevRevTime_Sec_f32	0.193000004
k_StandstillThresh_Kph_f32	1.91999996
k_StandstillTime_Sec_f32	0.193000004
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-726.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	562.539978		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.29999995		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	240.899994		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	250.699997		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-20.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	6576	6576 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	30480	30480 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.25	6.25 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	49.5999985	49.5999985 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	49.5999985	49.5999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.287999988	0.287999988 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	25.6200008	25.6200008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.43 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36446.7383
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	11790
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	76432.1094
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	7882
TrqOviSta_HapticTime_Sec_M_f32	33485
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.80999994
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.111000001
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	50.7000008
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.560000002
k_APAMaxHwTrq_HwNm_f32	3.92000008
k_APAMaxVehSpd_Kph_f32	88.1999969
k_ESCMaxVehSpd_Kph_f32	118.099998
k_HapticAmplitude_MtrNm_f32	0.294
k_HapticDuration_Sec_f32	0.735000014
k_HapticFreq_Hz_f32	26.1340008
k_HapticReacttime_Sec_f32	0.783999979
k_LKAMaxVehSpd_Kph_f32	93.0999985
k_LKAMinVehSpd_Kph_f32	85.75
k_MaxHapticReqTime_Sec_f32	0.75
k_SWARLimiter_HwDeg_f32	2.20000005
k_ShiftLevRevTime_Sec_f32	0.196999997
k_StandstillThresh_Kph_f32	1.96000004
k_StandstillTime_Sec_f32	0.196999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResponse_Cnt_Igc	target_TrqOviSta_Per1_APAResponse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-715.909973
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	567.47998
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.5
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	245.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	255.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-18.7000008
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36446.7383	36446.7383 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	11790	11790 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	7882	7882 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.3315897	6.33159018 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.294	0.294 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	26.1340008	26.1340008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.44 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	14960
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	12024
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11800
TrqOviSta_HapticTime_Sec_M_f32	36490.3516
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.48999977
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.143999994
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	51.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.569999993
k_APAMaxHwTrq_HwNm_f32	0.100000001
k_APAMaxVehSpd_Kph_f32	93.5999985
k_ESCMaxVehSpd_Kph_f32	120.5
k_HapticAmplitude_MtrNm_f32	0.312000006
k_HapticDuration_Sec_f32	0.779999971
k_HapticFreq_Hz_f32	26.6480007
k_HapticReactTime_Sec_f32	0.832000017
k_LKAMaxVehSpd_Kph_f32	98.8000031
k_LKAMinVehSpd_Kph_f32	91
k_MaxHapticReqTime_Sec_f32	0.795000017
k_SWARLimiter_HwDeg_f32	2.5
k_ShiftLevRevTime_Sec_f32	0.209000006
k_StandstillThresh_Kph_f32	2.07999992
k_StandstillTime_Sec_f32	0.209000006
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-705.809998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	572.419983
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	250.699997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	260.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.1000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	14960	14960 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	11800	11800 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.71384001	7.71384001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	51.7999992	51.7999992 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	51.7999992	51.7999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.312000006	0.312000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	26.6480007	26.6480007 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.45 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	18130
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	18570
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	13106
TrqOviSta_HapticTime_Sec_M_f32	39495
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	52.9000015

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.579999983
k_APAMaxHwTrq_HwNm_f32	0.5
k_APAMaxVehSpd_Kph_f32	95.4000015
k_ESCMaxVehSpd_Kph_f32	122.900002
k_HapticAmplitude_MtrNm_f32	0.317999989
k_HapticDuration_Sec_f32	0.795000017
k_HapticFreq_Hz_f32	27.1620007
k_HapticReacttime_Sec_f32	0.84799999
k_LKAMaxVehSpd_Kph_f32	100.699997
k_LKAMinVehSpd_Kph_f32	92.75
k_MaxHapticReqTime_Sec_f32	0.810000002
k_SWARLimiter_HwDeg_f32	2.79999995
k_ShiftLevRevTime_Sec_f32	0.213
k_StandstillThresh_Kph_f32	2.11999989
k_StandstillTime_Sec_f32	0.213
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-695.710022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	577.359985
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.29999995
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	265.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	18130	18130 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	18570	18570	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	13106	13106 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.15875053	8.15874958 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.317999989	0.317999989 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	27.1620007	27.1620007 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.46 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	684235.25
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	21300
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	25116
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	14412
TrqOviSta_HapticTime_Sec_M_f32	42500
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.609999966
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.165999994
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	54
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.589999974
k_APAMaxHwTrq_HwNm_f32	7
k_APAMaxVehSpd_Kph_f32	97.1999969
k_ESCMaxVehSpd_Kph_f32	125.300003
k_HapticAmplitude_MtrNm_f32	0.324000001
k_HapticDuration_Sec_f32	0.810000002
k_HapticFreq_Hz_f32	27.6760006
k_HapticReactTime_Sec_f32	0.864000022
k_LKAMaxVehSpd_Kph_f32	102.599998
k_LKAMinVehSpd_Kph_f32	94.5
k_MaxHapticReqTime_Sec_f32	0.824999988
k_SWARLimiter_HwDeg_f32	3.0999999
k_ShiftLevRevTime_Sec_f32	0.216999993
k_StandstillThresh_Kph_f32	2.16000009
k_StandstillTime_Sec_f32	0.216999993
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-685.609985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	582.299988		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.5		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	260.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	270.299988		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-13.8999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	684235.25	684235.25 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	14412	14412 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	42500	42500 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.59573936	8.59574032 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	54	54 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	54	54 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.324000001	0.324000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	27.6760006	27.6760006 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.47 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	152460
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	24470
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	31662
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	18330
TrqOviSta_HapticTime_Sec_M_f32	45505
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.199000001
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	55.0999985
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.600000024
k_APAMaxHwTrq_HwNm_f32	4.55999994
k_APAMaxVehSpd_Kph_f32	102.599998
k_ESCMaxVehSpd_Kph_f32	127.699997
k_HapticAmplitude_MtrNm_f32	0.342000008
k_HapticDuration_Sec_f32	0.855000019
k_HapticFreq_Hz_f32	28.1900005
k_HapticReactTime_Sec_f32	0.912
k_LKAMaxVehSpd_Kph_f32	108.300003
k_LKAMinVehSpd_Kph_f32	99.75
k_MaxHapticReqTime_Sec_f32	0.870000005
k_SWARLimiter_HwDeg_f32	3.4000001
k_ShiftLevRevTime_Sec_f32	0.229000002
k_StandstillThresh_Kph_f32	2.27999997
k_StandstillTime_Sec_f32	0.229000002
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-675.51001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	587.23999
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	265.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	275.200012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-12.3000002
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	152460	152460 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	18330	18330 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	45505	45505 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.1940999	-7.1940999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.342000008	0.342000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	28.1900005	28.1900005 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.48 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	186423
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	27640
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	38208
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	19636
TrqOviSta_HapticTime_Sec_M_f32	48510.3398
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.209999993
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	56.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.610000014
k_APAMaxHwTrq_HwNm_f32	4.63999987
k_APAMaxVehSpd_Kph_f32	104.400002
k_ESCMaxVehSpd_Kph_f32	130.100006
k_HapticAmplitude_MtrNm_f32	0.34799999
k_HapticDuration_Sec_f32	0.870000005
k_HapticFreq_Hz_f32	28.7040005
k_HapticReactTime_Sec_f32	0.927999973
k_LKAMaxVehSpd_Kph_f32	110.199997
k_LKAMinVehSpd_Kph_f32	101.5
k_MaxHapticReqTime_Sec_f32	0.88499999
k_SWARLimiter_HwDeg_f32	3.70000005
k_ShiftLevRevTime_Sec_f32	0.232999995
k_StandstillThresh_Kph_f32	2.31999993
k_StandstillTime_Sec_f32	0.232999995
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-665.409973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	592.179993
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.30000019
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	270.299988
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	280.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-10.6999998
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	186423	186423 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	19636	19636 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.80300045	8.80300045 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	56.2000008	56.2000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	56.2000008	56.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.34799999	0.34799999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	28.7040005	28.7040005 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.49 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	220386
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	30810
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	44754
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	20942
TrqOviSta_HapticTime_Sec_M_f32	51515
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.221000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	57.2999992

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.620000005
k_APAMaxHwTrq_HwNm_f32	4.71999979
k_APAMaxVehSpd_Kph_f32	106.199997
k_ESCMaxVehSpd_Kph_f32	132.5
k_HapticAmplitude_MtrNm_f32	0.354000002
k_HapticDuration_Sec_f32	0.88499999
k_HapticFreq_Hz_f32	29.2180004
k_HapticReacttime_Sec_f32	0.944000006
k_LKAMaxVehSpd_Kph_f32	112.099998
k_LKAMinVehSpd_Kph_f32	103.25
k_MaxHapticReqTime_Sec_f32	0.899999976
k_SWARLimiter_HwDeg_f32	4
k_ShiftLevRevTime_Sec_f32	0.237000003
k_StandstillThresh_Kph_f32	2.3599999
k_StandstillTime_Sec_f32	0.237000003
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-655.309998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	597.119995
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.5
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	275.200012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	285
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.10000038		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	30810	30810 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	20942	20942 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	51515	51515 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.994499981	0.994499981 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.354000002	0.354000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	29.2180004	29.2180004 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.50 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	254349
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	33980
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	51300
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	22248
TrqOviSta_HapticTime_Sec_M_f32	54520
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.231999993
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	58.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.629999995
k_APAMaxHwTrq_HwNm_f32	4.80000019
k_APAMaxVehSpd_Kph_f32	108
k_ESCMaxVehSpd_Kph_f32	134.899994
k_HapticAmplitude_MtrNm_f32	0.360000014
k_HapticDuration_Sec_f32	0.899999976
k_HapticFreq_Hz_f32	29.7320004
k_HapticReactTime_Sec_f32	0.959999979
k_LKAMaxVehSpd_Kph_f32	114
k_LKAMinVehSpd_Kph_f32	105
k_MaxHapticReqTime_Sec_f32	0.915000021
k_SWARLimiter_HwDeg_f32	4.30000019
k_ShiftLevRevTime_Sec_f32	0.240999997
k_StandstillThresh_Kph_f32	2.4000001
k_StandstillTime_Sec_f32	0.240999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAResquest_Cnt_Igc	target_TrqOvlSta_Per1_APAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAResquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngResquest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngResquest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAResquest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-645.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	602.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_LKAResquest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	289.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngResquest_HwDeg_f32.value	-7.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APASState_State_M_enum	4	4	✔
TrqOvlSta_ESCState_State_M_enum	4	4	✔
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	254349	254349 ± 1	✔
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	51300	51300	✔
TrqOvlSta_HapticState_State_M_enum	0	0	✔
TrqOvlSta_Hapticdur_Cnt_M_u16	22248	22248 ± 1	✔
TrqOvlSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.95951986	5.95951986 ± 0.000009	✔
TrqOvlSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOvlSta_LKASState_State_M_enum	4	4	✔
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	58.4000015	58.4000015 ± 0.0009765625	✔
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	58.4000015	58.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.360000014	0.360000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	29.7320004	29.7320004 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.51 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	288312
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	37150
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	57846
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	23554
TrqOviSta_HapticTime_Sec_M_f32	57525.3203
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.86000013
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	59.5
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.639999986
k_APAMaxHwTrq_HwNm_f32	4.88000011
k_APAMaxVehSpd_Kph_f32	109.800003
k_ESCMaxVehSpd_Kph_f32	137.300003
k_HapticAmplitude_MtrNm_f32	0.365999997
k_HapticDuration_Sec_f32	0.915000021
k_HapticFreq_Hz_f32	30.2460003
k_HapticReactTime_Sec_f32	0.976000011
k_LKAMaxVehSpd_Kph_f32	115.900002
k_LKAMinVehSpd_Kph_f32	106.75
k_MaxHapticReqTime_Sec_f32	0.930000007
k_SWARLimiter_HwDeg_f32	4.5999999
k_ShiftLevRevTime_Sec_f32	0.245000005
k_StandstillThresh_Kph_f32	2.44000006
k_StandstillTime_Sec_f32	0.245000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-635.109985
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	607
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	285
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	294.799988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	37150	37150 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	23554	23554 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_Str.SV_Uls_f32	-3.24532008	-3.24532008 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.365999997	0.365999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	30.2460003	30.2460003 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.52 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	322275
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	40320
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	64392
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	24860
TrqOviSta_HapticTime_Sec_M_f32	60530
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.9000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	60.5999985
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.649999976
k_APAMaxHwTrq_HwNm_f32	4.96000004
k_APAMaxVehSpd_Kph_f32	111.599998
k_ESCMaxVehSpd_Kph_f32	139.699997
k_HapticAmplitude_MtrNm_f32	0.372000009
k_HapticDuration_Sec_f32	0.930000007
k_HapticFreq_Hz_f32	30.7600002
k_HapticReactTime_Sec_f32	0.991999984
k_LKAMaxVehSpd_Kph_f32	117.800003
k_LKAMinVehSpd_Kph_f32	108.5
k_MaxHapticReqTime_Sec_f32	0.944999993
k_SWARLimiter_HwDeg_f32	4.9000001
k_ShiftLevRevTime_Sec_f32	0.248999998
k_StandstillThresh_Kph_f32	2.48000002
k_StandstillTime_Sec_f32	0.248999998
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-625.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	611.940002
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	289.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	299.700012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4.30000019
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	322275	322275 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	24860	24860 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	60530	60530 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.88493013	-6.88492966 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	60.5999985	60.5999985 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	60.5999985	60.5999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.372000009	0.372000009 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	30.7600002	30.7600002 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.53 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	356238
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	43490
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	70938
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	26166
TrqOviSta_HapticTime_Sec_M_f32	63535
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.55000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	61.7000008

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.660000026
k_APAMaxHwTrq_HwNm_f32	5.03999996
k_APAMaxVehSpd_Kph_f32	113.400002
k_ESCMaxVehSpd_Kph_f32	142.100006
k_HapticAmplitude_MtrNm_f32	0.377999991
k_HapticDuration_Sec_f32	0.944999993
k_HapticFreq_Hz_f32	31.2740002
k_HapticReacttime_Sec_f32	1.00800002
k_LKAMaxVehSpd_Kph_f32	119.699997
k_LKAMinVehSpd_Kph_f32	110.25
k_MaxHapticReqTime_Sec_f32	0.959999979
k_SWARLimiter_HwDeg_f32	5.19999981
k_ShiftLevRevTime_Sec_f32	0.252999991
k_StandstillThresh_Kph_f32	2.51999998
k_StandstillTime_Sec_f32	0.252999991
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-614.909973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	616.880005
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.30000019
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	294.799988
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	304.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.70000005		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	26166	26166 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	63535	63535 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.92737675	1.92737687 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.377999991	0.377999991 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	31.2740002	31.2740002 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.54 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	3	
TrqOviSta_ESCState_State_M_enum	1	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	390201	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	46660	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	77484	
TrqOviSta_HapticState_State_M_enum	0	
TrqOviSta_Hapticdur_Cnt_M_u16	27472	
TrqOviSta_HapticTime_Sec_M_f32	66540.6328	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0500000007	
TrqOviSta_LKAFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_u08	1	
TrqOviSta_LKASState_State_M_enum	1	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	62.7999992	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.670000017	
k_APAMaxHwTrq_HwNm_f32	5.11999989	
k_APAMaxVehSpd_Kph_f32	115.199997	
k_ESCMaxVehSpd_Kph_f32	144.5	
k_HapticAmplitude_MtrNm_f32	0.384000003	
k_HapticDuration_Sec_f32	0.959999979	
k_HapticFreq_Hz_f32	31.7880001	
k_HapticReactTime_Sec_f32	1.02400005	
k_LKAMaxVehSpd_Kph_f32	121.599998	
k_LKAMinVehSpd_Kph_f32	112	
k_MaxHapticReqTime_Sec_f32	0.975000024	
k_SWARLimiter_HwDeg_f32	5.5	
k_ShiftLevRevTime_Sec_f32	0.256999999	
k_StandstillThresh_Kph_f32	2.55999994	
k_StandstillTime_Sec_f32	0.256999999	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAREquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-604.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	621.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	299.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-1.10000002		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APASState_State_M_enum	4	4	✔
TrqOvlSta_ESCState_State_M_enum	4	4	✔
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	390201	390201 ± 1	✔
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	46660	46660 ± 1	✔
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOvlSta_HapticState_State_M_enum	0	0	✔
TrqOvlSta_Hapticdur_Cnt_M_u16	27472	27472 ± 1	✔
TrqOvlSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.61499977	-5.61499977 ± 0.000009	✔
TrqOvlSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOvlSta_LKASState_State_M_enum	4	4	✔
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	62.7999992	62.7999992 ± 0.0009765625	✔
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	62.7999992	62.7999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.384000003	0.384000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	31.7880001	31.7880001 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.55 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	424164
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	48750.6406
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	84030
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	28778
TrqOviSta_HapticTime_Sec_M_f32	69545
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.8499999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.287
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	63.9000015
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.680000007
k_APAMaxHwTrq_HwNm_f32	5.19999981
k_APAMaxVehSpd_Kph_f32	117
k_ESCMaxVehSpd_Kph_f32	146.899994
k_HapticAmplitude_MtrNm_f32	0.389999986
k_HapticDuration_Sec_f32	0.975000024
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactTime_Sec_f32	1.03999996
k_LKAMaxVehSpd_Kph_f32	123.5
k_LKAMinVehSpd_Kph_f32	113.75
k_MaxHapticReqTime_Sec_f32	0.99000001
k_SWARLimiter_HwDeg_f32	5.80000019
k_ShiftLevRevTime_Sec_f32	0.261000007
k_StandstillThresh_Kph_f32	2.5999999
k_StandstillTime_Sec_f32	0.261000007
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-594.710022
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	626.76001
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	314.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	48750.6406	48750.6406 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	84030	84030	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	28778	28778 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	69545	69545 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53515005	-2.53515005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.389999986	0.389999986 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.56 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	458127
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	49550.3203
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	90576
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	30084
TrqOviSta_HapticTime_Sec_M_f32	72550
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.298000008
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	65
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.689999998
k_APAMaxHwTrq_HwNm_f32	5.28000021
k_APAMaxVehSpd_Kph_f32	118.800003
k_ESCMaxVehSpd_Kph_f32	149.300003
k_HapticAmplitude_MtrNm_f32	0.395999998
k_HapticDuration_Sec_f32	0.99000001
k_HapticFreq_Hz_f32	32.8160019
k_HapticReactTime_Sec_f32	1.05599999
k_LKAMaxVehSpd_Kph_f32	125.400002
k_LKAMinVehSpd_Kph_f32	115.5
k_MaxHapticReqTime_Sec_f32	0.100000001
k_SWARLimiter_HwDeg_f32	6.0999999
k_ShiftLevRevTime_Sec_f32	0.264999986
k_StandstillThresh_Kph_f32	2.6400001
k_StandstillTime_Sec_f32	0.264999986
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-584.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	631.700012
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	309.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	319.299988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	2.0999999
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	30084	30084 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	72550	72550 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5	-5.5 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	65	65 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	65	65 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.395999998	0.395999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.8160019	32.8160019 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.57 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	492090
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	50350
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	97122
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	31390
TrqOviSta_HapticTime_Sec_M_f32	75555.5781
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.308999985
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	66.0999985

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.699999988
k_APAMaxHwTrq_HwNm_f32	5.36000013
k_APAMaxVehSpd_Kph_f32	120.599998
k_ESCMaxVehSpd_Kph_f32	151.699997
k_HapticAmplitude_MtrNm_f32	0.40200001
k_HapticDuration_Sec_f32	1.005
k_HapticFreq_Hz_f32	33.3300018
k_HapticReacttime_Sec_f32	1.07200003
k_LKAMaxVehSpd_Kph_f32	127.300003
k_LKAMinVehSpd_Kph_f32	117.25
k_MaxHapticReqTime_Sec_f32	0.200000003
k_SWARLimiter_HwDeg_f32	6.4000001
k_ShiftLevRevTime_Sec_f32	0.268999994
k_StandstillThresh_Kph_f32	2.68000007
k_StandstillTime_Sec_f32	0.268999994
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-574.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	636.640015
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	314.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	324.200012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	50350	50350 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	31390	31390 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.67375016	-1.67375004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCssillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.40200001	0.40200001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	33.3300018	33.3300018 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.58 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	526053
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	51149.6797
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	103668
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	32696
TrqOviSta_HapticTime_Sec_M_f32	78560
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.319999993
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	67.1999969
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.709999979
k_APAMaxHwTrq_HwNm_f32	5.44000006
k_APAMaxVehSpd_Kph_f32	122.400002
k_ESCMaxVehSpd_Kph_f32	154.100006
k_HapticAmplitude_MtrNm_f32	0.407999992
k_HapticDuration_Sec_f32	1.01999998
k_HapticFreq_Hz_f32	33.8440018
k_HapticReactTime_Sec_f32	1.08800006
k_LKAMaxVehSpd_Kph_f32	129.199997
k_LKAMinVehSpd_Kph_f32	119
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	6.69999981
k_ShiftLevRevTime_Sec_f32	0.273000002
k_StandstillThresh_Kph_f32	2.72000003
k_StandstillTime_Sec_f32	0.273000002
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-564.409973		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	641.580017		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.30000019		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	319.299988		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	329.100006		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.30000019		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	51149.6797	51149.6797 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	32696	32696 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	67.1999969	67.1999969 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	67.1999969	67.1999969 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.407999992	0.407999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	33.8440018	33.8440018 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.59 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	560016
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	51949.3594
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	110214
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	34002
TrqOviSta_HapticTime_Sec_M_f32	81565.6406
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.331
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	68.3000031
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.720000029
k_APAMaxHwTrq_HwNm_f32	5.51999998
k_APAMaxVehSpd_Kph_f32	124.199997
k_ESCMaxVehSpd_Kph_f32	156.5
k_HapticAmplitude_MtrNm_f32	0.414000005
k_HapticDuration_Sec_f32	1.03499997
k_HapticFreq_Hz_f32	34.3580017
k_HapticReactime_Sec_f32	1.10399997
k_LKAMaxVehSpd_Kph_f32	131.100006
k_LKAMinVehSpd_Kph_f32	120.75
k_MaxHapticReqTime_Sec_f32	0.400000006
k_SWARLimiter_HwDeg_f32	7
k_ShiftLevRevTime_Sec_f32	0.27700001
k_StandstillThresh_Kph_f32	2.75999999
k_StandstillTime_Sec_f32	0.27700001
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enun	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-554.309998
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	646.52002
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.5
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	324.200012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	334
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	34002	34002 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	81565.6406	81565.6406 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981	-4.44999981 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.414000005	0.414000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	34.3580017	34.3580017 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.60 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	593979
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	52749.0391
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	116760
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	35308
TrqOviSta_HapticTime_Sec_M_f32	84570
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342000008
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	69.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.730000019
k_APAMaxHwTrq_HwNm_f32	5.5999999
k_APAMaxVehSpd_Kph_f32	126
k_ESCMaxVehSpd_Kph_f32	158.899994
k_HapticAmplitude_MtrNm_f32	0.419999987
k_HapticDuration_Sec_f32	1.04999995
k_HapticFreq_Hz_f32	34.8720016
k_HapticReactTime_Sec_f32	1.12
k_LKAMaxVehSpd_Kph_f32	133
k_LKAMinVehSpd_Kph_f32	122.5
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	7.30000019
k_ShiftLevRevTime_Sec_f32	0.280999988
k_StandstillThresh_Kph_f32	2.79999995
k_StandstillTime_Sec_f32	0.280999988
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-544.210022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	651.460022
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	338.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	8.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	116760	116760	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	35308	35308 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999	-4.0999999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	69.4000015	69.4000015 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	69.4000015	69.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.419999987	0.419999987 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	34.8720016	34.8720016 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.61 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	627942
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	53548.7188
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	123306
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	36614
TrqOviSta_HapticTime_Sec_M_f32	87575
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.352999985
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	70.5

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.74000001
k_APAMaxHwTrq_HwNm_f32	5.67999983
k_APAMaxVehSpd_Kph_f32	127.800003
k_ESCMaxVehSpd_Kph_f32	161.300003
k_HapticAmplitude_MtrNm_f32	0.425999999
k_HapticDuration_Sec_f32	1.06500006
k_HapticFreq_Hz_f32	35.3860016
k_HapticReacttime_Sec_f32	1.13600004
k_LKAMaxVehSpd_Kph_f32	134.899994
k_LKAMinVehSpd_Kph_f32	124.25
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	7.5999999
k_ShiftLevRevTime_Sec_f32	0.284999996
k_StandstillThresh_Kph_f32	2.83999991
k_StandstillTime_Sec_f32	0.284999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-534.109985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	656.400024
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	334
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	343.799988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	53548.7188	53548.7188 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	36614	36614 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75	-3.75 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.425999999	0.425999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	35.3860016	35.3860016 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.62 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	797757
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	57547.1211
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	156036
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	43144
TrqOviSta_HapticTime_Sec_M_f32	102600
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	74.9000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.779999971
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003
k_ESCMaxVehSpd_Kph_f32	170.899994
k_HapticAmplitude_MtrNm_f32	0.456
k_HapticDuration_Sec_f32	1.13999999
k_HapticFreq_Hz_f32	37.4420013
k_HapticReactTime_Sec_f32	1.21599996
k_LKAMaxVehSpd_Kph_f32	144.399994
k_LKAMinVehSpd_Kph_f32	133
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	8.80000019
k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.03999996
k_StandstillTime_Sec_f32	0.305000007
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResquest_Cnt_Igc	target_TrqOviSta_Per1_APAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_Igc	target_TrqOviSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAResquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-524.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	661.340027		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	7.9000001		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAResquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	353.600006		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	363.399994		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	57547.1211	57547.1211 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	43144	43144 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.03919983	2.03920007 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.456	0.456 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	37.4420013	37.4420013 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.63 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	831720
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	58346.8008
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	162582
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	105605
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	76
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.790000021
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	173.300003
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	37.9560013
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	9.10000038
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-513.909973
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	666.280029
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	358.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	368.299988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.1000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	76	76 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	76	76 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	37.9560013	37.9560013 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.64 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	865683
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	59146.4805
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	169128
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	0
TrqOviSta_HapticTime_Sec_M_f32	108610.617
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	77.0999985
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.800000012
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003
k_ESCMaxVehSpd_Kph_f32	175.699997
k_HapticAmplitude_MtrNm_f32	0.456
k_HapticDuration_Sec_f32	1.13999999
k_HapticFreq_Hz_f32	38.4700012
k_HapticReactTime_Sec_f32	1.21599996
k_LKAMaxVehSpd_Kph_f32	144.399994
k_LKAMinVehSpd_Kph_f32	133
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	9.39999962
k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.03999996
k_StandstillTime_Sec_f32	0.305000007
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-503.809998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	671.219971
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	7.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	363.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	373.200012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	19.7000008
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	59146.4805	59146.4805 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.456	0.456 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	38.4700012	38.4700012 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.65 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	899646
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	59946.1602
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	175674
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	65535
TrqOviSta_HapticTime_Sec_M_f32	111615
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	78.1999969

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.810000002
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	178.100006
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	38.9840012
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	9.69999981
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-493.709991
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	676.159973
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	378.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARtgtAngRequest_HwDeg_f32.value	21.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	59946.1602	59946.1602 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	175674	175674	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	65535	65535 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	78.1999969	78.1999969 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	78.1999969	78.1999969 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	38.9840012	38.9840012 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.66 (Repeat Count = 1)			✔
Name	Input Value		
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta		
TrqOviSta_APASState_State_M_enum	1		
TrqOviSta_ESCState_State_M_enum	1		
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	933609		
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	60745.8398		
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	182220		
TrqOviSta_HapticState_State_M_enum	1		
TrqOviSta_Hapticdur_Cnt_M_u16	35640		
TrqOviSta_HapticTime_Sec_M_f32	114620		
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998		
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419		
TrqOviSta_LKAFault_Cnt_M_lgc	0		
TrqOviSta_LKAPermFault_Cnt_M_lgc	0		
TrqOviSta_LKAPermFault_Cnt_M_u08	2		
TrqOviSta_LKASState_State_M_enum	3		
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	79.3000031		
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1		
k_APAINcludeHaptic_Cnt_lgc	1		
k_APAMaxHwTrqTime_Sec_f32	0.819999993		
k_APAMaxHwTrq_HwNm_f32	6.15999985		
k_APAMaxVehSpd_Kph_f32	138.600006		
k_ESCMaxVehSpd_Kph_f32	11.3000002		
k_HapticAmplitude_MtrNm_f32	0.462000012		
k_HapticDuration_Sec_f32	1.15499997		
k_HapticFreq_Hz_f32	39.4980011		
k_HapticReactTime_Sec_f32	1.23199999		
k_LKAMaxVehSpd_Kph_f32	146.300003		
k_LKAMinVehSpd_Kph_f32	134.75		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	10		
k_ShiftLevRevTime_Sec_f32	0.308999985		
k_StandstillThresh_Kph_f32	3.07999992		
k_StandstillTime_Sec_f32	0.308999985		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc		

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-483.609985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	681.099976		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	373.200012		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	383		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.8999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	60745.8398	60745.8398 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	35640	35640 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	39.4980011	39.4980011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.67 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	967572
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	61545.5195
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	188766
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptidur_Cnt_M_u16	49674
TrqOvlSta_HapticTime_Sec_M_f32	117625.852
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKASState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	80.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.829999983
k_APAMaxHwTrq_HwNm_f32	6.48000002
k_APAMaxVehSpd_Kph_f32	145.800003
k_ESCMaxVehSpd_Kph_f32	16.3999996
k_HapticAmplitude_MtrNm_f32	0.486000001
k_HapticDuration_Sec_f32	1.21500003
k_HapticFreq_Hz_f32	40.012001
k_HapticReactTime_Sec_f32	1.296
k_LKAMaxVehSpd_Kph_f32	153.899994
k_LKAMinVehSpd_Kph_f32	141.75
k_MaxHapticReqTime_Sec_f32	0
k_SWARLimiter_HwDeg_f32	10.3000002
k_ShiftLevRevTime_Sec_f32	0.324999988
k_StandstillThresh_Kph_f32	3.24000001
k_StandstillTime_Sec_f32	0.324999988
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-473.51001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	686.039978
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	378.100006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	387.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	49674	49674 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	117625.852	117625.852 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25	-0.25 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	80.4000015	80.4000015 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	80.4000015	80.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.486000001	0.486000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	40.012001	40.012001 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.68 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1001535
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	62345.1992
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	195312
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	50980
TrqOviSta_HapticTime_Sec_M_f32	120630
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	81.5
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.8399999974
k_APAMaxHwTrq_HwNm_f32	6.559999994
k_APAMaxVehSpd_Kph_f32	147.600006
k_ESCMaxVehSpd_Kph_f32	21.5
k_HapticAmplitude_MtrNm_f32	0.492000014
k_HapticDuration_Sec_f32	1.23000002
k_HapticFreq_Hz_f32	40.526001
k_HapticReactTime_Sec_f32	1.31200004
k_LKAMaxVehSpd_Kph_f32	155.800003
k_LKAMinVehSpd_Kph_f32	143.5
k_MaxHapticReqTime_Sec_f32	1
k_SWARLimiter_HwDeg_f32	10.6000004
k_ShiftLevRevTime_Sec_f32	0.328999996
k_StandstillThresh_Kph_f32	3.27999997
k_StandstillTime_Sec_f32	0.328999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-463.410004
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	690.97998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	383
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	392.799988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	62345.1992	62345.1992 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	50980	50980 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	120630	120630 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001	0.100000001 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.492000014	0.492000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	40.526001	40.526001 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.69 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1035498
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	63144.8789
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	201858
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	52286
TrqOviSta_HapticTime_Sec_M_f32	123635
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	82.5999985

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.850000024
k_APAMaxHwTrq_HwNm_f32	6.639999987
k_APAMaxVehSpd_Kph_f32	149.3999994
k_ESCMaxVehSpd_Kph_f32	26.6000004
k_HapticAmplitude_MtrNm_f32	0.497999996
k_HapticDuration_Sec_f32	1.245
k_HapticFreq_Hz_f32	41.0400009
k_HapticReacttime_Sec_f32	1.32799995
k_LKAMaxVehSpd_Kph_f32	157.699997
k_LKAMinVehSpd_Kph_f32	145.25
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	10.8999996
k_ShiftLevRevTime_Sec_f32	0.333000004
k_StandstillThresh_Kph_f32	3.31999993
k_StandstillTime_Sec_f32	0.333000004
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-453.309998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	695.919983
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-7.5999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	387.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	397.700012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	27.7000008		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	63144.8789	63144.8789 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	52286	52286 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	123635	123635 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988	0.449999988 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	82.5999985	82.5999985 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	82.5999985	82.5999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.497999996	0.497999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	41.0400009	41.0400009 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.70 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	4	
TrqOviSta_ESCState_State_M_enum	0	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1069461	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	63944.5586	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	208404	
TrqOviSta_HapticState_State_M_enum	1	
TrqOviSta_Hapticdur_Cnt_M_u16	53592	
TrqOviSta_HapticTime_Sec_M_f32	126640.641	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.800000012	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992	
TrqOviSta_LKAFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_u08	3	
TrqOviSta_LKASState_State_M_enum	1	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	83.6999969	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	0	
k_APAMaxHwTrqTime_Sec_f32	0.860000014	
k_APAMaxHwTrq_HwNm_f32	6.71999979	
k_APAMaxVehSpd_Kph_f32	151.199997	
k_ESCMaxVehSpd_Kph_f32	31.7000008	
k_HapticAmplitude_MtrNm_f32	0.504000008	
k_HapticDuration_Sec_f32	1.25999999	
k_HapticFreq_Hz_f32	41.5540009	
k_HapticReactTime_Sec_f32	1.34399998	
k_LKAMaxVehSpd_Kph_f32	159.600006	
k_LKAMinVehSpd_Kph_f32	147	
k_MaxHapticReqTime_Sec_f32	0.600000024	
k_SWARLimiter_HwDeg_f32	11.1999998	
k_ShiftLevRevTime_Sec_f32	0	
k_StandstillThresh_Kph_f32	3.3599999	
k_StandstillTime_Sec_f32	0.100000001	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResquest_Cnt_Igc	target_TrqOviSta_Per1_APAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_Igc	target_TrqOviSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAResquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-443.209991		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	700.859985		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-7.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAResquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	402.600006		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	29.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	63944.5586	63944.5586 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	208404	208404	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	53592	53592 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	126640.641	126640.641 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.800000012	0.800000012 ± 0.0000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.504000008	0.504000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	41.5540009	41.5540009 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.71 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1103424
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	64744.2383
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	214950
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptidur_Cnt_M_u16	54898
TrqOvlSta_HapticTime_Sec_M_f32	129645
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.14999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	84.8000031
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.870000005
k_APAMaxHwTrq_HwNm_f32	6.80000019
k_APAMaxVehSpd_Kph_f32	153
k_ESCMaxVehSpd_Kph_f32	36.7999992
k_HapticAmplitude_MtrNm_f32	0.50999999
k_HapticDuration_Sec_f32	1.27499998
k_HapticFreq_Hz_f32	42.0680008
k_HapticReactTime_Sec_f32	1.36000001
k_LKAMaxVehSpd_Kph_f32	161.5
k_LKAMinVehSpd_Kph_f32	148.75
k_MaxHapticReqTime_Sec_f32	0.699999988
k_SWARLimiter_HwDeg_f32	11.5
k_ShiftLevRevTime_Sec_f32	0.5
k_StandstillThresh_Kph_f32	3.4000001
k_StandstillTime_Sec_f32	0.479999989
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHoscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-433.109985
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	705.799988
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.5999999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	397.700012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	407.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.8999996
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	64744.2383	64744.2383 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	54898	54898 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.14999998	1.14999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	84.8000031	84.8000031 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	84.8000031	84.8000031 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.50999999	0.50999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	42.0680008	42.0680008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.72 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1137387
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	65543.9219
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	221496
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	56204
TrqOviSta_HapticTime_Sec_M_f32	132650
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.518000007
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	85.9000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.879999995
k_APAMaxHwTrq_HwNm_f32	6.88000011
k_APAMaxVehSpd_Kph_f32	154.800003
k_ESCMaxVehSpd_Kph_f32	41.9000015
k_HapticAmplitude_MtrNm_f32	0.515999973
k_HapticDuration_Sec_f32	1.28999996
k_HapticFreq_Hz_f32	42.5820007
k_HapticReactTime_Sec_f32	1.37600005
k_LKAMaxVehSpd_Kph_f32	163.399994
k_LKAMinVehSpd_Kph_f32	150.5
k_MaxHapticReqTime_Sec_f32	0.800000012
k_SWARLimiter_HwDeg_f32	11.8000002
k_ShiftLevRevTime_Sec_f32	0.300000012
k_StandstillThresh_Kph_f32	3.44000006
k_StandstillTime_Sec_f32	0.349999994
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-423.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	710.73999
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	402.600006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	412.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	32.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	65543.9219	65543.9219 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	56204	56204 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5	1.5 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadyToPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.515999973	0.515999973 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	42.5820007	42.5820007 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.73 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1171350
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	66343.6016
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	228042
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	57510
TrqOviSta_HapticTime_Sec_M_f32	135655.938
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.85000002
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.528999984
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	87

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.889999986
k_APAMaxHwTrq_HwNm_f32	6.96000004
k_APAMaxVehSpd_Kph_f32	156.600006
k_ESCMaxVehSpd_Kph_f32	47
k_HapticAmplitude_MtrNm_f32	0.522000015
k_HapticDuration_Sec_f32	1.30499995
k_HapticFreq_Hz_f32	98.5240021
k_HapticReacttime_Sec_f32	1.39199996
k_LKAMaxVehSpd_Kph_f32	165.300003
k_LKAMinVehSpd_Kph_f32	152.25
k_MaxHapticReqTime_Sec_f32	0.899999976
k_SWARLimiter_HwDeg_f32	12.1000004
k_ShiftLevRevTime_Sec_f32	0.360000014
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.360000014
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-412.910004
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	715.679993
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-5.5999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	407.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	417.299988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-33.0999985		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	57510	57510 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	135655.938	135655.938 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.09104967	-2.09104991 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	87	87 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	87	87 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.522000015	0.522000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	98.5240021	98.5240021 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.74 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1205313
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	67143.2813
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	234588
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	58816
TrqOviSta_HapticTime_Sec_M_f32	138660
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.9000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.540000021
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	88.0999985
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.899999976
k_APAMaxHwTrq_HwNm_f32	7.03999996
k_APAMaxVehSpd_Kph_f32	158.399994
k_ESCMaxVehSpd_Kph_f32	52.0999985
k_HapticAmplitude_MtrNm_f32	0.527999997
k_HapticDuration_Sec_f32	1.32000005
k_HapticFreq_Hz_f32	85
k_HapticReactTime_Sec_f32	1.40799999
k_LKAMaxVehSpd_Kph_f32	167.199997
k_LKAMinVehSpd_Kph_f32	154
k_MaxHapticReqTime_Sec_f32	0.0149999997
k_SWARLimiter_HwDeg_f32	12.3999996
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	5
k_StandstillTime_Sec_f32	0.00100000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-402.809998		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	720.619995		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.69999981		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	412.399994		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	422.200012		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-31.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	58816	58816 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.9000001	-6.9000001 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.527999997	0.527999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	85	85 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.75 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1239276
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	67942.9609
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	241134
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	60122
TrqOviSta_HapticTime_Sec_M_f32	141665
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.55000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.550999999
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	89.1999969
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.910000026
k_APAMaxHwTrq_HwNm_f32	7.11999989
k_APAMaxVehSpd_Kph_f32	160.199997
k_ESCMaxVehSpd_Kph_f32	57.2000008
k_HapticAmplitude_MtrNm_f32	0.533999979
k_HapticDuration_Sec_f32	1.33500004
k_HapticFreq_Hz_f32	72.697998
k_HapticReactTime_Sec_f32	1.42400002
k_LKAMaxVehSpd_Kph_f32	169.100006
k_LKAMinVehSpd_Kph_f32	155.75
k_MaxHapticReqTime_Sec_f32	0.0299999993
k_SWARLimiter_HwDeg_f32	12.6999998
k_ShiftLevRevTime_Sec_f32	0.00499999989
k_StandstillThresh_Kph_f32	3.5
k_StandstillTime_Sec_f32	0.00499999989
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-392.709991
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	725.559998
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	427.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-29.8999996
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	67942.9609	67942.9609 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	241134	241134	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	60122	60122 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.55000019	-6.55000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	89.1999969	89.1999969 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	89.1999969	89.1999969 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.533999979	0.533999979 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	72.697998	72.697998 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.76 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1273239
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	68742.6406
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	247680
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	61428
TrqOviSta_HapticTime_Sec_M_f32	144670
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.561999977
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	10.1999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.920000017
k_APAMaxHwTrq_HwNm_f32	7.19999981
k_APAMaxVehSpd_Kph_f32	162
k_ESCMaxVehSpd_Kph_f32	62.2999992
k_HapticAmplitude_MtrNm_f32	0.540000021
k_HapticDuration_Sec_f32	1.35000002
k_HapticFreq_Hz_f32	44.6380005
k_HapticReactTime_Sec_f32	1.44000006
k_LKAMaxVehSpd_Kph_f32	171
k_LKAMinVehSpd_Kph_f32	157.5
k_MaxHapticReqTime_Sec_f32	0.0450000018
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.00899999961
k_StandstillThresh_Kph_f32	3.5999999
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-382.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	730.5
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	422.200012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	432
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	61428	61428 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	144670	144670 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981	-6.19999981 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadyToPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	44.6380005	44.6380005 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.77 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1307202
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	69542.3203
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	254226
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	62734
TrqOviSta_HapticTime_Sec_M_f32	147675.844
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.8499999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.573000014
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	11.6000004

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.930000007
k_APAMaxHwTrq_HwNm_f32	7.28000021
k_APAMaxVehSpd_Kph_f32	163.800003
k_ESCMaxVehSpd_Kph_f32	67.4000015
k_HapticAmplitude_MtrNm_f32	0.546000004
k_HapticDuration_Sec_f32	1.36500001
k_HapticFreq_Hz_f32	45.1520004
k_HapticReacttime_Sec_f32	1.45599997
k_LKAMaxVehSpd_Kph_f32	172.899994
k_LKAMinVehSpd_Kph_f32	159.25
k_MaxHapticReqTime_Sec_f32	0.0599999987
k_SWARLimiter_HwDeg_f32	13.3000002
k_ShiftLevRevTime_Sec_f32	0.0130000003
k_StandstillThresh_Kph_f32	3.6400001
k_StandstillTime_Sec_f32	0.5
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-372.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	735.440002
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.30000019
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	427.100006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	436.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7000008		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	62734	62734 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	147675.844	147675.844 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.8499999	-5.8499999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.6000004	11.6000004 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	11.6000004	11.6000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.546000004	0.546000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	45.1520004	45.1520004 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.78 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	3	
TrqOviSta_ESCState_State_M_enum	3	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1341165	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	70342	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	260772	
TrqOviSta_HapticState_State_M_enum	1	
TrqOviSta_Hapticdur_Cnt_M_u16	64040	
TrqOviSta_HapticTime_Sec_M_f32	150680	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.583999991	
TrqOviSta_LKAFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_u08	4	
TrqOviSta_LKASState_State_M_enum	1	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	13	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.939999998	
k_APAMaxHwTrq_HwNm_f32	7.36000013	
k_APAMaxVehSpd_Kph_f32	165.600006	
k_ESCMaxVehSpd_Kph_f32	72.5	
k_HapticAmplitude_MtrNm_f32	0.551999986	
k_HapticDuration_Sec_f32	1.38	
k_HapticFreq_Hz_f32	45.6660004	
k_HapticReactTime_Sec_f32	1.472	
k_LKAMaxVehSpd_Kph_f32	174.800003	
k_LKAMinVehSpd_Kph_f32	161	
k_MaxHapticReqTime_Sec_f32	0.075000003	
k_SWARLimiter_HwDeg_f32	13.6000004	
k_ShiftLevRevTime_Sec_f32	0.0170000009	
k_StandstillThresh_Kph_f32	3.68000007	
k_StandstillTime_Sec_f32	0.300000012	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResquest_Cnt_Igc	target_TrqOviSta_Per1_APAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_Igc	target_TrqOviSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAResquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-362.410004		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	740.380005		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.5		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAResquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	432		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	441.799988		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	70342	70342 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	64040	64040 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5	-5.5 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.551999986	0.551999986 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	45.6660004	45.6660004 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.79 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1375128
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	71141.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	267318
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Haptidur_Cnt_M_u16	65346
TrqOvlSta_HapticTime_Sec_M_f32	153685.688
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.595000029
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.949999988
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	167.399994
k_ESCMaxVehSpd_Kph_f32	77.5999985
k_HapticAmplitude_MtrNm_f32	0.558000028
k_HapticDuration_Sec_f32	1.39499998
k_HapticFreq_Hz_f32	46.1800003
k_HapticReactTime_Sec_f32	1.48800004
k_LKAMaxVehSpd_Kph_f32	176.699997
k_LKAMinVehSpd_Kph_f32	162.75
k_MaxHapticReqTime_Sec_f32	0.0900000036
k_SWARLimiter_HwDeg_f32	13.8999996
k_ShiftLevRevTime_Sec_f32	0.0209999997
k_StandstillThresh_Kph_f32	3.72000003
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1140.10999
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	745.320007
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	436.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	446.700012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1375128	1375128 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	71141.6797	71141.6797 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	65346	65346 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	153685.688	153685.688 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.30575037	1.30575001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996	14.3999996 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	14.3999996	14.3999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.558000028	0.558000028 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	46.1800003	46.1800003 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.80 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1409091
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	71941.3594
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	273864
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	1000
TrqOviSta_HapticTime_Sec_M_f32	156690
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.606000006
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	15.8000002
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.959999979
k_APAMaxHwTrq_HwNm_f32	10
k_APAMaxVehSpd_Kph_f32	169.199997
k_ESCMaxVehSpd_Kph_f32	82.6999969
k_HapticAmplitude_MtrNm_f32	0.56400001
k_HapticDuration_Sec_f32	1.40999997
k_HapticFreq_Hz_f32	46.6940002
k_HapticReactTime_Sec_f32	1.50399995
k_LKAMaxVehSpd_Kph_f32	178.600006
k_LKAMinVehSpd_Kph_f32	164.5
k_MaxHapticReqTime_Sec_f32	0.104999997
k_SWARLimiter_HwDeg_f32	14.1999998
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	3.75999999
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1130.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	750.26001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-21.8999996
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	273864	273864	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	1000	1000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	156690	156690 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.56400001	0.56400001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	46.6940002	46.6940002 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.81 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1443054
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	72741.0391
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	280410
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	3000
TrqOviSta_HapticTime_Sec_M_f32	159695.766
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.616999984
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	17.2000008

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.970000029
k_APAMaxHwTrq_HwNm_f32	7.6500001
k_APAMaxVehSpd_Kph_f32	171
k_ESCMaxVehSpd_Kph_f32	87.8000031
k_HapticAmplitude_MtrNm_f32	0.569999993
k_HapticDuration_Sec_f32	1.42499995
k_HapticFreq_Hz_f32	47.2080002
k_HapticReacttime_Sec_f32	1.51999998
k_LKAMaxVehSpd_Kph_f32	180.5
k_LKAMinVehSpd_Kph_f32	166.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	14.5
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	3.79999995
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1119.91003
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	755.200012
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	446.700012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	456.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-20.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	72741.0391	72741.0391 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	3000	3000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.05934954	2.05935001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	17.2000008	17.2000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	17.2000008	17.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.569999993	0.569999993 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	47.2080002	47.2080002 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.82 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1477017
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	73540.7188
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	286956
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	5000
TrqOviSta_HapticTime_Sec_M_f32	162700
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.628000021
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	18.6000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0
k_APAMaxHwTrq_HwNm_f32	0.699999988
k_APAMaxVehSpd_Kph_f32	172.800003
k_ESCMaxVehSpd_Kph_f32	92.9000015
k_HapticAmplitude_MtrNm_f32	0.575999975
k_HapticDuration_Sec_f32	1.44000006
k_HapticFreq_Hz_f32	47.7220001
k_HapticReactTime_Sec_f32	1.53600001
k_LKAMaxVehSpd_Kph_f32	182.399994
k_LKAMinVehSpd_Kph_f32	168
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	14.8000002
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	3.83999991
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1109.81006		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	760.140015		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.30000019		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	451.600006		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	461.399994		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-18.7000008		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1477017	1477017 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	73540.7188	73540.7188 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	5000	5000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43120003	2.43120003 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.575999975	0.575999975 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	47.7220001	47.7220001 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.83 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1510980
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	74340.3984
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	293502
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	7000
TrqOviSta_HapticTime_Sec_M_f32	165705
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.638999999
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	20
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	1
k_APAMaxHwTrq_HwNm_f32	1.10000002
k_APAMaxVehSpd_Kph_f32	174.600006
k_ESCMaxVehSpd_Kph_f32	98
k_HapticAmplitude_MtrNm_f32	0.582000017
k_HapticDuration_Sec_f32	1.45500004
k_HapticFreq_Hz_f32	48.2360001
k_HapticReactTime_Sec_f32	1.55200005
k_LKAMaxVehSpd_Kph_f32	184.300003
k_LKAMinVehSpd_Kph_f32	169.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	15.1000004
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	3.88000011
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1099.70996		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	765.080017		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.5		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAResult_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	456.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	466.299988		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1510980	1510980 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	7000	7000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	165705	165705 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.79974985	2.79975009 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	20	20 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	20	20 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.582000017	0.582000017 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	48.2360001	48.2360001 ± 0.0009765625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.84 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1544943
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	75140.0781
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	300048
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	9000
TrqOviSta_HapticTime_Sec_M_f32	168710.594
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.649999976
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	21.3999996
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.550000012
k_APAMaxHwTrq_HwNm_f32	2
k_APAMaxVehSpd_Kph_f32	176.399994
k_ESCMaxVehSpd_Kph_f32	103.099998
k_HapticAmplitude_MtrNm_f32	0.588
k_HapticDuration_Sec_f32	1.47000003
k_HapticFreq_Hz_f32	48.75
k_HapticReactTime_Sec_f32	1.56799996
k_LKAMaxVehSpd_Kph_f32	186.199997
k_LKAMinVehSpd_Kph_f32	171.5
k_MaxHapticReqTime_Sec_f32	0.165000007
k_SWARLimiter_HwDeg_f32	15.3999996
k_ShiftLevRevTime_Sec_f32	0.0410000011
k_StandstillThresh_Kph_f32	3.92000008
k_StandstillTime_Sec_f32	0.0410000011
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1089.60999
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	770.02002
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	461.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	471.200012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1544943	1544943 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	9000	9000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.16499996	3.16499996 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.588	0.588 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	48.75	48.75 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.85 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1578906
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	75939.7578
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	306594
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	11000
TrqOviSta_HapticTime_Sec_M_f32	171715
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.661000013
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	22.7999992

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.980000019
k_APAMaxHwTrq_HwNm_f32	7.92000008
k_APAMaxVehSpd_Kph_f32	0
k_ESCMaxVehSpd_Kph_f32	108.199997
k_HapticAmplitude_MtrNm_f32	0.593999982
k_HapticDuration_Sec_f32	1.48500001
k_HapticFreq_Hz_f32	49.2639999
k_HapticReacttime_Sec_f32	1.58399999
k_LKAMaxVehSpd_Kph_f32	188.100006
k_LKAMinVehSpd_Kph_f32	173.25
k_MaxHapticReqTime_Sec_f32	0.180000007
k_SWARLimiter_HwDeg_f32	15.6999998
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	3.96000004
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1079.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	774.960022
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	476.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-13.8999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	75939.7578	75939.7578 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	306594	306594	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11000	11000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995	-3.04999995 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	22.7999992	22.7999992 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	22.7999992	22.7999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.593999982	0.593999982 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	49.2639999	49.2639999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.86 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1612869
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	76739.4375
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	313140
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	13000
TrqOviSta_HapticTime_Sec_M_f32	174720.641
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.671999991
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	24.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.99000001
k_APAMaxHwTrq_HwNm_f32	8
k_APAMaxVehSpd_Kph_f32	220
k_ESCMaxVehSpd_Kph_f32	113.300003
k_HapticAmplitude_MtrNm_f32	0.600000024
k_HapticDuration_Sec_f32	1.5
k_HapticFreq_Hz_f32	49.7779999
k_HapticReactTime_Sec_f32	1.60000002
k_LKAMaxVehSpd_Kph_f32	190
k_LKAMinVehSpd_Kph_f32	175
k_MaxHapticReqTime_Sec_f32	0.194999993
k_SWARLimiter_HwDeg_f32	16
k_ShiftLevRevTime_Sec_f32	0.0489999987
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.0489999987
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1069.41003		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	779.900024		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	7.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	471.200012		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	481		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-12.3000002		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	13000	13000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	174720.641	174720.641 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.600000024	0.600000024 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	49.7779999	49.7779999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.87 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1646832
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	77539.1172
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	319686
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	15000
TrqOviSta_HapticTime_Sec_M_f32	177725
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.683000028
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	25.6000004
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	1
k_APAMaxHwTrq_HwNm_f32	8.07999992
k_APAMaxVehSpd_Kph_f32	100.360001
k_ESCMaxVehSpd_Kph_f32	118.400002
k_HapticAmplitude_MtrNm_f32	0.606000006
k_HapticDuration_Sec_f32	1.51499999
k_HapticFreq_Hz_f32	50.2919998
k_HapticReacttime_Sec_f32	1.61600006
k_LKAMaxVehSpd_Kph_f32	191.899994
k_LKAMinVehSpd_Kph_f32	176.75
k_MaxHapticReqTime_Sec_f32	0.0149999997
k_SWARLimiter_HwDeg_f32	16.2999992
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	4.03999996
k_StandstillTime_Sec_f32	0.00100000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1059.31006
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	784.840027
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	7.30000019
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	476.100006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	485.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-10.6999998
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	15000	15000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999	-2.3499999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	25.6000004	25.6000004 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	25.6000004	25.6000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.606000006	0.606000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	50.2919998	50.2919998 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.88 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1680795
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	78338.7969
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	326232
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	0
TrqOviSta_HapticTime_Sec_M_f32	180730
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	27
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.123000003
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003
k_ESCMaxVehSpd_Kph_f32	123.5
k_HapticAmplitude_MtrNm_f32	0.456
k_HapticDuration_Sec_f32	0
k_HapticFreq_Hz_f32	50.8059998
k_HapticReactTime_Sec_f32	1.21599996
k_LKAMaxVehSpd_Kph_f32	144.399994
k_LKAMinVehSpd_Kph_f32	133
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	16.6000004
k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.03999996
k_StandstillTime_Sec_f32	0.305000007
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1049.20996
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	789.780029
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	7.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	481
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	490.799988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.10000038
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	78338.7969	78338.7969 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.456	0.456 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	50.8059998	50.8059998 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.89 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1714758
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79138.4766
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	332778
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	3650
TrqOviSta_HapticTime_Sec_M_f32	183735
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	28.3999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.134000003
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	128.600006
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	2
k_HapticFreq_Hz_f32	51.3199997
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	16.8999996
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1039.10999
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	794.719971
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	485.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	495.700012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.5		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79138.4766	79138.4766 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	3650	3650 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	28.3999996	28.3999996 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	28.3999996	28.3999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	51.3199997	51.3199997 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.90 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	25475
TrqOviSta_HapticTime_Sec_M_f32	186740.953
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.55999994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.91 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1782684
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	80737.8438
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	345870
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptidur_Cnt_M_u16	0
TrqOvlSta_HapticTime_Sec_M_f32	189745
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqOvlSta_LKAFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKASState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	31.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.156000003
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003
k_ESCMaxVehSpd_Kph_f32	138.800003
k_HapticAmplitude_MtrNm_f32	0.629999995
k_HapticDuration_Sec_f32	0.5
k_HapticFreq_Hz_f32	52.3479996
k_HapticReactTime_Sec_f32	0
k_LKAMaxVehSpd_Kph_f32	199.5
k_LKAMinVehSpd_Kph_f32	183.75
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	17.5
k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.03999996
k_StandstillTime_Sec_f32	0.305000007
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1018.90997
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	804.599976
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	7.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	495.700012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	505.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4.30000019
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	80737.8438	80737.8438 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	31.2000008	31.2000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	31.2000008	31.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.629999995	0.629999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	52.3479996	52.3479996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.92 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1816647
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	81537.5234
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	352416
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	3650
TrqOviSta_HapticTime_Sec_M_f32	192750
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	32.5999985
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.166999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	143.899994
k_HapticAmplitude_MtrNm_f32	0.635999978
k_HapticDuration_Sec_f32	0.870000005
k_HapticFreq_Hz_f32	52.8619995
k_HapticReactTime_Sec_f32	2
k_LKAMaxVehSpd_Kph_f32	201.399994
k_LKAMinVehSpd_Kph_f32	185.5
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.7999992
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1008.81
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	809.539978
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	500.600006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	510.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.70000005
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	81537.5234	81537.5234 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	3650	3650 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.635999978	0.635999978 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	52.8619995	52.8619995 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.93 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1850610
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	82337.2031
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	358962
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	25475
TrqOviSta_HapticTime_Sec_M_f32	195755
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	34

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.178000003
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	149
k_HapticAmplitude_MtrNm_f32	0.64200002
k_HapticDuration_Sec_f32	1.65999997
k_HapticFreq_Hz_f32	53.3759995
k_HapticReacttime_Sec_f32	1.29999995
k_LKAMaxVehSpd_Kph_f32	203.300003
k_LKAMinVehSpd_Kph_f32	187.25
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	18.1000004
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-998.710022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	814.47998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	505.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	10.3000002
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-1.10000002		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	82337.2031	82337.2031 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	25475	25475 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	34	34 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	34	34 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.64200002	0.64200002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	53.3759995	53.3759995 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.94 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	29000
TrqOviSta_HapticTime_Sec_M_f32	198760.641
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	35.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.188999996
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	154.100006
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	53.8899994
k_HapticReactTime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	18.3999996
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-988.609985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	819.419983		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	510.399994		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	198760.641	198760.641 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	53.8899994	53.8899994 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.95 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1918536
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83936.5625
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	372054
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	31000
TrqOviSta_HapticTime_Sec_M_f32	201765
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	36.7999992
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	159.199997
k_HapticAmplitude_MtrNm_f32	0.653999984
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	54.4039993
k_HapticReactime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	207.100006
k_LKAMinVehSpd_Kph_f32	220
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	18.7000008
k_ShiftLevRevTime_Sec_f32	0.032999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-978.51001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	824.359985
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	30.8999996
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	2.0999999
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	372054	372054	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	31000	31000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	201765	201765 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	36.7999992	36.7999992 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	36.7999992	36.7999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.653999984	0.653999984 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	54.4039993	54.4039993 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.96 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	33000
TrqOviSta_HapticTime_Sec_M_f32	204770
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReactTime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	19
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-968.409973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	829.299988
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	2	2	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	33000	33000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008	38.2000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	38.2000008	38.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.97 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1986462
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	85535.9219
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	385146
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	29000
TrqOviSta_HapticTime_Sec_M_f32	207775
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	39.5999985

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAMIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.222000003
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	169.399994
k_HapticAmplitude_MtrNm_f32	0.666000009
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	55.4319992
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	194.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	19.2999992
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-958.309998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	834.23999
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.8999996
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	51.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.30000019		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	85535.9219	85535.9219 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	207775	207775 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	39.5999985	39.5999985 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCssillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	39.5999985	39.5999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.666000009	0.666000009 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	55.4319992	55.4319992 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.98 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	1	
TrqOviSta_ESCState_State_M_enum	3	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2020425	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	86335.6016	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	391692	
TrqOviSta_HapticState_State_M_enum	2	
TrqOviSta_Hapticdur_Cnt_M_u16	31000	
TrqOviSta_HapticTime_Sec_M_f32	210780	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007	
TrqOviSta_LKAFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_u08	2	
TrqOviSta_LKASState_State_M_enum	0	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	41	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	0	
k_APAMaxHwTrqTime_Sec_f32	0.232999995	
k_APAMaxHwTrq_HwNm_f32	8.72000027	
k_APAMaxVehSpd_Kph_f32	196.199997	
k_ESCMaxVehSpd_Kph_f32	174.5	
k_HapticAmplitude_MtrNm_f32	0.671999991	
k_HapticDuration_Sec_f32	1.63499999	
k_HapticFreq_Hz_f32	55.9459991	
k_HapticReactTime_Sec_f32	1.74399996	
k_LKAMaxVehSpd_Kph_f32	220	
k_LKAMinVehSpd_Kph_f32	196	
k_MaxHapticReqTime_Sec_f32	0.135000005	
k_SWARLimiter_HwDeg_f32	19.6000004	
k_ShiftLevRevTime_Sec_f32	0.0329999998	
k_StandstillThresh_Kph_f32	4.36000013	
k_StandstillTime_Sec_f32	0.0329999998	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-948.210022		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	839.179993		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	210780	210780 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.99 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2054388
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87135.2813
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	398238
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	33000
TrqOvlSta_HapticTime_Sec_M_f32	213785
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	42.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.244000003
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	179.600006
k_HapticAmplitude_MtrNm_f32	0.677999973
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	56.4599991
k_HapticReactime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	105.75
k_LKAMinVehSpd_Kph_f32	197.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	19.8999996
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-938.109985
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	844.119995
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	51.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	72.0999985
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	8.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87135.2813	87135.2813 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	33000	33000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	42.4000015	42.4000015 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	42.4000015	42.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.677999973	0.677999973 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	56.4599991	56.4599991 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.100 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	41000
TrqOviSta_HapticTime_Sec_M_f32	216790.344
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReactTime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	849.059998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784	404784	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	41000	41000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.101 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2122314
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	88734.6406
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	411330
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	43000
TrqOviSta_HapticTime_Sec_M_f32	219795.688
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.266000003
k_APAMaxHwTrq_HwNm_f32	9.19999981
k_APAMaxVehSpd_Kph_f32	207
k_ESCMaxVehSpd_Kph_f32	19.2999992
k_HapticAmplitude_MtrNm_f32	0.75
k_HapticDuration_Sec_f32	1.72500002
k_HapticFreq_Hz_f32	57.487999
k_HapticReacttime_Sec_f32	1.84000003
k_LKAMaxVehSpd_Kph_f32	218.5
k_LKAMinVehSpd_Kph_f32	201.25
k_MaxHapticReqTime_Sec_f32	0.224999994
k_SWARLimiter_HwDeg_f32	20.5
k_ShiftLevRevTime_Sec_f32	0.057
k_StandstillThresh_Kph_f32	4.5999999
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResponse_Cnt_lgc	target_TrqOviSta_Per1APAResponse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResponse_Cnt_lgc	target_TrqOviSta_Per1_LKAResponse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResponse_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-917.909973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	854
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-5.5999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResponse_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	72.0999985
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	92.6999969
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.6999998		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	43000	43000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	219795.688	219795.688 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.31479979	-4.31479979 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008	45.2000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	45.2000008	45.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	57.487999	57.487999 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.102 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	1	
TrqOviSta_ESCState_State_M_enum	2	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2156277	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	89534.3203	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	417876	
TrqOviSta_HapticState_State_M_enum	2	
TrqOviSta_Hapticdur_Cnt_M_u16	45000	
TrqOviSta_HapticTime_Sec_M_f32	222801.016	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029	
TrqOviSta_LKAFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_u08	2	
TrqOviSta_LKASState_State_M_enum	2	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	46.5999985	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.27700001	
k_APAMaxHwTrq_HwNm_f32	9.27999973	
k_APAMaxVehSpd_Kph_f32	208.800003	
k_ESCMaxVehSpd_Kph_f32	23	
k_HapticAmplitude_MtrNm_f32	0.300000012	
k_HapticDuration_Sec_f32	1.74000001	
k_HapticFreq_Hz_f32	58.0019989	
k_HapticReactTime_Sec_f32	1.85599995	
k_LKAMaxVehSpd_Kph_f32	20	
k_LKAMinVehSpd_Kph_f32	203	
k_MaxHapticReqTime_Sec_f32	0.239999995	
k_SWARLimiter_HwDeg_f32	20.7999992	
k_ShiftLevRevTime_Sec_f32	0.0610000007	
k_StandstillThresh_Kph_f32	4.63999987	
k_StandstillTime_Sec_f32	0.0610000007	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-907.809998		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	858.940002		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.3000002		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	45000	45000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	222801.016	222801.016 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	58.0019989	58.0019989 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.103 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2190240
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	90334
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	424422
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Haptidur_Cnt_M_u16	43000
TrqOvlSta_HapticTime_Sec_M_f32	225806.359
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKASState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	48
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.287999988
k_APAMaxHwTrq_HwNm_f32	9.19999981
k_APAMaxVehSpd_Kph_f32	207
k_ESCMaxVehSpd_Kph_f32	26.7000008
k_HapticAmplitude_MtrNm_f32	0.349999994
k_HapticDuration_Sec_f32	1.72500002
k_HapticFreq_Hz_f32	58.5159988
k_HapticReactTime_Sec_f32	1.84000003
k_LKAMaxVehSpd_Kph_f32	218.5
k_LKAMinVehSpd_Kph_f32	201.25
k_MaxHapticReqTime_Sec_f32	0.224999994
k_SWARLimiter_HwDeg_f32	21.1000004
k_ShiftLevRevTime_Sec_f32	0.057
k_StandstillThresh_Kph_f32	4.5999999
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHoscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-897.710022
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	863.880005
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-5.5999999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	92.6999969
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	113.300003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	14.8999996
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	43000	43000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.31479979	-4.31479979 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	48	48 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	48	48 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.349999994	0.349999994 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	58.5159988	58.5159988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.104 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	45000
TrqOviSta_HapticTime_Sec_M_f32	228811.703
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReactTime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.061000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0.061000007
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	868.820007
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	103
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.105 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	29000
TrqOviSta_HapticTime_Sec_M_f32	198760.641
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	50.7999992

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	34.0999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	21.7000008
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-877.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	873.76001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.400000006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	133.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.1000004		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	365508	365508	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	198760.641	198760.641 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	50.7999992	50.7999992 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	50.7999992	50.7999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.106 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1918536
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83936.5625
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	372054
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	31000
TrqOviSta_HapticTime_Sec_M_f32	201765
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	52.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.375999987
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	37.7999992
k_HapticAmplitude_MtrNm_f32	0.653999984
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	100
k_HapticReactTime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	207.100006
k_LKAMinVehSpd_Kph_f32	220
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	22
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-867.409973		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	878.700012		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	144.199997		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	19.7000008		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	201765	201765 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.653999984	0.653999984 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	100	100 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.107 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	33000
TrqOvlSta_HapticTime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	53.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.386999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	41.5
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	60.5719986
k_HapticReactTime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	22.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHoscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-857.309998
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	883.640015
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	133.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	154.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	21.2999992
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	53.5999985	53.5999985 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	53.5999985	53.5999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	60.5719986	60.5719986 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.108 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.26001
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	860.26001
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	860.26001
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	35000
TrqOviSta_HapticTime_Sec_M_f32	860.26001
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	62
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.398000002
k_APAMaxHwTrq_HwNm_f32	0.100000001
k_APAMaxVehSpd_Kph_f32	199.800003
k_ESCMaxVehSpd_Kph_f32	63.7000008
k_HapticAmplitude_MtrNm_f32	0.119999997
k_HapticDuration_Sec_f32	1.66499996
k_HapticFreq_Hz_f32	3.5
k_HapticReactTime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	0.400000006
k_MaxHapticReqTime_Sec_f32	0.165000007
k_SWARLimiter_HwDeg_f32	24.1000004
k_ShiftLevRevTime_Sec_f32	0.0379999988
k_StandstillThresh_Kph_f32	4.44000006
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-847.210022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	888.580017
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	216.300003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.8999996
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.26001	860.26001 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	860.26001	860.26001	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	35000	35000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.37099981	-6.37099981 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	62	62 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	62	62 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.119999997	0.119999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3.5	3.5 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.109 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1345
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1345
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	1345
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	37000
TrqOviSta_HapticTime_Sec_M_f32	1345
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	63.4000015

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.409000009
k_APAMaxHwTrq_HwNm_f32	0.0799999982
k_APAMaxVehSpd_Kph_f32	201.600006
k_ESCMaxVehSpd_Kph_f32	67.4000015
k_HapticAmplitude_MtrNm_f32	0.140000001
k_HapticDuration_Sec_f32	1.67999995
k_HapticFreq_Hz_f32	4.19999981
k_HapticReacttime_Sec_f32	0.0160000008
k_LKAMaxVehSpd_Kph_f32	1.89999998
k_LKAMinVehSpd_Kph_f32	1.75
k_MaxHapticReqTime_Sec_f32	0.180000007
k_SWARLimiter_HwDeg_f32	24.3999996
k_ShiftLevRevTime_Sec_f32	0.0390000008
k_StandstillThresh_Kph_f32	4.48000002
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-837.109985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	893.52002
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.30000019
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	514.799988
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	226.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	32.5		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1345	1345 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	37000	37000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	1345	1345 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999	-4.0999999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.140000001	0.140000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	4.19999981	4.19999981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.110 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1830.43005
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1830.43005
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	1830.43005
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	39000
TrqOviSta_HapticTime_Sec_M_f32	1830.43005
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	64.8000031
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.419999987
k_APAMaxHwTrq_HwNm_f32	0.159999996
k_APAMaxVehSpd_Kph_f32	203.399994
k_ESCMaxVehSpd_Kph_f32	71.0999985
k_HapticAmplitude_MtrNm_f32	0.159999996
k_HapticDuration_Sec_f32	1.69500005
k_HapticFreq_Hz_f32	4.9000001
k_HapticReactTime_Sec_f32	0.0320000015
k_LKAMaxVehSpd_Kph_f32	3.79999995
k_LKAMinVehSpd_Kph_f32	3.5
k_MaxHapticReqTime_Sec_f32	0.194999993
k_SWARLimiter_HwDeg_f32	24.7000008
k_ShiftLevRevTime_Sec_f32	0.0399999991
k_StandstillThresh_Kph_f32	4.51999998
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-827.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	898.460022		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-7.9000001		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	250.600006		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	236.899994		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1999998		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	39000	39000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75	-3.75 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	64.8000031	64.8000031 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	64.8000031	64.8000031 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.159999996	0.159999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.9000001	4.9000001 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.111 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2315.65991
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2315.65991
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	2315.65991
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Haptidur_Cnt_M_u16	41000
TrqOvlSta_HapticTime_Sec_M_f32	2315.65991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	66.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.430999994
k_APAMaxHwTrq_HwNm_f32	0.239999995
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	74.8000031
k_HapticAmplitude_MtrNm_f32	0.180000007
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	5.5999999
k_HapticReactTime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.69999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	25
k_ShiftLevRevTime_Sec_f32	0.0410000011
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHoscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-816.909973
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	903.400024
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-7.5
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	190.600006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.3000002
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2315.65991	2315.65991 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2315.65991	2315.65991 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	2315.65991	2315.65991 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.38850021	-5.38850021 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	5.5999999	5.5999999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.112 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2800.93994
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2800.93994
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	2800.93994
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	43000
TrqOviSta_HapticTime_Sec_M_f32	2800.93994
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	67.5999985
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.442000002
k_APAMaxHwTrq_HwNm_f32	0.319999993
k_APAMaxVehSpd_Kph_f32	207
k_ESCMaxVehSpd_Kph_f32	78.5
k_HapticAmplitude_MtrNm_f32	0.200000003
k_HapticDuration_Sec_f32	1.72500002
k_HapticFreq_Hz_f32	6.30000019
k_HapticReactTime_Sec_f32	0.064000003
k_LKAMaxVehSpd_Kph_f32	7.5999999
k_LKAMinVehSpd_Kph_f32	7
k_MaxHapticReqTime_Sec_f32	0.224999994
k_SWARLimiter_HwDeg_f32	25.2999992
k_ShiftLevRevTime_Sec_f32	0.0419999994
k_StandstillThresh_Kph_f32	4.5999999
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-806.809998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	908.340027
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-7.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	200.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	511
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	12.3999996
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	43000	43000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995	-3.04999995 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	67.5999985	67.5999985 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	67.5999985	67.5999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	6.30000019	6.30000019 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.113 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3285.03003
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3285.03003
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	3285.03003
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	45000
TrqOviSta_HapticTime_Sec_M_f32	3285.03003
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	69

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.453000009
k_APAMaxHwTrq_HwNm_f32	0.400000006
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	82.1999969
k_HapticAmplitude_MtrNm_f32	0.219999999
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	7
k_HapticReacttime_Sec_f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k_LKAMinVehSpd_Kph_f32	8.75
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	25.6000004
k_ShiftLevRevTime_Sec_f32	0.0430000015
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0.0410000011
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-796.710022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	913.280029
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	211.199997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	255.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.5		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	45000	45000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.219999999	0.219999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7	7 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.114 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3770.09009
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3770.09009
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	3770.09009
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	47000
TrqOviSta_HapticTime_Sec_M_f32	3770.09009
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.518000007
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	70.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.463999987
k_APAMaxHwTrq_HwNm_f32	0.479999989
k_APAMaxVehSpd_Kph_f32	210.600006
k_ESCMaxVehSpd_Kph_f32	85.9000015
k_HapticAmplitude_MtrNm_f32	0.239999995
k_HapticDuration_Sec_f32	1.755
k_HapticFreq_Hz_f32	7.69999981
k_HapticReactTime_Sec_f32	0.0960000008
k_LKAMaxVehSpd_Kph_f32	11.3999996
k_LKAMinVehSpd_Kph_f32	10.5
k_MaxHapticReqTime_Sec_f32	0.254999995
k_SWARLimiter_HwDeg_f32	25.8999996
k_ShiftLevRevTime_Sec_f32	0.0439999998
k_StandstillThresh_Kph_f32	4.67999983
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-786.609985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	918.219971		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.30000019		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	221.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	190.600006		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	14.6000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3770.09009	3770.09009 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	47000	47000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.39610004	-4.39610004 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	70.4000015	70.4000015 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	70.4000015	70.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.239999995	0.239999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.69999981	7.69999981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.115 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	425564
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	425564
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	425564
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	49000
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.528999984
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	71.8000031
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.474999994
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	212.399994
k_ESCMaxVehSpd_Kph_f32	89.5999985
k_HapticAmplitude_MtrNm_f32	0.25999999
k_HapticDuration_Sec_f32	1.76999998
k_HapticFreq_Hz_f32	8.39999962
k_HapticReactTime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.270000011
k_SWARLimiter_HwDeg_f32	26.2000008
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	4.71999979
k_StandstillTime_Sec_f32	0.0489999987
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-776.51001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	923.159973
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-5.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	200.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15.6999998
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	425564	425564	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	49000	49000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.25999999	0.25999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	8.39999962	8.39999962 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.116 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4740.43018
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	4740.43018
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4740.43018
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	51000
TrqOviSta_HapticTime_Sec_M_f32	4740.43018
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.540000021
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	73.1999969
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.486000001
k_APAMaxHwTrq_HwNm_f32	0.639999986
k_APAMaxVehSpd_Kph_f32	214.199997
k_ESCMaxVehSpd_Kph_f32	93.3000031
k_HapticAmplitude_MtrNm_f32	0.280000001
k_HapticDuration_Sec_f32	1.78499997
k_HapticFreq_Hz_f32	9.10000038
k_HapticReactTime_Sec_f32	0.128000006
k_LKAMaxVehSpd_Kph_f32	15.1999998
k_LKAMinVehSpd_Kph_f32	14
k_MaxHapticReqTime_Sec_f32	0.284999996
k_SWARLimiter_HwDeg_f32	26.5
k_ShiftLevRevTime_Sec_f32	0.0460000001
k_StandstillThresh_Kph_f32	4.76000023
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-766.409973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	928.099976
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-5.5
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	242.100006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	211.199997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.7999992
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	51000	51000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	73.1999969	73.1999969 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	73.1999969	73.1999969 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.280000001	0.280000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	9.10000038	9.10000038 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.117 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5225.33984
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5225.33984
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	5225.33984
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	53000
TrqOviSta_HapticTime_Sec_M_f32	5225.33984
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.29999995
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.550999999
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	74.5999985

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.497000009
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	216
k_ESCMaxVehSpd_Kph_f32	97
k_HapticAmplitude_MtrNm_f32	0.300000012
k_HapticDuration_Sec_f32	1.79999995
k_HapticFreq_Hz_f32	9.80000019
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	26.7999992
k_ShiftLevRevTime_Sec_f32	0.0469999984
k_StandstillThresh_Kph_f32	4.80000019
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-756.309998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	933.039978
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-5.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	252.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	221.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	17.8999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5225.33984	5225.33984 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5225.33984	5225.33984 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	53000	53000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	5225.33984	5225.33984 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.39380002	-3.39380002 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	9.80000019	9.80000019 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.118 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5710.16992
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5710.16992
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	5710.16992
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	55000
TrqOviSta_HapticTime_Sec_M_f32	5710.16992
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.949999988
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.561999977
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	76
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.508000016
k_APAMaxHwTrq_HwNm_f32	0.800000012
k_APAMaxVehSpd_Kph_f32	217.800003
k_ESCMaxVehSpd_Kph_f32	100.699997
k_HapticAmplitude_MtrNm_f32	0.319999993
k_HapticDuration_Sec_f32	1.81500006
k_HapticFreq_Hz_f32	10.5
k_HapticReactTime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	19
k_LKAMinVehSpd_Kph_f32	17.5
k_MaxHapticReqTime_Sec_f32	0.314999998
k_SWARLimiter_HwDeg_f32	27.1000004
k_ShiftLevRevTime_Sec_f32	0.0480000004
k_StandstillThresh_Kph_f32	4.84000015
k_StandstillTime_Sec_f32	0.0610000007
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAREquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-746.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	937.97998		
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	262.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	19		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APASState_State_M_enum	4	4	✔
TrqOvlSta_ESCState_State_M_enum	4	4	✔
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOvlSta_HapticState_State_M_enum	1	1	✔
TrqOvlSta_Hapticdur_Cnt_M_u16	55000	55000 ± 1	✔
TrqOvlSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.949999988	-0.949999988 ± 0.0000009	✔
TrqOvlSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✔
TrqOvlSta_LKASState_State_M_enum	4	4	✔
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	76	76 ± 0.0009765625	✔
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	76	76 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.319999993	0.319999993 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	10.5	10.5 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.119 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6195
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	6195
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	6195
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptidur_Cnt_M_u16	57000
TrqOvlSta_HapticTime_Sec_M_f32	6195
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.573000014
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKASState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	77.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.518999994
k_APAMaxHwTrq_HwNm_f32	0.879999995
k_APAMaxVehSpd_Kph_f32	219.600006
k_ESCMaxVehSpd_Kph_f32	104.400002
k_HapticAmplitude_MtrNm_f32	0.340000004
k_HapticDuration_Sec_f32	1.83000004
k_HapticFreq_Hz_f32	11.1999998
k_HapticReactTime_Sec_f32	0.175999999
k_LKAMaxVehSpd_Kph_f32	20.8999996
k_LKAMinVehSpd_Kph_f32	19.25
k_MaxHapticReqTime_Sec_f32	0.330000013
k_SWARLimiter_HwDeg_f32	27.3999996
k_ShiftLevRevTime_Sec_f32	0.0489999987
k_StandstillThresh_Kph_f32	4.88000011
k_StandstillTime_Sec_f32	0.0649999976
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-736.109985
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	942.919983
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-4.30000019
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	273
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	242.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.1000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	6195	6195 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	57000	57000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	6195	6195 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024	-0.600000024 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.340000004	0.340000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	11.1999998	11.1999998 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.120 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6679.83008
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	6679.83008
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	6679.83008
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	59000
TrqOviSta_HapticTime_Sec_M_f32	6679.83008
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.583999991
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	78.8000031
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.529999971
k_APAMaxHwTrq_HwNm_f32	0.959999979
k_APAMaxVehSpd_Kph_f32	0.200000003
k_ESCMaxVehSpd_Kph_f32	108.099998
k_HapticAmplitude_MtrNm_f32	0.360000014
k_HapticDuration_Sec_f32	1.84500003
k_HapticFreq_Hz_f32	11.8999996
k_HapticReactTime_Sec_f32	0.192000002
k_LKAMaxVehSpd_Kph_f32	22.7999992
k_LKAMinVehSpd_Kph_f32	21
k_MaxHapticReqTime_Sec_f32	0.344999999
k_SWARLimiter_HwDeg_f32	27.7000008
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	4.92000008
k_StandstillTime_Sec_f32	0.0689999983
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-726.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	947.859985
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-3.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	252.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	21.2000008
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6679.83008	6679.83008 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	6679.83008	6679.83008	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	59000	59000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.38160014	-2.3815999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	78.8000031	78.8000031 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	78.8000031	78.8000031 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.360000014	0.360000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	11.8999996	11.8999996 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.121 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7164.66016
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	7164.66016
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	7164.66016
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	61000
TrqOviSta_HapticTime_Sec_M_f32	7164.66016
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.595000029
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	80.1999969

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.541000009
k_APAMaxHwTrq_HwNm_f32	1.039999996
k_APAMaxVehSpd_Kph_f32	1.799999995
k_ESCMaxVehSpd_Kph_f32	111.8000003
k_HapticAmplitude_MtrNm_f32	0.379999995
k_HapticDuration_Sec_f32	1.860000001
k_HapticFreq_Hz_f32	12.60000004
k_HapticReacttime_Sec_f32	0.2080000004
k_LKAMaxVehSpd_Kph_f32	24.70000008
k_LKAMinVehSpd_Kph_f32	22.75
k_MaxHapticReqTime_Sec_f32	0.3600000014
k_SWARLimiter_HwDeg_f32	28
k_ShiftLevRevTime_Sec_f32	0.004999999989
k_StandstillThresh_Kph_f32	4.960000004
k_StandstillTime_Sec_f32	0.0729999989
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-715.909973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	952.799988
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-3.5
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	293.600006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	262.700012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	7164.66016	7164.66016 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	61000	61000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	7164.66016	7164.66016 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001	0.100000001 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.379999995	0.379999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	12.6000004	12.6000004 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.122 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	4	
TrqOviSta_ESCState_State_M_enum	2	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7649.49023	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	7649.49023	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	7649.49023	
TrqOviSta_HapticState_State_M_enum	2	
TrqOviSta_Hapticdur_Cnt_M_u16	63000	
TrqOviSta_HapticTime_Sec_M_f32	7649.49023	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.606000006	
TrqOviSta_LKAFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_u08	4	
TrqOviSta_LKASState_State_M_enum	4	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	81.5999985	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.551999986	
k_APAMaxHwTrq_HwNm_f32	1.12	
k_APAMaxVehSpd_Kph_f32	3.5999999	
k_ESCMaxVehSpd_Kph_f32	115.5	
k_HapticAmplitude_MtrNm_f32	0.400000006	
k_HapticDuration_Sec_f32	1.875	
k_HapticFreq_Hz_f32	13.3000002	
k_HapticReactTime_Sec_f32	0.224000007	
k_LKAMaxVehSpd_Kph_f32	26.6000004	
k_LKAMinVehSpd_Kph_f32	24.5	
k_MaxHapticReqTime_Sec_f32	0.375	
k_SWARLimiter_HwDeg_f32	28.2999992	
k_ShiftLevRevTime_Sec_f32	0.00899999961	
k_StandstillThresh_Kph_f32	0	
k_StandstillTime_Sec_f32	0.0769999996	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-705.809998		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	957.73999		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-3.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	303.899994		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	273		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	23.3999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	63000	63000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988	0.449999988 ± 0.0000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	81.5999985	81.5999985 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	81.5999985	81.5999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.400000006	0.400000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	13.3000002	13.3000002 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.123 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8134.31982
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	8134.31982
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	8134.31982
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	1596
TrqOviSta_HapticTime_Sec_M_f32	8134.31982
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.800000012
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.616999984
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	83
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.563000023
k_APAMaxHwTrq_HwNm_f32	1.20000005
k_APAMaxVehSpd_Kph_f32	5.4000001
k_ESCMaxVehSpd_Kph_f32	119.199997
k_HapticAmplitude_MtrNm_f32	0.419999987
k_HapticDuration_Sec_f32	1.88999999
k_HapticFreq_Hz_f32	14
k_HapticReactTime_Sec_f32	0.239999995
k_LKAMaxVehSpd_Kph_f32	28.5
k_LKAMinVehSpd_Kph_f32	26.25
k_MaxHapticReqTime_Sec_f32	0.389999986
k_SWARLimiter_HwDeg_f32	28.6000004
k_ShiftLevRevTime_Sec_f32	0.0130000003
k_StandstillThresh_Kph_f32	0.0399999991
k_StandstillTime_Sec_f32	0.00499999989
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-695.710022
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	962.679993
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-2.70000005
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	314.200012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	283.299988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8134.31982	8134.31982 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	8134.31982	8134.31982 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	1596	1596 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	8134.31982	8134.31982 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.35949993	-1.35950005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.419999987	0.419999987 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	14	14 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.124 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8619.15039
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	8619.15039
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	8619.15039
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	2578
TrqOviSta_HapticTime_Sec_M_f32	8619.15039
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.14999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.628000021
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	84.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.574000001
k_APAMaxHwTrq_HwNm_f32	1.27999997
k_APAMaxVehSpd_Kph_f32	7.19999981
k_ESCMaxVehSpd_Kph_f32	122.900002
k_HapticAmplitude_MtrNm_f32	0.439999998
k_HapticDuration_Sec_f32	1.90499997
k_HapticFreq_Hz_f32	14.6999998
k_HapticReactTime_Sec_f32	0.256000012
k_LKAMaxVehSpd_Kph_f32	30.3999996
k_LKAMinVehSpd_Kph_f32	28
k_MaxHapticReqTime_Sec_f32	0.405000001
k_SWARLimiter_HwDeg_f32	28.8999996
k_ShiftLevRevTime_Sec_f32	0.017000009
k_StandstillThresh_Kph_f32	0.0799999982
k_StandstillTime_Sec_f32	0.00899999961
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-685.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	967.619995
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-2.29999995
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	324.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	293.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	25.6000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	2578	2578 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.14999998	1.14999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	84.4000015	84.4000015 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	84.4000015	84.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.439999998	0.439999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	14.6999998	14.6999998 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.125 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	9103.98047
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	9103.98047
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	9103.98047
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	3560
TrqOviSta_HapticTime_Sec_M_f32	9103.98047
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.638999999
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	85.8000031

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.584999979
k_APAMaxHwTrq_HwNm_f32	1.36000001
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	126.599998
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.91999996
k_HapticFreq_Hz_f32	15.3999996
k_HapticReacttime_Sec_f32	0.272000015
k_LKAMaxVehSpd_Kph_f32	32.2999992
k_LKAMinVehSpd_Kph_f32	29.75
k_MaxHapticReqTime_Sec_f32	0.419999987
k_SWARLimiter_HwDeg_f32	29.2000008
k_ShiftLevRevTime_Sec_f32	0.0209999997
k_StandstillThresh_Kph_f32	0.119999997
k_StandstillTime_Sec_f32	0.013000003
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-675.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	972.559998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-1.89999998
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	303.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.7000008		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	9103.98047	9103.98047 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	3560	3560 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	9103.98047	9103.98047 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5	1.5 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	15.3999996	15.3999996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.126 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	9588.80957
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	9588.80957
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	9588.80957
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	4542
TrqOviSta_HapticTime_Sec_M_f32	9588.80957
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.85000002
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.649999976
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	87.1999969
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.596000016
k_APAMaxHwTrq_HwNm_f32	1.44000006
k_APAMaxVehSpd_Kph_f32	10.8000002
k_ESCMaxVehSpd_Kph_f32	130.300003
k_HapticAmplitude_MtrNm_f32	0.479999989
k_HapticDuration_Sec_f32	1.93499994
k_HapticFreq_Hz_f32	16.1000004
k_HapticReactTime_Sec_f32	0.287999988
k_LKAMaxVehSpd_Kph_f32	34.2000008
k_LKAMinVehSpd_Kph_f32	31.5
k_MaxHapticReqTime_Sec_f32	0.435000002
k_SWARLimiter_HwDeg_f32	29.5
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	0.159999996
k_StandstillTime_Sec_f32	0.0170000009
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-665.409973		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	977.5		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-1.5		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	345.100006		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	314.200012		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.00999999978		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	27.7999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	4542	4542 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.327499747	-0.327499986 ± 0.0000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	87.1999969	87.1999969 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	87.1999969	87.1999969 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.479999989	0.479999989 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	16.1000004	16.1000004 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.127 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	10073.6396
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	10073.6396
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	10073.6396
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	5524
TrqOviSta_HapticTime_Sec_M_f32	10073.6396
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.661000013
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	88.5999985
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.606999993
k_APAMaxHwTrq_HwNm_f32	1.51999998
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	134
k_HapticAmplitude_MtrNm_f32	0.5
k_HapticDuration_Sec_f32	1.95000005
k_HapticFreq_Hz_f32	16.7999992
k_HapticReactTime_Sec_f32	0.30399999
k_LKAMaxVehSpd_Kph_f32	36.0999985
k_LKAMinVehSpd_Kph_f32	33.25
k_MaxHapticReqTime_Sec_f32	0.449999988
k_SWARLimiter_HwDeg_f32	29.7999992
k_ShiftLevRevTime_Sec_f32	0.028999992
k_StandstillThresh_Kph_f32	0.200000003
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enun	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-655.309998
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	982.440002
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-1.10000002
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	355.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	324.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-34.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	10073.6396	10073.6396 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	5524	5524 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	10073.6396	10073.6396 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005	2.20000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.100000001	0.100000001 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.100000001	0.100000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.5	0.5 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	16.7999992	16.7999992 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.128 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	10558.4697
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	10558.4697
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	10558.4697
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	6506
TrqOviSta_HapticTime_Sec_M_f32	10558.4697
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.54999995
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.671999991
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	10.1999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.617999971
k_APAMaxHwTrq_HwNm_f32	1.60000002
k_APAMaxVehSpd_Kph_f32	14.3999996
k_ESCMaxVehSpd_Kph_f32	137.699997
k_HapticAmplitude_MtrNm_f32	0.519999981
k_HapticDuration_Sec_f32	1.96500003
k_HapticFreq_Hz_f32	17.5
k_HapticReactTime_Sec_f32	0.319999993
k_LKAMaxVehSpd_Kph_f32	38
k_LKAMinVehSpd_Kph_f32	35
k_MaxHapticReqTime_Sec_f32	0.465000004
k_SWARLimiter_HwDeg_f32	30.1000004
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	0.239999995
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-645.210022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	987.380005
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-0.699999988
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	365.700012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	334.799988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	6506	6506 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.54999995	2.54999995 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	10.1999998	10.1999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	10.1999998	10.1999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.519999981	0.519999981 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	17.5	17.5 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.129 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11043.2998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	11043.2998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	11043.2998
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	7488
TrqOviSta_HapticTime_Sec_M_f32	11043.2998
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.9000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.683000028
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	20.2000008

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.629000008
k_APAMaxHwTrq_HwNm_f32	1.67999995
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	141.399994
k_HapticAmplitude_MtrNm_f32	0.540000021
k_HapticDuration_Sec_f32	1.98000002
k_HapticFreq_Hz_f32	18.2000008
k_HapticReacttime_Sec_f32	0.335999995
k_LKAMaxVehSpd_Kph_f32	39.9000015
k_LKAMinVehSpd_Kph_f32	36.75
k_MaxHapticReqTime_Sec_f32	0.479999989
k_SWARLimiter_HwDeg_f32	30.3999996
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-635.109985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	992.320007
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-0.300000012
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	376
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	345.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	11043.2998	11043.2998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	7488	7488 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	11043.2998	11043.2998 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.714400053	0.714399993 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.119999997	0.119999997 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.119999997	0.119999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	18.2000008	18.2000008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.130 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11528.1299
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	11528.1299
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	11528.1299
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	8470
TrqOviSta_HapticTime_Sec_M_f32	11528.1299
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.25
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.694000006
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	30.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.639999986
k_APAMaxHwTrq_HwNm_f32	1.75999999
k_APAMaxVehSpd_Kph_f32	18
k_ESCMaxVehSpd_Kph_f32	145.100006
k_HapticAmplitude_MtrNm_f32	0.560000002
k_HapticDuration_Sec_f32	1.995
k_HapticFreq_Hz_f32	18.8999996
k_HapticReactTime_Sec_f32	0.351999998
k_LKAMaxVehSpd_Kph_f32	41.7999992
k_LKAMinVehSpd_Kph_f32	38.5
k_MaxHapticReqTime_Sec_f32	0.495000005
k_SWARLimiter_HwDeg_f32	30.7000008
k_ShiftLevRevTime_Sec_f32	0.0410000011
k_StandstillThresh_Kph_f32	0.319999993
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-625.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	997.26001		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.100000001		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	355.399994		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	8470	8470 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.25	3.25 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	30.2000008	30.2000008 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	30.2000008	30.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.560000002	0.560000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	18.8999996	18.8999996 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.131 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12012.96
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12012.96
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	12012.96
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptidur_Cnt_M_u16	9452
TrqOvlSta_HapticTime_Sec_M_f32	12012.96
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.5999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.704999983
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	40.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.651000023
k_APAMaxHwTrq_HwNm_f32	1.84000003
k_APAMaxVehSpd_Kph_f32	19.7999992
k_ESCMaxVehSpd_Kph_f32	148.800003
k_HapticAmplitude_MtrNm_f32	0.579999983
k_HapticDuration_Sec_f32	0.50999999
k_HapticFreq_Hz_f32	19.6000004
k_HapticReactTime_Sec_f32	0.368000001
k_LKAMaxVehSpd_Kph_f32	43.7000008
k_LKAMinVehSpd_Kph_f32	40.25
k_MaxHapticReqTime_Sec_f32	0.50999999
k_SWARLimiter_HwDeg_f32	31
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAResponse_Cnt_Igc	target_TrqOvlSta_Per1_APAResponse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-614.909973
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1002.20001
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.5
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	396.600006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	365.700012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.6000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	12012.96	12012.96 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	9452	9452 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	12012.96	12012.96 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.5999999	3.5999999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.140000001	0.140000001 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.140000001	0.140000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.579999983	0.579999983 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	19.6000004	19.6000004 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.132 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12497.79
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	12497.79
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	12497.79
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	10434
TrqOviSta_HapticTime_Sec_M_f32	12497.79
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.95000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	50.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.662
k_APAMaxHwTrq_HwNm_f32	1.91999996
k_APAMaxVehSpd_Kph_f32	21.6000004
k_ESCMaxVehSpd_Kph_f32	152.5
k_HapticAmplitude_MtrNm_f32	0.600000024
k_HapticDuration_Sec_f32	0.0149999997
k_HapticFreq_Hz_f32	20.2999992
k_HapticReactTime_Sec_f32	0.384000003
k_LKAMaxVehSpd_Kph_f32	45.5999985
k_LKAMinVehSpd_Kph_f32	42
k_MaxHapticReqTime_Sec_f32	0.524999976
k_SWARLimiter_HwDeg_f32	31.2999992
k_ShiftLevRevTime_Sec_f32	0.0489999987
k_StandstillThresh_Kph_f32	0.400000006
k_StandstillTime_Sec_f32	0.0410000011
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-604.809998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1007.14001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.899999976
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	406.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	376
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-32.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12497.79	12497.79 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	10434	10434 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.94390011	3.94390011 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	50.2000008	50.2000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	50.2000008	50.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.600000024	0.600000024 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	20.2999992	20.2999992 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.133 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12982.6201
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	12982.6201
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	12982.6201
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	11416
TrqOviSta_HapticTime_Sec_M_f32	12982.6201
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.30000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	60.2000008

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAMIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.672999978
k_APAMaxHwTrq_HwNm_f32	2
k_APAMaxVehSpd_Kph_f32	23.3999996
k_ESCMaxVehSpd_Kph_f32	156.199997
k_HapticAmplitude_MtrNm_f32	0.620000005
k_HapticDuration_Sec_f32	0.0299999993
k_HapticFreq_Hz_f32	21
k_HapticReacttime_Sec_f32	0.400000006
k_LKAMaxVehSpd_Kph_f32	47.5
k_LKAMinVehSpd_Kph_f32	43.75
k_MaxHapticReqTime_Sec_f32	0.540000021
k_SWARLimiter_HwDeg_f32	31.6000004
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	0.439999998
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-594.710022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1012.08002
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.29999995
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	417.200012
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	386.299988
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	12982.6201	12982.6201 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11416	11416 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	12982.6201	12982.6201 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.30000019	4.30000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.620000005	0.620000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	21	21 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.134 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13467.4502
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	13467.4502
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	13467.4502
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	12398
TrqOviSta_HapticTime_Sec_M_f32	13467.4502
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.6500001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	70.1999969
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.684000015
k_APAMaxHwTrq_HwNm_f32	2.07999992
k_APAMaxVehSpd_Kph_f32	25.2000008
k_ESCMaxVehSpd_Kph_f32	159.899994
k_HapticAmplitude_MtrNm_f32	0.639999986
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	21.7000008
k_HapticReactTime_Sec_f32	0.416000009
k_LKAMaxVehSpd_Kph_f32	49.4000015
k_LKAMinVehSpd_Kph_f32	45.5
k_MaxHapticReqTime_Sec_f32	0.555000007
k_SWARLimiter_HwDeg_f32	31.8999996
k_ShiftLevRevTime_Sec_f32	0.057
k_StandstillThresh_Kph_f32	0.479999989
k_StandstillTime_Sec_f32	0.0489999987
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAREquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1140.10999		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360		
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.70000005		
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	427.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	396.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APASState_State_M_enum	4	4	✔
TrqOvlSta_ESCState_State_M_enum	4	4	✔
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOvlSta_HapticState_State_M_enum	2	2	✔
TrqOvlSta_Hapticdur_Cnt_M_u16	12398	12398 ± 1	✔
TrqOvlSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.6500001	4.6500001 ± 0.000009	✔
TrqOvlSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOvlSta_LKASState_State_M_enum	4	4	✔
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.1999969	70.1999969 ± 0.0009765625	✔
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	70.1999969	70.1999969 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.639999986	0.639999986 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	21.7000008	21.7000008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.135 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13952.2803
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	13952.2803
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13952.2803
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Haptidur_Cnt_M_u16	13380
TrqOvlSta_HapticTime_Sec_M_f32	13952.2803
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKASState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	80.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.694999993
k_APAMaxHwTrq_HwNm_f32	2.16000009
k_APAMaxVehSpd_Kph_f32	27
k_ESCMaxVehSpd_Kph_f32	163.600006
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	0.0599999987
k_HapticFreq_Hz_f32	22.3999996
k_HapticReactime_Sec_f32	0.432000011
k_LKAMaxVehSpd_Kph_f32	51.2999992
k_LKAMinVehSpd_Kph_f32	47.25
k_MaxHapticReqTime_Sec_f32	0.569999993
k_SWARLimiter_HwDeg_f32	32.2000008
k_ShiftLevRevTime_Sec_f32	0.061000007
k_StandstillThresh_Kph_f32	0.519999981
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAResponse_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1100.20996
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1440.10999
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	406.899994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13952.2803	13952.2803 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	13952.2803	13952.2803 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	13952.2803	13952.2803	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	13380	13380 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	13952.2803	13952.2803 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.97505999	4.97505999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.180000007	0.180000007 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	22.3999996	22.3999996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.136 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	14437.1104
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	14437.1104
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	14437.1104
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	14362
TrqOviSta_HapticTime_Sec_M_f32	14437.1104
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.3499999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.70599997
k_APAMaxHwTrq_HwNm_f32	2.24000001
k_APAMaxVehSpd_Kph_f32	28.7999992
k_ESCMaxVehSpd_Kph_f32	167.300003
k_HapticAmplitude_MtrNm_f32	0.680000007
k_HapticDuration_Sec_f32	0.075000003
k_HapticFreq_Hz_f32	23.1000004
k_HapticReactTime_Sec_f32	0.448000014
k_LKAMaxVehSpd_Kph_f32	53.2000008
k_LKAMinVehSpd_Kph_f32	49
k_MaxHapticReqTime_Sec_f32	0.584999979
k_SWARLimiter_HwDeg_f32	32.5
k_ShiftLevRevTime_Sec_f32	0.0649999976
k_StandstillThresh_Kph_f32	0.560000002
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1060.31006
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	750
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.5
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	448.100006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	417.200012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-24.8999996
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	14362	14362 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.3499999	5.3499999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004	11.1000004 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	11.1000004	11.1000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.680000007	0.680000007 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	23.1000004	23.1000004 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.137 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15891.5996
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	15891.5996
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	15891.5996
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	17308
TrqOviSta_HapticTime_Sec_M_f32	15891.5996
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.739000022
k_APAMaxHwTrq_HwNm_f32	2.48000002
k_APAMaxVehSpd_Kph_f32	34.2000008
k_ESCMaxVehSpd_Kph_f32	19.2000008
k_HapticAmplitude_MtrNm_f32	0.129999995
k_HapticDuration_Sec_f32	0.119999997
k_HapticFreq_Hz_f32	25.2000008
k_HapticReacttime_Sec_f32	0.495999992
k_LKAMaxVehSpd_Kph_f32	58.9000015
k_LKAMinVehSpd_Kph_f32	54.25
k_MaxHapticReqTime_Sec_f32	0.629999995
k_SWARLimiter_HwDeg_f32	33.4000015
k_ShiftLevRevTime_Sec_f32	0.0769999996
k_StandstillThresh_Kph_f32	0.680000007
k_StandstillTime_Sec_f32	0.0689999983
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1440.10999
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1007.14001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.70000005
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	479
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	448.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	15891.5996	15891.5996 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	17308	17308 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	15891.5996	15891.5996 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.4000001	6.4000001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.119999997	0.119999997 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.119999997	0.119999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	25.2000008	25.2000008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.138 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16376.4297
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	16376.4297
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	16376.4297
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	18290
TrqOviSta_HapticTime_Sec_M_f32	16376.4297
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.75
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0196000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	15.5
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.75
k_APAMaxHwTrq_HwNm_f32	2.55999994
k_APAMaxVehSpd_Kph_f32	36
k_ESCMaxVehSpd_Kph_f32	24.7000008
k_HapticAmplitude_MtrNm_f32	0.150000006
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	25.8999996
k_HapticReactTime_Sec_f32	0.512000024
k_LKAMaxVehSpd_Kph_f32	60.7999992
k_LKAMinVehSpd_Kph_f32	56
k_MaxHapticReqTime_Sec_f32	0.644999981
k_SWARLimiter_HwDeg_f32	33.7000008
k_ShiftLevRevTime_Sec_f32	0.0810000002
k_StandstillThresh_Kph_f32	0.720000029
k_StandstillTime_Sec_f32	0.0729999989
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-360		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1012.08002		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	489.299988		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	458.399994		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	18290	18290 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.75	6.75 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	15.5	15.5 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	15.5	15.5 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.8999996	25.8999996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.139 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16861.2598
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	16861.2598
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	16861.2598
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Haptidur_Cnt_M_u16	19272
TrqOvlSta_HapticTime_Sec_M_f32	16861.2598
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.0999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.760999978
k_APAMaxHwTrq_HwNm_f32	2.6400001
k_APAMaxVehSpd_Kph_f32	37.7999992
k_ESCMaxVehSpd_Kph_f32	30.2000008
k_HapticAmplitude_MtrNm_f32	0.170000002
k_HapticDuration_Sec_f32	0.150000006
k_HapticFreq_Hz_f32	26.6000004
k_HapticReactTime_Sec_f32	0.527999997
k_LKAMaxVehSpd_Kph_f32	62.7000008
k_LKAMinVehSpd_Kph_f32	57.75
k_MaxHapticReqTime_Sec_f32	0.660000026
k_SWARLimiter_HwDeg_f32	34
k_ShiftLevRevTime_Sec_f32	0.085000009
k_StandstillThresh_Kph_f32	0.75999999
k_StandstillTime_Sec_f32	0.0769999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-750
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1017.02002
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.5
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	499.600006
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	468.700012
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16861.2598	16861.2598 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	16861.2598	16861.2598 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	19272	19272 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	16861.2598	16861.2598 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.0433197	7.04332018 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.140000001	0.140000001 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.140000001	0.140000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	26.6000004	26.6000004 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.140 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	18460.6309
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	22218
TrqOviSta_HapticTime_Sec_M_f32	18460.6309
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.14999962
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0284000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	19.8999996
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.79400003
k_APAMaxHwTrq_HwNm_f32	2.88000011
k_APAMaxVehSpd_Kph_f32	43.2000008
k_ESCMaxVehSpd_Kph_f32	46.7000008
k_HapticAmplitude_MtrNm_f32	0.230000004
k_HapticDuration_Sec_f32	0.194999993
k_HapticFreq_Hz_f32	28.7000008
k_HapticReactTime_Sec_f32	0.575999975
k_LKAMaxVehSpd_Kph_f32	68.4000015
k_LKAMinVehSpd_Kph_f32	63
k_MaxHapticReqTime_Sec_f32	0.704999983
k_SWARLimiter_HwDeg_f32	34.9000015
k_ShiftLevRevTime_Sec_f32	0.0970000029
k_StandstillThresh_Kph_f32	0.879999995
k_StandstillTime_Sec_f32	0.0890000015
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1020.40997
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1021.96002
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	499.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.69999981
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309	18460.6309 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	18460.6309	18460.6309	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	22218	22218 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.08041954	8.08041954 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	19.8999996	19.8999996 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	19.8999996	19.8999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.230000004	0.230000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	28.7000008	28.7000008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.141 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21465
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	21465
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	21465
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	23200
TrqOviSta_HapticTime_Sec_M_f32	21465
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	21

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.805000007
k_APAMaxHwTrq_HwNm_f32	2.96000004
k_APAMaxVehSpd_Kph_f32	45
k_ESCMaxVehSpd_Kph_f32	52.2000008
k_HapticAmplitude_MtrNm_f32	0.25
k_HapticDuration_Sec_f32	0.209999993
k_HapticFreq_Hz_f32	29.3999996
k_HapticReacttime_Sec_f32	0.592000008
k_LKAMaxVehSpd_Kph_f32	70.3000031
k_LKAMinVehSpd_Kph_f32	64.75
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	35.2000008
k_ShiftLevRevTime_Sec_f32	0.101000004
k_StandstillThresh_Kph_f32	0.920000017
k_StandstillTime_Sec_f32	0.0930000022
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-980.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1026.90002
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	113.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	98.8000031
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.80000019		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	21465	21465 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	23200	23200 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	21465	21465 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.5	8.5 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCssillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.25	0.25 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	29.3999996	29.3999996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.142 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	24470
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	24470
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	24470
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	24182
TrqOviSta_HapticTime_Sec_M_f32	24470
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.85000038
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0328000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.815999985
k_APAMaxHwTrq_HwNm_f32	3.03999996
k_APAMaxVehSpd_Kph_f32	46.7999992
k_ESCMaxVehSpd_Kph_f32	57.7000008
k_HapticAmplitude_MtrNm_f32	0.270000011
k_HapticDuration_Sec_f32	0.224999994
k_HapticFreq_Hz_f32	30.1000004
k_HapticReactTime_Sec_f32	0.60799998
k_LKAMaxVehSpd_Kph_f32	72.1999969
k_LKAMinVehSpd_Kph_f32	66.5
k_MaxHapticReqTime_Sec_f32	0.735000014
k_SWARLimiter_HwDeg_f32	35.5
k_ShiftLevRevTime_Sec_f32	0.104999997
k_StandstillThresh_Kph_f32	0.959999979
k_StandstillTime_Sec_f32	0.0970000029
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-940.609985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1031.83997		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.5		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	118.400002		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103.699997		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	24182	24182 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.85000038	8.85000038 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004	22.1000004 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	22.1000004	22.1000004 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.270000011	0.270000011 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	30.1000004	30.1000004 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.143 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	27475.5293
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	27475.5293
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	27475.5293
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Haptidur_Cnt_M_u16	25164
TrqOvlSta_HapticTime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.19999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0350000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKASState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	23.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.827000022
k_APAMaxHwTrq_HwNm_f32	3.11999989
k_APAMaxVehSpd_Kph_f32	48.5999985
k_ESCMaxVehSpd_Kph_f32	63.2000008
k_HapticAmplitude_MtrNm_f32	0.289999992
k_HapticDuration_Sec_f32	0.239999995
k_HapticFreq_Hz_f32	30.7999992
k_HapticReactTime_Sec_f32	0.624000013
k_LKAMaxVehSpd_Kph_f32	74.0999985
k_LKAMinVehSpd_Kph_f32	68.25
k_MaxHapticReqTime_Sec_f32	0.75
k_SWARLimiter_HwDeg_f32	35.7999992
k_ShiftLevRevTime_Sec_f32	0.108999997
k_StandstillThresh_Kph_f32	1
k_StandstillTime_Sec_f32	0.101000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-900.710022
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1036.78003
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	6.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	123.300003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	108.599998
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	27475.5293	27475.5293 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	27475.5293	27475.5293 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	25164	25164 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	27475.5293	27475.5293 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.11950016	9.11950016 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.180000007	0.180000007 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.289999992	0.289999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	30.7999992	30.7999992 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.144 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	30480
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	30480
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	30480
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	26146
TrqOviSta_HapticTime_Sec_M_f32	30480
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.55000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0372000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.838
k_APAMaxHwTrq_HwNm_f32	3.20000005
k_APAMaxVehSpd_Kph_f32	50.4000015
k_ESCMaxVehSpd_Kph_f32	68.6999969
k_HapticAmplitude_MtrNm_f32	0.310000002
k_HapticDuration_Sec_f32	0.254999995
k_HapticFreq_Hz_f32	31.5
k_HapticReactTime_Sec_f32	0.671999991
k_LKAMaxVehSpd_Kph_f32	79.8000031
k_LKAMinVehSpd_Kph_f32	73.5
k_MaxHapticReqTime_Sec_f32	0.764999986
k_SWARLimiter_HwDeg_f32	36.0999985
k_ShiftLevRevTime_Sec_f32	0.112999998
k_StandstillThresh_Kph_f32	1.03999996
k_StandstillTime_Sec_f32	0.104999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-860.809998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1041.71997
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	7.30000019
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	128.199997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	113.5
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.0999999
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	26146	26146 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.55000019	9.55000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992	24.2999992 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	24.2999992	24.2999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.310000002	0.310000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	31.5	31.5 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.145 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	33485
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	33485
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	33485
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	27128
TrqOviSta_HapticTime_Sec_M_f32	33485
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.89999962
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0394000001
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	25.3999996

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.848999977
k_APAMaxHwTrq_HwNm_f32	3.27999997
k_APAMaxVehSpd_Kph_f32	52.2000008
k_ESCMaxVehSpd_Kph_f32	74.1999969
k_HapticAmplitude_MtrNm_f32	0.330000013
k_HapticDuration_Sec_f32	0.270000011
k_HapticFreq_Hz_f32	32.2000008
k_HapticReacttime_Sec_f32	0.688000023
k_LKAMaxVehSpd_Kph_f32	81.6999969
k_LKAMinVehSpd_Kph_f32	75.25
k_MaxHapticReqTime_Sec_f32	0.779999971
k_SWARLimiter_HwDeg_f32	36.4000015
k_ShiftLevRevTime_Sec_f32	0.116999999
k_StandstillThresh_Kph_f32	1.08000004
k_StandstillTime_Sec_f32	0.108999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-820.909973
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1046.66003
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	7.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	118.400002
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-0.200000003		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	33485	33485 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	33485	33485	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	27128	27128 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	33485	33485 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.89999962	9.89999962 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.100000001	0.100000001 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.100000001	0.100000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.330000013	0.330000013 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.2000008	32.2000008 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.146 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	2	
TrqOviSta_ESCState_State_M_enum	0	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36490.3516	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	36490.3516	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	36490.3516	
TrqOviSta_HapticState_State_M_enum	0	
TrqOviSta_Hapticdur_Cnt_M_u16	28110	
TrqOviSta_HapticTime_Sec_M_f32	36490.3516	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.10000002	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0416000001	
TrqOviSta_LKAFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_u08	2	
TrqOviSta_LKASState_State_M_enum	2	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	26.5	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.860000014	
k_APAMaxHwTrq_HwNm_f32	3.3599999	
k_APAMaxVehSpd_Kph_f32	54	
k_ESCMaxVehSpd_Kph_f32	79.6999969	
k_HapticAmplitude_MtrNm_f32	0.349999994	
k_HapticDuration_Sec_f32	0.284999996	
k_HapticFreq_Hz_f32	32.9000015	
k_HapticReactTime_Sec_f32	0.703999996	
k_LKAMaxVehSpd_Kph_f32	83.5999985	
k_LKAMinVehSpd_Kph_f32	77	
k_MaxHapticReqTime_Sec_f32	0.795000017	
k_SWARLimiter_HwDeg_f32	36.7000008	
k_ShiftLevRevTime_Sec_f32	0.120999999	
k_StandstillThresh_Kph_f32	1.12	
k_StandstillTime_Sec_f32	0.112999998	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-781.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1051.59998		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	138		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.300003		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1.70000005		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	28110	28110 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.39120007	1.39119995 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	26.5	26.5 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	26.5	26.5 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.349999994	0.349999994 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.9000015	32.9000015 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.147 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	3	
TrqOviSta_ESCState_State_M_enum	1	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	39495	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	39495	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	39495	
TrqOviSta_HapticState_State_M_enum	1	
TrqOviSta_Haptidur_Cnt_M_u16	29092	
TrqOviSta_HapticTime_Sec_M_f32	39495	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.20000005	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001	
TrqOviSta_LKAFault_Cnt_M_Igc	1	
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	
TrqOviSta_LKAPermFault_Cnt_M_u08	3	
TrqOviSta_LKASState_State_M_enum	3	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004	
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	
k_APAINcludeHaptic_Cnt_Igc	0	
k_APAMaxHwTrqTime_Sec_f32	0.870999992	
k_APAMaxHwTrq_HwNm_f32	3.44000006	
k_APAMaxVehSpd_Kph_f32	55.7999992	
k_ESCMaxVehSpd_Kph_f32	85.1999969	
k_HapticAmplitude_MtrNm_f32	0.370000005	
k_HapticDuration_Sec_f32	0.300000012	
k_HapticFreq_Hz_f32	33.5999985	
k_HapticReacttime_Sec_f32	0.720000029	
k_LKAMaxVehSpd_Kph_f32	85.5	
k_LKAMinVehSpd_Kph_f32	78.75	
k_MaxHapticReqTime_Sec_f32	0.810000002	
k_SWARLimiter_HwDeg_f32	37	
k_ShiftLevRevTime_Sec_f32	0.125	
k_StandstillThresh_Kph_f32	1.15999997	
k_StandstillTime_Sec_f32	0.116999999	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResponse_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-741.109985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1056.54004		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.20000005		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAResult_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.5999999		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	39495	39495 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	29092	29092 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	39495	39495 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.20000005	1.20000005 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.119999997	0.119999997 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✔
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✔
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✔
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.119999997	0.119999997 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.370000005	0.370000005 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	33.5999985	33.5999985 ± 0.0009765625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.148 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	42500
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	42500
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	42500
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	30074
TrqOviSta_HapticTime_Sec_M_f32	42500
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.29999995
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.046000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.882000029
k_APAMaxHwTrq_HwNm_f32	3.51999998
k_APAMaxVehSpd_Kph_f32	57.5999985
k_ESCMaxVehSpd_Kph_f32	90.6999969
k_HapticAmplitude_MtrNm_f32	0.389999986
k_HapticDuration_Sec_f32	0.314999998
k_HapticFreq_Hz_f32	34.2999992
k_HapticReactTime_Sec_f32	0.736000001
k_LKAMaxVehSpd_Kph_f32	87.4000015
k_LKAMinVehSpd_Kph_f32	80.5
k_MaxHapticReqTime_Sec_f32	0.824999988
k_SWARLimiter_HwDeg_f32	37.2999992
k_ShiftLevRevTime_Sec_f32	0.128999993
k_StandstillThresh_Kph_f32	1.20000005
k_StandstillTime_Sec_f32	0.120999999
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-701.210022
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1061.47998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.5
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	147.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	133.100006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	30074	30074 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.29999995	1.29999995 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008	28.7000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	28.7000008	28.7000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.389999986	0.389999986 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	34.2999992	34.2999992 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.149 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	45505
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	45505
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	45505
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	31056
TrqOviSta_HapticTime_Sec_M_f32	45505
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.39999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0482000001
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.893000007
k_APAMaxHwTrq_HwNm_f32	3.5999999
k_APAMaxVehSpd_Kph_f32	59.4000015
k_ESCMaxVehSpd_Kph_f32	96.1999969
k_HapticAmplitude_MtrNm_f32	0.409999996
k_HapticDuration_Sec_f32	0.330000013
k_HapticFreq_Hz_f32	35
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.839999974
k_SWARLimiter_HwDeg_f32	37.5999985
k_ShiftLevRevTime_Sec_f32	0.133000001
k_StandstillThresh_Kph_f32	1.24000001
k_StandstillTime_Sec_f32	0.125
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-661.309998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1066.42004
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.79999995
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	152.699997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	138
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	7.4000001		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	45505	45505 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	31056	31056 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	45505	45505 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.41927993	1.41928005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.140000001	0.140000001 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.140000001	0.140000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.409999996	0.409999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	35	35 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.150 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	48510.3398
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	48510.3398
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	48510.3398
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	32038
TrqOviSta_HapticTime_Sec_M_f32	48510.3398
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0504000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	30.8999996
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.903999984
k_APAMaxHwTrq_HwNm_f32	3.68000007
k_APAMaxVehSpd_Kph_f32	61.2000008
k_ESCMaxVehSpd_Kph_f32	101.699997
k_HapticAmplitude_MtrNm_f32	0.430000007
k_HapticDuration_Sec_f32	0.344999999
k_HapticFreq_Hz_f32	35.7000008
k_HapticReactTime_Sec_f32	0.768000007
k_LKAMaxVehSpd_Kph_f32	91.1999969
k_LKAMinVehSpd_Kph_f32	84
k_MaxHapticReqTime_Sec_f32	0.855000019
k_SWARLimiter_HwDeg_f32	37.9000015
k_ShiftLevRevTime_Sec_f32	0.136999995
k_StandstillThresh_Kph_f32	1.27999997
k_StandstillTime_Sec_f32	0.128999993
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-621.409973		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1071.35999		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	9.30000019		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	48510.3398	48510.3398	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	32038	32038 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5	1.5 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	30.8999996	30.8999996 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	30.8999996	30.8999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.430000007	0.430000007 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	35.7000008	35.7000008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.151 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	51515
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51515
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	51515
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptidur_Cnt_M_u16	33020
TrqOvlSta_HapticTime_Sec_M_f32	51515
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKASState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	32
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.915000021
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	63
k_ESCMaxVehSpd_Kph_f32	0
k_HapticAmplitude_MtrNm_f32	0.449999988
k_HapticDuration_Sec_f32	0.360000014
k_HapticFreq_Hz_f32	36.4000015
k_HapticReactime_Sec_f32	0.783999979
k_LKAMaxVehSpd_Kph_f32	93.0999985
k_LKAMinVehSpd_Kph_f32	85.75
k_MaxHapticReqTime_Sec_f32	0.870000005
k_SWARLimiter_HwDeg_f32	38.2000008
k_ShiftLevRevTime_Sec_f32	0.141000003
k_StandstillThresh_Kph_f32	1.32000005
k_StandstillTime_Sec_f32	0.133000001
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHoscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-581.51001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1076.30005
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.4000001
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	0	0	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	51515	51515 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	33020	33020 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	51515	51515 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002	1.60000002 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.449999988	0.449999988 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	36.4000015	36.4000015 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.152 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	54520
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	54520
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	54520
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	34002
TrqOviSta_HapticTime_Sec_M_f32	54520
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	33.0999985
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.925999999
k_APAMaxHwTrq_HwNm_f32	3.83999991
k_APAMaxVehSpd_Kph_f32	64.8000031
k_ESCMaxVehSpd_Kph_f32	180
k_HapticAmplitude_MtrNm_f32	0.469999999
k_HapticDuration_Sec_f32	0.375
k_HapticFreq_Hz_f32	37.0999985
k_HapticReactime_Sec_f32	0.800000012
k_LKAMaxVehSpd_Kph_f32	95
k_LKAMinVehSpd_Kph_f32	87.5
k_MaxHapticReqTime_Sec_f32	0.88499999
k_SWARLimiter_HwDeg_f32	38.5
k_ShiftLevRevTime_Sec_f32	0.144999996
k_StandstillThresh_Kph_f32	1.36000001
k_StandstillTime_Sec_f32	0.136999995
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-541.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1081.23999
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.70000005
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	167.399994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	152.699997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.1000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	0	0	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	34002	34002 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.75480008	1.75479996 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	33.0999985	33.0999985 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	33.0999985	33.0999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.469999999	0.469999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	37.0999985	37.0999985 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.153 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	57525.3203
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	57525.3203
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	57525.3203
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	34984
TrqOviSta_HapticTime_Sec_M_f32	57525.3203
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79999995
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.936999977
k_APAMaxHwTrq_HwNm_f32	3.92000008
k_APAMaxVehSpd_Kph_f32	66.5999985
k_ESCMaxVehSpd_Kph_f32	100.5
k_HapticAmplitude_MtrNm_f32	0.49000001
k_HapticDuration_Sec_f32	0.389999986
k_HapticFreq_Hz_f32	37.7999992
k_HapticReacttime_Sec_f32	0.815999985
k_LKAMaxVehSpd_Kph_f32	96.9000015
k_LKAMinVehSpd_Kph_f32	89.25
k_MaxHapticReqTime_Sec_f32	0.899999976
k_SWARLimiter_HwDeg_f32	38.7999992
k_ShiftLevRevTime_Sec_f32	0.149000004
k_StandstillThresh_Kph_f32	1.39999998
k_StandstillTime_Sec_f32	0.141000003
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-501.709991
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1086.18005
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	172.300003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	0	0	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	57525.3203	57525.3203 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	34984	34984 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	57525.3203	57525.3203 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79999995	1.79999995 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.180000007	0.180000007 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_GMOSHOCssillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.49000001	0.49000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	37.7999992	37.7999992 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.154 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	60530
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	60530
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	60530
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	35966
TrqOviSta_HapticTime_Sec_M_f32	60530
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.89999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	35.2999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAlncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.948000014
k_APAMaxHwTrq_HwNm_f32	4
k_APAMaxVehSpd_Kph_f32	68.4000015
k_ESCMaxVehSpd_Kph_f32	110.199997
k_HapticAmplitude_MtrNm_f32	0.50999999
k_HapticDuration_Sec_f32	0.405000001
k_HapticFreq_Hz_f32	38.5
k_HapticReactime_Sec_f32	0.832000017
k_LKAMaxVehSpd_Kph_f32	98.8000031
k_LKAMinVehSpd_Kph_f32	91
k_MaxHapticReqTime_Sec_f32	0.915000021
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0.152999997
k_StandstillThresh_Kph_f32	1.44000006
k_StandstillTime_Sec_f32	0.144999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResquest_Cnt_Igc	target_TrqOviSta_Per1_APAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAResquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-461.809998		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1091.12		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.29999995		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	177.199997		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.8999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	35966	35966 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.89999998	1.89999998 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	35.2999992	35.2999992 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	35.2999992	35.2999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.50999999	0.50999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	38.5	38.5 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.155 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	63535
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	63535
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	63535
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	36948
TrqOviSta_HapticTime_Sec_M_f32	63535
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0614
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	36.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.958999991
k_APAMaxHwTrq_HwNm_f32	4.07999992
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	115.599998
k_HapticAmplitude_MtrNm_f32	0.529999971
k_HapticDuration_Sec_f32	0.419999987
k_HapticFreq_Hz_f32	39.2000008
k_HapticReactTime_Sec_f32	0.84799999
k_LKAMaxVehSpd_Kph_f32	100.699997
k_LKAMinVehSpd_Kph_f32	92.75
k_MaxHapticReqTime_Sec_f32	0.930000007
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.157000005
k_StandstillThresh_Kph_f32	1.48000002
k_StandstillTime_Sec_f32	0.149000004
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-421.910004
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1096.06006
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.5999999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.7999992
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	63535	63535 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	63535	63535	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	36948	36948 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	63535	63535 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.0982399	2.0982399 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.100000001	0.100000001 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.100000001	0.100000001 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.529999971	0.529999971 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	39.2000008	39.2000008 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.156 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	66540.6328
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	66540.6328
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	66540.6328
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	37930
TrqOviSta_HapticTime_Sec_M_f32	66540.6328
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.0999999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0636000037
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	37.5
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.970000029
k_APAMaxHwTrq_HwNm_f32	4.15999985
k_APAMaxVehSpd_Kph_f32	72
k_ESCMaxVehSpd_Kph_f32	121
k_HapticAmplitude_MtrNm_f32	0.550000012
k_HapticDuration_Sec_f32	0.435000002
k_HapticFreq_Hz_f32	39.9000015
k_HapticReactTime_Sec_f32	0.864000022
k_LKAMaxVehSpd_Kph_f32	102.599998
k_LKAMinVehSpd_Kph_f32	94.5
k_MaxHapticReqTime_Sec_f32	0.944999993
k_SWARLimiter_HwDeg_f32	30.2999992
k_ShiftLevRevTime_Sec_f32	0.160999998
k_StandstillThresh_Kph_f32	1.51999998
k_StandstillTime_Sec_f32	0.152999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-382.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1101
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.9000001
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	187
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	172.300003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.7000008
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	37930	37930 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.0999999	2.0999999 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	37.5	37.5 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	37.5	37.5 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.550000012	0.550000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	39.9000015	39.9000015 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.157 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	18460.6309
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	38912
TrqOviSta_HapticTime_Sec_M_f32	18460.6309
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0658000037
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.981000006
k_APAMaxHwTrq_HwNm_f32	4.23999977
k_APAMaxVehSpd_Kph_f32	193.800003
k_ESCMaxVehSpd_Kph_f32	126.400002
k_HapticAmplitude_MtrNm_f32	0.569999993
k_HapticDuration_Sec_f32	0.449999988
k_HapticFreq_Hz_f32	40.5999985
k_HapticReacttime_Sec_f32	0.879999995
k_LKAMaxVehSpd_Kph_f32	104.5
k_LKAMinVehSpd_Kph_f32	96.25
k_MaxHapticReqTime_Sec_f32	0.959999979
k_SWARLimiter_HwDeg_f32	45.5999985
k_ShiftLevRevTime_Sec_f32	0.165000007
k_StandstillThresh_Kph_f32	1.55999994
k_StandstillTime_Sec_f32	0.157000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-461.809998
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1105.93994
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.19999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	191.899994
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	177.199997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.6000004		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	2	2	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309	18460.6309 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	38912	38912 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005	2.20000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0	0 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.569999993	0.569999993 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	40.5999985	40.5999985 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.158 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21465
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	21465
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	21465
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	39894
TrqOviSta_HapticTime_Sec_M_f32	21465
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.29999995
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0680000037
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	90
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.150000006
k_APAMaxHwTrq_HwNm_f32	4.32000017
k_APAMaxVehSpd_Kph_f32	197.600006
k_ESCMaxVehSpd_Kph_f32	131.800003
k_HapticAmplitude_MtrNm_f32	0.589999974
k_HapticDuration_Sec_f32	0.465000004
k_HapticFreq_Hz_f32	41.2999992
k_HapticReactTime_Sec_f32	0.896000028
k_LKAMaxVehSpd_Kph_f32	106.400002
k_LKAMinVehSpd_Kph_f32	98
k_MaxHapticReqTime_Sec_f32	0.975000024
k_SWARLimiter_HwDeg_f32	50.0999985
k_ShiftLevRevTime_Sec_f32	0.169
k_StandstillThresh_Kph_f32	1.67999995
k_StandstillTime_Sec_f32	0.169
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-421.910004		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1110.88		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.5		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	196.800003		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	182.100006		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	39894	39894 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.44959998	2.44959998 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	90	90 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	90	90 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.589999974	0.589999974 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	41.2999992	41.2999992 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.159 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	24470
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	24470
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	24470
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	40876
TrqOviSta_HapticTime_Sec_M_f32	24470
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0702000037
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	50.5
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	4.4000001
k_APAMaxVehSpd_Kph_f32	207.399994
k_ESCMaxVehSpd_Kph_f32	137.199997
k_HapticAmplitude_MtrNm_f32	0.610000014
k_HapticDuration_Sec_f32	0.479999989
k_HapticFreq_Hz_f32	42
k_HapticReactTime_Sec_f32	0.912
k_LKAMaxVehSpd_Kph_f32	108.300003
k_LKAMinVehSpd_Kph_f32	99.75
k_MaxHapticReqTime_Sec_f32	0.109999999
k_SWARLimiter_HwDeg_f32	54.5999985
k_ShiftLevRevTime_Sec_f32	0.172999993
k_StandstillThresh_Kph_f32	1.72000003
k_StandstillTime_Sec_f32	0.172999993
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-382.01001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1115.81995
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.80000019
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	201.699997
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	187
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	2	2	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	24470	24470 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	40876	40876 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.4000001	2.4000001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	50.5	50.5 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	50.5	50.5 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.610000014	0.610000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	42	42 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 2.160 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	0
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	0
k_ESCMaxVehSpd_Kph_f32	0
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	0
k_HapticFreq_Hz_f32	3
k_HapticReactTime_Sec_f32	0
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1440.10999
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	360
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-10
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-34.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10	-10 ± 0.00009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0	0 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.161 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4.2949673e+009
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	65535
TrqOviSta_HapticTime_Sec_M_f32	4.2949673e+009
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	4
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	90

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAMIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	1
k_APAMaxHwTrq_HwNm_f32	10
k_APAMaxVehSpd_Kph_f32	220
k_ESCMaxVehSpd_Kph_f32	180
k_HapticAmplitude_MtrNm_f32	0.75
k_HapticDuration_Sec_f32	2
k_HapticFreq_Hz_f32	100
k_HapticReacttime_Sec_f32	2
k_LKAMaxVehSpd_Kph_f32	220
k_LKAMinVehSpd_Kph_f32	220
k_MaxHapticReqTime_Sec_f32	1
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.5
k_StandstillThresh_Kph_f32	5
k_StandstillTime_Sec_f32	0.5
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResponse_Cnt_lgc	target_TrqOviSta_Per1APAResponse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResponse_Cnt_lgc	target_TrqOviSta_Per1_LKAResponse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APAResponse_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-360
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	1440.10999
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	10
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResponse_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	514.799988
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	511
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	65535	65535 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	10 ± 0.00009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	100	100 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓



Test Case 3: Path Test



TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Specification Performance Metrics (With "None"
Instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 874 Cycles
TS3.2 1041 Cycles
TS3.3 905 Cycles
TS3.4 890 Cycles
TS3.5 899 Cycles
TS3.6 904 Cycles
TS3.7 947 Cycles
TS3.8 932 Cycles
TS3.9 914 Cycles
TS3.10 900 Cycles
TS3.11 903 Cycles
TS3.12 896 Cycles
TS3.13 880 Cycles
TS3.14 891 Cycles
TS3.15 907 Cycles
TS3.16 919 Cycles
TS3.17 932 Cycles
TS3.18 911 Cycles
TS3.19 952 Cycles
TS3.20 923 Cycles
TS3.21 914 Cycles
TS3.22 900 Cycles
TS3.23 875 Cycles
TS3.24 938 Cycles
TS3.25 937 Cycles
TS3.26 931 Cycles
TS3.27 935 Cycles
TS3.28 933 Cycles
TS3.29 910 Cycles
TS3.30 924 Cycles
TS3.31 913 Cycles
TS3.32 928 Cycles
TS3.33 1008 Cycles
TS3.34 1022 Cycles
TS3.35 1030 Cycles
TS3.36 1027 Cycles
TS3.37 1019 Cycles
TS3.38 1018 Cycles
TS3.39 1018 Cycles
TS3.40 1015 Cycles
TS3.41 1012 Cycles
TS3.42 918 Cycles
TS3.43 1023 Cycles
TS3.44 1005 Cycles
TS3.45 1039 Cycles
TS3.46 1017 Cycles
TS3.47 963 Cycles
TS3.48 969 Cycles
TS3.49 966 Cycles
TS3.50 969 Cycles
TS3.51 930 Cycles
TS3.52 936 Cycles
TS3.53 934 Cycles
TS3.54 925 Cycles
TS3.55 923 Cycles
TS3.56 930 Cycles
TS3.57 937 Cycles
TS3.58 931 Cycles
TS3.59 941 Cycles
TS3.60 932 Cycles
TS3.61 936 Cycles
TS3.62 922 Cycles
TS3.63 939 Cycles
TS3.64 952 Cycles
TS3.65 939 Cycles
TS3.66 941 Cycles
TS3.67 909 Cycles
TS3.68 905 Cycles
TS3.69 1037 Cycles
TS3.70 939 Cycles
TS3.71 946 Cycles
TS3.72 949 Cycles
TS3.73 903 Cycles
TS3.74 942 Cycles
TS3.75 953 Cycles
TS3.76 987 Cycles
TS3.77 870 Cycles
TS3.78 908 Cycles
TS3.79 931 Cycles
TS3.80 935 Cycles
TS3.81 933 Cycles
TS3.82 910 Cycles
TS3.83 1037 Cycles
TS3.84 939 Cycles

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Description	Vector Description:
	<pre>TS3.1"(LKARquest_Cnt_T_f32 > D_ZERO_ULS_F32)=False (HapticRequest_Cnt_T_Igc == TRUE)=False (ShiftLeverIsInReverse_Cnt_T_Igc == TRUE)=False (MaxSecureVehicleSpeed_Kph_T_f32 >= k_StandstillThresh_Kph_f32)=False (TrqOviSta_HapticState_State_M_enum != Haptic_Inactive)=True (Abs_f32_m(HwTrqFilt_HwNm_T_f32) >= k_APAMaxHwTrq_HwNm_f32)=False ((LKAFault_Cnt_T_Igc == TRUE) && (TrqOviSta_LKAPermFault_Cnt_M_Igc == FALSE) && (TrqOviSta_LKAFault_Cnt_M_Igc == FALSE))=False if((CWFound_Cnt_T_Igc == TRUE) && (CCWFound_Cnt_T_Igc == TRUE))=True (GMOSH_APAMfgEnable_Cnt_T_Igc == FALSE)=False" TS3.2"(LKARquest_Cnt_T_f32 > D_ZERO_ULS_F32)=True (HapticRequest_Cnt_T_Igc == TRUE)=True (TrqOviSta_HapticTime_Sec_M_f32 >= (k_MaxHapticReqTime_Sec_f32))=False (ShiftLeverIsInReverse_Cnt_T_Igc == TRUE)=True (TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32 > (k_ShiftLevRevTime_Sec_f32))=False (MaxSecureVehicleSpeed_Kph_T_f32 >= k_StandstillThresh_Kph_f32)=True (TrqOviSta_ElapsedStandstillTime_Sec_M_f32 >= k_StandstillTime_Sec_f32)=False (TrqOviSta_HapticState_State_M_enum != Haptic_Inactive)=False (Abs_f32_m(HwTrqFilt_HwNm_T_f32) >= k_APAMaxHwTrq_HwNm_f32)=True (TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 >= ((k_APAMaxHwTrqTime_Sec_f32)))=False ((LKAFault_Cnt_T_Igc == TRUE) && (TrqOviSta_LKAPermFault_Cnt_M_Igc == FALSE) && (TrqOviSta_LKAFault_Cnt_M_Igc == FALSE))=True" TS3.3"(HapticRequest_Cnt_T_Igc == TRUE)=True (TrqOviSta_HapticTime_Sec_M_f32 >= (k_MaxHapticReqTime_Sec_f32))=True (ShiftLeverIsInReverse_Cnt_T_Igc == TRUE)=True (TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32 > (k_ShiftLevRevTime_Sec_f32))=True (MaxSecureVehicleSpeed_Kph_T_f32 >= k_StandstillThresh_Kph_f32)=True (TrqOviSta_ElapsedStandstillTime_Sec_M_f32 >= k_StandstillTime_Sec_f32)=True (Abs_f32_m(HwTrqFilt_HwNm_T_f32) >= k_APAMaxHwTrq_HwNm_f32)=True (TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 >= ((k_APAMaxHwTrqTime_Sec_f32)))=True ((CWFound_Cnt_T_Igc == TRUE) && (CCWFound_Cnt_T_Igc == TRUE))=False (GMOSH_APAMfgEnable_Cnt_T_Igc == FALSE)=True (GMOSH_LKAMfgEnable_Cnt_T_Igc == FALSE)=False (GMOSH_ESCMfgEnable_Cnt_T_Igc == FALSE)=False" TS3.4((LKAFault_Cnt_T_Igc == TRUE)=True && (TrqOviSta_LKAPermFault_Cnt_M_Igc == FALSE)=False && (TrqOviSta_LKAFault_Cnt_M_Igc == FALSE)) TS3.5((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) (APARrecoverableFaults_Cnt_T_Igc==TRUE)) ((HapticRequest_Cnt_T_Igc==TRUE)&&(HapticReqDurExceeded_Cnt_T_Igc==TRUE)) (LmEOTFound_Cnt_T_Igc == FALSE))=True TS3.6(TrqOviSta_LKAPermFault_Cnt_M_u08 >= 3U)=True TS3.7(APANonRecoverableFaults_Cnt_T_Igc ==TRUE)=False TS3.8"((LKAFault_Cnt_T_Igc == TRUE)=True && (TrqOviSta_LKAPermFault_Cnt_M_Igc == FALSE)=True&& (TrqOviSta_LKAFault_Cnt_M_Igc == FALSE)=False) case(APA_TemplInhibit):True (APANonRecoverableFaults_Cnt_T_Igc ==TRUE)=False" TS3.9((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32)=False (MaxSecureVehicleSpeed_Kph_T_f32 > k_LKAMaxVehSpd_Kph_f32)=True) TS3.10"case(APA_AvailCntrl):True if(APANonRecoverableFaults_Cnt_T_Igc ==TRUE)=True" TS3.11"(TrqOviSta_LKAPermFault_Cnt_M_Igc == TRUE)=False (LKARquest_Cnt_T_Igc == FALSE)=True" TS3.12"case(APA_TemplInhibit):True (APANonRecoverableFaults_Cnt_T_Igc ==TRUE)=True" TS3.13(TrqOviSta_LKAPermFault_Cnt_M_Igc == TRUE)=True TS3.14(APARquest_Cnt_T_Igc ==FALSE)=True TS3.15"((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) (APARrecoverableFaults_Cnt_T_Igc==TRUE)) ((HapticRequest_Cnt_T_Igc==TRUE)&&(HapticReqDurExceeded_Cnt_T_Igc==TRUE)) (LmEOTFound_Cnt_T_Igc == FALSE))=False (LKARquest_Cnt_T_Igc == FALSE)=False" TS3.16"((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) (APARrecoverableFaults_Cnt_T_Igc==TRUE)) (HapticRequest_Cnt_T_Igc==TRUE)) (APAIntervention_Cnt_T_Igc==TRUE) (LmEOTFound_Cnt_T_Igc == FALSE))=False ((TrqOviSta_APASState_State_M_enum == APA_Active) && (PosTrajEnable_Cnt_T_Igc == TRUE))=True" TS3.17((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) =False (APARrecoverableFaults_Cnt_T_Igc==TRUE)=false ((HapticRequest_Cnt_T_Igc==TRUE)=true&&(HapticReqDurExceeded_Cnt_T_Igc==TRUE)) =True (LmEOTFound_Cnt_T_Igc == FALSE)) TS3.18"(k_APAIncludeHaptic_Cnt_Igc == FALSE)=True ((StandstillTime_Cnt_T_Igc==TRUE)=False && (APARquest_Cnt_T_Igc ==TRUE)=True&&(ShiftLevRevTime_Cnt_T_Igc==TRUE))" TS3.19((StandstillTime_Cnt_T_Igc==FALSE) =False (APARquest_Cnt_T_Igc ==FALSE)=True (ShiftLevRevTime_Cnt_T_Igc==FALSE)) TS3.20((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) (MaxSecureVehicleSpeed_Kph_T_f32 > k_LKAMaxVehSpd_Kph_f32))=False TS3.21((ESCRquest_Cnt_T_Igc == FALSE)=False (MinSecureVehicleSpeed_Kph_T_f32 > k_ESCMaxVehSpd_Kph_f32)=True) TS3.22((ESCRquest_Cnt_T_Igc == FALSE) (MinSecureVehicleSpeed_Kph_T_f32 > k_ESCMaxVehSpd_Kph_f32))=True TS3.23((CWFound_Cnt_T_Igc == TRUE)=True && (CCWFound_Cnt_T_Igc == TRUE)=False) TS3.24((MaxSecureVehicleSpeed_Kph_T_f32 <= k_APAMaxVehSpd_Kph_f32) && (APARquest_Cnt_T_Igc ==FALSE) && (HapticRequest_Cnt_T_Igc==FALSE) && (APARrecoverableFaults_Cnt_T_Igc==FALSE) && (LmEOTFound_Cnt_T_Igc == TRUE))=true TS3.25((MaxSecureVehicleSpeed_Kph_T_f32 <= k_APAMaxVehSpd_Kph_f32) =True&& (APARquest_Cnt_T_Igc ==FALSE) =True && (HapticRequest_Cnt_T_Igc==FALSE) =True && (APARrecoverableFaults_Cnt_T_Igc==FALSE) =True && (LmEOTFound_Cnt_T_Igc == TRUE)=False) TS3.26((MaxSecureVehicleSpeed_Kph_T_f32 <= k_APAMaxVehSpd_Kph_f32) =True&& (APARquest_Cnt_T_Igc ==FALSE) =True&& (HapticRequest_Cnt_T_Igc==FALSE) =True && (APARrecoverableFaults_Cnt_T_Igc==FALSE)=False && (LmEOTFound_Cnt_T_Igc == TRUE)) TS3.27((MaxSecureVehicleSpeed_Kph_T_f32 <= k_APAMaxVehSpd_Kph_f32) =True&& (APARquest_Cnt_T_Igc ==FALSE) =True&& (HapticRequest_Cnt_T_Igc==FALSE)=False && (APARrecoverableFaults_Cnt_T_Igc==FALSE) && (LmEOTFound_Cnt_T_Igc == TRUE)) TS3.28((MaxSecureVehicleSpeed_Kph_T_f32 <= k_APAMaxVehSpd_Kph_f32) =True&& (APARquest_Cnt_T_Igc ==FALSE) =False&& (HapticRequest_Cnt_T_Igc==FALSE) && (APARrecoverableFaults_Cnt_T_Igc==FALSE) && (LmEOTFound_Cnt_T_Igc == TRUE)) TS3.29((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32)=False (APARrecoverableFaults_Cnt_T_Igc==TRUE)=False (HapticRequest_Cnt_T_Igc==TRUE)=False&&(HapticReqDurExceeded_Cnt_T_Igc==TRUE)) (LmEOTFound_Cnt_T_Igc == FALSE)=True) TS3.30((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) =False (APARrecoverableFaults_Cnt_T_Igc==TRUE)=True ((HapticRequest_Cnt_T_Igc==TRUE)&&(HapticReqDurExceeded_Cnt_T_Igc==TRUE)) (LmEOTFound_Cnt_T_Igc == FALSE)) TS3.31((StandstillTime_Cnt_T_Igc==TRUE) && (APARquest_Cnt_T_Igc ==TRUE)&&(ShiftLevRevTime_Cnt_T_Igc==TRUE))=True TS3.32((StandstillTime_Cnt_T_Igc==TRUE)=True && (APARquest_Cnt_T_Igc ==TRUE)=True&&(ShiftLevRevTime_Cnt_T_Igc==TRUE)=False) TS3.33((HapticRequest_Cnt_T_Igc==FALSE) && (TrqOviSta_ReadytoPulse_Cnt_M_Igc==FALSE))=True TS3.34((HapticRequest_Cnt_T_Igc==FALSE)=True && (TrqOviSta_ReadytoPulse_Cnt_M_Igc==FALSE)=False) TS3.35(TrqOviSta_ReadytoPulse_Cnt_M_Igc== TRUE)=False TS3.36((StandstillTime_Cnt_T_Igc==TRUE) && (APARquest_Cnt_T_Igc ==TRUE)&&(HapticRequest_Cnt_T_Igc==TRUE)&&(ShiftLevRevTime_Cnt_T_Igc==TRUE))=True TS3.37"(HapticRequest_Cnt_T_Igc==TRUE)=False ((StandstillTime_Cnt_T_Igc==FALSE) (APARquest_Cnt_T_Igc ==FALSE)) (ShiftLevRevTime_Cnt_T_Igc==FALSE))=False (TrqOviSta_Hapticdur_Cnt_M_u16 > (uint16)(k_HapticDuration_Sec_f32/D_2MS_SEC_F32))=True" TS3.38"((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) (APARrecoverableFaults_Cnt_T_Igc==TRUE)) </pre>

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

```
((HapticRequest_Cnt_T_Igc==TRUE)&&(HapticReqDurExceeded_Cnt_T_Igc==TRUE)) || (LmEOTFound_Cnt_T_Igc == FALSE) )=False
(LKARquest_Cnt_T_Igc == FALSE)=False"
TS3.39((StandstillTime_Cnt_T_Igc==FALSE)=true || (APARquest_Cnt_T_Igc ==FALSE)) || (ShiftLevRevTime_Cnt_T_Igc==FALSE))
TS3.40((StandstillTime_Cnt_T_Igc==FALSE)=False || (APARquest_Cnt_T_Igc ==FALSE)=True || (ShiftLevRevTime_Cnt_T_Igc==FALSE))
TS3.41((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 >
k_LKAMaxVehSpd_Kph_f32) || (LKARquest_Cnt_T_Igc == FALSE)=True
TS3.42((StandstillTime_Cnt_T_Igc==TRUE) && (APARquest_Cnt_T_Igc ==TRUE)&&(ShiftLevRevTime_Cnt_T_Igc==TRUE))=False
TS3.43((StandstillTime_Cnt_T_Igc==TRUE)=True && (APARquest_Cnt_T_Igc
==TRUE)=False&&(HapticRequest_Cnt_T_Igc==TRUE)&&(ShiftLevRevTime_Cnt_T_Igc==TRUE))
TS3.44((StandstillTime_Cnt_T_Igc==TRUE)=True && (APARquest_Cnt_T_Igc
==TRUE)=True&&(HapticRequest_Cnt_T_Igc==TRUE)=False&&(ShiftLevRevTime_Cnt_T_Igc==TRUE))
TS3.45((StandstillTime_Cnt_T_Igc==TRUE)=True && (APARquest_Cnt_T_Igc
==TRUE)=True&&(HapticRequest_Cnt_T_Igc==TRUE)=True&&(ShiftLevRevTime_Cnt_T_Igc==TRUE)=False)
TS3.46((StandstillTime_Cnt_T_Igc==FALSE)=False || (APARquest_Cnt_T_Igc ==FALSE)=False ||
(ShiftLevRevTime_Cnt_T_Igc==FALSE)=True)
TS3.47((StandstillTime_Cnt_T_Igc==FALSE) || (APARquest_Cnt_T_Igc ==FALSE)) || (ShiftLevRevTime_Cnt_T_Igc==FALSE))=False
TS3.48((TrqOviSta_Hapticdur_Cnt_M_u16 > (uint16)(k_HapticReactime_Sec_f32/D_2MS_SEC_F32))=False
TS3.49((StandstillTime_Cnt_T_Igc==FALSE)=False || (APARquest_Cnt_T_Igc ==FALSE)=False ||
(ShiftLevRevTime_Cnt_T_Igc==FALSE)=True)
TS3.50((StandstillTime_Cnt_T_Igc==FALSE) || (APARquest_Cnt_T_Igc ==FALSE)) || (ShiftLevRevTime_Cnt_T_Igc==FALSE))=True
TS3.51((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32)=False || (APARrecoverableFaults_Cnt_T_Igc==TRUE)=False ||
(HapticRequest_Cnt_T_Igc==TRUE)=False || (APARquest_Cnt_T_Igc==TRUE)=False || (LmEOTFound_Cnt_T_Igc == FALSE)=True)
TS3.52((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32)=False || (APARrecoverableFaults_Cnt_T_Igc==TRUE)=False ||
(HapticRequest_Cnt_T_Igc==TRUE)=False || (APARquest_Cnt_T_Igc==TRUE)=True || (LmEOTFound_Cnt_T_Igc == FALSE))
TS3.53((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32)=False || (APARrecoverableFaults_Cnt_T_Igc==TRUE)=False ||
(HapticRequest_Cnt_T_Igc==TRUE)=True || (APARquest_Cnt_T_Igc==TRUE) || (LmEOTFound_Cnt_T_Igc == FALSE))
TS3.54((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32)=False || (APARrecoverableFaults_Cnt_T_Igc==TRUE)=True ||
(HapticRequest_Cnt_T_Igc==TRUE) || (APARquest_Cnt_T_Igc==TRUE) || (LmEOTFound_Cnt_T_Igc == FALSE))
TS3.55((LKARquest_Cnt_T_Igc == TRUE)=True && ((LKAFault_Cnt_T_Igc == TRUE)=False || (LKAlnhibit_Cnt_T_Igc == TRUE)=True ||
(TrqOviSta_ESCState_State_M_enum == ESC_Active) || (TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited)))
TS3.56((LKARquest_Cnt_T_Igc == TRUE) && ((LKAFault_Cnt_T_Igc == TRUE) || (LKAlnhibit_Cnt_T_Igc == TRUE) ||
(TrqOviSta_ESCState_State_M_enum == ESC_Active) || (TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited)))=True
TS3.57((LKARquest_Cnt_T_Igc == TRUE)=True && ((LKAFault_Cnt_T_Igc == TRUE)=False || (LKAlnhibit_Cnt_T_Igc == TRUE)=False ||
(TrqOviSta_ESCState_State_M_enum == ESC_Active)=False || (TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited))=False )
TS3.58((LKARquest_Cnt_T_Igc == TRUE)=True && ((LKAFault_Cnt_T_Igc == TRUE)=False || (LKAlnhibit_Cnt_T_Igc == TRUE)=False ||
(TrqOviSta_ESCState_State_M_enum == ESC_Active)=True || (TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited)))
TS3.59((LKARquest_Cnt_T_Igc == TRUE) && ((LKAFault_Cnt_T_Igc == TRUE) || (LKAlnhibit_Cnt_T_Igc == TRUE) ||
(TrqOviSta_ESCState_State_M_enum == ESC_Active) || (TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited)))
TS3.60((LKARquest_Cnt_T_Igc == TRUE)=True
(MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32)=False || (MinSecureVehicleSpeed_Kph_T_f32 >
k_LKAMaxVehSpd_Kph_f32)=False || (LKARquest_Cnt_T_Igc == FALSE)=True)"
TS3.61((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 >
k_LKAMaxVehSpd_Kph_f32) || (LKARquest_Cnt_T_Igc == FALSE))=FALSE
((LKAlnhibit_Cnt_T_Igc == TRUE) || (LKAFault_Cnt_T_Igc == TRUE) || (TrqOviSta_ESCState_State_M_enum == ESC_Active) ||
(TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited))=True"
TS3.62((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 >
k_LKAMaxVehSpd_Kph_f32) || (LKARquest_Cnt_T_Igc == FALSE)=True
TS3.63((LKAlnhibit_Cnt_T_Igc == TRUE) || (LKAFault_Cnt_T_Igc == TRUE) || (TrqOviSta_ESCState_State_M_enum == ESC_Active) ||
(TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited))=False
TS3.64((LKAlnhibit_Cnt_T_Igc == TRUE)=False || (LKAFault_Cnt_T_Igc == TRUE)=False || (TrqOviSta_ESCState_State_M_enum ==
ESC_Active)=False || (TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited)=True)
TS3.65((LKAlnhibit_Cnt_T_Igc == TRUE)=False || (LKAFault_Cnt_T_Igc == TRUE)=False || (TrqOviSta_ESCState_State_M_enum ==
ESC_Active)=True || (TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited))
TS3.66((LKAlnhibit_Cnt_T_Igc == TRUE)=false || (LKAFault_Cnt_T_Igc == TRUE)=True || (TrqOviSta_ESCState_State_M_enum ==
ESC_Active)=false || (TrqOviSta_ESCState_State_M_enum == ESC_TemporarilyLimited))
"
TS3.67(ESCRequest_Cnt_T_Igc == TRUE)=True
TS3.68(ESCRequest_Cnt_T_Igc == TRUE)=False
TS3.69(ESCIsLimited_Cnt_T_Igc == TRUE)=False
TS3.70(ESCRequest_Cnt_T_Igc == FALSE)=True
TS3.71((ESCFault_Cnt_T_Igc == TRUE)=False
(ESCIsLimited_Cnt_T_Igc == FALSE)=False"
TS3.72(ESCIsLimited_Cnt_T_Igc == FALSE)=True
TS3.73(ESCRequest_Cnt_T_Igc == FALSE)=True
TS3.74((TrqOviSta_APASState_State_M_enum == APA_Active) =True&& (PosTrajEnable_Cnt_T_Igc == TRUE )=False)
((TrqOviSta_APASState_State_M_enum != APA_Active) && (PosTrajEnable_Cnt_T_Igc == TRUE ))=False"
TS3.75((LKARquest_Cnt_T_Igc == TRUE)=False
TS3.76"switch (TrqOviSta_APASState_State_M_enum)
Default:True"
TS3.77"case ( ESC_ManufacturingDisable):True
case(APA_PermanentlyFailed):=True"
TS3.78"switch (TrqOviSta_ESCState_State_M_enum)
default:True
switch (TrqOviSta_HapticState_State_M_enum)
default:True"
TS3.79(TrqOviSta_Hapticdur_Cnt_M_u16 > (k_HapticDuration_Sec_f32/D_2MS_SEC_F32))=>FALSE
TS3.80((StandstillTime_Cnt_T_Igc==FALSE)F || (APARquest_Cnt_T_Igc ==FALSE)T || (ShiftLevRevTime_Cnt_T_Igc==FALSE)F)>=>TRUE
TS3.81((StandstillTime_Cnt_T_Igc==FALSE)F || (APARquest_Cnt_T_Igc ==FALSE)F || (ShiftLevRevTime_Cnt_T_Igc==FALSE)T)>=>TRUE
TS3.82((StandstillTime_Cnt_T_Igc==FALSE)F || (APARquest_Cnt_T_Igc ==FALSE)F || (ShiftLevRevTime_Cnt_T_Igc==FALSE)F)>=>False
TS3.83(TrqOviSta_Hapticdur_Cnt_M_u16 > (k_HapticReactime_Sec_f32 / D_2MS_SEC_F32))=>False
TS3.84((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) || (APARrecoverableFaults_Cnt_T_Igc==TRUE)) ||
((HapticRequest_Cnt_T_Igc==TRUE)&&(HapticReqDurExceeded_Cnt_T_Igc==TRUE)) || (LmEOTFound_Cnt_T_Igc == FALSE) )=>TRUE
```

Test Step 3.1 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_Hapticdur_Cnt_M_u16	5270
TrqOviSta_HapticTime_Sec_M_f32	27475.5293
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReactTime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-360.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	360.980011
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APASIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
k_HapticReacttime_Sec_f32	1.23199999		
k_LKAMaxVehSpd_Kph_f32	146.300003		
k_LKAMinVehSpd_Kph_f32	100		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	30		
k_ShiftLevRevTime_Sec_f32	0.308999985		
k_StandstillThresh_Kph_f32	0		
k_StandstillTime_Sec_f32	0.308999985		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCssillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCssillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	2	2	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	45
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0.850000024
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	15
TrqOviSta_HapticTime_Sec_M_f32	75.6800003
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85999966
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.109999999
k_APAMaxHwTrq_HwNm_f32	0.100000001
k_APAMaxVehSpd_Kph_f32	0.200000003
k_ESCMaxVehSpd_Kph_f32	10.1000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	0.509999999
k_HapticFreq_Hz_f32	3
k_HapticReactTime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	0.400000006
k_MaxHapticReqTime_Sec_f32	0.0149999997
k_SWARLimiter_HwDeg_f32	32.2000008
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.00100000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_Igc	target_TrqOviSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-366.450012		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.040009		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.209999993		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	25.2999992		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	35.0999985		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-32.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008	0.460000008 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	45	45 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	15	15 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	75.6800003	75.6800003 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.83985996	-9.83985996 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✔
TrqOviSta_LKASState_State_M_enum	4	4	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005	1.20000005 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1.20000005	1.20000005 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.4 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7.11999989
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	132.839996
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	8
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	240
TrqOviSta_HapticTime_Sec_M_f32	150
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	2.29999995
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.119999997
k_APAMaxHwTrq_HwNm_f32	0.0799999982
k_APAMaxVehSpd_Kph_f32	1.79999995
k_ESCMaxVehSpd_Kph_f32	12.5
k_HapticAmplitude_MtrNm_f32	0.00600000005
k_HapticDuration_Sec_f32	0.0149999997
k_HapticFreq_Hz_f32	3.53999996
k_HapticReactTime_Sec_f32	0.0160000008
k_LKAMaxVehSpd_Kph_f32	1.89999998
k_LKAMinVehSpd_Kph_f32	1.75
k_MaxHapticReqTime_Sec_f32	0.0299999993
k_SWARLimiter_HwDeg_f32	33.5999985
k_ShiftLevRevTime_Sec_f32	0.00499999989
k_StandstillThresh_Kph_f32	0.0399999991
k_StandstillTime_Sec_f32	0.00499999989
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-364.670013
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.070007
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.560000002
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.2000008
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	40
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	240	240 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25	-9.25 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.00600000005	0.00600000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3.53999996	3.53999996 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.5 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	225.639999

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	684
TrqOviSta_HapticTime_Sec_M_f32	1345
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	4.5
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.140000001
k_APAMaxHwTrq_HwNm_f32	0.239999995
k_APAMaxVehSpd_Kph_f32	5.4000001
k_ESCMaxVehSpd_Kph_f32	17.2999992
k_HapticAmplitude_MtrNm_f32	0.0179999992
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	4.55999994
k_HapticReactime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.69999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.0599999987
k_SWARLimiter_HwDeg_f32	36.4000015
k_ShiftLevRevTime_Sec_f32	0.0130000003
k_StandstillThresh_Kph_f32	0.119999997
k_StandstillTime_Sec_f32	0.0130000003
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-362.890015
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.100006
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.25999999		
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOviSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOviSta_Per1_LKAResponse_Cnt_lgc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	308.519989 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	684	684 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973	-8.02999973 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.017999992	0.017999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	4.55999994	4.55999994 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.6 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	1050
TrqOviSta_HapticTime_Sec_M_f32	2315.65991
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	6.69999981
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.400000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.0299999993
k_HapticDuration_Sec_f32	0.075000003

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
k_HapticFreq_Hz_f32	5.57399988		
k_HapticReactime_Sec_f32	0.0799999982		
k_LKAMaxVehSpd_Kph_f32	9.5		
k_LKAMinVehSpd_Kph_f32	8.75		
k_MaxHapticReqTime_Sec_f32	0.0900000036		
k_SWARLimiter_HwDeg_f32	39.2000008		
k_ShiftLevRevTime_Sec_f32	0.0209999997		
k_StandstillThresh_Kph_f32	0.200000003		
k_StandstillTime_Sec_f32	0.0209999997		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-361.109985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.130005		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.96000004		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	2315.65991	2315.65991 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0299999993	0.0299999993 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	6100
TrqOviSta_HapticTime_Sec_M_f32	3285.03003
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	26.8999996
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReactTime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	42
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResponse_Cnt_Igc	target_TrqOviSta_Per1_LKAResponse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-379.329987		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.160004		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✔
target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✔
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.8 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	44.7999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-377.549988
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.190002
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.9 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6198.2998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	6149.16016
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	16200
TrqOviSta_HapticTime_Sec_M_f32	5225.33984
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	13.3000002
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APANcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.219999999
k_APAMaxHwTrq_HwNm_f32	0.879999995
k_APAMaxVehSpd_Kph_f32	19.7999992
k_ESCMaxVehSpd_Kph_f32	36.5
k_HapticAmplitude_MtrNm_f32	0.0659999996
k_HapticDuration_Sec_f32	0.165000007
k_HapticFreq_Hz_f32	8.65799999
k_HapticReacttime_Sec_f32	0.175999999
k_LKAMaxVehSpd_Kph_f32	20.8999996
k_LKAMinVehSpd_Kph_f32	19.25
k_MaxHapticReqTime_Sec_f32	0.180000007
k_SWARLimiter_HwDeg_f32	47.5999985
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	0.439999998
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAIhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-375.769989
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.220001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	4.05999994
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAlnhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	89
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-11.6000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999	1011.23999 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	16200	16200 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001	-3.1500001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0659999996	0.0659999996 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	8.65799999	8.65799999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.10 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11536.2598
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1538.28003
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	11367.5996
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	31350
TrqOviSta_HapticTime_Sec_M_f32	8134.31982
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.50999999
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0394000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	19.8999996
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAlncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.280000001
k_APAMaxHwTrq_HwNm_f32	1.36000001
k_APAMaxVehSpd_Kph_f32	30.6000004
k_ESCMaxVehSpd_Kph_f32	50.9000015
k_HapticAmplitude_MtrNm_f32	0.101999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
k_HapticDuration_Sec_f32	0.254999995		
k_HapticFreq_Hz_f32	11.7419996		
k_HapticReacttime_Sec_f32	0.272000015		
k_LKAMaxVehSpd_Kph_f32	32.2999992		
k_LKAMinVehSpd_Kph_f32	29.75		
k_MaxHapticReqTime_Sec_f32	0.270000011		
k_SWARLimiter_HwDeg_f32	56		
k_ShiftLevRevTime_Sec_f32	0.0689999983		
k_StandstillThresh_Kph_f32	0.680000007		
k_StandstillTime_Sec_f32	0.0689999983		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-373.98999		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.25		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-3.9000001		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	108.599998		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	118.400002		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-0.200000003		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	31350	31350 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	8134.31982	8134.31982 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.50999999	0.50999999 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.101999998	0.101999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	11.7419996	11.7419996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.11 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13315.5801
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	13107.0801
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	36400
TrqOviSta_HapticTime_Sec_M_f32	9103.98047
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.300000012
k_APAMaxHwTrq_HwNm_f32	1.51999998
k_APAMaxVehSpd_Kph_f32	34.2000008
k_ESCMaxVehSpd_Kph_f32	55.7000008
k_HapticAmplitude_MtrNm_f32	0.114
k_HapticDuration_Sec_f32	0.284999996
k_HapticFreq_Hz_f32	12.7700005
k_HapticReactTime_Sec_f32	0.30399999
k_LKAMaxVehSpd_Kph_f32	36.0999985
k_LKAMinVehSpd_Kph_f32	33.25
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	58.7999992
k_ShiftLevRevTime_Sec_f32	0.0769999996
k_StandstillThresh_Kph_f32	0.75999999
k_StandstillTime_Sec_f32	0.0769999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCssillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCssillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_lgc	target_TrqOviSta_Per1_LKAResquest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-372.209991		
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.279999		
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.80000019		
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	118.400002		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.5999999		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996	1713.95996 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	36400	36400 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002	1.73000002 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_GMOSHOCssillateState_State_enum.value	2	2	✔
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.114	0.114 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	12.7700005	12.7700005 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.12 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16874.2207
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	16586.0391
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	46500
TrqOviSta_HapticTime_Sec_M_f32	11043.2998
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	26.5
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.340000004
k_APAMaxHwTrq_HwNm_f32	1.84000003
k_APAMaxVehSpd_Kph_f32	41.4000015
k_ESCMaxVehSpd_Kph_f32	65.3000031
k_HapticAmplitude_MtrNm_f32	0.137999997
k_HapticDuration_Sec_f32	0.344999999
k_HapticFreq_Hz_f32	14.8260002
k_HapticReactTime_Sec_f32	0.368000001
k_LKAMaxVehSpd_Kph_f32	43.7000008
k_LKAMinVehSpd_Kph_f32	40.25
k_MaxHapticReqTime_Sec_f32	0.360000014
k_SWARLimiter_HwDeg_f32	34.4000015
k_ShiftLevRevTime_Sec_f32	0.0930000022
k_StandstillThresh_Kph_f32	0.920000017
k_StandstillTime_Sec_f32	0.0930000022
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-370.429993
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.309998
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-4.4000001
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	138
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007	2065.32007 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	46500	46500 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008	4.17000008 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.137999997	0.137999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	14.8260002	14.8260002 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.13 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18653.5391

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	2241
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	18325.5195
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	51550
TrqOviSta_HapticTime_Sec_M_f32	12012.96
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKAShield_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.360000014
k_APAMaxHwTrq_HwNm_f32	2
k_APAMaxVehSpd_Kph_f32	45
k_ESCMaxVehSpd_Kph_f32	70.0999985
k_HapticAmplitude_MtrNm_f32	0.150000006
k_HapticDuration_Sec_f32	0.375
k_HapticFreq_Hz_f32	15.8540001
k_HapticReactTime_Sec_f32	0.400000006
k_LKAMaxVehSpd_Kph_f32	47.5
k_LKAMinVehSpd_Kph_f32	43.75
k_MaxHapticReqTime_Sec_f32	0.389999986
k_SWARLimiter_HwDeg_f32	37.2000008
k_ShiftLevRevTime_Sec_f32	0.101000004
k_StandstillThresh_Kph_f32	1
k_StandstillTime_Sec_f32	0.101000004
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAShield_State_enum	target_TrqOviSta_Per1_APAShield_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFAult_Cnt_lgc	target_TrqOviSta_Per1_ESCFAult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAShield_State_enum	target_TrqOviSta_Per1_LKAShield_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.649994
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.339996
target_TrqOviSta_Per1_ESCFAult_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-2.29999995		
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOviSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	51550	51550 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987	5.38999987 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	15.8540001	15.8540001 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.14 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	2	
TrqOviSta_ESCState_State_M_enum	4	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31108.7793	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	3470.76001	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	30501.8809	
TrqOviSta_HapticState_State_M_enum	1	
TrqOviSta_Hapticdur_Cnt_M_u16	50530	
TrqOviSta_HapticTime_Sec_M_f32	35000.5391	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548	
TrqOviSta_LKAFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_u08	2	
TrqOviSta_LKASState_State_M_enum	2	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	44.0999985	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	
k_APAINcludeHaptic_Cnt_lgc	0	
k_APAMaxHwTrqTime_Sec_f32	0.5	
k_APAMaxHwTrq_HwNm_f32	3.11999989	
k_APAMaxVehSpd_Kph_f32	70.1999969	
k_ESCMaxVehSpd_Kph_f32	103.699997	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
k_HapticAmplitude_MtrNm_f32	0.233999997		
k_HapticDuration_Sec_f32	0.584999979		
k_HapticFreq_Hz_f32	23.0499992		
k_HapticReacttime_Sec_f32	0.624000013		
k_LKAMaxVehSpd_Kph_f32	74.0999985		
k_LKAMinVehSpd_Kph_f32	68.25		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	56.7999992		
k_ShiftLevRevTime_Sec_f32	0.157000005		
k_StandstillThresh_Kph_f32	1.55999994		
k_StandstillTime_Sec_f32	0.157000005		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResponse_Cnt_Igc	target_TrqOviSta_Per1APAResponse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResponse_Cnt_Igc	target_TrqOviSta_Per1_LKAResponse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1APARecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1APAResponse_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-366.869995		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.369995		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	0.5		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAResponse_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	216.399994		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	226.199997		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	50530	50530 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	1.21000004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.233999997	0.233999997 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	23.0499992	23.0499992 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.15 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	220386
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	30810
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	44754
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	20942
TrqOviSta_HapticTime_Sec_M_f32	51515
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.221000001
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	57.2999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.620000005
k_APAMaxHwTrq_HwNm_f32	4.71999979
k_APAMaxVehSpd_Kph_f32	106.199997
k_ESCMaxVehSpd_Kph_f32	132.5
k_HapticAmplitude_MtrNm_f32	0.354000002
k_HapticDuration_Sec_f32	0.88499999
k_HapticFreq_Hz_f32	29.2180004
k_HapticReactTime_Sec_f32	0.944000006
k_LKAMaxVehSpd_Kph_f32	112.099998
k_LKAMinVehSpd_Kph_f32	103.25
k_MaxHapticReqTime_Sec_f32	0.899999976
k_SWARLimiter_HwDeg_f32	34
k_ShiftLevRevTime_Sec_f32	0.237000003
k_StandstillThresh_Kph_f32	2.3599999
k_StandstillTime_Sec_f32	0.237000003
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARrecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARrecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARrequest_Cnt_lgc	target_TrqOviSta_Per1APARrequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAResquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-365.089996		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.399994		
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	275.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	285		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.10000038		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APASState_State_M_enum	3	3	✔
TrqOvlSta_ESCState_State_M_enum	3	3	✔
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	30810	30810 ± 1	✔
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOvlSta_HapticState_State_M_enum	0	0	✔
TrqOvlSta_Hapticdur_Cnt_M_u16	20942	20942 ± 1	✔
TrqOvlSta_HapticTime_Sec_M_f32	51515	51515 ± 1	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.994499981	0.994499981 ± 0.0000009	✔
TrqOvlSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✔
TrqOvlSta_LKASState_State_M_enum	2	2	✔
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✔
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOvlSta_Per1_APASState_State_enum.value	3	3	✔
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✔
target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✔
target_TrqOvlSta_Per1_LKASState_State_enum.value	2	2	✔
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.354000002	0.354000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	29.2180004	29.2180004 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.16 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	33000
TrqOviSta_HapticTime_Sec_M_f32	204770
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReactTime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	49
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-363.309998
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.429993
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIhhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	2	2	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptiodur_Cnt_M_u16	33000	33000 ± 1	✓
TrqOviSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008	38.2000008 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	38.2000008	38.2000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.17 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	1	
TrqOviSta_ESCState_State_M_enum	3	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2020425
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	86335.6016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	391692
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	31000
TrqOvlSta_HapticTime_Sec_M_f32	210780
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAMIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.232999995
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	174.5
k_HapticAmplitude_MtrNm_f32	0.671999991
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	55.9459991
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	220
k_LKAMinVehSpd_Kph_f32	196
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	49.5999985
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAREquest_Cnt_lgc	target_TrqOvlSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.529999
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.459991
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	61.7999992
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	210780	210780 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.18 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	41000
TrqOviSta_HapticTime_Sec_M_f32	216790.344
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
k_ESCMaxVehSpd_Kph_f32	15.6000004		
k_HapticAmplitude_MtrNm_f32	0		
k_HapticDuration_Sec_f32	1.71000004		
k_HapticFreq_Hz_f32	56.973999		
k_HapticReacttime_Sec_f32	1.824		
k_LKAMaxVehSpd_Kph_f32	216.600006		
k_LKAMinVehSpd_Kph_f32	199.5		
k_MaxHapticReqTime_Sec_f32	0.209999993		
k_SWARLimiter_HwDeg_f32	50.2000008		
k_ShiftLevRevTime_Sec_f32	0.0529999994		
k_StandstillThresh_Kph_f32	4.55999994		
k_StandstillTime_Sec_f32	0.0529999994		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-389.75		
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.48999		
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.19 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	45000
TrqOviSta_HapticTime_Sec_M_f32	228811.703
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReactTime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.061000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0.061000007
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-387.970001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.519989		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	103		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.20 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	29000
TrqOviSta_HapticTime_Sec_M_f32	198760.641
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReactTime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	23.2000008
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResponse_Cnt_Igc	target_TrqOviSta_Per1APAResponse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1CCWFound_Cnt_Igc	target_TrqOviSta_Per1CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1CCWPosition_HwDeg_f32	target_TrqOviSta_Per1CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1CWFound_Cnt_Igc	target_TrqOviSta_Per1CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1CWPosition_HwDeg_f32	target_TrqOviSta_Per1CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1ESCFault_Cnt_Igc	target_TrqOviSta_Per1ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1ESCRequest_Cnt_Igc	target_TrqOviSta_Per1ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1ESCState_State_enum	target_TrqOviSta_Per1ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1GMOSHOScillateState_State_enum	target_TrqOviSta_Per1GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1GMOSHAPAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1GMOSHAPAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1GMOSHESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1GMOSHESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1GMOSHLKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1GMOSHLKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1HapticRequest_Cnt_Igc	target_TrqOviSta_Per1HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1HwTorque_HwNm_f32	target_TrqOviSta_Per1HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1LKAFault_Cnt_Igc	target_TrqOviSta_Per1LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1LKAInhibit_Cnt_Igc	target_TrqOviSta_Per1LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1LKAResponse_Cnt_Igc	target_TrqOviSta_Per1LKAResponse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1LKASState_State_enum	target_TrqOviSta_Per1LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1MaxSecureVehicleSpeed_Kph_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-386.190002
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.549988
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	198760.641	198760.641 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.21 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	2	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	51515
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	51515
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	51515
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	33020
TrqOviSta_HapticTime_Sec_M_f32	51515
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKAShield_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	32
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.915000021
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	63
k_ESCMaxVehSpd_Kph_f32	0
k_HapticAmplitude_MtrNm_f32	0.449999988
k_HapticDuration_Sec_f32	0.360000014
k_HapticFreq_Hz_f32	36.4000015
k_HapticReactTime_Sec_f32	0.783999979
k_LKAMaxVehSpd_Kph_f32	93.0999985
k_LKAMinVehSpd_Kph_f32	85.75
k_MaxHapticReqTime_Sec_f32	0.870000005
k_SWARLimiter_HwDeg_f32	38.2000008
k_ShiftLevRevTime_Sec_f32	0.141000003
k_StandstillThresh_Kph_f32	1.32000005
k_StandstillTime_Sec_f32	0.133000001
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAShield_State_enum	target_TrqOviSta_Per1_APAShield_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOviSta_Per1_GMOSHOCscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAShield_State_enum	target_TrqOviSta_Per1_LKAShield_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-384.410004
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.579987
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.4000001		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKARequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	0	0	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	51515	51515 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	33020	33020 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	51515	51515 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002	1.60000002 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✔
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✔
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.449999988	0.449999988 ± 0.0009765625	✔
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	36.4000015	36.4000015 ± 0.0009765625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.22 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	54520
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	54520
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	54520
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	34002
TrqOviSta_HapticTime_Sec_M_f32	54520
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	33.0999985
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAIncludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.925999999
k_APAMaxHwTrq_HwNm_f32	3.839999991

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
k_APAMaxVehSpd_Kph_f32	64.8000031		
k_ESCMaxVehSpd_Kph_f32	180		
k_HapticAmplitude_MtrNm_f32	0.469999999		
k_HapticDuration_Sec_f32	0.375		
k_HapticFreq_Hz_f32	37.0999985		
k_HapticReacttime_Sec_f32	0.800000012		
k_LKAMaxVehSpd_Kph_f32	95		
k_LKAMinVehSpd_Kph_f32	87.5		
k_MaxHapticReqTime_Sec_f32	0.88499999		
k_SWARLimiter_HwDeg_f32	38.5		
k_ShiftLevRevTime_Sec_f32	0.144999996		
k_StandstillThresh_Kph_f32	1.36000001		
k_StandstillTime_Sec_f32	0.136999995		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enum	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-382.630005		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.609985		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.70000005		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	167.399994		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	152.699997		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_ESCState_State_M_enum	0	0	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	34002	34002 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.75480008	1.75479996 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	33.0999985	33.0999985 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	33.0999985	33.0999985 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.469999999	0.469999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	37.0999985	37.0999985 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.23 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	5270
TrqOviSta_HapticTime_Sec_M_f32	27475.5293
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReactTime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARequest_Cnt_lgc	target_TrqOviSta_Per1APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enum	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResponse_Cnt_Igc	target_TrqOviSta_Per1_LKAResponse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-380.850006		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.640015		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAResponse_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✔
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.24 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APANcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-379.070007
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.670013
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.25 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-377.290009
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.700012

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOviSta_Per1_ShiftLeversInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.26 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOCscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOCscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAINhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAINhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAREquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOvlSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-375.51001
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.730011
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKAINhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.27 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-373.730011		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.76001		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAINhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Haptidur_Cnt_M_u16	11150	11150 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✔
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✔
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.28 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAlncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enum	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAlnhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAlnhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-371.950012
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.790009
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOvlSta_APASState_State_M_enum	0	0	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOvlSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKASState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.29 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	41000
TrqOviSta_HapticTime_Sec_M_f32	216790.344
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReactTime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	15.6000004
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-370.170013
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.820007
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	41000	41000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.30 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2020425
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	86335.6016
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	391692
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	31000
TrqOviSta_HapticTime_Sec_M_f32	210780
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	41

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.232999995
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	174.5
k_HapticAmplitude_MtrNm_f32	0.671999991
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	55.9459991
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	220
k_LKAMinVehSpd_Kph_f32	196
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	17.7000008
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.390015
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.850006
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	61.7999992
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	210780	210780 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.31 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	41000
TrqOviSta_HapticTime_Sec_M_f32	216790.344
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReactTime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	15.6000004
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-366.609985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.880005		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.32 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	41000
TrqOvlSta_HapticTime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReactTime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	15.6000004
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-364.829987
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.910004
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.33 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-363.049988
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.940002
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.34 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	1.39999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-361.269989
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.970001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.35 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-390.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	2	2	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.36 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	1
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-388.230011
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.029999
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.37 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	1
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-386.450012
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.059998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.38 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	1
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	0
TrqOviSta_Haptictime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-384.670013
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.089996
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	0	1 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.39 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	0
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResquest_Cnt_Igc	target_TrqOviSta_Per1_APAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_Igc	target_TrqOviSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngResquest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngResquest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAResquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-382.890015		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.119995		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAResquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOviSta_Per1_SWARTrgtAngResquest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	2	2	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.40 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	33000
TrqOvlSta_HapticTime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReactTime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	58.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-361.529999
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.480011
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.41 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	33000
TrqOviSta_HapticTime_Sec_M_f32	204770
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReactTime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	59.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-389.75
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.51001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499	1952499 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	33000	33000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.42 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	33000
TrqOviSta_HapticTime_Sec_M_f32	204770
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	38.2000008

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APANcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	59.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResquest_Cnt_lgc	target_TrqOviSta_Per1APAResquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASate_State_enum	target_TrqOviSta_Per1APASate_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResquest_Cnt_lgc	target_TrqOviSta_Per1_LKAResquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASate_State_enum	target_TrqOviSta_Per1_LKASate_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-387.970001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.540009
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	204770	204770 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.43 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	33000
TrqOviSta_HapticTime_Sec_M_f32	204770
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReactTime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	59.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-386.190002		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.570007		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.44 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Haptidur_Cnt_M_u16	29000
TrqOvlSta_HapticTime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReactTime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	0.119999997
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-384.410004
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.600006
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	198760.641	198760.641 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.45 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	29000
TrqOviSta_HapticTime_Sec_M_f32	198760.641
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReactTime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	0.119999997
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-382.630005
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.630005
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	29000	29000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	198760.641	198760.641 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.46 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	29000
TrqOviSta_HapticTime_Sec_M_f32	198760.641
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	52.5999985
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARequest_Cnt_lgc	target_TrqOviSta_Per1APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enum	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc	target_TrqOviSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1APARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-380.850006
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.660004
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	198760.641	198760.641 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	1	1	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.47 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	29000
TrqOviSta_HapticTime_Sec_M_f32	198760.641
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReactTime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	53.5999985
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-379.070007		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.690002		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	198760.641	198760.641 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✔
TrqOviSta_LKASState_State_M_enum	2	2	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.48 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	29000
TrqOviSta_HapticTime_Sec_M_f32	198760.641
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_Igc	1
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	0
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReactTime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	54.5999985
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-377.290009
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.720001
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.69999981
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	198760.641	198760.641 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.49 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-375.51001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.75
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.50 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APANcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-373.730011
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.779999
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.51 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-371.950012		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.809998		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	14		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.52 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Haptidur_Cnt_M_u16	11150
TrqOvlSta_HapticTime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKASState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAResponse_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-370.170013
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.839996
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	1	1	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.53 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	32.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.390015
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.869995
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.54 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	33.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-366.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.899994
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	2	2	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.55 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	0
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	11150
TrqOviSta_HapticTime_Sec_M_f32	425564
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReactTime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-364.829987		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.929993		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	425564	425564 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	2	2	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.56 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptiodur_Cnt_M_u16	1050
TrqOvlSta_HapticTime_Sec_M_f32	2315.65991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOvlSta_LKAFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKASState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.400000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.0299999993
k_HapticDuration_Sec_f32	0.075000003
k_HapticFreq_Hz_f32	5.57399988
k_HapticReactTime_Sec_f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k_LKAMinVehSpd_Kph_f32	8.75
k_MaxHapticReqTime_Sec_f32	0.0900000036
k_SWARLimiter_HwDeg_f32	22.1000004
k_ShiftLevRevTime_Sec_f32	0.0209999997
k_StandstillThresh_Kph_f32	0.200000003
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-363.049988
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.959991
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.96000004
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	1	1	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	2315.65991	2315.65991 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.029999993	0.029999993 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.57 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	4
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	1050
TrqOviSta_HapticTime_Sec_M_f32	2315.65991
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.1599999996
k_APAMaxHwTrq_HwNm_f32	0.400000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.0299999993
k_HapticDuration_Sec_f32	0.075000003
k_HapticFreq_Hz_f32	5.57399988
k_HapticReactTime_Sec_f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k_LKAMinVehSpd_Kph_f32	8.75
k_MaxHapticReqTime_Sec_f32	0.0900000036
k_SWARLimiter_HwDeg_f32	22.1000004
k_ShiftLevRevTime_Sec_f32	0.0209999997
k_StandstillThresh_Kph_f32	0.200000003
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-361.269989
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	362.98999
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.96000004
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	0	0	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Haptidur_Cnt_M_u16	1050	1050 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	2315.65991	2315.65991 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0299999993	0.0299999993 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.58 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-390.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	363.019989
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	1	1	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.59 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	6100
TrqOviSta_HapticTime_Sec_M_f32	3285.03003
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	26.8999996
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReactTime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	28.8999996
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-388.230011		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	363.049988		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✔
TrqOviSta_ESCState_State_M_enum	0	0	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	0	0	✔
TrqOviSta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.60 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	6100
TrqOviSta_HapticTime_Sec_M_f32	3285.03003
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	26.8999996
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReactTime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	28.8999996
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-386.450012
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	363.079987
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.66000009
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.5999985
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	6100	6100 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.61 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	2
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	6100
TrqOviSta_HapticTime_Sec_M_f32	3285.03003
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	1
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	26.8999996
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReactTime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	28.8999996
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-384.670013
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	363.109985
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.66000009
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.5999985
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	1	1	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	6100	6100 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.62 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	3
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	225.639999
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	684
TrqOviSta_HapticTime_Sec_M_f32	1345
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	4.5

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.140000001
k_APAMaxHwTrq_HwNm_f32	0.239999995
k_APAMaxVehSpd_Kph_f32	5.4000001
k_ESCMaxVehSpd_Kph_f32	17.2999992
k_HapticAmplitude_MtrNm_f32	0.017999992
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	4.55999994
k_HapticReacttime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.69999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.0599999987
k_SWARLimiter_HwDeg_f32	20.2999992
k_ShiftLevRevTime_Sec_f32	0.0130000003
k_StandstillThresh_Kph_f32	0.119999997
k_StandstillTime_Sec_f32	0.0130000003
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-382.890015
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	363.140015
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	1.25999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	40
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	0	0	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	308.519989 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	684	684 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973	-8.02999973 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.017999992	0.017999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	4.55999994	4.55999994 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.63 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	2
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	33000
TrqOviSta_HapticTime_Sec_M_f32	204770
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReactTime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAREquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-381.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.170013		
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.60000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.20000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APASState_State_M_enum	2	2	✔
TrqOvlSta_ESCState_State_M_enum	3	3	✔
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✔
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOvlSta_HapticState_State_M_enum	1	1	✔
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	✔
TrqOvlSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✔
TrqOvlSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOvlSta_LKASState_State_M_enum	0	0	✔
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3.70000005	3.70000005 ± 0.0009765625	✔
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3.70000005	3.70000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.64 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Haptidur_Cnt_M_u16	33000
TrqOvlSta_HapticTime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKASState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReactTime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_lgc	target_TrqOvlSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-379.329987
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	363.200012
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.60000004
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	121
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	2	2	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	3.70000005	3.70000005 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3.70000005	3.70000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.65 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	5
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	100.599998
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-377.549988
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	363.230011
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	5	5	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	5	5	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.66 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	3
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	5270
TrqOviSta_HapticTime_Sec_M_f32	27475.5293
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvHwAngle_HwDeg_M_f32	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-375.769989
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	363.26001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	3	3	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.690000006	5.690000006 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.67 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta	
TrqOviSta_APASState_State_M_enum	1	
TrqOviSta_ESCState_State_M_enum	5	
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219	
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	5450	
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	
TrqOviSta_HapticState_State_M_enum	3	
TrqOviSta_Hapticdur_Cnt_M_u16	5270	
TrqOviSta_HapticTime_Sec_M_f32	27475.5293	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.690000006	
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015	
TrqOviSta_LKAFault_Cnt_M_lgc	0	
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	
TrqOviSta_LKAPermFault_Cnt_M_u08	4	
TrqOviSta_LKASState_State_M_enum	3	
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1	
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAINcludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.376666665	
k_APAMaxHwTrq_HwNm_f32	3.75999999	
k_APAMaxVehSpd_Kph_f32	84.5999985	
k_ESCMaxVehSpd_Kph_f32	1	
k_HapticAmplitude_MtrNm_f32	0.282000005	
k_HapticDuration_Sec_f32	0.704999983	
k_HapticFreq_Hz_f32	3.25	
k_HapticReactTime_Sec_f32	0.751999974	
k_LKAMaxVehSpd_Kph_f32	89.3000031	
k_LKAMinVehSpd_Kph_f32	82.25	
k_MaxHapticReqTime_Sec_f32	0.720000029	
k_SWARLimiter_HwDeg_f32	60	
k_ShiftLevRevTime_Sec_f32	0.188999996	
k_StandstillThresh_Kph_f32	4	
k_StandstillTime_Sec_f32	0.188999996	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-373.98999		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	360.200012		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	5	5	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	✔
TrqOviSta_HapticState_State_M_enum	3	3	✔
TrqOviSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	5	5	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.68 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8867.28027
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1274.76001
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	8758.37988
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	23775
TrqOviSta_HapticTime_Sec_M_f32	6679.83008
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0328000002
TrqOviSta_LKAFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.25
k_APAMaxHwTrq_HwNm_f32	1.12
k_APAMaxVehSpd_Kph_f32	25.2000008
k_ESCMaxVehSpd_Kph_f32	43.7000008
k_HapticAmplitude_MtrNm_f32	0.0839999989
k_HapticDuration_Sec_f32	0.209999993
k_HapticFreq_Hz_f32	10.1999998
k_HapticReactTime_Sec_f32	0.224000007
k_LKAMaxVehSpd_Kph_f32	26.6000004
k_LKAMinVehSpd_Kph_f32	24.5
k_MaxHapticReqTime_Sec_f32	0.224999994
k_SWARLimiter_HwDeg_f32	21.7999992
k_ShiftLevRevTime_Sec_f32	0.057
k_StandstillThresh_Kph_f32	0.560000002
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enum	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	1
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-998.710022
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	429.160004
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	5.11000013
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103.699997
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.90000001
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	4	4	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1274.76001	1274.76001 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	8758.37988	8758.37988	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Hapticdur_Cnt_M_u16	23775	23775 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	6679.83008	6679.83008 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005	-1.32000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	4	4	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004	16.6000004 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	16.6000004	16.6000004 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0839999989	0.0839999989 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	10.1999998	10.1999998 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.69 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	41000
TrqOviSta_HapticTime_Sec_M_f32	216790.344
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReactTime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	849.059998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784	404784	✓
TrqOviSta_HapticState_State_M_enum	1	1	✓
TrqOviSta_Haptidur_Cnt_M_u16	41000	41000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.70 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	41000
TrqOviSta_HapticTime_Sec_M_f32	216790.344
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	3
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	849.059998
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOviSta_Per1_ShiftLevrIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	2	2	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784	404784	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992	43.7999992 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	43.7999992	43.7999992 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.71 (Repeat Count = 1)					✓
Name	Input Value				
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta				
TrqOviSta_APASState_State_M_enum	1				
TrqOviSta_ESCState_State_M_enum	0				
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351				
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609				
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784				
TrqOviSta_HapticState_State_M_enum	1				
TrqOviSta_Hapticdur_Cnt_M_u16	41000				
TrqOviSta_HapticTime_Sec_M_f32	216790.344				
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001				
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014				
TrqOviSta_LKAFault_Cnt_M_lgc	0				
TrqOviSta_LKAPermFault_Cnt_M_lgc	1				
TrqOviSta_LKAPermFault_Cnt_M_u08	3				
TrqOviSta_LKASState_State_M_enum	2				
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992				
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1				
k_APAINcludeHaptic_Cnt_lgc	0				
k_APAMaxHwTrqTime_Sec_f32	0.254999995				
k_APAMaxHwTrq_HwNm_f32	9.11999989				
k_APAMaxVehSpd_Kph_f32	205.199997				
k_ESCMaxVehSpd_Kph_f32	15.6000004				
k_HapticAmplitude_MtrNm_f32	0				
k_HapticDuration_Sec_f32	1.71000004				
k_HapticFreq_Hz_f32	56.973999				
k_HapticReactTime_Sec_f32	1.824				
k_LKAMaxVehSpd_Kph_f32	216.600006				
k_LKAMinVehSpd_Kph_f32	199.5				
k_MaxHapticReqTime_Sec_f32	0.209999993				
k_SWARLimiter_HwDeg_f32	20.2000008				
k_ShiftLevRevTime_Sec_f32	0.0529999994				
k_StandstillThresh_Kph_f32	4.55999994				
k_StandstillTime_Sec_f32	0.0529999994				
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc				

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	849.059998		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	3	3	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	404784	404784	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.72 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.01001
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.73 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.01001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.74 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.01001
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.75 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Hapticdur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	2	2	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✔
TrqOviSta_HapticState_State_M_enum	1	1	✔
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOviSta_LKASState_State_M_enum	0	0	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	0 ± 1	

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHoscillateState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.76 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	25475
TrqOviSta_HapticTime_Sec_M_f32	186740.953
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.55999994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHoscillateState_State_enun	target_TrqOviSta_Per1_GMOSHoscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	799.659973
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.90000001
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	✓
TrqOviSta_HapticState_State_M_enum	2	2	✓
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.77 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	25475
TrqOviSta_HapticTime_Sec_M_f32	186740.953
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.46200012
k_HapticDuration_Sec_f32	1.55999994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	799.659973
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAIInhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.78 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Haptidur_Cnt_M_u16	25475
TrqOviSta_HapticTime_Sec_M_f32	186740.953
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APANcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.55999994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	799.659973
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	3	3	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.79 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	0
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOviSta_HapticState_State_M_enum	1
TrqOviSta_Hapticdur_Cnt_M_u16	0
TrqOviSta_HapticTime_Sec_M_f32	186740.953
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	3
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOviSta_ReadytoPulse_Cnt_M_lgc	1
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.55999994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReactTime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAResquest_Cnt_Igc	target_TrqOvlSta_Per1_APAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAResquest_Cnt_Igc	target_TrqOvlSta_Per1_LKAResquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKASState_State_enum	target_TrqOvlSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngResquest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngResquest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APAResquest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAResquest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngResquest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APASState_State_M_enum	1	1	✔
TrqOvlSta_ESCState_State_M_enum	3	3	✔
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	✔
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	✔
TrqOvlSta_HapticState_State_M_enum	1	1	✔
TrqOvlSta_Hapticdur_Cnt_M_u16	1	1 ± 1	✔
TrqOvlSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✔
TrqOvlSta_LKAFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✔
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✔
TrqOvlSta_LKASState_State_M_enum	3	3	✔
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✔
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.80 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APASState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Haptidur_Cnt_M_u16	45000
TrqOvlSta_HapticTime_Sec_M_f32	228811.703
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKASState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_Igc	0
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReactTime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.061000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAREquest_Cnt_Igc	target_TrqOvlSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APASState_State_enum	target_TrqOvlSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	868.820007
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Test Step 3.81 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	45000
TrqOviSta_HapticTime_Sec_M_f32	228811.703
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReactTime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.061000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_lgc	target_TrqOviSta_Per1_APAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_lgc	target_TrqOviSta_Per1_LKAREquest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	868.820007
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5
target_TrqOviSta_Per1_ShiftLeverInReverse_Cnt_lgc.value	0

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Haptidur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOScillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.82 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Haptidur_Cnt_M_u16	45000
TrqOviSta_HapticTime_Sec_M_f32	228811.703
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.061000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APAResult_Cnt_lgc	target_TrqOviSta_Per1APAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1APASState_State_enum	target_TrqOviSta_Per1APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_OscillateState_State_enum	target_TrqOviSta_Per1_GMOSH_OscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOviSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAINhibit_Cnt_lgc	target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_lgc	target_TrqOviSta_Per1_LKAResult_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APARecoverableFaults_Cnt_lgc.value	0
target_TrqOviSta_Per1APAResult_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985
target_TrqOviSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	868.820007
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOviSta_Per1_LKAINhibit_Cnt_lgc.value	0
target_TrqOviSta_Per1_LKAResult_Cnt_lgc.value	0
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998
target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOviSta_Per1_ShiftLevrlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	2	2	✓
TrqOviSta_ESCState_State_M_enum	4	4	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797	91133.6797 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOviSta_LKASState_State_M_enum	3	3	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015	49.4000015 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	49.4000015	49.4000015 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.83 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	4
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOviSta_HapticState_State_M_enum	2
TrqOviSta_Hapticdur_Cnt_M_u16	0
TrqOviSta_HapticTime_Sec_M_f32	228811.703
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOviSta_LKAFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOviSta_LKASState_State_M_enum	2
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAINcludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReactTime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_lgc

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREquest_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum	target_TrqOviSta_Per1_GMOSHOScillateState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAREquest_Cnt_Igc	target_TrqOviSta_Per1_LKAREquest_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0		
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	868.820007		
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1		
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0		
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	0		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	1	1	✔
TrqOviSta_ESCState_State_M_enum	4	4	✔
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797	91133.6797 ± 1	✔
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	✔
TrqOviSta_HapticState_State_M_enum	2	2	✔
TrqOviSta_Hapticdur_Cnt_M_u16	1	1 ± 1	✔
TrqOviSta_HapticTime_Sec_M_f32	0	0 ± 1	✔
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✔
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_Igc	1	1 ± 1	✔
TrqOviSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✔
TrqOviSta_LKASState_State_M_enum	3	3	✔
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✔
TrqOviSta_ReadytoPulse_Cnt_M_Igc	0	0 ± 1	✔

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	1	1	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOviSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	3	3	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.84 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOviSta
TrqOviSta_APASState_State_M_enum	1
TrqOviSta_ESCState_State_M_enum	1
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOviSta_HapticState_State_M_enum	0
TrqOviSta_Haptidur_Cnt_M_u16	44450
TrqOviSta_HapticTime_Sec_M_f32	0
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOviSta_LKAFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_Igc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOviSta_LKASState_State_M_enum	0
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1
k_APAINcludeHaptic_Cnt_Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc	target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAResponse_Cnt_Igc	target_TrqOviSta_Per1_APAREquest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APASState_State_enum	target_TrqOviSta_Per1_APASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target_TrqOviSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc	target_TrqOviSta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target_TrqOviSta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun	target_TrqOviSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOviSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOviSta_Per1_HwTorque_HwNm_f32

TEST DETAILS REPORT

2015-02-04, 12:34:40+0530



TrqOviSta_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc	target_TrqOviSta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAIInhibit_Cnt_Igc	target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAResult_Cnt_Igc	target_TrqOviSta_Per1_LKAResult_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKASState_State_enum	target_TrqOviSta_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc	target_TrqOviSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREcoverableFaults_Cnt_Igc.value	0
target_TrqOviSta_Per1_APAREquest_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOviSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value	361.01001
target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value	0
target_TrqOviSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOviSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOviSta_Per1_LKAFault_Cnt_Igc.value	1
target_TrqOviSta_Per1_LKAIInhibit_Cnt_Igc.value	0
target_TrqOviSta_Per1_LKAREquest_Cnt_Igc.value	1
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003
target_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0
target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

Name	Actual Value	Expected Value	Result
TrqOviSta_APASState_State_M_enum	0	0	✓
TrqOviSta_ESCState_State_M_enum	2	2	✓
TrqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOviSta_HapticState_State_M_enum	0	0	✓
TrqOviSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOviSta_HapticTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOviSta_LKAFault_Cnt_M_Igc	1	1 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	✓
TrqOviSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOviSta_LKASState_State_M_enum	0	0	✓
TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOviSta_ReadytoPulse_Cnt_M_Igc	1	1 ± 1	✓
target_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_APASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOviSta_Per1_GMOSHOCillateState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_LKASState_State_enum.value	0	0	✓
target_TrqOviSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	✓
target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	✓
target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP0_CheckpointReached	1	✓
Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOviSta_Per1_CP1_CheckpointReached	1	✓

