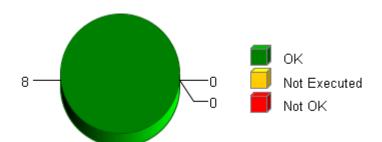


Summary

Overall Test Object Results (including Coverage)



Date: 2016-04-22 **Time:** 16:31:23+0530



Selected Project Items

Test Object "CBD UnitTest/PICurrCntrl/CalLowPassFiltBilinearOut"

Test Object "CBD UnitTest/PICurrCntrl/CalLowPassFiltBilinearTerm"

Test Object "CBD_UnitTest/PICurrCntrl/CalLowPassFiltVecuOut"

Test Object "CBD UnitTest/PICurrCntrl/IntegralStateVarNonOperState"

Test Object "CBD_UnitTest/PICurrCntrl/LoaMtgtnSclFac"

Test Object "CBD_UnitTest/PICurrCntrl/PICurrCntrl_Init"

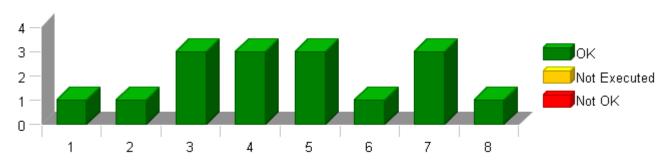
Test Object "CBD_UnitTest/PICurrCntrl/PICurrCntrl_Per1"

Test Object "CBD_UnitTest/PICurrCntrl/PICurrCntrl_Per2"

Used Test Environments

GNU GCC GNU GVD (Default)

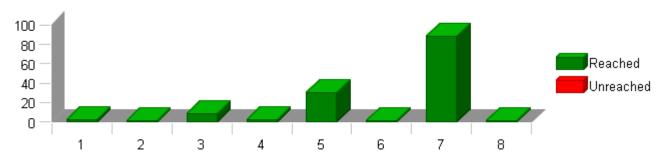
Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

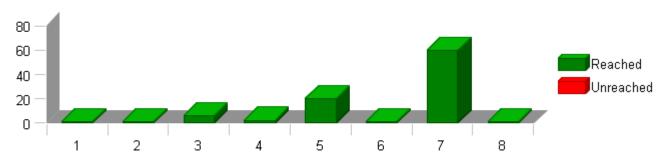


Statement (C0) Coverage: Total Statements for Each Test Object



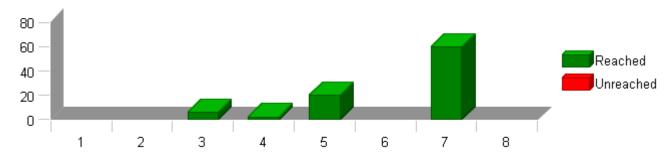
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

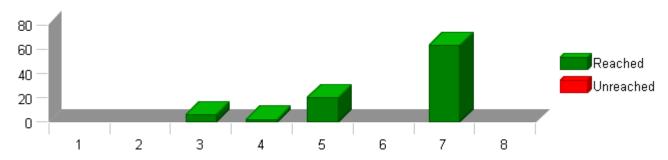


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



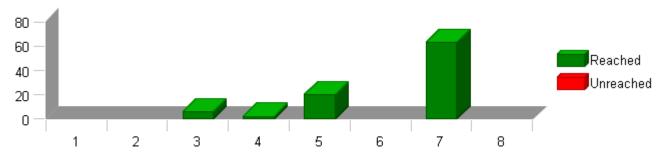
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2016-04-22, 16:31:23+0530



Test Object List

Project Ap_PICurrCntrl

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases Resul
	Ap_PICurrCntrl	100 %	100 %	100 %	100 %	100 %	16 of 16 passed
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	16 of 16 passed
	PICurrCntrl	100 %	100 %	100 %	100 %	100 %	16 of 16 passed
1	<u>CalLowPassFiltBilinearOut</u>	100 %	100 %	-	-	-	1 of 1 passed
2	<u>CalLowPassFiltBilinearTerm</u>	100 %	100 %	-	-	-	1 of 1 passed
3	<u>CalLowPassFiltVecuOut</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
4	<u>IntegralStateVarNonOperState</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
5	<u>LoaMtgtnSclFac</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
6	PICurrCntrl Init	100 %	100 %	-	-	-	1 of 1 passed
7	PICurrCntrl_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
8	PICurrCntrl Per2	100 %	100 %	-	-	-	1 of 1 passed

© Report created by TESSY V3.1.11, report template V2.0

2016-04-22, 16:27:19+0530



CalLowPassFiltBilinearTerm

Project Ap_PICurrCntrl

Test Object CalLowPassFiltBilinearTerm

PICurrCntrl

Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

Statistics

Module

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Project Root Directory	D:\Synergy_Work_Area\Plcurrentcontrol
Configuration File	D:\Synergy_Work_Area\PIcurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PlCurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(Program Files (x86)\text{Nexas})\text{Instruments\ccsv4\tools\compiler\tms470_4.9.5\include}

Comments/Description/Sp	ecification
Name	Text
Module 'PICurrCntrl'	Unit Test Information* Name of Tester:Priyanka Shitole Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:15 Module Design Document:PlCurrentContrl.doc Module Design Document Version:11 Data Dictionary Version:14 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):NA Total RAM Used (Bytes):NA Total RAM Used (Bytes):NA Special Test Requirements: Test Date:4/22/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnSclFac "". for variables ""k_CLOAFdbackSignalSclFacSlew_UlspS_f32_k ILOAFdbackSignalSclFac_Uls_M_f32" to achieve 100% path coverage. NOTE 3: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9



Description

Vector Description:

TS1.1All Min
TS1.2All Max
TS1.3Freq_Hz_T_f32=Min
TS1.4Freq_Hz_T_f32=Max
TS1.5Freq_Hz_T_f32=Pos
TS1.6TimeCons_Sec_T_f32=Min
TS1.7TimeCons_Sec_T_f32=Max
TS1.8TimeCons_Sec_T_f32=Pos

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Freq_Hz_T_f32	0.100000001		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
TimeCons_Sec_T_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	50928.5781	50928.582	~
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1.963457e-005	1.96345682e-005	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.2 (Repeat Count = 1)				
Name	Input Value			
Freq_Hz_T_f32	2.14748365e+009			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	_Str		
TimeCons_Sec_T_f32	0.100000001			
Name	Actual Value	Expected Value	Result	
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-1	-1	~	
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1	1	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 1.3 (Repeat Count = 1)				
Name	Input Value			
Freq_Hz_T_f32	0.10000001			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	Str		
TimeCons_Sec_T_f32	0.00240000011			
Name	Actual Value	Expected Value	Result	
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1325.29102	1325.29114	~	
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.000753414293	0.000753414177	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.4 (Repeat Count = 1)				
Name	Input Value			
Freq_Hz_T_f32	2.14748365e+009			
LowPassFiltBilinear_T_Str	wPassFiltBilinear_T_Str target_LowPassFiltBilinear_T_Str			
TimeCons_Sec_T_f32	0.0214000009			
Name	Actual Value	Expected Value	Result	
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-1	-1	~	
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1	1	✓	

CalLowPassFiltBilinearTerm

none

0



0

Actual Function	Count Expected Function	Count Result
Test Step Call Trace		🗸

*** No Call Expected ***

Test Step 1.5 (Repeat Count = 1)			~
Name	Input Value		
Freq_Hz_T_f32	25348.7129		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
TimeCons_Sec_T_f32	0.0365200005		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-0.999656141	-0.999656141	~
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.99965626	0.99965626	~

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
Freq_Hz_T_f32	250.412292		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T	_Str	
TimeCons_Sec_T_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	19.3382893	19.3382912	✓
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.0468641147	0.046864111	✓

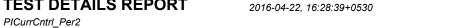
Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
Freq_Hz_T_f32	1050.32556		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
TimeCons_Sec_T_f32	0.10000001		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-0.996969402	-0.996969402	✓
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.996978581	0.996978581	✓

Test Step Call Trace			•	0		
	Actual Function	Count	Expected Function	Count	Resul	t
	none	0	*** No Call Expected ***	0	•	ī

Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
Freq_Hz_T_f32	286.223602		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
TimeCons_Sec_T_f32	0.00300000003		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-0.629299283	-0.629299283	✓
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.729553878	0.729553878	✓

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~





Project Ap_PICurrCntrl Module PICurrCntrl **Test Object** PICurrCntrl_Per2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Project Root Directory	D:\Synergy_Work_Area\Plcurrentcontrol		
Configuration File	D:\Synergy_Work_Area\PIcurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml		
Target Environment	GNU GCC GNU GVD (Default)		
Kind of Test	Unit Test		
Linker Options			
Source File(s)			
File	\$(SOURCEROOT)\src\Ap_PlCurrCntrl.c		
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include		
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c		
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(Program Files (x86)\text{Nexas})\text{Instruments\ccsv4\tools\compiler\tms470_4.9.5\include}		

Comments/Description/Spe	ecification
Name	Text
Module 'PICurrCntri'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:15 Module Design Document:PlCurrentContrl.doc Module Design Document Version:11 Data Dictionary Version:14 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):NA Total RAM Used (Bytes):NA Total RAM Used (Bytes):NA Special Test Requirements: Test Date:4/22/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnScIFac "". for variables ""k_CLOAFdbackSignalScIFacSlew_UlspS_f32,k_ILOAFdbackSignalScIFacSlew_UlspS_f32, PlCurrCntrl_CurrSensFailScIFac_Uls_M_f32 and PlCurrCntrl_InverterFailScIFac_Uls_M_f32 and PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9



Description

Vector Description:

TS1.1All Min

TS1.1All Min
TS1.2All Max
TS1.3EstKe_VpRadpS_f32 = MIN
TS1.4EstKe_VpRadpS_f32 = Max
TS1.5EstKe_VpRadpS_f32 = Pos
TS1.6EstR_Ohm_f32 = Min
TS1.7EstR_Ohm_f32 = Max
TS1.8EstR_Ohm_f32 = Pos
TS1.9MRFMtrVel_MtrRadpS_f32 = Min
TS1.10MRFMtrVel_MtrRadpS_f32 = Min
TS1.11MRFMtrVel_MtrRadpS_f32 = Neg
TS1.12MRFMtrVel_MtrRadpS_f32 = Neg
TS1.12MRFMtrVel_MtrRadpS_f32 = Zero

TS1.11MRFMtrVel_MtrRadpS_f32 = Neg
TS1.12MRFMtrVel_MtrRadpS_f32 = Zero
TS1.13MRFMtrVel_MtrRadpS_f32 = Pos
TS1.14CorrMtrPosElec_Rev_f32 = Min
TS1.15CorrMtrPosElec_Rev_f32 = Max
TS1.16CorrMtrPosElec_Rev_f32 = Pos
TS1.17MtrCurrDaxRef_Amp_f32 = Min
TS1.18MtrCurrDaxRef_Amp_f32 = Max
TS1.19MtrCurrDaxRef_Amp_f32 = Pos
TS1.20MtrCurrDaxRef_Amp_f32 = Zero
TS1.21MtrCurrDaxRef_Amp_f32 = Zero
TS1.21MtrCurrDaxRef_Amp_f32 = Neg

TS1.20MtrCurrDaxRef_Amp_f32 = Zero
TS1.21MtrCurrDaxRef_Amp_f32 = Neg
TS1.22MtrVoltDax_Volt_M_f32[2] = Min
TS1.23MtrVoltDax_Volt_M_f32[2] = Neg
TS1.23MtrVoltDax_Volt_M_f32[2] = Neg
TS1.25MtrVoltDax_Volt_M_f32[2] = Neg
TS1.25MtrVoltDax_Volt_M_f32[2] = Pos
TS1.26MtrVoltDax_Volt_M_f32[2] = Min
TS1.28MtrVoltQax_Volt_M_f32[2] = Min
TS1.28MtrVoltQax_Volt_M_f32[2] = Max
TS1.29MtrVoltQax_Volt_M_f32[2] = Neg
TS1.30MtrVoltQax_Volt_M_f32[2] = Pos
TS1.31MtrVoltQax_Volt_M_f32[2] = Pos
TS1.32MtrCurrQaxFinalRef_Amp_M_f32[2] = Min
TS1.33MtrCurrQaxFinalRef_Amp_M_f32[2] = Max
TS1.35MtrCurrQaxFinalRef_Amp_M_f32[2] = Neg
TS1.35MtrCurrQaxFinalRef_Amp_M_f32[2] = Pos
TS1.35MtrCurrQaxFinalRef_Amp_M_f32[2] = Pos
TS1.35MtrCurrQaxFinalRef_Amp_M_f32[2] = Pos
TS1.35MtrCurrQaxFinalRef_Amp_M_f32[2] = Pos
TS1.37SlowDataAccessBufIndex_Cnt_M_u16 = Min

TS1.37SlowDataAccessBufIndex_Cnt_M_u16 = Min TS1.38SlowDataAccessBufIndex_Cnt_M_u16 = Max

	1 (3)		
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220		
MtrVoltDax_Volt_M_f32[0]	-31		
MtrVoltDax_Volt_M_f32[1]	-31		
MtrVoltQax_Volt_M_f32[0]	-31		
MtrVoltQax_Volt_M_f32[1]	-31		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0250000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.00499999989		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1118		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-220		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f3222222222222222222222222222222222222$	target_PICurrCntrl_Per2_DervLambdaAlphaI	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f3	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	•
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-30.0473728	-30.0473728 ± 0.00048828	•
target PICurrCntrl Per2 DervLambdaBetaDiag Volt f32.value	2.09737158	2.09737206 ± 0.00048828	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓





Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	220		
MtrVoltDax_Volt_M_f32[0]	31		
MtrVoltDax_Volt_M_f32[1]	31		
MtrVoltQax_Volt_M_f32[0]	31		
MtrVoltQax_Volt_M_f32[1]	31		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.999984741		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.075000003		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.125650004		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	1118		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	220		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f3	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PlCurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	7.06780577	7.06782198 ± 0.00048828	~
target_PlCurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	76.7888641	76.7888641 ± 0.00048828	<u> </u>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-154.242996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-57.1660004		
MtrVoltDax_Volt_M_f32[0]	-29.9080009		
MtrVoltDax_Volt_M_f32[1]	-13.9139996		
MtrVoltQax_Volt_M_f32[0]	1.25399995		
MtrVoltQax_Volt_M_f32[1]	-20.5		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.476000011		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0250000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0790000036		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	688.880005		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	213.205994		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	43.1762505	43.1762466 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-12.0380945	-12.0380983 ± 0.00048828	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓



Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-4.47100019		
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.723999		
MtrVoltDax_Volt_M_f32[0]	5.57999992		
MtrVoltDax_Volt_M_f32[1]	14.0050001		
MtrVoltQax_Volt_M_f32[0]	26.6100006		
MtrVoltQax_Volt_M_f32[1]	-26.9500008		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.915000021		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.075000003		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.123000003		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	409.079987		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	210.386002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32 target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	32.0637131	32.0637398 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	70.7293549	70.7293396 ± 0.00048828	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	~

Test Step 1.5 (Repeat Count = 1)			~
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	97.2080002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	209.817993		
MtrVoltDax_Volt_M_f32[0]	-17.2469997		
MtrVoltDax_Volt_M_f32[1]	-29.434		
MtrVoltQax_Volt_M_f32[0]	16.6630001		
MtrVoltQax_Volt_M_f32[1]	-7.27199984		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.694000006		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0260000005		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0209999997		
target_PlCurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	163.839996		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-49.0620003		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_I	Rev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_flowerspace and the property of the$	32 target_PICurrCntrl_Per2_DervLambdaAlph	aDiag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3 \\$	2 target_PICurrCntrl_Per2_DervLambdaBeta	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_	f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrR	adpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_A	.mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	24.2441082	24.2441139 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	21.5036201	21.5036144 ± 0.00048828	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~	
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓	



Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-192.279007		
MtrCurrQaxFinalRef_Amp_M_f32[1]	104.983002		
MtrVoltDax_Volt_M_f32[0]	24.5849991		
MtrVoltDax_Volt_M_f32[1]	-1.83800006		
MtrVoltQax_Volt_M_f32[0]	-13.7519999		
MtrVoltQax_Volt_M_f32[1]	-20.2409992		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.370999992		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0610000007		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.00499999989		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-314.470001		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-11.6210003		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-12.4378471	-12.4378471 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	22.1681595	22.1681633 ± 0.00048828	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.7 (Repeat Count = 1)			✓.
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-2.46000004		
MtrCurrQaxFinalRef_Amp_M_f32[1]	153.630005		
MtrVoltDax_Volt_M_f32[0]	5		
MtrVoltDax_Volt_M_f32[1]	1.52900004		
MtrVoltQax_Volt_M_f32[0]	-11.7320004		
MtrVoltQax_Volt_M_f32[1]	-9.96500015		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.779999971		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0689999983		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.125650004		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	279.26001		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-113.587997		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	lev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f3$	2 target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32000000000000000000000000000000000000$	target_PICurrCntrl_Per2_DervLambdaBetaL	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_1	732	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Ar	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	47.7400093	47.7400093 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-5.03275585	-5.03276062 ± 0.00048828	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~	
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	•	



Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-57.2830009		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-109.836998		
MtrVoltDax_Volt_M_f32[0]	-7.07700014		
MtrVoltDax_Volt_M_f32[1]	-12.2460003		
MtrVoltQax_Volt_M_f32[0]	13.4899998		
MtrVoltQax_Volt_M_f32[1]	-24.4689999		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.180000007		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0540000014		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.104999997		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	692.190002		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	26.1779995		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	ıp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-20.9283752	-20.9283733 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-0.60405916	-0.604057014 ± 0.00048828	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	111.466003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	3.68799996		
MtrVoltDax_Volt_M_f32[0]	-9.87199974		
MtrVoltDax_Volt_M_f32[1]	13.0209999		
MtrVoltQax_Volt_M_f32[0]	2.79699993		
MtrVoltQax_Volt_M_f32[1]	2.49399996		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.479000002		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0419999994		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.029999993		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1118		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	120.931		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	ev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f3 \\$	2 target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3222222222222222222222222222222222222$	target_PICurrCntrl_Per2_DervLambdaBetaE	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_t	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Ar	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-3.30000544	-3.29999614 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	50.2249184	50.2249184 ± 0.00048828	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~	
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	•	





Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-131.636002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	143.960999		
MtrVoltDax_Volt_M_f32[0]	19.8519993		
MtrVoltDax_Volt_M_f32[1]	-9.42500019		
MtrVoltQax_Volt_M_f32[0]	-21.5189991		
MtrVoltQax_Volt_M_f32[1]	30.2460003		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.933000028		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0450000018		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0219999999		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	1118		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	66.2429962		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	45.2947922	45.2948074 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	55.6719589	55.6719437 ± 0.00048828	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.11 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	128.199005		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-49.973999		
MtrVoltDax_Volt_M_f32[0]	-4.33300018		
MtrVoltDax_Volt_M_f32[1]	-12.9139996		
MtrVoltQax_Volt_M_f32[0]	27.3500004		
MtrVoltQax_Volt_M_f32[1]	10.0089998		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.967000008		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0469999984		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0199999996		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1010.56		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	188.177002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-27.8432713	-27.8432884 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-53.7911644	-53.7911606 ± 0.00048828	<u> </u>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	•



Test Step 1.12 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-13.3149996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-190.268997		
MtrVoltDax_Volt_M_f32[0]	13.0489998		
MtrVoltDax_Volt_M_f32[1]	8.26099968		
MtrVoltQax_Volt_M_f32[0]	-6.94999981		
MtrVoltQax_Volt_M_f32[1]	-11.8059998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.648999989		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0340000018		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.098999995		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	0		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	114.566002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-2.70378375	-2.70378375 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	3.92948556	3.92948604 ± 0.00048828	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.13 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-127.372002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-119.935997		
MtrVoltDax_Volt_M_f32[0]	30.4549999		
MtrVoltDax_Volt_M_f32[1]	-8.5340004		
MtrVoltQax_Volt_M_f32[0]	28.2479992		
MtrVoltQax_Volt_M_f32[1]	-15.4549999		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.907999992		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0529999994		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0450000018		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	12.5		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	135.257004		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_F	Rev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_force and the property of the prope$	target_PICurrCntrl_Per2_DervLambdaAlpha	aDiag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3222222222222222222222222222222222222$	target_PlCurrCntrl_Per2_DervLambdaBetal	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_	<u>f</u> 32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	adpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_A	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-5.30953932	-5.30953503 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	17.1272545	17.1272564 ± 0.00048828	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~	
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	•	





Test Step 1.14 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	189.746994		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-111.083		
MtrVoltDax_Volt_M_f32[0]	-8.92099953		
MtrVoltDax_Volt_M_f32[1]	-24.1280003		
MtrVoltQax_Volt_M_f32[0]	10.2139997		
MtrVoltQax_Volt_M_f32[1]	-16.2560005		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0590000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0799999982		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	226.929993		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-139.737		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	0.760263801	0.760263324 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	16.3209267	16.3209286 ± 0.00048828	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.15 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-150.468002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	153.128006		
MtrVoltDax_Volt_M_f32[0]	30.1329994		
MtrVoltDax_Volt_M_f32[1]	-14.7460003		
MtrVoltQax_Volt_M_f32[0]	19.8519993		
MtrVoltQax_Volt_M_f32[1]	-9.42500019		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.999984741		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.039999991		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0869999975		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-768.669983		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-77.0139999		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	lev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f3$	2 target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3222222222222222222222222222222222222$	target_PICurrCntrl_Per2_DervLambdaBetaE	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_t	732	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Ar	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-8.9443779	-8.94437885 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-9.78363514	-9.78363419 ± 0.00048828	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~	
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	•	



Test Step 1.16 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	179.229996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	20.4080009		
MtrVoltDax_Volt_M_f32[0]	-17.1889992		
MtrVoltDax_Volt_M_f32[1]	-21.1000004		
MtrVoltQax_Volt_M_f32[0]	-4.33300018		
MtrVoltQax_Volt_M_f32[1]	-12.9139996		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.856000006		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.074000001		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.074000001		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	750.690002		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	183.016006		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PlCurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	38.250412	38.2504349 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	66.8344421	66.8344345 ± 0.00048828	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.17 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	145.681		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-198.222		
MtrVoltDax_Volt_M_f32[0]	19.6550007		
MtrVoltDax_Volt_M_f32[1]	0.603999972		
MtrVoltQax_Volt_M_f32[0]	13.0489998		
MtrVoltQax_Volt_M_f32[1]	8.26099968		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.261000007		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0549999997		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.115000002		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	397.470001		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-220		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_F	Rev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f3000000000000000000000000000000000000$	2 target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3222222222222222222222222222222222222$	target_PICurrCntrl_Per2_DervLambdaBetaI	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_	f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_A	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-26.1354809	-26.135479 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	39.8556824	39.8556824 ± 0.00048828	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~	
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	•	





Test Step 1.18 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-138.488007		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-188.169006		
MtrVoltDax_Volt_M_f32[0]	-25.8899994		
MtrVoltDax_Volt_M_f32[1]	-9.69200039		
MtrVoltQax_Volt_M_f32[0]	30.4549999		
MtrVoltQax_Volt_M_f32[1]	-8.5340004		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.479999989		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0700000003		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.120999999		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-159.699997		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	220		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	55.5946236	55.5946236 ± 0.00048828	~
target_PlCurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	49.569046	49.5690422 ± 0.00048828	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.19 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-22.4979992		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-38.9280014		
MtrVoltDax_Volt_M_f32[0]	-21.9990005		
MtrVoltDax_Volt_M_f32[1]	-11.2440004		
MtrVoltQax_Volt_M_f32[0]	-8.92099953		
MtrVoltQax_Volt_M_f32[1]	-24.1280003		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.460999995		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.032999998		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0850000009		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	715.830017		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	126.559998		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	Rev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_formula (Section 1) and the property of the propert$	target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3222222222222222222222222222222222222$	target_PICurrCntrl_Per2_DervLambdaBetaL	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_1	f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	ndpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Ar	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	9.05713081	9.05712509 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-48.5318794	-48.5318794 ± 0.00048828	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	✓





Test Step 1.20 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-22.8479996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-171.873993		
MtrVoltDax_Volt_M_f32[0]	18.2189999		
MtrVoltDax_Volt_M_f32[1]	10.0150003		
MtrVoltQax_Volt_M_f32[0]	30.1329994		
MtrVoltQax_Volt_M_f32[1]	-14.7460003		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.975000024		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0599999987		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0179999992		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	611.549988		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	0		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PlCurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	18.9651871	18.9651871 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	3.27737713	3.27737594 ± 0.00048828	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.21 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.838997		
MtrCurrQaxFinalRef_Amp_M_f32[1]	99.3130035		
MtrVoltDax_Volt_M_f32[0]	8.61600018		
MtrVoltDax_Volt_M_f32[1]	18.5569992		
MtrVoltQax_Volt_M_f32[0]	-17.1889992		
MtrVoltQax_Volt_M_f32[1]	-21.1000004		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.0270000007		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0289999992		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.123999998		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-694.679993		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-210.649994		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	38.6252594	38.6252632 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	18.4046631	18.4046631 ± 0.00048828	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte Call PlCurrCntrl Per2 CP1 CheckpointReached	1	Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	✓





Test Step 1.22 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	10.8450003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	78.3980026		
MtrVoltDax_Volt_M_f32[0]	-31		
MtrVoltDax_Volt_M_f32[1]	-31		
MtrVoltQax_Volt_M_f32[0]	19.6550007		
MtrVoltQax_Volt_M_f32[1]	0.603999972		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.927999973		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0309999995		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.125		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	276.529999		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	101.086998		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-42.0549355	-42.0549355 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	9.4221344	9.42212677 ± 0.00048828	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	~

Test Step 1.23 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-159.959		
MtrCurrQaxFinalRef_Amp_M_f32[1]	10.2200003		
MtrVoltDax_Volt_M_f32[0]	31		
MtrVoltDax_Volt_M_f32[1]	31		
MtrVoltQax_Volt_M_f32[0]	-25.8899994		
MtrVoltQax_Volt_M_f32[1]	-9.69200039		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.215000004		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0680000037		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0350000001		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	76.1100006		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	44.9939995		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_F	Rev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_force and the property of the prope$	2 target_PICurrCntrl_Per2_DervLambdaAlpha	nDiag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3222222222222222222222222222222222222$	target_PICurrCntrl_Per2_DervLambdaBetaI	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_	f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	adpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_A	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-8.34680748	-8.34680462 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	32.2332726	32.2332726 ± 0.00048828	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	✓





Test Step 1.24 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	200.919998		
MtrCurrQaxFinalRef_Amp_M_f32[1]	174.417007		
MtrVoltDax_Volt_M_f32[0]	-28.3600006		
MtrVoltDax_Volt_M_f32[1]	-25.6900005		
MtrVoltQax_Volt_M_f32[0]	-4.33300018		
MtrVoltQax_Volt_M_f32[1]	-12.9139996		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.379999995		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0250000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0450000018		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	365.820007		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	61.6679993		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f3	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRac	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Am	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	7.8387332	7.83873224 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-36.5921974	-36.5921974 ± 0.00048828	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.25 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-122.484001		
MtrCurrQaxFinalRef_Amp_M_f32[1]	211.005005		
MtrVoltDax_Volt_M_f32[0]	0		
MtrVoltDax_Volt_M_f32[1]	0		
MtrVoltQax_Volt_M_f32[0]	13.0489998		
MtrVoltQax_Volt_M_f32[1]	8.26099968		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.658999979		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0489999987		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0469999984		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	113.730003		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	98.3949966		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_F	Rev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f3000000000000000000000000000000000000$	2 target_PICurrCntrl_Per2_DervLambdaAlpha	nDiag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3222222222222222222222222222222222222$	target_PICurrCntrl_Per2_DervLambdaBetaI	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_	f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	adpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_A	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-8.31243134	-8.31243038 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	10.1112747	10.1112766 ± 0.00048828	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte Call PlCurrCntrl Per2 CP1 CheckpointReached	1	Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	✓



Test Step 1.26 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	88.7070007		
MtrCurrQaxFinalRef_Amp_M_f32[1]	108.487999		
MtrVoltDax_Volt_M_f32[0]	25.9799995		
MtrVoltDax_Volt_M_f32[1]	10.2399998		
MtrVoltQax_Volt_M_f32[0]	30.4549999		
MtrVoltQax_Volt_M_f32[1]	-8.5340004		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.375		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0689999983		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0869999975		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	734.219971		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	171.177002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	•
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-45.7582397	-45.7582436 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-49.5160789	-49.5160866 ± 0.00048828	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.27 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	200.264008		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.67499995		
MtrVoltDax_Volt_M_f32[0]	-9.53600025		
MtrVoltDax_Volt_M_f32[1]	20.9009991		
MtrVoltQax_Volt_M_f32[0]	-31		
MtrVoltQax_Volt_M_f32[1]	-31		
Rte_Inst_Ap_PlCurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.222000003		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0599999987		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0799999982		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1103.18994		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	6.81699991		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	38.4061546	38.4061623 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	13.9216318	13.9216299 ± 0.00048828	<u> </u>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	Rte Call PlCurrCntrl Per2 CP1 CheckpointReached	1	✓



Test Step 1.28 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-174.345993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	123.915001		
MtrVoltDax_Volt_M_f32[0]	20.3360004		
MtrVoltDax_Volt_M_f32[1]	-5.22200012		
MtrVoltQax_Volt_M_f32[0]	31		
MtrVoltQax_Volt_M_f32[1]	31		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.00700000022		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0280000009		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0979999974		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	283.220001		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	13.2919998		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-5.7920208	-5.7920208 ± 0.00048828	•
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-12.8201752	-12.8201752 ± 0.00048828	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.29 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	190.841003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-181.727997		
MtrVoltDax_Volt_M_f32[0]	13.4899998		
MtrVoltDax_Volt_M_f32[1]	-24.4689999		
MtrVoltQax_Volt_M_f32[0]	-21.3600006		
MtrVoltQax_Volt_M_f32[1]	-29.6900005		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.995000005		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0320000015		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0790000036		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	907.409973		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	200.867004		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_	Rev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_flowerspace and the property of the$	32 target_PICurrCntrl_Per2_DervLambdaAlph	aDiag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3 and target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3 and target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3 and target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3 and target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3 and target_Rte_Inst_Ap_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3 and target_Rte_Inst_Ap_PICurrCntrl_Per3_DervLambdaBetaDiag_Volt_f3 and target_Rte_Inst_Ap_PICurrCntrl_Per3_DervLambdaBetaDiag_Volt_f3 and target_PicurrCntrl_Per3_DervLambdaBetaDiag_Volt_f3 and target_PicurrCntrl$	2 target_PICurrCntrl_Per2_DervLambdaBeta	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS	_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrR	adpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_A	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	1.74072468	1.74073541 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	63.4303246	63.4303207 ± 0.00048828	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	✓



Test Step 1.30 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	135.572006		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-60.6110001		
MtrVoltDax_Volt_M_f32[0]	2.79699993		
MtrVoltDax_Volt_M_f32[1]	2.49399996		
MtrVoltQax_Volt_M_f32[0]	0		
MtrVoltQax_Volt_M_f32[1]	0		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.537		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.057		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.125		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	576.669983		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-16.8379993		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PlCurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	1.86064625	1.86064291 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-26.595644	-26.5956459 ± 0.00048828	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.31 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-30.0400009		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.0149994		
MtrVoltDax_Volt_M_f32[0]	-21.5189991		
MtrVoltDax_Volt_M_f32[1]	30.2460003		
MtrVoltQax_Volt_M_f32[0]	9.97999954		
MtrVoltQax_Volt_M_f32[1]	23.2399998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.0410000011		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0280000009		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0130000003		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-41.7599983		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	188.953995		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-19.9393387	-19.9393406 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-17.1327972	-17.1327972 ± 0.00048828	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	•





Test Step 1.32 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220		
MtrVoltDax_Volt_M_f32[0]	27.3500004		
MtrVoltDax_Volt_M_f32[1]	10.0089998		
MtrVoltQax_Volt_M_f32[0]	5.57999992		
MtrVoltQax_Volt_M_f32[1]	14.0050001		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.195999995		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0680000037		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0120000001		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	887.52002		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	13.9949999		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-38.2654343	-38.2654266 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	23.965292	23.9652901 ± 0.00048828	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	~

Test Step 1.33 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	220		
MtrVoltDax_Volt_M_f32[0]	-6.94999981		
MtrVoltDax_Volt_M_f32[1]	-11.8059998		
MtrVoltQax_Volt_M_f32[0]	-17.2469997		
MtrVoltQax_Volt_M_f32[1]	-29.434		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.236000001		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0599999987		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0280000009		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	945.109985		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	65.5070038		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	lev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f3$	2 target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32000000000000000000000000000000000000$	target_PICurrCntrl_Per2_DervLambdaBetaE	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_t	732	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Ar	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-79.7318726	-79.7318726 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-1.53988278	-1.53987384 ± 0.00048828	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~	
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓	



Test Step 1.34 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-115.349998		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-56.8400002		
MtrVoltDax_Volt_M_f32[0]	28.2479992		
MtrVoltDax_Volt_M_f32[1]	-15.4549999		
MtrVoltQax_Volt_M_f32[0]	24.5849991		
MtrVoltQax_Volt_M_f32[1]	-1.83800006		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.407000005		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0480000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0579999983		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1058.32996		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-170.300003		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	34.340477	34.340477 ± 0.00048828	•
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	39.4124794	39.4124794 ± 0.00048828	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.35 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	0		
MtrCurrQaxFinalRef_Amp_M_f32[1]	0		
MtrVoltDax_Volt_M_f32[0]	10.2139997		
MtrVoltDax_Volt_M_f32[1]	-16.2560005		
MtrVoltQax_Volt_M_f32[0]	27.3500004		
MtrVoltQax_Volt_M_f32[1]	10.0089998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.779999971		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0610000007		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0790000036		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	781.340027		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	37.9490013		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_R	Rev_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f3$	2 target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32000000000000000000000000000000000000$	target_PICurrCntrl_Per2_DervLambdaBetaL	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_1	f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	ndpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Ar	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	21.3793755	21.3793736 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-3.67670774	-3.67671037 ± 0.00048828	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	Rte Call PlCurrCntrl Per2 CP1 CheckpointReached	1	✓





Test Step 1.36 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	95.4800034		
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.419998		
MtrVoltDax_Volt_M_f32[0]	23.3439999		
MtrVoltDax_Volt_M_f32[1]	11.6280003		
MtrVoltQax_Volt_M_f32[0]	-6.94999981		
MtrVoltQax_Volt_M_f32[1]	-11.8059998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.462000012		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.050999999		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.120999999		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	202.050003		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-136.925003		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PICurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PlCurrCntrl_Per2_EstKe_VpRadpS_f	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_An	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-33.42799	-33.4279938 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-27.4988766	-27.4988689 ± 0.00048828	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.37 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-22.8479996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-171.873993		
MtrVoltDax_Volt_M_f32[0]	20.3360004		
MtrVoltDax_Volt_M_f32[1]	-5.22200012		
MtrVoltQax_Volt_M_f32[0]	5.57999992		
MtrVoltQax_Volt_M_f32[1]	14.0050001		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.927999973		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0280000009		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0120000001		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	734.219971	734.219971	
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	98.3949966		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_from the property of the property o$	target_PICurrCntrl_Per2_DervLambdaAlpha	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f3222222222222222222222222222222222222$	target_PlCurrCntrl_Per2_DervLambdaBetal	Diag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_	f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRa	adpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_A	mp_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	~
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	23.8140182	23.8140163 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	4.81566048	4.81566477 ± 0.00048828	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	Rte Call PICurrCntrl Per2 CP1 CheckpointReached	1	✓

2016-04-22, 16:28:39+0530





Test Step 1.38 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.838997		
MtrCurrQaxFinalRef_Amp_M_f32[1]	99.3130035		
MtrVoltDax_Volt_M_f32[0]	13.4899998		
MtrVoltDax_Volt_M_f32[1]	-24.4689999		
MtrVoltQax_Volt_M_f32[0]	-17.2469997		
MtrVoltQax_Volt_M_f32[1]	-29.434		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.215000004		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0320000015		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0280000009		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1103.18994		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	171.177002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Re	ev_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PlCurrCntrl_Per2_DervLambdaAlphal	Diag_Volt_f32	
$target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32$	target_PlCurrCntrl_Per2_DervLambdaBetaD	iag_Volt_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f3	32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PlCurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRad	dpS_f32	
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Am	np_f32	
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	•
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-2.86667562	-2.86667776 ± 0.00048828	~
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-28.6853104	-28.6853104 ± 0.00048828	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	~
Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PlCurrCntrl_Per2_CP1_CheckpointReached	1	✓

2016-04-22, 16:28:00+0530



PICurrCntrl_Init

 Project
 Ap_PICurrCntrl

 Module
 PICurrCntrl

 Test Object
 PICurrCntrl_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Project Root Directory	D:\Synergy_Work_Area\Plcurrentcontrol
Configuration File	D:\Synergy_Work_Area\Plcurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PiCurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Spe	ecification
Name	Text
Module 'PICurrCntrl'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:15 Module Design Document:PlCurrentContrl.doc Module Design Document Version:11 Data Dictionary Version:14 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):NA Total RAM Used (Bytes):NA Total RAM Used (Bytes):NA Special Test Requirements: Test Date:4/22/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnSclFac "". for variables "k_CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32, PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 and PlCurrCntrl_InverterFailSclFac_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9



Description

Vector Description:

TS1.1All Min

TS1.1All Min
TS1.2All Max
TS1.3k_MtrVoltVecuFiltKn_Hz_f32==>Min
TS1.3k_MtrVoltVecuFiltKn_Hz_f32==>Max
TS1.5k_MtrVoltVecuFiltKn_Hz_f32==>Pos
TS1.6k_MtrVoltVecuFiltKn_Hz_f32==>Def
TS1.7k_PiSamplingTs_Sec_f32==>Max
TS1.8k_PiSamplingTs_Sec_f32==>Max
TS1.9k_PiSamplingTs_Sec_f32==>Pos/Def
TS1.10k_MtrVoltQaxFiltFFKn_Hz_f32==>Max
TS1.11k_MtrVoltQaxFiltFFKn_Hz_f32==>Max
TS1.12k_MtrVoltQaxFiltFFKn_Hz_f32==>Def

Test Step 1.1 (Repeat Count = 1)			· ·
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	0.10000001		
k_MtrVoltVecuFiltKn_Hz_f32	0.10000001		
k_PiSamplingTs_Sec_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50928.5781	50928.582	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.963457e-005	1.96345682e-005	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50928.5781	50928.582	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.963457e-005	1.96345682e-005	~

7	Test Step Call Trace				✓
Α	ctual Function	Count	Expected Function	Count	Result
С	alLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.2 (Repeat Count = 1) ✓			
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	1000		
k_MtrVoltVecuFiltKn_Hz_f32	1000		
k_PiSamplingTs_Sec_f32	0.10000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.996816874	-0.996816874	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996827006	0.996827006	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996816874	-0.996816874	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996827006	0.996827006	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.3 (Repeat Count = 1)			
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	709.776001		
k_MtrVoltVecuFiltKn_Hz_f32	0.10000001		
k_PiSamplingTs_Sec_f32	0.10000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	30.830986	30.8309879	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0304590296	0.0304590277	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.995515347	-0.995515347	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.995535374	0.995535374	✓





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.4 (Repeat Count = 1)				
Name	Input Value			
k_MtrVoltQaxFiltFFKn_Hz_f32	45.4935989			
k_MtrVoltVecuFiltKn_Hz_f32	1000			
k_PiSamplingTs_Sec_f32	0.10000001			
Name	Actual Value	Expected Value	Result	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	-0.996816874	-0.996816874	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996827006	0.996827006	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.930031955	-0.930031955	~	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.934607327	0.934607327	✓	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	414.50531		
k_MtrVoltVecuFiltKn_Hz_f32	59.2360001		
k_PiSamplingTs_Sec_f32	0.100000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.946264148	-0.946264088	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.949004412	0.949004412	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.992320716	-0.992320716	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.992379248	0.992379248	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	863.495728		
k_MtrVoltVecuFiltKn_Hz_f32	200		
k_PiSamplingTs_Sec_f32	0.10000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.984084487	-0.984084487	•
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.984333813	0.984333813	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996313691	-0.996313691	•
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.996327221	0.996327221	✓

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.7 (Repeat Count = 1)		
Name	Input Value	
k_MtrVoltQaxFiltFFKn_Hz_f32	791.291382	
k_MtrVoltVecuFiltKn_Hz_f32	289.923096	

2016-04-22, 16:28:00+0530



PICurrCntrl_Init

Name	Input Value		
k_PiSamplingTs_Sec_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5665817	16.5665836	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0538602099	0.0538602062	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	5.4362607	5.43626118	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.134476185	0.134476185	✓

Test Step Call Trace					✓
	Actual Function	Count	Expected Function	Count	Result
	CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	801.490417		
k_MtrVoltVecuFiltKn_Hz_f32	302.319794		
k_PiSamplingTs_Sec_f32	0.10000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.989471078	-0.989471078	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.98958081	0.98958081	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996028543	-0.996028543	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996044219	0.996044219	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	325.598114		
k_MtrVoltVecuFiltKn_Hz_f32	775.537415		
k_PiSamplingTs_Sec_f32	0.000125000006		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.28350234	2.28350258	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.233453825	0.23345381	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.82092667	6.82092714	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.113366775	0.113366768	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.10 (Repeat Count = 1)			
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	0.10000001		
k_MtrVoltVecuFiltKn_Hz_f32	14.1302996		
k_PiSamplingTs_Sec_f32	0.0988999978		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.77222687	-0.77222687	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.814482749	0.814482749	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	31.1850243	31.1850243	~
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.0301340744	0.0301340744	





Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.11 (Repeat Count = 1)				
Name	Input Value			
k_MtrVoltQaxFiltFFKn_Hz_f32	1000			
k_MtrVoltVecuFiltKn_Hz_f32	761.508179			
k_PiSamplingTs_Sec_f32	0.034099998			
Name	Actual Value	Expected Value	Result	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.987741947	-0.987741947	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.987890422	0.987890422	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.990665376	-0.990665376	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.990751743	0.990751743	✓	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.12 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	213.235001		
k_MtrVoltVecuFiltKn_Hz_f32	815.32312		
k_PiSamplingTs_Sec_f32	0.034099998		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.988551021	-0.988551021	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.988680661	0.988680661	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.956223905	-0.956223905	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.958059847	0.958059847	✓

Test Step Call Trace					V
-	actual Function	Count	Expected Function	Count	Result
C	CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.13 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	500		
k_MtrVoltVecuFiltKn_Hz_f32	815.32312		
k_PiSamplingTs_Sec_f32	0.0302000009		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	-0.987072527	-0.987072527	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.987237513	0.987237513	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.978919864	-0.978919864	•
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.979355097	0.979355097	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	•

2016-04-22, 16:26:59+0530



CalLowPassFiltBilinearOut

Project Ap_PICurrCntrl
Module PICurrCntrl

Test Object CalLowPassFiltBilinearOut

Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

Statistics

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Project Root Directory	D:\Synergy_Work_Area\Plcurrentcontrol
Configuration File	D:\Synergy_Work_Area\Plcurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PlCurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas

Comments/Description/Sp	ecification
Name	Text
Module 'PICurrCntrl'	Unit Test Information* Name of Tester:Priyanka Shitole Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:15 Module Design Document:PlCurrentContrl.doc Module Design Document Version:11 Data Dictionary Version:14 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):NA Total RAM Used (Bytes):NA Total RAM Used (Bytes):NA Special Test Requirements: Test Date:4/22/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnSclFac "". for variables ""k_CLOAFdbackSignalSclFacSlew_UlspS_f32_k ILOAFdbackSignalSclFac_Uls_M_f32" to achieve 100% path coverage. NOTE 3: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9



Description

Vector Description:

TS1.1All Min

TS1.1All Min
TS1.2All Max
TS1.3Input_Uls_T_f32=Min
TS1.4Input_Uls_T_f32=Max
TS1.5Input_Uls_T_f32=Max
TS1.5Input_Uls_T_f32=Pos
TS1.6Input_Uls_T_f32=Pos
TS1.7Input_Uls_T_f32=Neg
TS1.8LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Max
TS1.9LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Max
TS1.10LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Pos
TS1.12LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Neg
TS1.13LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Neg
TS1.13LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Max
TS1.15LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Max
TS1.15LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Pos
TS1.17LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Pos
TS1.17LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Neg
TS1.19LowPassFiltBilinear_T_Str.TermN_Uls_f32=Max
TS1.20LowPassFiltBilinear_T_Str.TermN_Uls_f32=Max
TS1.20LowPassFiltBilinear_T_Str.TermN_Uls_f32=Pos
TS1.21LowPassFiltBilinear_T_Str.TermN_Uls_f32=Neg
TS1.23LowPassFiltBilinear_T_Str.TermD_Uls_f32=Neg
TS1.23LowPassFiltBilinear_T_Str.TermD_Uls_f32=Neg
TS1.23LowPassFiltBilinear_T_Str.TermD_Uls_f32=Max
TS1.25LowPassFiltBilinear_T_Str.TermD_Uls_f32=Max
TS1.25LowPassFiltBilinear_T_Str.TermD_Uls_f32=Max
TS1.25LowPassFiltBilinear_T_Str.TermD_Uls_f32=Neg
TS1.25LowPassFiltBilinear_T_Str.TermD_Uls_f32=Pos
TS1.25LowPassFiltBilinear_T_Str.TermD_Uls_f32=Pos
TS1.26LowPassFiltBilinear_T_Str.TermD_Uls_f32=Pos
TS1.27LowPassFiltBilinear_T_Str.TermD_Uls_f32=Pos
TS1.27LowPassFiltBilinear_T_Str.TermD_Uls_f32=Pos

Test Step 1.1 (Repeat Count = 1)			~
Name	Input Value		
Input_Uls_T_f32	-2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-9.90352031e+027	-9.90352031e+027	•
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009	-2.14748365e+009	•
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-9.90352031e+027	-9.90352031e+027	→

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
Input_Uls_T_f32	2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Si	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	9.90352031e+027	9.90352031e+027	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009	2.14748365e+009	✓
target LowPassFiltBilinear T Str.PrevOutput Uls f32	9.90352031e+027	9.90352031e+027	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.3 (Repeat Count = 1)		✓
Name	Input Value	
Input_Uls_T_f32	-2.14748365e+009	
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str	
target LowPassFiltBilinear T Str.PrevInput Uls f32	153.836899	

2016-04-22, 16:26:59+0530



CalLowPassFiltBilinearOut

Name	Input Value		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	8286.36523		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1000.22357		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	5.5236001		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-1.18160589e+010	-1.18160589e+010	~
target_LowPassFiltBilinear_T_Str.PrevInput_UIs_f32	-2.14748365e+009	-2.14748365e+009	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1.18160589e+010	-1.18160589e+010	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	42.2523003		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	123.3657		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1050.87891		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	99.9235992		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.14597255e+011	2.14597255e+011	✓
target_LowPassFiltBilinear_T_Str.PrevInput_UIs_f32	2.14748365e+009	2.14748365e+009	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14597255e+011	2.14597255e+011	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.5 (Repeat Count = 1)	1 (1)			
Name	Input Value			
Input_Uls_T_f32	0			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-895.362122			
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-89.7412033			
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	85.9630966			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-5.36210012	-5.36210012		
Name	Actual Value	Expected Value	Result	
CalLowPassFiltBilinearOut()	46166.5781	46166.5742	✓	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	0	0	✓	
target LowPassFiltBilinear T Str.PrevOutput Uls f32	46166.5781	46166.5742	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0		

Name	Input Value		
Input UIs T f32	2546.15991		
LowPassFiltBilinear T Str	target LowPassFiltBilinear T S	Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	258.325989		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	423.574005		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1101.47412		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1283.396		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	602375104	602375104	•
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2546.15991	2546.15991	•
target LowPassFiltBilinear T Str.PrevOutput Uls f32	602375104	602375104	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	•	
none	0	*** No Call Expected ***	0	L	





Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-2546.15991		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	58.3260002		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	423.574005		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1101.47412		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1283.396		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	595582976	595582976	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2546.15991	-2546.15991	~
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	595582976	595582976	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	252.320999		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_St	г	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	724.321228		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1152.31238		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	6845.52148		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-1.46949297e+013	-1.46949297e+013	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	252.320999	252.320999	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1.46949297e+013	-1.46949297e+013	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	356.623108		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1024.547		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	5000.3208		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	12407.3613		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.67081744e+013	2.67081744e+013	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	356.623108	356.623108	~
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.67081744e+013	2.67081744e+013	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

CalLowPassFiltBilinearOut





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.11 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	11423.2314		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	3582.41992		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1325.12305		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1253.22302		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	17969.7402		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	3.01115412e+010	3.01115412e+010	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	11423.2314	11423.2314	~
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	3.01115412e+010	3.01115412e+010	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.12 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	11423.2314		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-3582.41992		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1325.12305		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1253.22302		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	17969.7402		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.99827896e+010	2.99827896e+010	•
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	11423.2314	11423.2314	✓
target LowPassFiltBilinear T Str.PrevOutput Uls f32	2.99827896e+010	2.99827896e+010	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.13 (Repeat Count = 1)			✓.
Name	Input Value		
Input_Uls_T_f32	286.321014		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	12.5235996		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1303.82361		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	23531.3203		
Name	Actual Value Expec	ted Value	Result
CalLowPassFiltBilinearOut()	-6.58862801e+016 -6.5886	62844e+016	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	286.321014 286.32	1014	~

CalLowPassFiltBilinearOut

2016-04-22, 16:26:59+0530



Name	Actual Value	Expected Value	Result
target LowPassFiltBilinear T Str.PrevOutput Uls f32	-6.58862801e+016	-6.58862844e+016	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.14 (Repeat Count = 1)				
Name	Input Value			
Input_Uls_T_f32	1123.36523			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	102.823601			
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14748365e+009			
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1354.42297			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	29093.4473			
Name	Actual Value	Expected Value	Result	
CalLowPassFiltBilinearOut()	8.46212324e+016	8.4621241e+016	✓	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	1123.36523	1123.36523	✓	
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	8.46212324e+016	8.4621241e+016	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.15 (Repeat Count = 1)	Test Step 1.15 (Repeat Count = 1)				
Name	Input Value				
Input_Uls_T_f32	563.231018				
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T	_Str			
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-5657.51416				
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	0				
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-73.3619995				
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-89.6539993				
Name	Actual Value	Expected Value	Result		
CalLowPassFiltBilinearOut()	456722.844	456722.875	~		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	563.231018	563.231018	~		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	456722.844	456722.875	~		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.16 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	968.314514		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-93.1122971		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	525.830017		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1405.12415		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	34655.3242		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.56356413e+010	2.56356413e+010	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	968.314514	968.314514	~
target LowPassFiltBilinear T Str.PrevOutput Uls f32	2.56356413e+010	2.56356413e+010	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	





Test Step 1.17 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	968.314514		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	193.112305		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1525.82996		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1405.12415		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	34655.3242		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-7.42600868e+010	-7.4260095e+010	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	968.314514	968.314514	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-7.42600868e+010	-7.4260095e+010	~

Test Step Call Trace					✓
Actual Function Count Expected Function				Count	Result
	none	0	*** No Call Expected ***	0	~

Test Step 1.18 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	12.3512001		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	283.423615		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.4230001		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	40217.2891		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-2.09264749e+014	-2.09264732e+014	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	12.3512001	12.3512001	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-2.09264749e+014	-2.09264732e+014	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.19 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-88.9124527		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	373.723114		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	56.7743988		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	45779.3203		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	5.58151079e+015	5.58151079e+015	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-88.9124527	-88.9124527	✓
target LowPassFiltBilinear T Str.PrevOutput Uls f32	5.58151079e+015	5.58151079e+015	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.20 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-12.3620005		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	56.1230011		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	0		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-741.236511		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	67691.2031	67691.2031	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-12.3620005	-12.3620005	•

CalLowPassFiltBilinearOut

2016-04-22, 16:26:59+0530



Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	67691.2031	67691.2031	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.21 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	753.745605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_St	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	464.321014		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	111.321404		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	990.090027		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	51341.1641		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	5.72126822e+009	5.72126771e+009	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	753.745605	753.745605	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	5.72126822e+009	5.72126771e+009	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.22 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	753.745605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T	_Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	464.321014		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	111.321404		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-89.6500015		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	51341.1641		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-449846016	-449845984	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	753.745605	753.745605	~
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-449846016	-449845984	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.23 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	186.523605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	554.330017		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	165.357407		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	86.6320038		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-3.23541963e+013	-3.23541942e+013	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	186.523605	186.523605	~
target LowPassFiltBilinear T Str.PrevOutput Uls f32	-3.23541963e+013	-3.23541942e+013	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~





Test Step 1.24 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	32.5564003		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	644.63208		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	219.654694		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	3.32200003		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	3.02125469e+012	3.02125495e+012	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	32.5564003	32.5564003	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	3.02125469e+012	3.02125495e+012	✓

Test Step Call Trace				✓	
	Actual Function	Count	Expected Function	Count	Result
	none	0	*** No Call Expected ***	0	~

Test Step 1.25 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-89.6320038		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T	_Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-89.471199		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32 -78.9599991			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	0	0	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-89.6320038	-89.6320038	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	0	0	✓

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.26 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	236.350006		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	99999.3203		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1000.32098		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	78.4514008		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	10250.0703		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	1.83181312e+009	1.83181312e+009	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	236.350006	236.350006	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1.83181312e+009	1.83181312e+009	✓

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.27 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-78.9599991		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	125.629997		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	86.7409973		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	78.4514008		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-20.0699997		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-137512.063	-137512.078	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991	-78.9599991	✓

CalLowPassFiltBilinearOut

2016-04-22, 16:26:59+0530



Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-137512.063	-137512.078	~

Test Step Call Trace					V
Actual Function	Count	Expected Function	Count	Resu	ılt
none	0	*** No Call Expected ***	0		~

2016-04-22, 16:27:33+0530

CalLowPassFiltVecuOut



 Project
 Ap_PICurrCntrl

 Module
 PICurrCntrl

 Test Object
 CalLowPassFiltVecuOut

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Plcurrentcontrol
Configuration File D:\Synergy_Work_Area\Plcurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Col	
Target Environment GNU GCC GNU GVD (Default)	
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract)\utp\contract -I\$(SOURCEROOT)\utp\contract)\utp\contract -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Descripti	Comments/Description/Specification				
Name	Text				
Module 'PICurrCntrl'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:15 Module Design Document:PlCurrentContrl.doc Module Design Document Version:11 Data Dictionary Version:14 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):NA Total RAM Used (Bytes):NA Total RAM Used (Bytes):NA Special Test Requirements: Test Date:4/22/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnSclFac"". for variables ""k_CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32, PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 and PlCurrCntrl_InverterFailSclFac_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range."				

Attributes	
Name	Value
CTE File	<pre>\$(PROJECTROOT)\tessy\persist\tessy\project\0000055e\00002645\.database\.tdb \00002659\CalLowPassFiltVecuOut.cte</pre>
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5

CalLowPassFiltVecuOut

2016-04-22, 16:27:33+0530



Float Precision

۹



Test Case 1: Metric Test

Description Vector Description:

 $TS 1.1Shortest Path==>(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = True\&\&(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path==>(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32) = False\&\&\&(*Vecu_Volt_T_f32<=D_VECUMIN_VOLTS_F32) = True \\ True = TRUE) = True + Tru$

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009	2.14748365e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	~
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	360171232		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	5.23559999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	5.23559999	5.23559999	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-6.48613864e+026	-6.48614011e+026	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472	-954881024	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009	1.88593997e+009	✓
target_Vecu_Volt_T_f32	5	5	✓

Te	st Step Call Trace				✓
Act	tual Function	Count	Expected Function	Count	Result
Cal	LowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~



Test Case 2: Boundary Test

Description

Vector Description:

TS 2.1All Min

TS 2.2All Max
TS 2.3k_MtrVoltVecuFiltEnable_Cnt_lgc => min
TS 2.4k_MtrVoltVecuFiltEnable_Cnt_lgc => max

TS 2.4k_MtrVoltVecuFiltEnable_Cnt_igc => max
TS 2.5Vecu_Volt1_T_f32 => min
TS 2.6Vecu_Volt1_T_f32 => max
TS 2.7Vecu_Volt1_T_f32 => min
TS 2.8PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => min
TS 2.8PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => max
TS 2.10PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => max
TS 2.10PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => pos
TS 2.11PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => neg
TS 2.13PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 => min
TS 2.14PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 => max
TS 2.15PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 => max
TS 2.16PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 => pos
TS 2.17PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 => pos
TS 2.17PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 => pos
TS 2.18PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 => neg
TS 2.18PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 => neg TS 2.17PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 =>r TS 2.18PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 =>min TS 2.19PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 =>max TS 2.20PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 =>zero TS 2.21PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 =>pos TS 2.22PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 =>nen TS 2.23PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 =>min TS 2.24PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 =>max TS 2.25PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 =>zero TS 2.26PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 =>pos TS 2.27PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 =>neg

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-2.14748006e+009	-2.14748006e+009	•
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	-2.14748006e+009	•
target Vecu Volt T f32	5	5	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009	2.14748365e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	✓
target_Vecu_Volt_T_f32	31	31	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~



CalLowPassFiltVecuOut

Test Step 2.3 (Repeat Count = 1)			V
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	6.12529993		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009	1.25422003e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009	-1.78840998e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009	2.03557005e+009	•
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	-453076992	-453076992	~
target_Vecu_Volt_T_f32	6.12529993	6.12529993	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.59821005e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.50935002e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-334438016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.17052006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	7.23559999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	7.23559999	7.23559999	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	5.90859838e+026	5.90859986e+026	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-334438016	-334438016	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.17052006e+009	1.17052006e+009	✓
target Vecu Volt T f32	31	31	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1.30338995e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-526129984		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.46070003e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.44466995e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1.30338995e+009	1.30338995e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-526129984	-526129984	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.46070003e+009	1.46070003e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.44466995e+009	1.44466995e+009	✓
target_Vecu_Volt_T_f32	5	5	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.6 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-1.93612864e+009

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Name	Input Value		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	1.5751543e+009		
PICurrCntrl MtrVecuFilt M str.TermN UIs f32	255735808		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	1.20936077e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	31	31	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	4.87158773e+026	4.87158994e+026 ± 9E+20	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	255735808	255736000	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.20936077e+009	1.20936e+009	~
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.44801997e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.04112998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-608366976		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.91424998e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	23.0214005		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.44801997e+009	1.44801997e+009	•
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.04112998e+009	-2.04112998e+009	•
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-608366976	-608366976	•
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.91424998e+009	-1.91424998e+009	•
target_Vecu_Volt_T_f32	23.0214005	23.0214005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	360171232		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	5.23559999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	5.23559999	5.23559999	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-6.48613864e+026	-6.48614011e+026	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472	-954881024	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009	1.88593997e+009	✓
target_Vecu_Volt_T_f32	5	5	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.9 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748006e+009
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.26342003e+009
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	552748032
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-256136992

CalLowPassFiltVecuOut

2016-04-22, 16:27:33+0530



Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	12.3255997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748006e+009	2.14748006e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.26342003e+009	-1.26342003e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	552748032	552748032	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-256136992	-256136992	✓
target Vecu Volt T f32	12 3255997	12 3255997	_

Test .	Step Call Trace				✓
Actual	Function	Count	Expected Function	Count	Result
none		0	*** No Call Expected ***	0	~

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	6524180		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20154800		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	231546		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	7.25360012		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0	0	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	6524180	6524180	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20154800	20154800	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	231546	231546	~
target_Vecu_Volt_T_f32	7.25360012	7.25360012	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.11 (Repeat Count = 1)			~
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	32145700		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	12365500		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23651500		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12546.2998		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	8.21450043		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	8.21450043	8.21450043	-
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	3.66932429e+018	3.66931989e+018 ± 900000000000	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23651500	23651500	-
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12546.2998	12546.2998	✓
target Vecu Volt T f32	31	31	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~



CalLowPassFiltVecuOut

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-96547904		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63254200		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	11254700		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	96321		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	9.32559967		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-96547904	-96547904	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63254200	63254200	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	11254700	11254700	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	96321	96321	✓
target_Vecu_Volt_T_f32	9.32559967	9.32559967	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.75868147e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.92536422e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	795439744		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	5.21449995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	5.21449995	5.21449995	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	3.28889538e+027	3.28890011e+027 ± 9E+21	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.92536422e+009	-1.92536422e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	795439744	795440000	✓
target Vecu Volt T f32	31	31	•

Test Step Call Trace ✓					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	91816200		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.02622003e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-237474000		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	13.3255997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	13.3255997	13.3255997	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.03331282e+027	1.03331001e+027 ± 9E+21	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.02622003e+009	-2.02622003e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-237474000	-237474000	✓
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.15 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-465380000

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.63317005e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.11672e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.30057997e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	8.12450027		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-465380000	-465380000	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.63317005e+009	1.63317005e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.11672e+009	-1.11672e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.30057997e+009	-1.30057997e+009	~
target_Vecu_Volt_T_f32	8.12450027	8.12450027	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2543700		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63527100		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	152639		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2543700	2543700	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63527100	63527100	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008	21456.3008	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	152639	152639	✓
target_Vecu_Volt_T_f32	5.32560015	5.32560015	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	96412600		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-65981500		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25634		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	3654.02002		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.21460009		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.21460009	6.21460009	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-6.17994684e+015	-6.17995006e+015	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25634	25634	•
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	3654.02002	3654.02002	✓
target Vecu Volt T f32	5	5	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.18 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.084e+009
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.28743002e+009
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	-2.14748006e+009
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	-302148992

CalLowPassFiltVecuOut

2016-04-22, 16:27:33+0530



Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.32560015	6.32560015	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	8.35360436e+026	8.35359994e+026	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-302148992	-302148992	✓
target_Vecu_Volt_T_f32	31	31	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.41749005e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-721043968		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.06957005e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	22.1424999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.41749005e+009	-1.41749005e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-721043968	-721043968	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748006e+009	2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.06957005e+009	2.06957005e+009	✓
target_Vecu_Volt_T_f32	22.1424999	22.1424999	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-45511800		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	758132992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.75474995e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.26762995e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.32560015	6.32560015	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.68637094e+027	-1.68637006e+027	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	1.75474995e+009	1.75474995e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.26762995e+009	-1.26762995e+009	✓
target_Vecu_Volt_T_f32	5	5	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~



CalLowPassFiltVecuOut

Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	65478900		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1235700		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124563		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	7.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	65478900	65478900	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1235700	1235700	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0	0	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124563	124563	~
target_Vecu_Volt_T_f32	7.32560015	7.32560015	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	23564200		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	65987400		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-12456.2998		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	132645		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	6.21449995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	23564200	23564200	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	65987400	65987400	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-12456.2998	-12456.2998	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	132645	132645	✓
target Vecu Volt T f32	6.21449995	6.21449995	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.23 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-980435008		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	991660032		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-416540992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	11.2563		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-980435008	-980435008	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	991660032	991660032	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-416540992	-416540992	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	-2.14748006e+009	✓
target_Vecu_Volt_T_f32	11.2563	11.2563	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.24 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.12730099e+009

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut	
-----------------------	--

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.10160973e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	1.93757453e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	30.2355995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	30.2355995	30.2355995	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-8.7446079e+027	-8.74461026e+027	•
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.93757453e+009	1.93757453e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	•
target_Vecu_Volt_T_f32	5	5	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•

Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.06284006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.40768998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.76896e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12455600		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5.21449995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.06284006e+009	-2.06284006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.40768998e+009	1.40768998e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.76896e+009	1.76896e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12455600	12455600	✓
target Vecu Volt T f32	5.21449995	5.21449995	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	•	

Test Step 2.26 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	112563		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1223650		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	124556		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.32567978		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.32567978	6.32567978	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0	0	•
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	124556	124556	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0	0	•
target Vecu Volt T f32	5	5	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	

Test Step 2.27 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	325648
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	302145
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124540000

CalLowPassFiltVecuOut

target_Vecu_Volt_T_f32

2016-04-22, 16:27:33+0530



11.2546997

Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	11.2546997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	325648	325648	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	302145	302145	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008	21456.3008	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124540000	124540000	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	•	

11.2546997

Test Case 3: Path Test

Description

Vector Description:

 $TS 3.1(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = False \& (*Vecu_Volt_T_f32 >= D_VECUMIN_VOLTS_F32) = False \& (*Vecu_Volt_T_f32 <= D_VECUMIN_VOLTS_F32) = True \\ TS 3.2(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = True \& (*Vecu_Volt_T_f32 >= D_VECUMIN_VOLTS_F32) = True \\ TS 3.3(*Vecu_Volt_T_f32 <= D_VECUMIN_VOLTS_F32) = False \\ TS 3.3(*Vecu_Volt_T_f32 <= D_VECUMI$

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009	-2.14748006e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	-2.14748006e+009	✓
target_Vecu_Volt_T_f32	5	5	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	*
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009	2.14748365e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	✓
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	6.12529993		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009	1.25422003e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009	-1.78840998e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009	2.03557005e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992	-453076992	~
target_Vecu_Volt_T_f32	6.12529993	6.12529993	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	•	

2016-04-22, 16:28:30+0530

PICurrCntrl_Per1



 Project
 Ap_PICurrCntrl

 Module
 PICurrCntrl

 Test Object
 PICurrCntrl_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Plcurrentcontrol
Configuration File	D:\Synergy_Work_Area\Plcurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Descript Name	Text
Module 'PICurrCntrl'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:15 Module Design Document:PlCurrentContrl.doc Module Design Document:PlCurrentContrl.doc Module Design Document Version:11 Data Dictionary Version:14 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):NA Total RAM Used (Bytes):NA Total RAM Used (Bytes):NA Special Test Requirements: Test Date:4/22/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnSclFac "". for variables "k_CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32, PlCurrCntrl_CurrSentSelisClFac_Uls_M_f32 and PlCurrCntrl_InverterSiolSclFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9



Test Case 1: Metric Test

Description

Vector Description:

 $TRUE) = True \& \& (k_MtrVoltQaxFiltFFEnable_Cnt_lgc == TRUE) = False \& \& (ModIdxSrlComSvcDft_Cnt_T_lgc == TRUE) = False \& \& (k_MtrCurrQaxRefMc == FALSE) = True$

Test Step 1.1 (Repeat Count = 1) Name	Input Value
	0
FastDataAccessBufIndex_Cnt_M_u16 MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
MtrCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(var)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
VtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	171.485992
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
VtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.037000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80200005
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.740999997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-489.436005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	938.341003
MtrCtrl MtrImpedDax Ohm M f32[0]	0.019999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl MtrImpedQax Ohm M f32[1]	0.0560000017
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	1.10899997
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.47999989
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	916.997009
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	1002.97998
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.1960001
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-2.83699989
MtrCtrl_Vecu_Volt_M_f32[0]	5.33099985
WtrCtrl_Vecu_Volt_M_f32[1]	7.69099998
MtrCurrDaxPrevIntg_Volt_M_f32	6.17600012
MtrCurrDaxRef_Amp_M_f32[0]	-146.173996
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxCog_Amp_M_f32	152.016006
MtrCurrQaxFinalRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxPrevIntg_Volt_M_f32	1.08770001
MtrCurrQaxRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxRef_Amp_M_f32[1]	-184.923996
virCurrQaxRei_Amp_M_f32[0]	-133.947006
MtrCurrQaxRpl_Amp_M_i32[i]	75.7020035
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1]	-6.23799992 2.11599994
	2.11599994
MtrVoltDax_Volt_M_f32[0]	
WtrVoltDax_Volt_M_f32[1]	-2.83699989 15.1960001
/trVoltQax_Volt_M_f32[0]	-2.83699989
MtrVoltQax_Volt_M_f32[1]	-2.83099989
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	
	0.010999999
PICurrCntrl_MtrCurrOaxSatFluxRatio_Uls_M_f32	0.335599989
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.851999998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12079.9004

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.620700002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	12079.9004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.620700002		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2475.81006		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2645.06006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.179000005		
k_MtrCtrlVirualResQax_Ohm_f32	0.0120000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	7.70650005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.0999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.614899993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.26999998		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9449997		
k_deadtimeVScale_Uls_f32	0.962000012		
t_CommOffsetTblX_Uls_u3p13[0]	4809		
t_CommOffsetTblX_Uls_u3p13[1]	5553		
t_CommOffsetTblY_Cnt_u16[0]	663		
t_CommOffsetTblY_Cnt_u16[1]	905		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	114.946999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1956		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1956	1956	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.381894678	0.381895989 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.79481602	-4.79481983 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32410	32410 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035	75.7020035 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 1.2 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1

2016-04-22, 16:28:30+0530





Name	Input Value
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	37.4550018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-2.84500003
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0160000008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0109999999
MtrCtrl MtrDampTermQax Ohm M f32[1]	0.0390000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.4149996
WtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.84899977
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-824.46698
WtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	454.670013
	0.076999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.029999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.196999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.954
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-729.622009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	640.599976
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-1.92999995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.432000011
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	26.6639996
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004
MtrCurrDaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxCog_Amp_M_f32	-198.285995
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556
MtrCurrQaxFinalRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxPrevIntg_Volt_M_f32	31
MtrCurrQaxRef Amp M f32[0]	140.289001
MtrCurrQaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxRel_Amp_M_f32[0]	-200.556
· - · - · - · · ·	-98.4449997
MtrCurrQaxRpl_Amp_M_f32[1]	
MtrPosComputationDelay_Rad_M_f32[0]	-5.37699986
MtrPosComputationDelay_Rad_M_f32[1]	-2.19300008
MtrVoltDax_Volt_M_f32[0]	-1.92999995
MtrVoltDax_Volt_M_f32[1]	0.432000011
MtrVoltQax_Volt_M_f32[0]	-1.92999995
MtrVoltQax_Volt_M_f32[1]	0.432000011
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.810000002
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0489999987
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.932299972
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-453.029999
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	6.96400023
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.331699997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-453.029999
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	6.96400023
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.331699997
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1416.70996
k ILOAFdbackSignalSclFacSlew UlspS f32	5107.18018
k_ILOAFubacksigiraisciracsiew_0isps_isz k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
MtrCtrlFeedbackControlDisable Cnt lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.057
k_MtrCtrlVirualResQax_Ohm_f32	0.179000005
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	8.02270031
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	24.8586998

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-11.7589998		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.63000011		
k_deadtimeVScale_Uls_f32	0.984000027		
t_CommOffsetTblX_Uls_u3p13[0]	2154		
t_CommOffsetTblX_Uls_u3p13[1]	6783		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	253		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1110	1110	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64487	64487 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.35975122	7.35974979 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.151220575	0.151221007 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	58832	58832 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556	-200.556 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Test Case 2: Boundary Test





Description Vector Description:

```
TS 2.1All Min
     TS 2.2AII_Max
   TS 2.3MtrCurrQaxRef_Amp_M_f32[2] = Min
TS 2.4MtrCurrQaxRef_Amp_M_f32[2] = Max
TS 2.5MtrCurrQaxRef_Amp_M_f32[2] = zero
TS 2.6MtrCurrQaxRef_Amp_M_f32[2] = Neg
TS 2.6MtrCurrQaxRef_Amp_M_f32[2] = Pos
TS 2.6MtrCurrQaxRef_Amp_M_f32[2] = Min
TS 2.8MtrCurrDaxRef_Amp_M_f32[2]= Min
TS 2.9MtrCurrDaxRef_Amp_M_f32[2] = Max
TS 2.10MtrCurrDaxRef_Amp_M_f32[2] = zero
TS 2.11MtrCurrDaxRef_Amp_M_f32[2] = Neg
TS 2.12MtrCurrDaxRef_Amp_M_f32[2] = Pos
TS 2.13MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2] = Min
TS 2.14MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2] = Max
TS 2.15MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2] = Pos
TS 2.16MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Min
TS 2.17MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Max
TS 2.18MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Zero
TS 2.19MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Zero
TS 2.19MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Zero
   TS 2.19MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Neg TS 2.29MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Neg TS 2.20MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Pos TS 2.21MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Min TS 2.22MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Neg TS 2.23MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[2] = Pos TS 2.24MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Min TS 2.54MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Min TS 2.54MtrCtrl_MtrQaxPropotionalG
     TS 2.25MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Max TS 2.26MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = zero TS 2.27MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Neg
   TS 2.28MtrCtrl_MtrQaxPropotionalGain_Ohm_M_52[2] = Neg
TS 2.28MtrCtrl_MtrQaxPropotionalGain_Ohm_M_52[2] = Pos
TS 2.29MtrPosComputationDelay_Rad_M_f32[2] = Min
TS 2.30MtrPosComputationDelay_Rad_M_f32[2] = Max
TS 2.31MtrPosComputationDelay_Rad_M_f32[2] = Zero
 TS 2.31MtrPosComputationDelay_Rad_M_f32[2] = Zero TS 2.32MtrPosComputationDelay_Rad_M_f32[2] = Neg TS 2.33MtrPosComputationDelay_Rad_M_f32[2] = Pos TS 2.34MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Min TS 2.35MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Max TS 2.36MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Zero TS 2.37MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Neg TS 2.38MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Neg TS 2.38MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Pos TS 2.39MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Min TS 2.40MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Max TS 2.41MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Zero TS 2.42MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg TS 2.43MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Pos TS 2.44MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Pos TS 2.44MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg TS 2.44MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg TS 2.44MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg TS 2.44MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min TS 2.44MtrCtrl_MtrDampTermDax_Ohm_f42[2] = Min TS 2.44MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min TS
   TS 2.43MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min
TS 2.44MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min
TS 2.45MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Max
TS 2.46MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Pos
TS 2.47MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Min
TS 2.48MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Max
TS 2.49MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Pos
   1S 2.49MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Min
TS 2.50MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Min
TS 2.51MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Max
TS 2.52MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Pos
TS 2.53MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Min
     TS 2.54MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Max
TS 2.55MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Pos
TS 2.56MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2]= Min
   TS 2.59MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Max
TS 2.58MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Zero
TS 2.59MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Neg
TS 2.69MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Pos
     TS 2.61MtrCurrQaxRpl_Amp_M_f32[2]= Min
TS 2.62MtrCurrQaxRpl_Amp_M_f32[2] = Max
TS 2.63MtrCurrQaxRpl_Amp_M_f32[2] = zero
     TS 2.64MtrCurrQaxRpl_Amp_M_f32[2] = Neg
TS 2.65MtrCurrQaxRpl_Amp_M_f32[2] = Pos
TS 2.66MtrCurrQaxCog_Amp_M_f32= Min
     TS 2.67MtrCurrQaxCog_Amp_M_f32 = Max
TS 2.68MtrCurrQaxCog_Amp_M_f32 = zero
TS 2.69MtrCurrQaxCog_Amp_M_f32 = Pos
     TS 2.70MtrCurrQaxCog_Amp_M_f32 = Neg
TS 2.71PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = Min
TS 2.72PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = Max
      TS 2.73PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = mid
   TS 2.74PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 = Min
TS 2.75PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 = Max
      TS 2.76PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 = mid
     TS 2.77MtrCurrQaxPrevIntg_Volt_M_f32 = Min
TS 2.78MtrCurrQaxPrevIntg_Volt_M_f32 = Max
TS 2.78MtrCurrQaxPrevIntg_Volt_M_f32 = Max
TS 2.79MtrCurrQaxPrevIntg_Volt_M_f32 = zero
TS 2.80MtrCurrQaxPrevIntg_Volt_M_f32 = Pos
TS 2.81MtrCurrQaxPrevIntg_Volt_M_f32 = Neg
TS 2.81MtrCurrDaxPrevIntg_Volt_M_f32 = Min
TS 2.83MtrCurrDaxPrevIntg_Volt_M_f32 = Min
TS 2.83MtrCurrDaxPrevIntg_Volt_M_f32 = Max
TS 2.84MtrCurrDaxPrevIntg_Volt_M_f32 = zero
TS 2.85MtrCurrDaxPrevIntg_Volt_M_f32 = Pos
TS 2.86MtrCurrDaxPrevIntg_Volt_M_f32 = Neg
TS 2.87k_MtrCtrlFeedbackControlDisable_Cnt_lgc = Min/Def
TS 2.88k_MtrCtrlFeedbackControlDisable_Cnt_lgc = Max
TS 2.89k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc = 0/Def
TS 2.90k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc = 1
TS 2.91k_CLOAFdbackSignalSclFacSlew_UlspS_f32 = Min
TS 2.92k_CLOAFdbackSignalSclFacSlew_UlspS_f32 = Pos
     TS 2.93k_CLOAFdbackSignalSclFacSlew_UlspS_f32 = Pos
TS 2.94k_CLOAFdbackSignalSclFacSlew_UlspS_f32 = Def
```



```
TS 2.95PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 = Min
TS 2.96PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 = Max
TS 2.97PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 = mid
TS 2.98k_ILOAFdbackSignalSclFacSlew_UlspS_f32 = Min
 TS 2.99k_ILOAFdbackSignalSclFacSlew_UlspS_f32 = Max
TS 2.100k_ILOAFdbackSignalSclFacSlew_UlspS_f32 = Pos
TS 2.101k_ILOAFdbackSignalSclFacSlew_UlspS_f32 = Def
 TS 2.102PICurrCntrl InverterFailSclFac Uls M f32 = Min TS 2.103PICurrCntrl InverterFailSclFac Uls M f32 = Max TS 2.104PICurrCntrl InverterFailSclFac Uls M f32 = mid
TS 2.105k_deadtimeVScale_UIs_f32 = Min
TS 2.106k_deadtimeVScale_UIs_f32 = Max/Def
TS 2.107k_deadtimeVScale_UIs_f32 = Pos
IS 2.107k_deadtimeVScale_UIs_f32 = Pos
TS 2.108k_MtrCurrQaxRefModifDsb_Cnt_Igc = Min/Def
TS 2.109k_MtrCurrQaxRefModifDsb_Cnt_Igc = Max
TS 2.110MtrCurrQax_Amp_f32 = Min
TS 2.111MtrCurrQax_Amp_f32 = Max
TS 2.112MtrCurrQax_Amp_f32 = Zero
TS 2.113MtrCurrQax_Amp_f32 = Pos
TS 2.114MtrCurrQax_Amp_f32 = Neg
 TS 2.115MtrCurrDax Amp_f32 = Min
TS 2.116MtrCurrDax_Amp_f32 = Max
TS 2.117MtrCurrDax_Amp_f32 = Zero
TS 2.117MtrCurrDax_Amp_f32 = Pos
TS 2.118MtrCurrDax_Amp_f32 = Pos
TS 2.119MtrCurrDax_Amp_f32 = Neg
TS 2.120MtrCtrl_Vecu_Volt_M_f32= Min
TS 2.121MtrCtrl_Vecu_Volt_M_f32 = Max
TS 2.122MtrCtrl_Vecu_Volt_M_f32 = Pos
TS 2.123ModIdxSrlComSvcDft_Cnt_lgc = Min
IS 2.123ModIdxSrIComSvcDrt_Cnt_Igc = Min
TS 2.124ModIdxSrIComSvcDrt_Cnt_Igc = Max
TS 2.125SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_DISABLE
TS 2.126SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OPF
TS 2.127SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OPERATE
TS 2.128SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_WARMINIT
TS 2.129SysState_Cnt_T_Enum=>RTE_TRANSITION_StaMd_Mode
TS 2.130FastDataAccessBufIndex_Cnt_M_u16 = Min
TS 2.131FastDataAccessBufIndex_Cnt_M_u16 = Min
 TS 2.131FastDataAccessBufIndex_Cnt_M_u16 = Max TS 2.132MotCurrLoaMtgtnEn_Cnt_lgc = Min TS 2.133MotCurrLoaMtgtnEn_Cnt_lgc = Max
 TS 2.134IvtrLoaMtgtnEn_Cnt_lgc = Min
TS 2.135IvtrLoaMtgtnEn_Cnt_lgc = Max
TS 2.136SlowDataAccessBufIndex_Cnt_M_u16 = Min
 TS 2.137SlowDataAccessBufIndex_Cnt_M_u16 = Max TS 2.138t_CommOffsetTblX_Uls_u3p13[2] = Min TS 2.139t_CommOffsetTblX_Uls_u3p13[2] = Max
 TS 2.140t_CommOffsetTbIX_UIs_u3p13[2] = Pos
TS 2.140t_CommOffsetTbIX_OIS_LISP13[2] = Pos
TS 2.141t_CommOffsetTbIY_Cnt_u16[2] = Min
TS 2.142t_CommOffsetTbIY_Cnt_u16[2] = Max
TS 2.143t_CommOffsetTbIY_Cnt_u16[2] = Pos
TS 2.144k_MtrCtrlVirualResDax_Ohm_f32 = Min/Def
TS 2.145k_MtrCtrlVirualResDax_Ohm_f32 = Max
TS 2.146k_MtrCtrlVirualResDax_Ohm_f32 = Pos
TS 2.147k_MtrCtrlVirualResQax_Ohm_f32 = Min/Def TS 2.147k_MtrCtrlVirualResQax_Ohm_f32 = Min/Def TS 2.148k_MtrCtrlVirualResQax_Ohm_f32 = Max TS 2.149k_MtrCtrlVirualResQax_Ohm_f32 = Pos TS 2.150k_VoltSatDaxPolyCoeff_Uls_f32 = Min TS 2.151k_VoltSatDaxPolyCoeff_Uls_f32 = Max TS 2.152k_VoltSatDaxPolyCoeff_Uls_f32 = Zero/Def
TS 2.153k_VoltSatDaxPolyCoeff_Uls_f32 = Zero
TS 2.153k_VoltSatDaxPolyCoeff_Uls_f32 = Neg
TS 2.154k_VoltSatDaxPolyCoeff_Uls_f32 = Pos
TS 2.155k_VoltSatQaxPolyCoeff_Uls_f32 = Min
TS 2.156k_VoltSatQaxPolyCoeff_Uls_f32 = Max
 TS 2.157k_VoltSatQaxPolyCoeff_UIs_f32 = Zero/Def
TS 2.158k_VoltSatQaxPolyCoeff_UIs_f32 = Neg
TS 2.159k_VoltSatQaxPolyCoeff_UIs_f32 = Pos
 TS 2.160MtrCurrOffComOffset_Cnt_u16 = Min
TS 2.161MtrCurrOffComOffset_Cnt_u16 = Max
TS 2.162MtrCurrOffComOffset_Cnt_u16 = Pos
 TS 2.163k_MtrVoltQaxFiltFFEnable_Cnt_lgc= Min TS 2.164k_MtrVoltQaxFiltFFEnable_Cnt_lgc= Max TS 2.165k_MtrVoltVecuFiltEnable_Cnt_lgc= Min
 TS 2.166k_MtrVoltVecuFiltEnable_Cnt_lgc= Max
 TS 2.167k_MtrVoltQaxIntegLoLim_Volt_f32 = >min
TS 2.168k_MtrVoltQaxIntegLoLim_Volt_f32 = >max
 TS 2.169k_MtrVoltQaxIntegLoLim_Volt_f32 = >neg
TS 2.170k_MtrVoltQaxIntegLoLim_Volt_f32 = >default
TS 2.171k_MtrVoltQaxIntegHiLim_Volt_f32=>min
 TS 2.172k_MtrVoltQaxIntegHiLim_Volt_f32=>max
TS 2.173k_MtrVoltQaxIntegHiLim_Volt_f32=>default
TS 2.174k_MtrVoltQaxIntegHiLim_Volt_f32=>mid
TS 2.174k_MtrVoltQaxIntegHiLim_Volt_f32=>mid
TS 2.175k_MtrVoltDaxIntegLoLim_Volt_f32=>min
TS 2.176k_MtrVoltDaxIntegLoLim_Volt_f32=>max
TS 2.176k_MtrVoltDaxIntegLoLim_Volt_f32=>neg
TS 2.178k_MtrVoltDaxIntegLoLim_Volt_f32=>default
TS 2.179k_MtrVoltDaxIntegHiLim_Volt_f32=>min
TS 2.180k_MtrVoltDaxIntegHiLim_Volt_f32=>min
TS 2.180k_MtrVoltDaxIntegHiLim_Volt_f32=>mid
TS 2.181k_MtrVoltDaxIntegHiLim_Volt_f32=>mid
TS 2.182k_MtrVoltDaxIntegHiLim_Volt_f32=>default
TS 2.183k_MtrCurrQaxRefModifRplEn_Cnt_lgc=>min
TS 2.184k_MtrCurrQaxRefModifRplEn_Cnt_lgc=>max/Default
```

Test Step 2.1 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0

2016-04-22, 16:28:30+0530





Name	Input Value
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-31
MtrCtrl MtrVoltDaxFF Volt M f32[1]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5
MtrCtrl_Vecu_Volt_M_f32[1]	5
MtrCurrDaxPrevIntg_Volt_M_f32	-31
MtrCurrDaxRef_Amp_M_f32[0]	-220
MtrCurrDaxRef_Amp_M_f32[1]	-220
MtrCurrQaxCog_Amp_M_f32	-220
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220
MtrCurrQaxPrevIntg_Volt_M_f32	-31
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220
MtrCurrQaxRpl_Amp_M_f32[0]	-220
MtrCurrQaxRpl_Amp_M_f32[1]	-220
MtrPosComputationDelay_Rad_M_f32[0]	-6.28318977
MtrPosComputationDelay_Rad_M_f32[1]	-6.28318977
MtrVoltDax_Volt_M_f32[0]	-31
MtrVoltDax_Volt_M_f32[1]	-31
MtrVoltQax_Volt_M_f32[0]	-31
MtrVoltQax_Volt_M_f32[1]	-31
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaySatEluyPatio_Ule_M_f32	0
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0 0
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1350 -1350
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	-0.996816993
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.96346009e-005
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996816993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.96346009e-005
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0
k_MtrCtrlVirualResQax_Ohm_f32	0
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	0
k_MtrVoltDaxIntegLoLim_Volt_f32	-31
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	-31

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530





Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-31		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-25		
k_VoltSatQaxPolyCoeff_Uls_f32	-25		
k_deadtimeVScale_Uls_f32	0.94999988		
t_CommOffsetTbIX_UIs_u3p13[0]	0		
t_CommOffsetTbIX_UIs_u3p13[1]	0		
t_CommOffsetTblY_Cnt_u16[0]	0		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62259	62259 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.33995581	-3.3399601 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.37745404	-3.37744999 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40903	40903	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.2 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 2 2 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl MtrQaxPropotionalGain Ohm M f32[0] 1024 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 1024 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] 31 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 31 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] 31 $MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]$ 31 MtrCtrl_Vecu_Volt_M_f32[0] 31 MtrCtrl_Vecu_Volt_M_f32[1] 31 MtrCurrDaxPrevIntg_Volt_M_f32 31 MtrCurrDaxRef_Amp_M_f32[0] 220 MtrCurrDaxRef_Amp_M_f32[1] 220 220 MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0] 220 MtrCurrQaxFinalRef_Amp_M_f32[1] 220 $MtrCurrQaxPrevIntg_Volt_M_f32$ 31 MtrCurrQaxRef_Amp_M_f32[0] 220 MtrCurrQaxRef_Amp_M_f32[1] 220 MtrCurrQaxRpl_Amp_M_f32[0] 220 MtrCurrQaxRpl_Amp_M_f32[1] 220 MtrPosComputationDelay_Rad_M_f32[0] 6.28318977 MtrPosComputationDelay_Rad_M_f32[1] 6.28318977 MtrVoltDax_Volt_M_f32[0] 31 MtrVoltDax_Volt_M_f32[1] 31 MtrVoltQax_Volt_M_f32[0] 31 MtrVoltQax_Volt_M_f32[1] 31 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 1 PICurrCntrl InverterFailSclFac Uls M f32 1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 1 PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 1350 PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32 1350 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 50928.6016 0.996827006 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 1350 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 1350 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 50928.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.996827006 SlowDataAccessBufIndex Cnt M u16 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 8000 k ILOAFdbackSignalSclFacSlew_UlspS_f32 8000 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.200000003 $k_MtrCtrlVirualResDax_Ohm_f32$ k_MtrCtrlVirualResQax_Ohm_f32 0.200000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc k MtrVoltDaxIntegHiLim Volt f32 31 k_MtrVoltDaxIntegLoLim_Volt_f32 0 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 31 k_MtrVoltQaxIntegLoLim_Volt_f32 0 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 25 k_VoltSatQaxPolyCoeff_Uls_f32 25 k_deadtimeVScale_Uls_f32 8192 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] 8192 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 $target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val$ $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 220 target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr 5000 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 220 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Actual Value **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 5000 5000 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 -220 -220 ± 7.81E-03 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 21.9203072 21.9202995 ± 4.88E-04

2016-04-22, 16:28:30+0530





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	21.9203072	21.9202995 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8192	8192 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef Amp M f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0430000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.43400002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.70599997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	362.112
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	65.1259995
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.064000003
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0209999997
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.356999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.65200001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-894.130005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-888.995972
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.9890003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	29.243
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	19.7509995
MtrCurrDaxRef Amp M f32[0]	67.4899979
MtrCurrDaxRef_Amp_M_f32[1]	119.721001
MtrCurrQaxCog_Amp_M_f32	-181.929001
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxPrevIntg_Volt_M_f32	7.82140017
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef Amp M f32[1]	-220

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
MtrCurrQaxRpl_Amp_M_f32[0]	-82.2979965		
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008		
MtrPosComputationDelay_Rad_M_f32[0]	-0.541999996		
MtrPosComputationDelay_Rad_M_f32[1]	6.08400011		
MtrVoltDax_Volt_M_f32[0]	-29.7110004		
MtrVoltDax_Volt_M_f32[1]	3.61899996		
MtrVoltQax_Volt_M_f32[0]	-29.7110004		
MtrVoltQax_Volt_M_f32[1]	3.61899996		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.416999996		
PICurrCntrl InverterFailSclFac Uls M f32	0.787		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.190799996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.708000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	48410.1016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0835999995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	48410.1016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0835999995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10.1009998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4233.2002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.087999995		
k_MtrCtrlVirualResQax_Ohm_f32	0.0099999978		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.9371996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.1975002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.59399998		
k_VoltSatQaxPolyCoeff_Uls_f32	8.35700035		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTblX_Uls_u3p13[0]	4914		
t_CommOffsetTblX_Uls_u3p13[1]	7782		
t_CommOffsetTblY_Cnt_u16[0]	1099		
t_CommOffsetTblY_Cnt_u16[1]	1672		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-72.4260025		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4932		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	77.189003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1672	1672	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62324	62324 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	44.2269897	44.2270012 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	9.26780319	9.26780033 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-9.9237175	-9.92372036 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	19279	19279 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	12.9371996	12.9371996 ± 0.0625	
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1]	12.9371996 44.2269897	12.9371996 ± 0.0625 44.2270012 ± 7.81E-03	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
/dtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(vtr)	target_MtrCntrl Read_MotCurrLoaMtgtnEn Cnt Igc ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
htrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
htrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.115000002
htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.057999983
ItrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0320000015
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0869999975
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0649999976
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.227
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	537.232971
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	67.9840012
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.109999999
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0270000007
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0920000002
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.703000009
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.75199997
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	462.437012
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-685.195984
ftrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.6930008
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.219999999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.45499992
htrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.1830006
ltrCtrl_Vecu_Volt_M_f32[0]	22.3540001
ItrCtrl_Vecu_Volt_M_f32[1]	24.7140007
ltrCurrDaxPrevIntg_Volt_M_f32	-23.0620003
ItrCurrDaxRef_Amp_M_f32[0]	37.4550018
htrCurrDaxRef_Amp_M_f32[1]	-2.84500003
1trCurrQaxCog_Amp_M_f32	-55.5390015
htrCurrQaxFinalRef_Amp_M_f32[0]	160.044006
ItrCurrQaxFinalRef_Amp_M_f32[1]	165.242004
1trCurrQaxPrevIntg_Volt_M_f32	8.08899975
ItrCurrQaxRef_Amp_M_f32[0]	220
ItrCurrQaxRef_Amp_M_f32[1]	220
ltrCurrQaxRpl_Amp_M_f32[0]	160.044006
ItrCurrQaxRpI Amp M f32[1]	165.242004
ItrPosComputationDelay_Rad_M_f32[0]	-5.08799982
ItrPosComputationDelay Rad M f32[1]	-4.26300001
ttrVoltDax_Volt_M_f32[0]	3.45499992
htrVoltDax Volt M f32[1]	-19.1830006
/trVoltQax Volt M f32[0]	3.45499992
/trVoltQax_Volt_M_f32[1]	-19.1830006

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

	1		
Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.638000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.880900025		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.978999972		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47050.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0229000002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47050.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0229000002		
SlowDataAccessBufIndex Cnt M u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k ILOAFdbackSignalSclFacSlew UlspS f32	7088.3501		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.194999993		
k_MtrCtrlVirualResQax_Ohm_f32	0.142000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	17.9123993		
k MtrVoltDaxIntegLoLim Volt f32	-0.699999988		
k MtrVoltQaxFiltFFEnable Cnt Igc	0		
k MtrVoltQaxInter Eriabic_ont_ige	19.4449997		
k MtrVoltQaxIntegLoLim Volt f32	-0.69999988		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-19.4559994		
k VoltSatQaxPolyCoeff Uls f32	-18.6200008		
- ,	0.95599997		
k_deadtimeVScale_Uls_f32	4170		
t_CommOffsetTbIX_UIs_u3p13[0]			
t_CommOffsetTbIX_UIs_u3p13[1]	6749		
t_CommOffsetTblY_Cnt_u16[0]	177		
t_CommOffsetTblY_Cnt_u16[1]	340		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	335		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-145.169006		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	315	315	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	50872	50872 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	110.296997	110.296997 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.219999999	0.219999999 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-19.1830006	-19.1830006 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	53720	53720 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	-0.69999988	-0.699999988 ± 0.0625	-
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006	160.044006 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	110.296997	110.296997 ± 7.81E-03	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.5 (Repeat Count = 1) Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModIdxSrlComSvcDft Cnt lgc Val
VtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-69.0940018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	161.973007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0489999987
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.108000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0820000023
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.824000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.423999995
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-284.230011
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	346.425995
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00700000022
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.123999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0790000036
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.00899999961
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.625
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	351.605011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	882.085999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-24.6650009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	18.8299999
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.4720001
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.0999999
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	-6.72300005
MtrCurrDaxRef_Amp_M_f32[0]	94.3150024
MtrCurrDaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxCog_Amp_M_f32	146.660995
MtrCurrQaxFinalRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972
MtrCurrQaxPrevIntg_Volt_M_f32	24.8390999
MtrCurrQaxRef_Amp_M_f32[0]	0
VtrCurrQaxRef_Amp_M_f32[1]	0
MtrCurrQaxRpI_Amp_M_f32[0]	-65.1900024
VtrCurrQaxRpl_Amp_M_f32[1]	-216.972
MtrPosComputationDelay_Rad_M_f32[0]	0.35800001
MtrPosComputationDelay_Rad_M_f32[1]	-1.33899999
//dtrVoltDax_Volt_M_f32[0]	26.4720001
VtrVoltDax_Volt_M_f32[1]	6.0999999
VtrVoltQax_Volt_M_f32[0]	26.4720001
VtrVoltQax_Volt_M_f32[1]	6.0999999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0170000009
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.653999984
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.588400006
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.407000005
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47672
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.660899997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47672
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.660899997
SlowDataAccessBufIndex_Cnt_M_u16	0
c_CLOAFdbackSignalSclFacSlew_UlspS_f32	2305.86011
c_ILOAFdbackSignalSclFacSlew_UlspS_f32	5143.29004
c_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
<_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0260000005
<_MtrCtrlVirualResQax_Ohm_f32	0.199000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	4.14720011





Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.800000012		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	2.33150005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.800000012		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	18.1009998		
k_VoltSatQaxPolyCoeff_Uls_f32	12.7729998		
k_deadtimeVScale_Uls_f32	0.984000027		
t_CommOffsetTblX_Uls_u3p13[0]	4013		
t_CommOffsetTblX_Uls_u3p13[1]	4882		
t_CommOffsetTblY_Cnt_u16[0]	790		
t_CommOffsetTblY_Cnt_u16[1]	931		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4626		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-100.035004		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	931	931	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64487	64487 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-81.470993	-81.4710007 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.922617	-13.9225998 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.88431573	1.88431001 ± 4.88E-04	·
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	54289	54289 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-81.470993	-81.4710007 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972	-216.972 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.6 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.912
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.71200001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	355.987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-300.080994
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0939999968

PICurrCntrl_Per1



Name	Input Value	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0329999998	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0140000004	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0860000029	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0359999985	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	363.006989	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	428.059998	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	25.0079994	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltDaxFE_Volt_M_f32[0]	19.2439995	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	14.4589996 -5.13000011	
MtrCtrl Vecu Volt M f32[0]	20.2549992	
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998	
MtrCurrDaxPrevIntg_Volt_M_f32	-17.5849991	
MtrCurrDaxRef_Amp_M_f32[0]	212.455994	
MtrCurrDaxRef_Amp_M_f32[1]	89.8619995	
MtrCurrQaxCog_Amp_M_f32	-172.485001	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.723007	
MtrCurrQaxFinalRef_Amp_M_f32[1]	-121.943001	
MtrCurrQaxPrevIntg_Volt_M_f32	16.4962006	
MtrCurrQaxRef_Amp_M_f32[0]	-115.696999	
MtrCurrQaxRef_Amp_M_f32[1]	-141.417007	
MtrCurrQaxRpl_Amp_M_f32[0]	-146.723007	
MtrCurrQaxRpl_Amp_M_f32[1]	-121.943001	
MtrPosComputationDelay_Rad_M_f32[0]	-4.90199995	
MtrPosComputationDelay_Rad_M_f32[1]	-2.21700001	
MtrVoltDax_Volt_M_f32[0]	14.4589996	
MtrVoltDax_Volt_M_f32[1]	-5.13000011	
MtrVoltQax_Volt_M_f32[0]	14.4589996	
MtrVoltQax_Volt_M_f32[1]	-5.13000011	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.041999994	
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.861999989 0.897000015	
PICurrCntrl MtrCurrQaxSatFluxRatio_Uis_M f32	0.652999997	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	29506.5	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.499300003	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	29506.5	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.499300003	
SlowDataAccessBufIndex_Cnt_M_u16	1	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7811.3999	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5154.22021	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0	
k_MtrCtrlVirualResDax_Ohm_f32	0.0099999978	
k_MtrCtrlVirualResQax_Ohm_f32	0.0780000016	
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1	
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0	
k_MtrVoltDaxIntegHiLim_Volt_f32	7.24790001	
k_MtrVoltDaxIntegLoLim_Volt_f32 k MtrVoltQaxFiltFFEnable Cnt lgc	-1.5 1	
k_MtrVoltQaxIntegHiLim_Volt_f32	11.4308004	
k MtrVoltQaxIntegLoLim Volt f32	-1.5	
k MtrVoltVecuFiltEnable Cnt lgc	1	
k_VoltSatDaxPolyCoeff_Uls_f32	-20.1009998	
k VoltSatQaxPolyCoeff Uls f32	15.8879995	
k_deadtimeVScale_Uls_f32	0.978999972	
t_CommOffsetTbIX_UIs_u3p13[0]	6717	
t_CommOffsetTbIX_UIs_u3p13[1]	7750	
t_CommOffsetTblY_Cnt_u16[0]	59	
t_CommOffsetTblY_Cnt_u16[1]	1827	
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1	
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1	
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	80.8180008	
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	970	
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-184.522003	
	2	
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	-	
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val)		Result

PICurrCntrl_Per1



Name	Actual Value	Expected Value	Result
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	153.011002	153.011002 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	10.6815271	10.6815004 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.84744525	-2.84745002 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61513	61513 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-1.5	-1.5 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.723007	-146.723007 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	153.011002	153.011002 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.173996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-213.335007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.40199995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.232999995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	115.644997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	546.737976
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0869999975
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.032999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.89699996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.37399995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-766.185974
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-58.2080002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-20.0429993
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	7.43900013
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	13.085
MtrCtrl_Vecu_Volt_M_f32[1]	15.4449997
MtrCurrDaxPrevIntg_Volt_M_f32	-17.3029995
MtrCurrDaxRef_Amp_M_f32[0]	-108.124001
MtrCurrDaxRef_Amp_M_f32[1]	178.639008
MtrCurrQaxCog_Amp_M_f32	39.7939987
MtrCurrQaxFinalRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxFinalRef_Amp_M_f32[1]	-27.9839993

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxPrevIntg_Volt_M_f32 19.8957996 MtrCurrQaxRef_Amp_M_f32[0] 140.470001 MtrCurrQaxRef_Amp_M_f32[1] 93.5790024 MtrCurrQaxRpl_Amp_M_f32[0] -208.287994 MtrCurrQaxRpl_Amp_M_f32[1] -27.9839993 MtrPosComputationDelay_Rad_M_f32[0] -0.896000028 MtrPosComputationDelay_Rad_M_f32[1] 1.35300004 MtrVoltDax_Volt_M_f32[0] -7.3130002 MtrVoltDax_Volt_M_f32[1] 3.05299997 MtrVoltQax_Volt_M_f32[0] -7 3130002 MtrVoltQax_Volt_M_f32[1] 3.05299997 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.961000025 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.958999991 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.954400003 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.123000003 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -627.179993 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 947.73999 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 39240.1992 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.217500001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -627.179993 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 947.73999 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 39240.1992 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.217500001 SlowDataAccessBufIndex_Cnt_M_u16 k CLOAFdbackSignalSclFacSlew UlspS f32 2988.07007 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 1052.21997 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k MtrCtrlVirualResDax Ohm f32 0.165000007 k_MtrCtrlVirualResQax_Ohm_f32 0.192000002 k MtrCurrQaxRefModifDsb Cnt lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k MtrVoltDaxIntegHiLim Volt f32 10.6739998 k_MtrVoltDaxIntegLoLim_Volt_f32 -2.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 22.7896004 k_MtrVoltQaxIntegLoLim_Volt_f32 -2.5 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 -0.714999974 k VoltSatQaxPolyCoeff_Uls_f32 10.5810003 k_deadtimeVScale_Uls_f32 0.996999979 t CommOffsetTblX_Uls_u3p13[0] 3841 t_CommOffsetTblX_Uls_u3p13[1] 4727 t_CommOffsetTblY_Cnt_u16[0] 222 t_CommOffsetTblY_Cnt_u16[1] 974 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -44 2579994 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1850 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -197.354996 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1850 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 81.7690048 81.7689972 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 30.7472401 30.7472 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -3.13844824 -3.13844991 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 31557 31557 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] -208.287994 -208.287994 ± 7.81E-03 81.7689972 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[1] 81.7690048



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.8 (Repeat Count = 1)	Input Value	
	1	
FastDataAccessBufIndex_Cnt_M_u16	·	
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0759999976	
htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0240000002	
ftrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0120000001	
ftrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0710000023	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.349000007	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.930000007	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	923.77301	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	220.951996	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0430000015	
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0329999998	
ltrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.00800000038	
htrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0189999994	
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.12600005	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60000002	
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-994.463989	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-659.200989	
1trCtrl MtrVoltDaxFF Volt M f32[0]	22.5750008	
1trCtrl MtrVoltDaxFF Volt M f32[1]	22.8969994	
1trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008	
htrCtrl MtrVoltQaxFF Volt M f32[1]	27.5049992	
ItrCtrl Vecu Volt M f32[0]	25.4869995	
ItrCtrl Vecu Volt M f32[1]	27.8470001	
trCurrDaxPrevIntg Volt M f32	-6.4460001	
/trCurrDaxRef_Amp_M_f32[0]	-220	
/trCurrDaxRef Amp M f32[1]	-220	
/trCurrQaxCog_Amp_M_f32	161.921005	
trCurrQaxFinalRef_Amp_M_f32[0]	31.5869999	
ItrCurrQaxFinalRef Amp M f32[1]	-186.395996	
	18.0524998	
htrCurrQaxPrevIntg_Volt_M_f32	-82.2979965	
/trCurrQaxRef_Amp_M_f32[0]	1 1111	
MtrCurrQaxRef_Amp_M_f32[1]	46.8180008	
/trCurrQaxRpl_Amp_M_f32[0]	31.5869999	
MtrCurrQaxRpl_Amp_M_f32[1]	-186.395996	
/trPosComputationDelay_Rad_M_f32[0]	3.94899988	
MtrPosComputationDelay_Rad_M_f32[1]	-2.61299992	
//dtrVoltDax_Volt_M_f32[0]	-8.79500008	
MtrVoltDax_Volt_M_f32[1]	27.5049992	

PICurrCntrl_Per1





Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-8.79500008		
MtrVoltQax_Volt_M_f32[1]	27.5049992		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.681999981		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.151999995		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.910700023		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.742999971		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17955.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.958999991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	17955.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.958999991		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	5278.47998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6189.22021		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.00700000022		
k_MtrCtrlVirualResQax_Ohm_f32	0.140000001		
	1		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_Mtr/cutrQaxRefModifRplEn_Cnt_lgc			
k_MtrVoltDaxIntegHiLim_Volt_f32	30.5515995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-3.5 1		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc			
k_MtrVoltQaxIntegHiLim_Volt_f32	27.4305		
k_MtrVoltQaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1 4700004		
k_VoltSatDaxPolyCoeff_Uls_f32	-17.1700001		
k_VoltSatQaxPolyCoeff_Uls_f32	11.927		
k_deadtimeVScale_UIs_f32	0.998000026		
t_CommOffsetTblX_Uls_u3p13[0]	1065		
t_CommOffsetTblX_Uls_u3p13[1]	1483		
t_CommOffsetTblY_Cnt_u16[0]	45		
t_CommOffsetTblY_Cnt_u16[1]	1687		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-40.9220009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4262		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	152.016006		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4262	4262	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	71.2929916	71.2929993 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	19.7938614	19.7938995 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	23.777359	23.7773991 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	45522	45522 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	
MtrCurrQaxFinalRef Amp M f32[0]	31.5869999	31.5869999 ± 7.81E-03	
MtrCurrQaxFinalRef_Amp_M_f32[1]	71.2929916	71.2929993 ± 7.81E-03	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
//witchtrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] MtrCtrl MtrDampTermDax Ohm M f32[1]	0.039000008
	0.0179999992
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0460000001
MtrCtrl_MtrDawpTermQax_Ohm_M_f32[1]	0.167999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62100005
htrCtrl_mtrDaxIntegralGain_Onm_in_r32[1] MtrCtrl_mtrDaxPropotionalGain_Ohm_M_f32[0]	720,525024
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-487.845001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.064999976
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.60500002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.33500004
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-418.748993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	590.754028
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.3319998
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.40700006
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
/trCtrl_Vecu_Volt_M_f32[0]	16.8080006
/trCtrl_Vecu_Volt_M_f32[1]	19.1679993
/trCurrDaxPrevIntg_Volt_M_f32	14.7060003
MtrCurrDaxRef_Amp_M_f32[0]	220
MtrCurrDaxRef_Amp_M_f32[1]	220
/trCurrQaxCog_Amp_M_f32	177.763
/trCurrQaxFinalRef_Amp_M_f32[0]	-133.947006
/trCurrQaxFinalRef_Amp_M_f32[1]	75.7020035
/trCurrQaxPrevIntg_Volt_M_f32	12.4979
/ltrCurrQaxRef_Amp_M_f32[0]	160.044006
/ltrCurrQaxRef_Amp_M_f32[1]	165.242004
/ltrCurrQaxRpl_Amp_M_f32[0]	-133.947006
/ltrCurrQaxRpl_Amp_M_f32[1]	75.7020035
/ltrPosComputationDelay_Rad_M_f32[0]	0.277999997
MtrPosComputationDelay_Rad_M_f32[1]	-5.2579999
MtrVoltDax_Volt_M_f32[0]	-22.3460007
/trVoltDax_Volt_M_f32[1]	-17.8169994

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-22.3460007		
MtrVoltQax_Volt_M_f32[1]	-17.8169994		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.426999986		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.469999999		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.194700003		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.860000014		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	31081.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.797699988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	31081.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.797699988		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	5764.10986		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3350.96997		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.175999999		
k MtrCtrlVirualResQax Ohm f32	0.061999999		
k MtrCurrQaxRefModifDsb Cnt Igc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.2978001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.2735996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.7950001		
k_VoltSatQaxPolyCoeff_Uls_f32	21.1380005		
k_deadtimeVScale_Uls_f32	0.958999991		
t CommOffsetTbIX UIs u3p13[0]	4432		
	5751		
t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0]	132		
	216		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	0		
	75.0830002		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val			
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3800		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	54.1119995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4	I=	1
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3800	3800	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	116.228012	116.227997 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.68477249	4.68477011 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.02222037	-1.02222002 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	21524	21524 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	116.228012	116.227997 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035	75.7020035 ± 7.81E-03	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

lame	Input Value	
rastDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val	
htrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
ItrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
htrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
ItrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
ttrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_MtrCdntQax_Arrip_isz_val	
	106.072998	
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]		
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002	
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.119000003	
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0659999996	
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.010999999	
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982	
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20299995	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.354000002	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	868.213013	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.690002	
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112000003	
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0930000022	
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0579999983	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.104000002	
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.319999993	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.254999995	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-236.619003	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-663.224976	
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-15.8149996	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-9.85200024	
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.448	
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.9260006	
trCtrl_Vecu_Volt_M_f32[0]	5.56799984	
trCtrl_Vecu_Volt_M_f32[1]	7.92799997	
trCurrDaxPrevIntg_Volt_M_f32	5.13399982	
trCurrDaxRef_Amp_M_f32[0]	0	
trCurrDaxRef_Amp_M_f32[1]	0	
trCurrQaxCog_Amp_M_f32	160.160004	
ItrCurrQaxFinalRef Amp M f32[0]	209.052002	
ItrCurrQaxFinalRef_Amp_M_f32[1]	-124.994003	
ItrCurrQaxPrevIntg_Volt_M_f32	12.7323999	
trCurrQaxRef Amp M f32[0]	-65.1900024	
trCurrQaxRef_Amp_M_f32[1]	-216.972	
trCurrQaxRpl Amp M f32[0]	209.052002	
trCurrQaxRpl Amp M f32[1]	-124.994003	
trPosComputationDelay_Rad_M_f32[0]	3.03600001	
ItrPosComputationDelay Rad M f32[1]	-5.3920002	
htrVoltDax_Volt_M_f32[0]	-23.448	
ItrVoltDax_Volt_M_132[0]	-24.9260006	

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



	1		
Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-23.448		
MtrVoltQax_Volt_M_f32[1]	-24.9260006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.109999999		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.214000002		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.85650003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.504000008		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	38607.8008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.253199995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	38607.8008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.253199995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5287.27002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3540.21997		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0329999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.0209999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.5352001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.4337997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	0.853999972		
k_VoltSatQaxPolyCoeff_Uls_f32	-2.3499999		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTbIX_UIs_u3p13[0]	4529		
t_CommOffsetTbIX_UIs_u3p13[1]	6659		
t_CommOffsetTblY_Cnt_u16[0]	120		
t_CommOffsetTblY_Cnt_u16[1]	597		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	74.0660019		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2932		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-17.6900005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	597	597	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63897	63897 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.96537185	-3.96536994 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.70778918	3.7077899 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	23125	23125 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	~

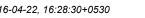
Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

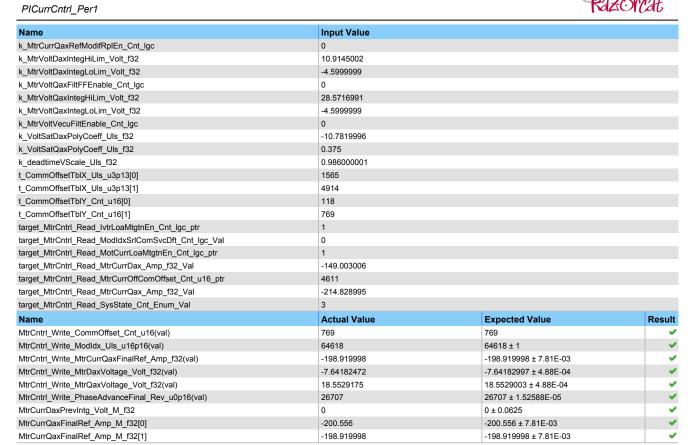
-124.994003

-124.994003 ± 7.81E-03



Test Step 2.11 (Repeat Count = 1)	Innuit Value	
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16 MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	1 target MtrCotrl Read lutri caMtetnEn Cot loc ptr	
MtrCntrl Read ModldxSrlComSvcDft Cnt lqc(Val)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target MtrCntrl Read ModIdxSrlComSvcDft Cnt lgc Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(var)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	24.6130009	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-20.9400005	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0769999996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.103	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0529999994	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.023	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.88600004	
MtrCtrl_MtrDayProportionalCain_Ohm_M_f32[1]	1.31599998 417.908997	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-364.772003	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0099999978	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0729999989	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0450000018	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.368000001	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.432000011	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-959.400024	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-873.330017	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-28.7189999	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-12.0450001	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.9890003	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	29.243	
MtrCtrl_Vecu_Volt_M_f32[0]	17.9899998	
MtrCtrl_Vecu_Volt_M_f32[1]	20.3500004	
MtrCurrDaxPrevIntg_Volt_M_f32	9.50599957	
MtrCurrDavRef_Amp_M_f32[0]	-115.696999	
MtrCurrOavCog Amp M #32	-141.417007 175.421997	
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556	
MtrCurrQaxFinalRef_Amp_M_f32[1]	-98.4449997	
MtrCurrQaxPrevIntg_Volt_M_f32	27.8554001	
MtrCurrQaxRef Amp M f32[0]	-146.723007	
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001	
MtrCurrQaxRpl_Amp_M_f32[0]	-200.556	
MtrCurrQaxRpl_Amp_M_f32[1]	-98.4449997	
MtrPosComputationDelay_Rad_M_f32[0]	0.0939999968	
MtrPosComputationDelay_Rad_M_f32[1]	-3.33200002	
MtrVoltDax_Volt_M_f32[0]	-29.9890003	
MtrVoltDax_Volt_M_f32[1]	29.243	
MtrVoltQax_Volt_M_f32[0]	-29.9890003	
MtrVoltQax_Volt_M_f32[1]	29.243	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	0.591000021 0.361999989	
	0.692700028	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.643000007	
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	0.043000007	
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	386.220001	
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	21678.8008	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0943000019	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	21678.8008	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0943000019	
SlowDataAccessBufIndex_Cnt_M_u16	1	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5215.41016	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1901.98999	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0	
k_MtrCtrlVirualResDax_Ohm_f32	0.138999999	
k_MtrCtrlVirualResQax_Ohm_f32	0.192000002	
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0	





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

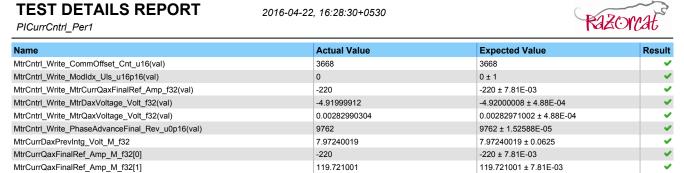
Test Step 2.12 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0480000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.122000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.509000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.29100001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-447.415009





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-391.990997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0860000029
MtrCtrl MtrImpedQax Ohm M f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00600000005
	0.213
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.442000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	451.035004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	847.624023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-19.4680004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-24.3309994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	30.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.219999999
MtrCtrl_Vecu_Volt_M_f32[0]	20.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	23.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	24.7740002
MtrCurrDaxRef_Amp_M_f32[0]	140.470001
MtrCurrDaxRef_Amp_M_f32[1]	93.5790024
MtrCurrQaxCog_Amp_M_f32	-8.45100021
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	6.23339987
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpI_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-3.77699995
MtrPosComputationDelay_Rad_M_f32[1]	-5.58500004
MtrVoltDax_Volt_M_f32[0]	30.6930008
MtrVoltDax_Volt_M_f32[1]	0.219999999
	30.6930008
MtrVoltQax_Volt_M_f32[0]	
MtrVoltQax_Volt_M_f32[1]	0.219999999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.127000004
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.860000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.588100016
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.179000005
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	18254.6992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.766499996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18254.6992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.766499996
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7975.79004
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3201.42993
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k MtrCtrlFeedbackControlDisable Cnt lgc	1
k MtrCtrlVirualResDax Ohm f32	0.020999997
k_MtrCtrlVirualResQax_Ohm_f32	0.101999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k MtrCurrQaxRefModifRplEn Cnt lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	7.97240019
k MtrVoltDaxIntegLoLim Volt f32	-5.5
k MtrVoltQaxFiltFFEnable Cnt lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	1.26639998
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	3.04200006
k_VoltSatQaxPolyCoeff_Uls_f32	14.8559999
k_deadtimeVScale_Uls_f32	0.984000027
t_CommOffsetTblX_Uls_u3p13[0]	1262
t_CommOffsetTblX_Uls_u3p13[1]	5333
t_CommOffsetTblY_Cnt_u16[0]	311
t_CommOffsetTblY_Cnt_u16[1]	1141
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-124.758003
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3668
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-96.3310013
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2
g.svai	-





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	140.289001
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	178.235992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.120999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.029999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	248.748993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	78.5080032
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0199999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.155000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0590000004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	853.911011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-267.251007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-24.6650009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	18.8299999
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	-14.5480003
MtrCurrDaxRef_Amp_M_f32[0]	-213.026993
MtrCurrDaxRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxCog_Amp_M_f32	-35.144001
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.173996

© Report created by TESSY V3.1.11, report template V2.1

30

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007		
MtrCurrQaxPrevIntg_Volt_M_f32	1.25670004		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32[0]	-146.173996		
MtrCurrQaxRpl_Amp_M_f32[1]	-213.335007		
MtrPosComputationDelay_Rad_M_f32[0]	-2.12100005		
MtrPosComputationDelay_Rad_M_f32[1]	-2.55599999		
MtrVoltDax_Volt_M_f32[0]	-24.6650009		
MtrVoltDax_Volt_M_f32[1]	18.8299999		
MtrVoltQax_Volt_M_f32[0]	-24.6650009		
MtrVoltQax_Volt_M_f32[1]	18.8299999		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.23999995		
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.75999999 0.671500027		
PICurrCotrl_MtrCurrOavSatFluxRatio_Uls_M_f32			
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0.572000027 -43.1699982		
	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23863		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1391		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	23863		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.1391		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	6335.39014		
k ILOAFdbackSignalSclFacSlew UlspS f32	7999.74023		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.172000006		
k_MtrCtrlVirualResQax_Ohm_f32	0.128999993		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.8404007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.35560036		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.08800006		
k_VoltSatQaxPolyCoeff_Uls_f32	-2.53399992		
k_deadtimeVScale_Uls_f32	0.995000005		
t_CommOffsetTblX_Uls_u3p13[0]	1229		
t_CommOffsetTblX_Uls_u3p13[1]	1416		
t_CommOffsetTblY_Cnt_u16[0]	1102		
t_CommOffsetTblY_Cnt_u16[1]	1272		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4293		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-168.113007 0		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	· ·	E	la .
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1272	1272	,
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65208	65208 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	212.904999	212.904999 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.565230429	-0.565230012 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	14.1953611	14.1954002 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42998	42998 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef Amp M f32[0]	212.904999	212.904999 ± 7.81E-03	



Test Step Call Trace	Test Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
FastDataAccessBufIndex Cnt M u16	1
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	91.8850021
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	182.261002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.064000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.101999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0430000015
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-813.039001
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	76.7679977
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00700000022
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0839999989
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0189999994
ftrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.075000003
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.91199994
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.33000004
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	572.697998
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-525.994019
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	3.45499992
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-19.1830006
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	25.0079994
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	19.2439995
/trCtrl_Vecu_Volt_M_f32[0]	21.2989998
1trCtrl_Vecu_Volt_M_f32[1]	23.6590004
1trCurrDaxPrevIntg Volt M f32	6.20800018
/trCurrDaxRef Amp M f32[0]	-212.632996
/trCurrDaxRef_Amp_M_f32[1]	-205.085007
AtrCurrQaxCog Amp M f32	79.6880035
/trCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
htrCurrQaxFinalRef_Amp_M_f32[1]	133.692993
ItrCurrQaxPrevIntg Volt M f32	28.2577
/trCurrQaxRef_Amp_M_f32[0]	-133.947006
htrCurrQaxRef_Amp_M_f32[1]	75.7020035
ttrCurrQaxRpl_Amp_M_f32[0]	-91.4420013
ItrCurrQaxRpl Amp M f32[1]	133.692993
htrPosComputationDelay_Rad_M_f32[0]	2.0480001
htrPosComputationDelay Rad M f32[1]	5.41300011
MtrVoltDax_Volt_M_f32[0]	25.0079994
MtrVoltDax_volt_M_132[0]	19.2439995
MtrVoltQax_Volt_M_132[0]	25.0079994
//trVoltQax_Volt_M_f32[1]	19.2439995

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.0549999997 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.0209999997 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.627399981 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.523000002 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 1118 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 1838.12 0.523899972 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$ PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 570 700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 1838.12 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.523899972 SlowDataAccessBufIndex_Cnt_M_u16 5138 27002 k CLOAFdbackSignalSclFacSlew UlspS f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 1882.53003 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.0289999992 $k_MtrCtrlVirualResQax_Ohm_f32$ 0.181999996 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 11.2284002 k_MtrVoltDaxIntegHiLim_Volt_f32 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -7.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 7.49779987 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -15.625 k_VoltSatQaxPolyCoeff_Uls_f32 3.14400005 k_deadtimeVScale_Uls_f32 0.977999985 t CommOffsetTblX Uls u3p13[0] 4858 t_CommOffsetTblX_Uls_u3p13[1] 7209 t CommOffsetTblY Cnt u16[0] 1186 t_CommOffsetTblY_Cnt_u16[1] 1407 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3506 target MtrCntrl Read MtrCurrQax Amp f32 Val -5.66300011 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Expected Value** Name Actual Value Result MtrCntrl Write CommOffset Cnt u16(val) 1407 1407 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 64094 64094 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -137.678986 -137.679001 ± 7.81E-03 -16.3353996 -16.3353996 ± 4.88E-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 16.3873444 16.3873005 ± 4.88E-04 48284 + 1 52588F-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 48284 0 ± 0.0625 MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0] -91 4420013 -91 4420013 + 7 81F-03

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

-137.678986

-137.679001 ± 7.81E-03



Test Step 2.15 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	-166.035004 183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.097000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0480000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.122000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.509000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.29100001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-447.415009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-391.990997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0060000005 0.213
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.442000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	451.035004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	847.624023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-19.4680004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-24.3309994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	30.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.219999999
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDayPer Amp. M. (2010)	24.7740002
MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	140.470001 93.5790024
MtrCurrQaxCog_Amp_M_f32	-8.45100021
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	5.45940018
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay Rad M f32[1]	-3.77699995
MtrVoltDax_Volt_M_f32[0]	-5.58500004 30.6930008
MtrVoltDax Volt M f32[1]	0.219999999
MtrVoltQax_Volt_M_f32[0]	30.6930008
MtrVoltQax_Volt_M_f32[1]	0.219999999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.127000004
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.860000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.594299972
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.179000005
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	30983.1992 0.636799991
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	-717.299988
PlCurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	30983.1992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.636799991
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7975.79004
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3201.42993
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.020999997
k_MtrCtrlVirualResQax_Ohm_f32 k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.101999998
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	7.56930017
·	

PICurrCntrl_Per1





Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.6809006		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	3.04200006		
k_VoltSatQaxPolyCoeff_Uls_f32	14.8559999		
k_deadtimeVScale_Uls_f32	0.984000027		
t_CommOffsetTblX_Uls_u3p13[0]	1262		
t_CommOffsetTblX_Uls_u3p13[1]	5333		
t_CommOffsetTblY_Cnt_u16[0]	311		
t_CommOffsetTblY_Cnt_u16[1]	1141		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-124.758003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3668		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-96.3310013		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3668	3668	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.91999912	-4.92000008 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.00311908824	0.00311889988 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	9763	9763 ± 1.52588E-05	-
MtrCurrDaxPrevIntg_Volt_M_f32	7.56930017	7.56930017 ± 0.0625	-
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.16 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0370000005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.824000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024





Input Value
-1024
0.0700000003
0.0130000003
0.0970000029
0.0160000008
0.286000013
1.41499996
-730.362
-412.898987
14.4589996
-5.13000011
22.5750008
22.8969994
18.7189999
21.0790005
15.9169998
-69.0940018
161.973007
-152.050995
106.072998
-112.455002
20.0867996
-200.556
-98.4449997
106.072998
-112.455002
-0.430000007
-5.92700005
22.5750008
22.8969994
22.5750008
22.8969994
0.418000013
0.300000012
0.902100027
0.675000012
1118
-194.190002
8419.69043
0.634800017
1118
-194.190002
8419.69043
0.634800017
0
6857.12012
2799.87988
1
0
0.0289999992
0.188999996
0
0
18.2152004
-3.5
1
18.2434006
3.5
1
12.026
-23.2660007
0.999000013
4342
7724
1124
1178
0
1
1 0
1 0 136.341003
1 0

PICurrCntrl_Per1



Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3317	3317	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-154.578003	-154.578003 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.454669058	0.454690993 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.97426367	-4.97425985 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27332	27332 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-154.578003	-154.578003 ± 7.81E-03	✓
MtrCurrOavFinalRef Amn M f32[1]	-112.455002	-112 455002 + 7 81F-03	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.17 (Repeat Count = 1) Name	Input Value
	0
FastDataAccessBufIndex_Cnt_M_u16	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.057
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.15900004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.762000024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.120999999
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0909999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87699997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.648999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-603.161987
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-712.994019
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-11.3319998
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-5.40700006
MtrCtrl_Vecu_Volt_M_f32[0]	16.4099998
MtrCtrl Vecu Volt M f32[1]	18.7700005
MtrCurrDaxPrevIntg_Volt_M_f32	-24.1620007
MtrCurrDaxRef Amp M f32[0]	-132.813004
MtrCurrDaxRef Amp M f32[1]	-9.14299965
MtrCurrQaxCog Amp M f32	-51.1100006

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxFinalRef_Amp_M_f32[0] 205.820999 MtrCurrQaxFinalRef_Amp_M_f32[1] -206.792007 MtrCurrQaxPrevIntg_Volt_M_f32 13.3757 MtrCurrQaxRef_Amp_M_f32[0] 67.4899979 MtrCurrQaxRef_Amp_M_f32[1] 119.721001 MtrCurrQaxRpl_Amp_M_f32[0] 205.820999 MtrCurrQaxRpl_Amp_M_f32[1] -206.792007 MtrPosComputationDelay_Rad_M_f32[0] -3.62700009 MtrPosComputationDelay_Rad_M_f32[1] -5.73500013 MtrVoltDax_Volt_M_f32[0] -11 3319998 MtrVoltDax_Volt_M_f32[1] -5.40700006 MtrVoltQax_Volt_M_f32[0] -11 3319998 MtrVoltQax_Volt_M_f32[1] -5.40700006 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.423999995 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.395000011 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.712199986 $PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32$ 0.651000023 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -38.7999992 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ -194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 12079.9004 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.298200011 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -38.7999992 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 12079.9004 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.298200011 SlowDataAccessBufIndex_Cnt_M_u16 k CLOAFdbackSignalSclFacSlew UlspS f32 3678.44995 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7603.6001 k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k MtrCtrlVirualResDax Ohm f32 0.0439999998 k_MtrCtrlVirualResQax_Ohm_f32 0.166999996 k MtrCurrQaxRefModifDsb Cnt lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltDaxIntegLoLim_Volt_f32 -4.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 8.95559978 k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTblX_Uls_u3p13[0] 1516 $t_CommOffsetTblX_Uls_u3p13[1]$ 5882 t_CommOffsetTblY_Cnt_u16[0] 1813 t CommOffsetTblY_Cnt_u16[1] 183 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target MtrCntrl Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3803 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 45.3779984 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 3803 3803 MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -87.2210007 -87.2210007 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -2.68405461 -2.68405008 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.15912867 -4.1591301 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 915 915 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 ± 0.0625 -87.2210007 MtrCurrQaxFinalRef_Amp_M_f32[0] -87.2210007 ± 7.81E-03

-206.792007

MtrCurrQaxFinalRef_Amp_M_f32[1]

-206.792007 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
VtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
//www.driningread_rvineoamiguren_cong_ge(ptr) //wtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lqc Val
VtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ytr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl Read_MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
	165.242004
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0.0759999976
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.071999969
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.116999999
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.25100005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.801999986
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	0
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0489999987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.414000005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.66700006
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	158.016998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-944.586975
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.79500008
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.5049992
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-15.8149996
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-9.85200024
/trCtrl_Vecu_Volt_M_f32[0]	14.2779999
/trCtrl_Vecu_Volt_M_f32[1]	16.6380005
/trCurrDaxPrevIntg_Volt_M_f32	30.7700005
/trCurrDaxRef_Amp_M_f32[0]	-146.173996
/trCurrDaxRef_Amp_M_f32[1]	-213.335007
/ltrCurrQaxCog_Amp_M_f32	76.5339966
/trCurrQaxFinalRef_Amp_M_f32[0]	-69.0940018
/trCurrQaxFinalRef_Amp_M_f32[1]	161.973007
/trCurrQaxPrevIntg_Volt_M_f32	10.9584999
/trCurrQaxRef_Amp_M_f32[0]	37.4550018
/trCurrQaxRef_Amp_M_f32[1]	-2.84500003
/trCurrQaxRpl_Amp_M_f32[0]	-69.0940018
/trCurrQaxRpl_Amp_M_f32[1]	161.973007
/trPosComputationDelay Rad M f32[0]	4.77099991
VtrPosComputationDelay Rad M f32[1]	-3.22900009
VtrVoltDax Volt M f32[0]	-15.8149996
VtrVoltDax Volt M f32[1]	-9.85200024

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value Name MtrVoltQax_Volt_M_f32[0] -15.8149996 MtrVoltQax_Volt_M_f32[1] -9.85200024 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.347000003 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.470999986 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.676199973 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.307999998 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -657.099976 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ 20.7000008 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 47476.6016 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$ 0.333499998 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -657.099976 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 20 7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 47476.6016 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.333499998 SlowDataAccessBufIndex_Cnt_M_u16 2757.25 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7944.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ k_MtrCtrlVirualResDax_Ohm_f32 0.104999997 $k_MtrCtrlVirualResQax_Ohm_f32$ 0.0839999989 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ Λ k_MtrVoltDaxIntegHiLim_Volt_f32 6.46549988 -6.5999999 $k_MtrVoltDaxIntegLoLim_Volt_f32$ k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 1.87349999 k_MtrVoltQaxIntegLoLim_Volt_f32 -6.5999999 k MtrVoltVecuFiltEnable Cnt lgc k_VoltSatDaxPolyCoeff_Uls_f32 20.5340004 k VoltSatQaxPolyCoeff Uls f32 -22.2229996 k_deadtimeVScale_Uls_f32 0.952000022 t CommOffsetTblX Uls u3p13[0] 1188 t_CommOffsetTblX_Uls_u3p13[1] 7029 t_CommOffsetTblY_Cnt_u16[0] 422 t CommOffsetTblY Cnt u16[1] 1383 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 1.62199998 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4003 103.652 target MtrCntrl Read MtrCurrQax Amp f32 Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 4003 4003 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 -220 ± 7.81E-03 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 27.5049992 27 5049992 + 4 88F-04 -9.85200024 -9.85200024 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 51828 51828 + 1 52588F-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32 0 ± 0.0625 -69.0940018 -69.0940018 ± 7.81E-03 MtrCurrQaxFinalRef Amp M f32[0]

-220

-220 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
astDataAccessBufIndex_Cnt_M_u16	0
ItrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt lgc ptr
htrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc Val
htrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
ItrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
htrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
ItrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
ttrCntrl_Read_SysState_Cnt_Enum(Val)	
	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0759999976
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.098999995
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.46000008
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10699999
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-980.567993
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-630.098022
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0170000009
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.119999997
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87699997
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.648999989
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	764.937988
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-605.708008
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.3460007
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.8169994
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.7189999
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-12.0450001
trCtrl_Vecu_Volt_M_f32[0]	22.3540001
trCtrl_Vecu_Volt_M_f32[1]	24.7140007
trCurrDaxPrevIntg_Volt_M_f32	11.6429996
ltrCurrDaxRef_Amp_M_f32[0]	-91.4420013
trCurrDaxRef_Amp_M_f32[1]	133.692993
trCurrQaxCog_Amp_M_f32	-161.751999
ItrCurrQaxFinalRef Amp M f32[0]	-132.813004
ItrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965
ItrCurrQaxPrevIntg_Volt_M_f32	4.84670019
trCurrQaxRef Amp M f32[0]	94.3150024
trCurrQaxRef_Amp_M_f32[1]	37.4959984
trCurrQaxRpl Amp M f32[0]	-132.813004
trCurrQaxRpl Amp M f32[1]	-9.14299965
ItrPosComputationDelay_Rad_M_f32[0]	-0.989000022
ItrPosComputationDelay Rad M f32[1]	-5.421
htrVoltDax_Volt_M_f32[0]	-28.7189999
ItrVoltDax_Volt_M_f32[1]	-12.0450001

PICurrCntrl_Per1



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-28.7189999		
MtrVoltQax Volt M f32[1]	-12.0450001		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.768000007		
PICurrCntrl InverterFailSclFac Uls M f32	0.610000014		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.385100007		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.82099998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	3431.37012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.870999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	3431.37012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.870999992		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	3636.04004		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	902.40802		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lqc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.023		
k_MtrCtrlVirualResQax_Ohm_f32	0.158000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.1237001		
k MtrVoltDaxIntegLoLim Volt f32	-7.19999981		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	22.1184998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6590004		
k_VoltSatQaxPolyCoeff_Uls_f32	0.441000015		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTbIX_UIs_u3p13[0]	786		
t_CommOffsetTblX_Uls_u3p13[1]	5267		
t_CommOffsetTblY_Cnt_u16[0]	267		
t_CommOffsetTblY_Cnt_u16[1]	723		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-126.640999		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	1231		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	99.348999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	1231	1231	~
MtrCntrl Write Modldx Uls u16p16(val)	0	0 ± 1	✓
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.00361538	-4.00362015 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.97006512	2.97006989 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	45494	45494 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	13.1237001	13.1237001 ± 0.0625	
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	~
MtrCurrQaxFinalRef Amp M f32[1]	-9.14299965	-9.14299965 ± 7.81E-03	
INTO CONTRACT INCIDENTIAL TOPE [1]	-0.17200000	-0.1720000 17.01L*00	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

lame	Input Value
astDataAccessBufIndex_Cnt_M_u16	0
ItrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt Igc ptr
trCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc Val
trCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
trCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
ItrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-208.287994
trCtrl MtrCurrDaxMaxVal Amp M f32[1]	-27.9839993
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0869999975
trCtrl MtrDampTermDax_Ohm M f32[1]	0.00700000022
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.075000003
trCtrl_MtrDaxIntegralGain_Onm_M_f32[1]	1.62199998
trCtrl MtrDaxPropotionalGain Ohm M f32[0]	376.326996
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	721.965027
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.107000001
trCtrl_MtrImpedDax_Ohm_M_i32[1]	0.0109999999
trCtrl_MtrImpedDax_Ohm_M_i32[i]	0.0520000011
: :	0.00800000038
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.55800028
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.930000026
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	614.892029
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1012.16998
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	15.5469999
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-22.8390007
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
trCtrl_Vecu_Volt_M_f32[0]	14.2779999
trCtrl_Vecu_Volt_M_f32[1]	16.6380005
trCurrDaxPrevIntg_Volt_M_f32	27.9990005
ItrCurrDaxRef_Amp_M_f32[0]	106.072998
trCurrDaxRef_Amp_M_f32[1]	-112.455002
trCurrQaxCog_Amp_M_f32	131.306
trCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
trCurrQaxFinalRef_Amp_M_f32[1]	133.692993
trCurrQaxPrevIntg_Volt_M_f32	22.6445999
trCurrQaxRef_Amp_M_f32[0]	-108.124001
trCurrQaxRef_Amp_M_f32[1]	178.639008
trCurrQaxRpl_Amp_M_f32[0]	-91.4420013
trCurrQaxRpl_Amp_M_f32[1]	133.692993
trPosComputationDelay_Rad_M_f32[0]	-1.926
ItrPosComputationDelay_Rad_M_f32[1]	5.88500023
ftrVoltDax_Volt_M_f32[0]	-29.7110004
ltrVoltDax_Volt_M_f32[1]	3.61899996

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-29.7110004		
MtrVoltQax_Volt_M_f32[1]	3.61899996		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.247999996		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.602999985		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.106299996		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.317000002		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13842.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.916499972		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	0		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	13842.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.916499972		
	0.910499972		
SlowDataAccessBufIndex_Cnt_M_u16	4450.8501		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32			
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2508.87012		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc			
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.18999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.118000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	26.2252998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.19999981		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.4771004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.3699999		
k_VoltSatQaxPolyCoeff_Uls_f32	11.9610004		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTblX_Uls_u3p13[0]	1729		
t_CommOffsetTblX_Uls_u3p13[1]	3269		
t_CommOffsetTblY_Cnt_u16[0]	502		
t_CommOffsetTblY_Cnt_u16[1]	707		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	727		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	41.1769981		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	707	707	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64552	64552 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-147.988007	-147.988007 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	6.52048111	6.52048016 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-12.4609261	-12.4609003 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	7651	7651 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	
MtrCurrQaxFinalRef Amp M f32[0]	-147.988007	-147.988007 ± 7.81E-03	
MtrCurrOayEinalDef Amp M f32[1]	133 602007	133 602003 ± 7 81E 03	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

133.692993

133.692993 ± 7.81E-03



Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.460000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-229.300995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-277.625
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.119999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	764.937988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-605.708008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.7189999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-12.0450001
MtrCtrl_Vecu_Volt_M_f32[0]	20.2549992
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998
MtrCurrDaxPrevIntg_Volt_M_f32	11.6429996
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	-161.751999
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxPrevIntg_Volt_M_f32	6.56619978
MtrCurrQaxRef_Amp_M_f32[0]	94.3150024
MtrCurrQaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxRpl_Amp_M_f32[0]	-132.813004
MtrCurrQaxRpl_Amp_M_f32[1]	-9.14299965
MtrPosComputationDelay_Rad_M_f32[0]	4.80499983
MtrPosComputationDelay_Rad_M_f32[1]	-6.10300016
MtrVoltDax_Volt_M_f32[0]	-28.7189999
MtrVoltDax_Volt_M_f32[1]	-12.0450001
MtrVoltQax_Volt_M_f32[0]	-28.7189999
MtrVoltQax_Volt_M_f32[1]	-12.0450001
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.768000007
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.610000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.495900005
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.82099998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	46503.6992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.730000019
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46503.6992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.730000019
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3636.04004
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	902.40802
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.023
k_MtrCtrlVirualResQax_Ohm_f32	0.158000007
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0

PICurrCntrl_Per1





Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	29.5695		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.19999981		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.8902998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6590004		
k_VoltSatQaxPolyCoeff_Uls_f32	0.441000015		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	786		
t_CommOffsetTblX_Uls_u3p13[1]	5267		
t_CommOffsetTblY_Cnt_u16[0]	267		
t_CommOffsetTblY_Cnt_u16[1]	723		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1231		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	99.348999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1231	1231	✓
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.5012114	-1.50120997 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.75358677	4.75359011 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	46927	46927 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	29.5695	29.5695 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965	-9.14299965 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.22 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0759999976	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0989999995	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.460000008	





Name	
MitCri_MixDaxIntegralGain_Ohm_M_S2[1] 1.0698989 MitCri_MixDaxPropotionalGain_Ohm_M_S2[0] -980.56793 MitCri_MixDaxPropotionalGain_Ohm_M_S2[1] -980.068022 MitCri_MixDaxPropotionalGain_Ohm_M_S2[1] -0.0170000001 MitCri_MixDaxPropotionalGain_Ohm_M_S2[1] -0.0170000001 MitCri_MixDaxPropotionalGain_Ohm_M_S2[1] -0.0170000001 MitCri_MixDaxActinegralGain_Ohm_M_S2[1] -0.019000001 MitCri_MixDaxActinegralGain_Ohm_M_S2[0] -2.019000001 MitCri_MixDaxIntegralGain_Ohm_M_S2[0] -2.019000001 MitCri_MixDaxIntegralGain_Ohm_M_S2[0] -2.019000001 MitCri_MixDaxIntegralGain_Ohm_M_S2[0] -0.0570808 MitCri_MixDaxPropotionalGain_Ohm_M_S2[1] -0.0570808 MitCri_MixDaxPropotionalGain_Ohm_M_S2[1] -1.0190000001 -1.0190000001 -1.0190000001 -1.0190000001 -1.01900000001 -1.01900000001 -1.0190000000000000000000000000000000000	
Mr.Crit_MrDaxPropotionalCain_Ohm_M_E2[0] -980.65793 Mr.Crit_MrthrapedDax_Ohm_M_E2[0] -830.098022 Mr.Crit_MrthrapedDax_Ohm_M_E2[0] -800.098022 Mr.Crit_MrthrapedDax_Ohm_M_E2[0] -800.098022 Mr.Crit_MrthrapedDax_Ohm_M_E2[0] -800.0980020 Mr.Crit_MrthrapedDax_Ohm_M_E2[0] -800.0980000011 Mr.Crit_MrthrapedDax_Ohm_M_E2[0] -800.0980000011 Mr.Crit_MrthrapedDax_Ohm_M_E2[0] -800.098000001 -800.098000001 -800.098000001 -800.098000001 -800.09800001 -800.09800001 -800.098000001 -800.09800001 -800.09800001 -800.098000001 -800.09800001 -800.098000001 -800.098000001 -800.09800001 -800.0980000001 -800.098000001 -800.098000001 -800.098000001 -800.098000001 -800.098000001 -800.098000001 -800.098000001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.09800001 -800.0990001 -800.0990001 -800.0990001 -800.0990001 -800.09900001 -800.0990001 -800.0990001 -800.09900001 -800.09900001 -800.0990001 -8	
MitCirl MirDasPropotionalCaim_Ohm_M_52(1) -630.06802 MitCirl MirtingedDax_Ohm_M_52(1) 0.0170000009 MitCirl MirtingedDax_Ohm_M_52(1) 0.0170000001 MitCirl MirtingedDax_Ohm_M_52(1) 0.0350000011 MitCirl MirtingedDax_Ohm_M_52(1) 0.13999997 MitCirl MirtingedDax_Ohm_M_52(1) 0.13999997 MitCirl MirtingedDax_Ohm_M_52(1) 2 MitCirl MirtingedDax_Ohm_M_52(1) 2 2 MitCirl MirtingedDax_Ohm_M_52(1) 764.937988 MitCirl MirtingedDax_Ohm_M_52(1) 764.937988 MitCirl MirtingedDax_Ohm_M_52(1) 764.937988 MitCirl MirtingedDax_Ohm_M_52(1) 764.937988 MitCirl MirtingedDax_Fortum_52(1) 764.937988 MitCirl MirtingedDax_Fortum_52(1) 764.937988 MitCirl MirtingedDax_Fortum_52(1) 764.937988 MitCirl MirtingedDax_Fortum_52(1) 764.937988 764.937988 764.93798	
MitCit MirimpedDax, Ohm, M. 132 1 0.0410000011 MitCit MirimpedDax, Ohm, M. 132 1 0.041000011 MitCit MirimpedDax, Ohm, M. 132 0 0.050000001 MitCit MirimpedDax, Ohm, M. 132 0 0.11999997 MitCit MirimpedDax, Ohm, M. 132 0 2 MitCit MirimpedDax, Ohm, M. 132 1 605.708008 MitCit MirimpedDax, Ohm, M. 132 1 605.708008 MitCit MirimpedDax, Ohm, M. 132 1 1-78.8169994 MitCit MirimpedDax, F. Volt, M. 132 0 1-78.8169994 MitCit MirimpedDax, F. Volt, M. 132 0 1-78.8169994 MitCit MirimpedDax, F. Volt, M. 132 0 1-78.8169999 MitCit MirimpedDax, F. Volt, M. 132 0 1-78.8169999 MitCit MirimpedDax, F. Volt, M. 132 0 1-78.8169999 MitCit Vecu Volt, M. 132 1 1-78.8169999 MitCit MirimpedDax, F. Volt, M. 132 1 1-78.8169999 MitCit MirimpedDax, F. Volt, M. 132 1 1-78.816999 MirimpedDax, F. Volt, M. 132 1	
MitCit MirimpedDax Ohm M, 182(1) 0.0410000011 MitCit MirimpedDax, Ohm M, 182(1) 0.0350000001 MitCit MirimpedDax, Ohm M, 182(1) 0.11999997 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 2 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 2 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 805,780808 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 805,780808 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 178,8169994 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 178,8169994 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 178,8169994 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 13,085 MitCit M, MirdaxintegraGain, Ohm M, 182(1) 13,085 MitCit M, 182(1) 13,085 MitCit M, 182(1) 13,085 MitCit M, 182(1) 13,449996 MitCit M, 182(1) 13,449996 MitCurrDaxinering Amp M, 182(1) 13,62993 MitCurrDaxinering Amp M, 182(1) 133,662933 MitCurrDaxinering Amp M, 182(1) 132,813004 MitCurrDaxinering Amp M, 182(1) 13,2813004 MitCurrDaxinering Amp M, 182(1) 14,2813004	
MicCirl_MirtinpedCax_Ohm_M_132[1]	
MITCHI MitrinpedOax_Ohm_M_G2[1] 0.11999997 MITCHI MitrCarkinegraGain_Ohm_M_G2[1] 2 MITCHI MitrCarkinegraGain_Ohm_M_G2[1] 2 MITCHI MitrCarkinegraGain_Ohm_M_G2[1] 2 764.937988 MITCHI_MITCAREPropotionalGain_Ohm_M_G2[1] 764.937988 MITCHI_MITCHINEGRAFTOpotionalGain_Ohm_M_G2[1] 765.937988 MITCHI_MITCHINEGRAFT_Volt_M_G2[0] 22.3460007 775.937988 MITCHI_MITCHINEGRAFT_Volt_M_G2[0] 22.3460007 775.939999 MITCHI_MITVOIDAXFF_Volt_M_G2[1] 1.72.9450001 1.72.945001 1.72.9450001 1.72.945001 1.72.9450001 1.72.945001 1.72.9450001	
MiCitr_MiCaxintegraGain_Ohm_M_f32[0] 2 2 MiCitr_MiCaxintegraGain_Ohm_M_f32[0] 7e4.937988 MiCitr_MiCaxintegraGain_Ohm_M_f32[0] 7e4.937988 MiCitr_MiCaxintegraGain_Ohm_M_f32[0] 7e4.937988 MiCitr_MiCaxintegraGain_Ohm_M_f32[1] -06.708008 MiCitr_Mici	
MiCitr_MiCaxintegraGain_Ohm_M_f32[0] 2 2 MiCitr_MiCaxintegraGain_Ohm_M_f32[0] 7e4.937988 MiCitr_MiCaxintegraGain_Ohm_M_f32[0] 7e4.937988 MiCitr_MiCaxintegraGain_Ohm_M_f32[0] 7e4.937988 MiCitr_MiCaxintegraGain_Ohm_M_f32[1] -06.708008 MiCitr_Mici	
MirClrf_MirCashropotonalGain_Ohm_M_132[1] 2 764.937988 MirClrf_MirCashropotonalGain_Ohm_M_152[1] 764.937988 MirClrf_MirCashropotonalGain_Ohm_M_152[1] 805.78808 MirClrf_MirCashropotonalGain_Ohm_M_152[1] 805.78808 MirClrf_MirClcdsxFF_Volt_M_132[1] 1.78.188994 MirClrf_MirVoltDaxFF_Volt_M_152[1] 1.78.18999 MirClrf_MirVoltDaxFF_Volt_M_152[1] 1.78.18999 MirClrf_MirVoltDaxFF_Volt_M_152[1] 1.20.450001 1.00.85 MirClrf_MirVoltDaxFF_Volt_M_152[1] 1.20.450001 1.00.85 MirClrf_MirVoltDaxFF_Volt_M_152[1] 1.54.489997 MirClrf_MirVoltDaxFF_Volt_M_152[1] 1.54.489997 MirClrd_MirVoltDaxFP_volt_M_152[1] 1.54.489997 MirClrd_MirVoltDaxFP_volt_M_152[1] 1.54.49999 MirClrd_MirClrd_MirVoltDaxFP_volt_M_152[1] 1.54.49999 MirClrd_MirClrd_MirVoltDaxFP_volt_M_152[1] 1.54.49999 MirClrd_MirClrd_MirVoltDaxFP_volt_M_152[1] 1.54.29998 MirClrd_MirClrd_MirVoltDaxFP_volt_M_152[1] 1.54.29998 MirClrd_MirClrd_MirVolt_M_152[1] 1.54.29998 MirClrd_MirClrd_MirVolt_M_152[1] 1.54.29998 MirClrd_MirClrd_MirVolt_M_152[1] 1.54.29998 MirClrd_MirVolt_M_152[1] 1.54.29998 MirClrd_MirVolt_M_152[1] 1.54.29998 MirClrd_MirVolt_M_152[1] 1.54.29998 MirClrd_MirVolt_M_152[1] 1.54.29998 MirClrd_MirVolt_M_152[1] 1.54.29998 MirClrd_MirVolt_M_152[1] 1.54.29998 MirVolt_MirVolt_M_152[1] 1.54.29999 1.52.299998 MirVolt_MirVolt_M_152[1] 1.54.29999 1.52.299999 MirVolt_M_152[1] 1.54.29999 1.54.29999 MirVolt_M_152[1] 1.54.29999 1.54.29999 MirVolt_M_152[1] 1.54.29999 MirVolt_M_152[1] 1.52.49999 MirVolt_M_152	
MITCIT_MITQAXPropotionalGain_Ohm_M_F32(1) 764.937088 MITCIT_MIVOIDBAFF_Voit_M_F32(1) -22.3460007 MITCIT_MIVOIDBAFF_Voit_M_F32(1) -17.8169994 MITCIT_MIVOIDBAFF_Voit_M_F32(1) -12.8469999 MITCIT_MIVOIDBAFF_Voit_M_F32(1) -12.0450001 MITCIT_WooL_Voit_M_F32(0) 13.085 MITCIT_VeoL_Voit_M_F32(1) 15.4449997 MITCIT_DBAFF_Voit_M_F32(0) 13.085 MITCUTDBAFF_AMD_M_F32(1) 133.692993 MITCUTDBAFF_AMD_M_F32(1) 133.692993 MITCUTDBAFF_AMD_M_F32(1) 133.692993 MITCUTDBAFF_AMD_M_F32(1) 133.692993 MITCUTDBAFF_AMD_M_F32(1) 133.692993 MITCUTDBAFF_AMD_M_F32(1) -9.14299865 MITCUTDBAFF_AMD_M_F32(1) -9.14299965 MITCUTDBAFF_AMD_M_F32(1) -9.14299965 MITCUTDBAFF_AMD_M_F32(1) -9.14299965 MITCUTDBAFF_AMD_M_F32(1) -132.813004 MITCUTDBAFF_AMD_M_F32(1) -132.813004 MITCUTDBAFF_AMD_M_F32(1) -132.813004 MITCUTDBAFF_AMD_M_F32(1) -132.813004 MITCUTDBAFF_AMD_M_F32(1) -9.4299965 MITCUTDBAFF_	
MitCit_MixQaxPropolionalGain_Ohm_M_[32[1] -805.708008 MitCit_MixVoliDaxFE_Voll_M_[32[0] -22.3460007 MitCit_MixVoltDaxFE_Voll_M_[32[1] -17.8169994 MitCit_MixVoltDaxFE_Voll_M_[32[0] -28.7189999 MitCit_MixVoltQaxFE_Voll_M_[32[0] -12.0450001 MitCit_Vecu_Volt_M_[32[0] 13.085 MitCit_Vecu_Volt_M_[32[0] 15.4449997 MitCurDaxFerviniq_Volt_M_[32[0] -91.4420013 MitCurDaxRef_Amp_M_[32[0] -91.4420013 MitCurDaxRef_Amp_M_[32[0] -91.4420013 MitCurDaxRef_Amp_M_[32[0] -91.4420013 MitCurDaxFer_Amp_M_[32[0] -91.420013 MitCurDaxFer_Amp_M_[32[0] -91.420013 MitCurDaxFer_Amp_M_[32[0] -91.4299965 MitCurDaxFer_Amp_M_[32[0] -91.4299965 MitCurDaxRef_Amp_M_[32[0] -91.4299965 MitCurDaxRef_Amp_M_[32[0] -91.4299965 MitCurDaxRef_Amp_M_[32[0] -93.2813004 MitCurDaxRef_Amp_M_[32[0] -93.2813004 MitCurDaxRef_Amp_M_[32[0] -93.2813004 MitCurDaxRef_May_M_[32[0] -93.2813004 MitCurDaxRef_May_M_[32[0] -93.2813004 </td <td></td>	
MtrCtrl_MtrVottDaxFE_Vott_M_f32(1) -22.3460007 MtrCtrl_MtrVottDaxFE_Vott_M_f32(1) -17.8169994 MtrCtrl_MtrVottDaxFE_Vott_M_f32(1) -12.0450001 MtrCtrl_MtrVottDaxFE_Vott_M_f32(1) -12.0450001 MtrCtrl_Veou_Vott_M_f32(1) 15.4449997 MtrCtrl_DaxFervintg_Vott_M_f32(1) 15.4449997 MtrCurrDaxRef_Amp_M_f32(1) -91.4420013 MtrCurrDaxRef_Amp_M_f32(1) 133.692993 MtrCurrDaxRef_Amp_M_f32(1) -132.613004 MtrCurrDaxFeranp_M_f32(1) -132.613004 MtrCurrQaxFinalRef_Amp_M_f32(1) -91.4299965 MtrCurrQaxFeranter_Amp_M_f32(1) -91.4299965 MtrCurrQaxFeranter_Amp_M_f32(1) -91.4299965 MtrCurrQaxRef_Amp_M_f32(1) -92.4299966 MtrCurrQaxRef_Amp_M_f32(1) <	
MITCH_MITVOIDARFE_Voit_M_[52[1]	
MITCIT_MITVOIDQAYEF_Volt_M_E32[1] -22.455001 MITCIT_Vecu_Volt_M_52[1] -12.045001 MITCIT_Vecu_Volt_M_52[1] 15.085 MITCIT_Vecu_Volt_M_52[1] 15.4449997 MITCUTDAXPREVINIQ_Volt_M_52[0] -91.4420013 MITCUTDAXPEV_MID_VOLT_M_52[0] -91.4420013 MITCUTDAXPEV_AMP_M_52[0] -91.4420013 MITCUTDAXPEV_AMP_M_52[0] -132.813004 MITCUTDAXPEV_AMP_M_52[0] -132.813004 MITCUTDAXPREVINIQ_Volt_M_52 8.98110008 MITCUTDAXPEV_AMP_M_52[0] 94.3150024 MITCUTDAXPEV_AMP_M_52[0] 94.3150024 MITCUTDAXPEV_AMP_M_52[0] 94.3150024 MITCUTDAXPEV_AMP_M_52[0] -132.813004 MITCUTDAXPEV_AMP_M_52[0] -132.813004 MITCUTDAXPEV_AMP_M_52[0] -132.813004 MITCUTDAXPEV_AMP_M_52[0] -132.813004 MITCUTDAXPEV_AMP_M_52[0] -132.813004 MITCUTDAXPEV_AMP_M_52[0] -9.989000022 MITCUTDAXPEV_AMP_M_52[0] -9.889000022 MITCUTDAXPEV_AMP_M_52[0] -9.889000022 MITCUTDAXPEV_AMP_M_52[0] -9.889000022 MITCUTDAXPEV_AMP_M_52[0] <td></td>	
MitCit MitVoltQaxFF_Volt_M_f32[1] -12.0450001 MitCit Veou_Volt_M_f32[1] 13.085 MitCit Veou_Volt_M_f32[1] 15.4449997 MitCurrDaxPrevintg_Volt_M_f32 11.6429966 MitCurrDaxRef_Amp_M_f32[1] 31.802993 MitCurrDaxRef_Amp_M_f32[1] 133.802993 MitCurrQaxRef_Amp_M_f32[1] -161.751999 MitCurrQaxFrevintg_Volt_M_f32 -161.751999 MitCurrQaxFrevintg_Volt_M_f32 8.98110008 MitCurrQaxFrevintg_Volt_M_f32 8.98110008 MitCurrQaxFrevintg_Volt_M_f32 8.98110008 MitCurrQaxFrevintg_Volt_M_f32 8.98110008 MitCurrQaxFrevintg_Volt_M_f32[1] 9.1350024 MitCurrQaxFrevintg_Amp_M_f32[1] 37.4959984 MitCurrQaxFrevintg_Amp_M_f32[1] -12.213004 MitCurrQaxFrevintg_Amp_M_f32[1] -12.249965 MitPosComputationDelay, Rad_M_f32[0] -9.989000022 MitPosComputationDelay, Rad_M_f32[1] -5.421 MitVoltDax_Volt_M_f32[0] -28.718999 MitVoltDax_Volt_M_f32[0] -28.718999 MitVoltDax_Volt_M_f32[1] -12.0450001 MitVoltDax_Volt_M_f32[1] -12.0450001	
MtrCtrl_Vecu_Voit_M_522[0] 13.085 MtrCtrl_Vecu_Voit_M_522[1] 15.4449997 MtrCurrDaxRef_Amp_M_522[0] 91.4429013 MtrCurrDaxRef_Amp_M_52[1] 13.692993 MtrCurrDaxRef_Amp_M_52[1] 133.692993 MtrCurrQaxFinalRef_Amp_M_52[0] -161.751999 MtrCurrQaxFinalRef_Amp_M_52[0] -132.813004 MtrCurrQaxFinalRef_Amp_M_52[0] -9.1429965 MtrCurrQaxFinalRef_Amp_M_52[0] 9.3150024 MtrCurrQaxRef_Amp_M_52[0] 9.3150024 MtrCurrQaxRef_Amp_M_52[0] 9.3150024 MtrCurrQaxRef_Amp_M_52[0] 132.813004 MtrCurrQaxRef_Amp_M_52[0] 19.2429965 MtrCurrQaxRef_Amp_M_52[0] 19.3429966 MtrCurrQaxRef_Amp_M_52[0] 19.3429966 MtrCurrQaxRef_Amp_M_52[0] 19.8990002 MtrPosComputationDelay_Rad_M_52[0] 19.899060 MtrVoitDax_Voit_M_52[0] 28.718999 MtrVoitDax_Voit_M_52[1] 1.20450001 MtrVoitDax_Voit_M_52[1] 1.20450001 MtrVoitDax_Voit_M_52[1] 1.20450001 MtrVoitDax_Voit_M_52[1] 1.20450001 MtrCurrCurrAssaffirexRatio_Ul	
MtrCtrl_Vecu_Voit_M_r32[1] 15.4449997 MtrCurrDaxPrevintg_Voit_M_r32 11.6429986 MtrCurrDaxRef_Amp_M_r32[1] 13.692993 MtrCurrDaxRef_Amp_M_r32[1] 133.692993 MtrCurrQaxFinalRef_Amp_M_r32[1] 1-16.751999 MtrCurrQaxFinalRef_Amp_M_r32[1] 9.14299965 MtrCurrQaxFinalRef_Amp_M_r32[1] 9.14299965 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 9.14299965 MtrCurrQaxRef_Amp_M_r32[1] 9.14299965 MtrCurrQaxRef_Amp_M_r32[1] 9.14299965 MtrVorComputationDelay_Rad_M_r32[0] 9.989000022 MtrPosComputationDelay_Rad_M_r32[0] 9.989000022 MtrVollQax_Voit_M_r32[1] 12.0450001 MtrVollQax_Voit_M_r32[1] 12.0450001 MtrVollQax_Voit_M_r32[1] 12.0450001 MtrVollQax_Voit_M_r32[1] 12.0450001 MtrVollQax_Voit_M_r32[1] 12.0450001	
MtrCtrl_Vecu_Voit_M_r32[1] 15.4449997 MtrCurrDaxPrevintg_Voit_M_r32 11.6429986 MtrCurrDaxRef_Amp_M_r32[1] 13.692993 MtrCurrDaxRef_Amp_M_r32[1] 133.692993 MtrCurrQaxFinalRef_Amp_M_r32[1] 1-16.751999 MtrCurrQaxFinalRef_Amp_M_r32[1] 9.14299965 MtrCurrQaxFinalRef_Amp_M_r32[1] 9.14299965 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 37.4959984 MtrCurrQaxRef_Amp_M_r32[1] 9.14299965 MtrCurrQaxRef_Amp_M_r32[1] 9.14299965 MtrCurrQaxRef_Amp_M_r32[1] 9.14299965 MtrVorComputationDelay_Rad_M_r32[0] 9.989000022 MtrPosComputationDelay_Rad_M_r32[0] 9.989000022 MtrVollQax_Voit_M_r32[1] 12.0450001 MtrVollQax_Voit_M_r32[1] 12.0450001 MtrVollQax_Voit_M_r32[1] 12.0450001 MtrVollQax_Voit_M_r32[1] 12.0450001 MtrVollQax_Voit_M_r32[1] 12.0450001	
MtrCurrDaxPrevintg, Volt, M_r32 11.6429996 MtrCurrDaxRef, Amp, M_r32(1) -91.4420013 MtrCurrDaxRef, Amp, M_r32(1) 133.69293 MtrCurrQaxCog_Amp, M_r32(1) -161.751999 MtrCurrQaxFinalRef, Amp, M_r32(0) -132.813004 MtrCurrQaxFinalRef, Amp, M_r32(1) -91.4299965 MtrCurrQaxRef, Amp, M_r32(1) -91.4299965 MtrCurrQaxRef, Amp, M_r32(1) -91.4299965 MtrCurrQaxRef, Amp, M_r32(1) -91.4299966 MtrOurCaxRef, Lamp, M_r32(1) -91.4299966 MtrDurrCaxRef, Lamp, M_r32(1) -91.4299966 MtrVoltQax, Volt, M_r32(1) -91.4299966 MtrVoltQax, Volt, M_r32(1) -12.0450001 MtrVoltQax, Volt, M_r32(1) -12.0450001 PlCurrCntr, MtrCurrCaxSatFluxRatio_Uls, M_r32	
MtrCurrDaxRef_Amp_M_[32[0] -91.4420013 MtrCurrDaxRef_Amp_M_[32[1] 133.692993 MtrCurrQaxCog_Amp_M_[32[0] -161.751999 MtrCurrQaxFinalRef_Amp_M_[32[0] -132.813004 MtrCurrQaxFinalRef_Amp_M_[32[1] -9.14299965 MtrCurrQaxRef_Amp_M_[32[0] 9.3150024 MtrCurrQaxRef_Amp_M_[32[0] 9.3150024 MtrCurrQaxRef_Amp_M_[32[1] 37.4959984 MtrCurrQaxRef_Amp_M_[32[1] -9.14299965 MtrCurrQaxRef_Amp_M_[32[1] -9.14299965 MtrCurrQaxRef_Amp_M_[32[1] -9.14299965 MtrPosComputationDelay_Rad_M_[32[0] -9.989000022 MtrPosComputationDelay_Rad_M_[32[0] -9.989000022 MtrVollQax_Volt_M_[32[1] -5.421 MtrVollQax_Volt_M_[32[1] -12.0450001 MtrVollQax_Volt_M_[32[1] -12.0450001 MtrVollQax_Volt_M_[32[1] -12.0450001 MtrVollQax_Volt_M_[32[1] -12.0450001 PlCurrCntt_J_MrCurrDaxSatFluxRatio_Uis_M_[32] 0.7235997 PlCurrCntt_MrCurrDaxSatFluxRatio_Uis_M_[32] 0.7235999 PlCurrCntt_MrCurrDaxSatFluxRatio_Uis_M_[32] 0.2030999 PlCurrCntt_MrCurrDaxSatFluxRatio_Ui	
MtrCurrDaxRef_Amp_M_[32] 133.692993 MtrCurrQaxCog_Amp_M_[32] 1-161.751999 MtrCurrQaxFinalRef_Amp_M_[32[1] 9.14299965 MtrCurrQaxFinalRef_Amp_M_[32[1] 9.14299965 MtrCurrQaxRef_Amp_M_[32[0] 94.3150024 MtrCurrQaxRef_Amp_M_[32[1] 37.4959984 MtrCurrQaxRef_Amp_M_[32[1] 37.4959984 MtrCurrQaxRp_Amp_M_[32[1] 9.14299965 MtrCurrQaxRp_Amp_M_[32[1] 9.14299965 MtrCurrQaxRp_Amp_M_[32[1] 9.14299965 MtrPosComputationDelay_Rad_M_[32[0] 0.989000022 MtrPosComputationDelay_Rad_M_[32[1] 5.421 MtrVoltDax_Volt_M_[32[0] 28.7189999 MtrVoltDax_Volt_M_[32[1] 1.20450001 MtrVoltDax_Volt_M_[32[1] 1.20450001 MtrVoltQax_Volt_M_[32[1] 1.20450001 MtrVolt_M_[32[1]	
MtrCurrQaxCog_Amp_M_f32 -161.751999 MtrCurrQaxFinalRef_Amp_M_f32(0) -132.813004 MtrCurrQaxFinalRef_Amp_M_f32(1) -9.14299965 MtrCurrQaxRereIvntg_Volt_M_f32 8.98110008 MtrCurrQaxRef_Amp_M_f32(0) 94.3150024 MtrCurrQaxRef_Amp_M_f32(1) 37.4959984 MtrCurrQaxRef_Amp_M_f32(1) -132.813004 MtrCurrQaxRpl_Amp_M_f32(1) -132.813004 MtrCurrQaxRpl_Amp_M_f32(1) -132.813004 MtrPosComputationDelay_Rad_M_f32(0) -9.989000022 MtrPosComputationDelay_Rad_M_f32(1) -5.421 MtrVolIDax_Volt_M_f32(1) -1.2 0450001 MtrVolIDax_Volt_M_f32(2) -28.7189999 MtrVolIDax_Volt_M_f32(2) -0.610000014 PlCurrCntt_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.610000007	
MtrCurrQaxFinalRef_Amp_M_f32[0] -132.813004 MtrCurrQaxFrevIngl Volt_M_f32 9-14299965 MtrCurrQaxPrevIntg_Volt_M_f32[0] 9-14329965 MtrCurrQaxRef_Amp_M_f32[0] 94.3150024 MtrCurrQaxRef_Amp_M_f32[0] 94.3150024 MtrCurrQaxRef_Amp_M_f32[1] 37.4959984 MtrCurrQaxRef_Amp_M_f32[1] -9-14299965 MtrCurrQaxRef_Amp_M_f32[1] -9-14299965 MtrCurrQaxRef_Amp_M_f32[1] -9-14299965 MtrPosComputationDelay_Rad_M_f32[0] -0.989000022 MtrPosComputationDelay_Rad_M_f32[0] -0.989000022 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 PICurrCntt_CurrSensFailSclFac_Uls_M_f32 0.76800007 PICurrCntt_MtrVcurrSensFailSclFac_Uls_M_f32 0.7235999 PICurrCntt_MtrVcurrDaxSatFluxRatio_Uls_M_f32 0.7235997 PICurrCntt_MtrVcurrDaxSatFluxRatio_Uls_M_f32 0.7235997 PICurrCntt_MtrVcurrDaxSatFluxRatio_Uls_M_f32 0.82099988 PICurrCntt_MtrVcurrIt_M_str.Previnput_Uls_f32 43.1699982 <td></td>	
MtrCurrQaxFinalRef_Amp_M_f32[1] -9.14299965 MtrCurrQaxRevIntg_Volt_M_f32 8.88110008 MtrCurrQaxRef_Amp_M_f32[0] 94.3150024 MtrCurrQaxRef_Amp_M_f32[1] 37.4959984 MtrCurrQaxRpl_Amp_M_f32[1] -132.813004 MtrCurrQaxRpl_Amp_M_f32[1] -9.14299965 MtrPosComputationDelay_Rad_M_f32[0] -0.989000022 MtrPosComputationDelay_Rad_M_f32[1] -5.421 MtrVolIDax_Volt_M_f32[0] -28.7189999 MtrVolIDax_Volt_M_f32[1] -12.0450001 MtrVolIDax_Volt_M_f32[1] -12.0450001 MtrVolIDax_Volt_M_f32[1] -12.0450001 MtrVolIDax_Volt_M_f32[1] -12.0450001 MtrVolIDax_Volt_M_f32[1] -12.0450001 MtrVolIDax_Volt_M_f32[1] -12.0450001 PlCurrCntrl_MtrCurrCensPailSclFac_Uls_M_f32 0.768000007 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.72359999 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.82099998 PlCurrCntrl_MtrVecuFilt_M_str_TermD_Uls_f32 0.82099998 PlCurrCntrl_MtrVecuFilt_M_str_TermD_Uls_f32 43.1699982 PlCurrCntrl_MtrVecuFilt_M_str_TermD_Uls_f32 43.1699982	
MtrCurrQaxPervIntg_Vol_M_[32] 8.98110008 MtrCurrQaxRef_Amp_M_[32]0] 94.3150024 MtrCurrQaxRef_Amp_M_[32]1] 37.4959984 MtrCurrQaxRp_Amp_M_[32]0] -132.813004 MtrCurrQaxRp_Amp_M_[32]1] -9.14299965 MtrPosComputationDelay_Rad_M_[32]0] -0.989000022 MtrPosComputationDelay_Rad_M_[32]1] -5.421 MtrVoltDax_Volt_M_[32]0] -28.7189999 MtrVoltDax_Volt_M_[32]0] -28.7189999 MtrVoltQax_Volt_M_[32]1] -12.0450001 MtrVoltQax_Volt_M_[32]1] -12.0450001 PICurrCntr_Cntr_CarrSensFailSciFac_Uis_M_[32] 0.768000007 PICurrCntr_Cntr_CarrSensFailSciFac_Uis_M_[32] 0.768000007 PICurrCntr_MtrCurrDaxSatFluxRatio_Uis_M_[32] 0.72359997 PICurrCntr_Mtr_MtrVecuFiit_M_str_Prevolupt_Uis_[32] -0.9209998 PICurrCntr_MtrVecuFiit_M_str_Prevolupt_Uis_[32] -43.1699982 PICurrCntr_MtrVecuFiit_M_str_TermD_Uis_[32] 0.316399992 PICurrCntr_MtrVolQaxFFFiit_M_str_Prevolupt_Uis_[32] -43.1699982 PICurrCntr_MtrVolQaxFFFiit_M_str_TermD_Uis_[32] 0.316399992 PICurrCntr_MtrVolQaxFFFiit_M_str_TermD_Uis_[32] 0.316399992	
MtrCurrQaxRef_Amp_M_f32[0] 94.3150024 MtrCurrQaxRef_Amp_M_f32[1] 37.4959984 MtrCurrQaxRef_Amp_M_f32[0] -132.813004 MtrCurrQaxRpl_Amp_M_f32[1] -9.14299965 MtrPosComputationDelay_Rad_M_f32[0] -0.989000022 MtrPosComputationDelay_Rad_M_f32[0] -0.989000022 MtrVoltDax_Volt_M_f32[1] -5.421 MtrVoltDax_Volt_M_f32[1] -12.0450001 MtrVoltDax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 PlCurrCntrL_MtrSepsFailSclFac_Uls_M_f32 0.768000007 PlCurrCntrL_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.72359997 PlCurrCntrL_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.82099998 PlCurrCntrL_MtrVocuFilt_M_str.Prevolupt_Uls_f32 43.1699982 PlCurrCntrL_MtrVocuFilt_M_str.PrevUlput_Uls_f32 43.1699982 PlCurrCntrL_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 43.1699982 PlCurrCntrL_MtrVoltQaxFFFilt_M_str.PrevD_Uls_f32 43.1699982 PlCurrCntrL_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399992 SlowDataAccessBufladex_Cnt	
MtrCurrQaxRef_Amp_M_[32[0] 94.3150024 MtrCurrQaxRef_Amp_M_[32[1] 37.4959984 MtrCurrQaxRef_Amp_M_[32[1] -132.813004 MtrCurrQaxRpl_Amp_M_[32[1] -914299965 MtrPosComputationDelay_Rad_M_[32[1] -9.41299965 MtrPosComputationDelay_Rad_M_[32[1] -5.421 MtrVoltDax_Volt_M_[52[1] -12.0450001 MtrVoltDax_Volt_M_[52[1] -12.0450001 MtrVoltQax_Volt_M_[52[1] -12.0450001 MtrCurrContrl_MtrVoluc_Mill_M_[52] -0.82359999 MtrCurrContrl_MtrVoluc_Mill_M_[52] -43.1699982 MtrCurrContrl_MtrVoluc_Mill_M_[51] -43.1699982	
MtrCurrQaxRef_Amp_M_f32[1] 37.4959984 MtrCurrQaxRpL_Amp_M_f32[0] -132.813004 MtrCurrQaxRpL_Amp_M_f32[1] -9.14299965 MtrPosComputationDelay_Rad_M_f32[0] -9.889000022 MtrPosComputationDelay_Rad_M_f32[1] -5.421 MtrVoltDax_Volt_M_f32[0] -28.7189999 MtrVoltDax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrCurrCort_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrCurrCort_M_f32[1] -12.0450001 MtrCurrCort_M_f32[1] -12.0450001 MtrCurrCort_M_f32[1] -12.0450001 MtrCurrCort_M_f32[1] -12.0450001 MtrCurrCort_M_f12[1] -12.0450001 MtrCurrOrt_M_f12[1] -12.0450001 MtrCurrOrt_M_f12[1] -12.0450001<	
MtrCurrQaxRpl_Amp_M_f32[0] -132.813004 MtrCurrQaxRpl_Amp_M_f32[1] -9.14299965 MtrPosComputationDelay_Rad_M_f32[0] -0.989000022 MtrPosComputationDelay_Rad_M_f32[1] -5.421 MtrVoltDax_Volt_M_f32[0] -28.7189999 MtrVoltDax_Volt_M_f32[0] -12.0450001 MtrVoltQax_Volt_M_f32[0] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.768000007 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.610000014 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.72359997 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.82099998 PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42.1699982 PICurrCntrl_MtrVelotQaxFFFilt_M_str.TermN_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 <t< td=""><td></td></t<>	
MtrCurrQaxRp_Amp_M_f32[1] -9.14299965 MtrPosComputationDelay_Rad_M_f32[0] -0.889000022 MtrPosComputationDelay_Rad_M_f32[1] -5.421 MtrVoltDax_Volt_M_f32[0] -28.718999 MtrVoltDax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.768000007 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.610000014 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.72359997 PICurrCntrl_MtrVecuFilt_M_str.Prevlout_Uls_f32 0.82099998 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 43.1699982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399992 SlowDataAccessBufindex_Ont_M_	
MtrPosComputationDelay_Rad_M_f32[0] -0.989000022 MtrPosComputationDelay_Rad_M_f32[1] -5.421 MtrVoltDax_Volt_M_f32[0] -28.7189999 MtrVoltDax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 PICurrCntr_CurSensFailSclFac_Uls_M_f32 0.768000007 PICurrCntr_I_MtrCurDaxSatFluxRatio_Uls_M_f32 0.72359997 PICurrCntrI_MtrCurQaxSatFluxRatio_Uls_M_f32 0.82099998 PICurrCntrI_MtrVecuFilt_M_str.Previnput_Uls_f32 0.82099998 PICurrCntrI_MtrVecuFilt_M_str.Previnput_Uls_f32 43.1699982 PICurrCntrI_MtrVecuFilt_M_str.PrevOutput_Uls_f32 43.1699982 PICurrCntrI_MtrVecuFilt_M_str.PrevOutput_Uls_f32 42029.6016 PICurrCntrI_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 43.1699982 PICurrCntrI_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 43.1699982 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 42029.6016 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 40.316399992 Slow	
MtrPosComputationDelay_Rad_M_[32[1] -5.421 MtrVoltDax_Volt_M_[32[0] -28.7189999 MtrVoltDax_Volt_M_[32[0] -28.7189999 MtrVoltDax_Volt_M_[32[0] -28.7189999 MtrVoltQax_Volt_M_[32[0] -28.7189999 MtrVoltQax_Volt_M_[32[1] -12.0450001 PICurrCntrl_CurrSensFailSclFac_Uls_M_[32] 0.768000007 PICurrCntrl_InverterFailSclFac_Uls_M_[32] 0.72359997 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.82099998 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_[32] -0.82099998 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_[32] -43.1699982 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_[32] 420.29.6016 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uls_[32] -43.1699982 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[32] -43.1699982 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[32] 43.1699982 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[32] -43.1699982 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[32] -43.1699982 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[32] 0.31639992 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[32] 0.31639992 PICurrCntr	
MtrVoltDax_Volt_M_f32[0] -28.7189999 MtrVoltDax_Volt_M_f32[1] -12.0450001 MtrVoltDax_Volt_M_f32[1] -12.0450001 MtrVoltQax_Volt_M_f32[1] -12.0450001 PICurrCntrd_CurrSensFailSolFac_Uls_M_f32 0.768000007 PICurrCntrl_InverterFailSolFac_Uls_M_f32 0.610000014 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.82099998 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 42.029.6016 PICurrCntrl_MtrVotQaxFFFiit_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVotIQaxFFFiit_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVotQaxFFFiit_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVotQaxFFFiit_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVotQaxFFFiit_M_str.TermD_Uls_f32 42029.6016 PICurrCntrl_MtrVotQaxFFFiit_M_str.TermD_Uls_f32 0.316399992 SlowDataAccessBufindex_Cnt_M_ufe 0 K_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 90.240802 k_MtrCritVirualResDa	
MtrVoltDax_Volt_M_[32[1] -12.0450001 MtrVoltQax_Volt_M_[32[1] -28.7189999 MtrVoltQax_Volt_M_[32[1] -12.0450001 PiCurrCntrl_CurrSensFailSclFac_Uls_M_[32] 0.768000007 PiCurrCntrl_InverterFailSclFac_Uls_M_[32] 0.610000014 PiCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.72359997 PiCurrCntrl_MtrVecuFiit_M_str.Pervinput_Uls_[32] 0.82099998 PiCurrCntrl_MtrVecuFiit_M_str.Pervinput_Uls_[32] -43.1699982 PiCurrCntrl_MtrVecuFiit_M_str.Pervinput_Uls_[32] 43.1699982 PiCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_[32] 0.316399992 PiCurrCntrl_MtrVoltQaxFFFiit_M_str.Pervinput_Uls_[32] 0.316399992 PiCurrCntrl_MtrVoltQaxFFFiit_M_str.Pervinput_Uls_[32] 43.1699982 PiCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[32] 43.1699982 PiCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[32] 0.316399992 SlowDataAccessBuflindex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_[32] 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_[32] 3636.04004 k_MtrCtrlOurLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_[32] 0.023 k_MtrCtrlVirualResQax_Ohm_[32	
MtrVoltQax_Volt_M_[32[0] -28.7189999 MtrVoltQax_Volt_M_[32[1] -12.0450001 PICurrCntrl_CurrSensFailSclFac_Uls_M_[32] 0.768000007 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.810000014 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_[32] 0.82099998 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_[32] 43.169982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_[32] 43.169982 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 42.029.6016 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 0.31639992 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 0.31639992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_[32] 43.169982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 43.169982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 4203.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 0.31639992 SlowDataAccessBufindex_Cnt_M_str.TermD_Uls_[32] 0.31639992 SlowDataAccessBufindex_Cnt_M_str.TermD_Uls_[32] <td></td>	
MtrVoltQax_Volt_M_[32[0] -28.7189999 MtrVoltQax_Volt_M_[32[1] -12.0450001 PICurrCntrl_CurrSensFailSclFac_Uls_M_[32] 0.768000007 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.810000014 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_[32] 0.82099998 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_[32] 43.169982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_[32] 43.169982 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 42.029.6016 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 0.31639992 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 0.31639992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_[32] 43.169982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 43.169982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 4203.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 0.31639992 SlowDataAccessBufindex_Cnt_M_str.TermD_Uls_[32] 0.31639992 SlowDataAccessBufindex_Cnt_M_str.TermD_Uls_[32] <td></td>	
MtrVoltQax_Volt_M_f32[1] -12.0450001 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.768000007 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.610000014 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.72359997 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.82099998 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.316399992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.31639992 SlowDataAccessBufindex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 0.023 k_MtrCrtIrVirualResQax_Ohm_f32 0.023 k_MtrCrtIrVi	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.768000007 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.610000014 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.72359997 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.8209998 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 43.1699982 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 43.1699982 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42029.6016 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.316399992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.31639992 SlowDataAccessBufindex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 0 k_MtrCtrlVirualResDax_Ohm_f32 0	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	
PICurrCntri_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.82099998 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42029.6016 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.316399992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399992 SlowDataAccessBufindex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResDax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModiffplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltQaxFitteFEnable_Cnt_l	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	
PiCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 42029.6016 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 0.316399992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.Prevlout_UIs_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.316399992 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResDax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_Igc 0 k_MtrCurrQaxRefModifRplEn_Cnt_Igc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltQaxFiltFFEnable_Cnt_Igc 1 k_MtrVoltQaxIntegLoLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 2.7.5 k_MtrVoltQaxIntegLoLim_Volt_f32 0.96999979 k_VoltSatQaxPolyCoeff_UIs_f32	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.316399992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.Prevlnput_Uls_f32 43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399992 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResDax_Ohm_f32 0.158000007 k_MtrCtrlCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltQaxFitFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0]	
PICurrCntr_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.316399992 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResDax_Ohm_f32 0.158000007 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_Igc 0 k_MtrCurrQaxRefModifDsb_Cnt_Igc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 1.0 1999998 k_MtrVoltQaxFitFFenable_Cnt_Igc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 24.6590004 k_VoltSatDaxPolyCoeff_UIs_f32 0.441000015 k_deadtimeVScale_UIs_f32 0.996999979 t_CommOffsetTbiX_UIs_u3p13[0] 786	
PICurrCntr_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399992 SlowDataAccessBuflndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResDax_Ohm_f32 0.158000007 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCvltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltQaxFitFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 24.6590004 k_VoltSatDaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[1] 5267	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.316399992 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResDax_Ohm_f32 0.158000007 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurQaxRefModifDsb_Cnt_Igc 0 k_MtrCurQaxRefModifRpIEn_Cnt_Igc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_VoltSatDaxPolyCoeff_UIs_f32 0.441000015 k_deadtimeVScale_UIs_f32 0.441000015 k_deadtimeVScale_UIs_f32 0.996999979 t_CommOffsetTblX_UIs_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0]	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.316399992 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResDax_Ohm_f32 0.158000007 k_MtrCtrrVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiftFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 -7.5 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_VoltSatDaxPolyCoeff_Uls_f32 0.445000015 k_CommOffsetTbiX_Uls_u3p13[0] 786 t_CommOffsetTbiX_Uls_u3p13[1] 5267 t_CommOffsetTbiY_Cnt_u16[0] 267 t_CommOffsetTbiY_Cnt_u16[1] 723	
SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModiffDsb_Cnt_lgc 0 k_MtrCurrQaxRefModiffDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 -7.5 k_MtrVoltQaxIntegHiLim_Volt_f32 2.75 k_MtrVoltQaxIntegDoLim_Volt_f32 1 k_VoltSatDaxPolyCoeff_Uls_f32 0.441000015 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3636.04004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 902.40802 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModiffDsb_Cnt_lgc 0 k_MtrVoltpaxRefModiffDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltQaxIntegLoLim_Volt_f32 2.7.5 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_VoltSatDaxPolyCoeff_Uls_f32 2.4.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_ILOAFdbackSignalScIFacSlew_UlspS_f32 902.40802 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 1 k_MtrCtrIVirualResDax_Ohm_f32 0.023 k_MtrCtrIVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 -7.5 k_MtrVoltQaxIntegHiLim_Volt_f32 1 k_MtrVoltQaxIntegDoLim_Volt_f32 2.75 k_MtrVoltQaxIntegDoLim_Volt_f32 2.75 k_MtrVoltQaxIntegDoLim_Volt_f32 0.46590004 k_VoltSatDaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 -7.5 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_MtrVoltQaxIntegDoLim_Volt_f32 2 k_MtrVoltQaxIntegDoLim_Volt_f32 0.7.5 k_MtrVoltQaxIntegDoLim_Volt_f32 0.46590004 k_VoltSatDaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 786 t_CommOffsetTbIY_Cnt_u16[0] 267 t_CommOffsetTbIY_Cnt_u16[1] 723	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegHiLim_Volt_f32 -7.5 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_MtrVoltQaxIntegDoLim_Volt_f32 2 k_MtrVoltQaxIntegDoLim_Volt_f32 0.7.5 k_MtrVoltQaxIntegDoLim_Volt_f32 0.46590004 k_VoltSatDaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 786 t_CommOffsetTbIY_Cnt_u16[0] 267 t_CommOffsetTbIY_Cnt_u16[1] 723	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_MtrVoltQaxIntegDoLim_Volt_f32 2.75 k_MtrVoltQaxIntegDoLim_Volt_f32 1 k_MtrVoltQaxIntegDoLim_Volt_f32 2 k_MtrVoltQaxIntegDoLim_Volt_f32 2 k_MtrVoltQaxIntegDoLim_Volt_f32 0 k_VoltSatDaxPolyCoeff_Uls_f32 0 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblX_Uls_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrCtrlVirualResQax_Ohm_f32 0.158000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblX_Uls_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblX_Uls_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrVoltDaxIntegHiLim_Volt_f32 2.74320006 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblX_Uls_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrVoltDaxIntegLoLim_Volt_f32 -10.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblX_Uls_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblX_Uls_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 786 t_CommOffsetTblX_Uls_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
k_MtrVoltQaxIntegHiLim_Volt_f32 12.4398003 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 786 t_CommOffsetTbIX_Uls_u3p13[1] 5267 t_CommOffsetTbIY_Cnt_u16[0] 267 t_CommOffsetTbIY_Cnt_u16[1] 723	
k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 786 t_CommOffsetTbIX_Uls_u3p13[1] 5267 t_CommOffsetTbIY_Cnt_u16[0] 267 t_CommOffsetTbIY_Cnt_u16[1] 723	
k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 786 t_CommOffsetTbIX_Uls_u3p13[1] 5267 t_CommOffsetTbIY_Cnt_u16[0] 267 t_CommOffsetTbIY_Cnt_u16[1] 723	
k_VoltSatDaxPolyCoeff_Uls_f32 24.6590004 k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 786 t_CommOffsetTbIX_Uls_u3p13[1] 5267 t_CommOffsetTbIY_Cnt_u16[0] 267 t_CommOffsetTbIY_Cnt_u16[1] 723	
k_VoltSatQaxPolyCoeff_Uls_f32 0.441000015 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 786 t_CommOffsetTbIX_Uls_u3p13[1] 5267 t_CommOffsetTbIY_Cnt_u16[0] 267 t_CommOffsetTbIY_Cnt_u16[1] 723	
k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 786 t_CommOffsetTbIX_Uls_u3p13[1] 5267 t_CommOffsetTbIY_Cnt_u16[0] 267 t_CommOffsetTbIY_Cnt_u16[1] 723	
t_CommOffsetTblX_UIs_u3p13[0] 786 t_CommOffsetTblX_UIs_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
t_CommOffsetTblX_UIs_u3p13[0] 786 t_CommOffsetTblX_UIs_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
t_CommOffsetTblX_UIs_u3p13[1] 5267 t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
t_CommOffsetTblY_Cnt_u16[0] 267 t_CommOffsetTblY_Cnt_u16[1] 723	
t_CommOffsetTblY_Cnt_u16[1] 723	
target Mtr/ 'ntrl Dead lytrl caMtathEn Ont loo atr	
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -126.640999	
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1231	

PICurrCntrl_Per1



Name	Input Value		
target MtrCntrl Read MtrCurrQax Amp f32 Val	99.348999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1231	1231	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.00388384	-4.00388002 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.96970439	2.9697001 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	45493	45493 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	2.74320006	2.74320006 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965	-9.14299965 ± 7.81E-03	✓

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.23 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-208.287994
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-27.9839993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0869999975
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00700000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62199998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	376.326996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	721.965027
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0520000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00800000038
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.558000028
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.483999997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	614.892029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1012.16998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	15.5469999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-22.8390007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_Vecu_Volt_M_f32[0]	25.4869995
MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001
MtrCurrDaxPrevIntg_Volt_M_f32	27.9990005
MtrCurrDaxRef_Amp_M_f32[0]	106.072998

PICurrCntrl_Per1



M	Invest Value		
Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog Amp M f32	-112.455002 131.306		
	-91.4420013		
MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993		
MtrCurrQaxPrevIntg_Volt_M_f32	30.6711006		
MtrCurrQaxRef Amp M f32[0]	-108.124001		
MtrCurrQaxRef_Amp_M_f32[1]	178.639008		
MtrCurrQaxRpl_Amp_M_f32[0]	-91.4420013		
MtrCurrQaxRpl_Amp_M_f32[1]	133.692993		
MtrPosComputationDelay_Rad_M_f32[0]	-1.926		
MtrPosComputationDelay_Rad_M_f32[1]	5.88500023		
MtrVoltDax_Volt_M_f32[0]	-29.7110004		
MtrVoltDax_Volt_M_f32[1]	3.61899996		
MtrVoltQax_Volt_M_f32[0]	-29.7110004		
MtrVoltQax_Volt_M_f32[1]	3.61899996		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.247999996		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.602999985		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0351000018		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.317000002		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17234.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.71329999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17234.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.71329999		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4450.8501		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2508.87012		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.189999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.118000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc k_MtrVoltDaxIntegHiLim_Volt_f32	29.1569004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.1117992		
k MtrVoltQaxIntegLoLim Volt f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.3699999		
k VoltSatQaxPolyCoeff Uls f32	11.9610004		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTbIX_UIs_u3p13[0]	1729		
t_CommOffsetTbIX_UIs_u3p13[1]	3269		
t_CommOffsetTblY_Cnt_u16[0]	502		
t_CommOffsetTblY_Cnt_u16[1]	707		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	727		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	41.1769981		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	707	707	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64552	64552 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-147.988007	-147.988007 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	11.63941	11.6393995 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-22.2434235	-22.2434006 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	7651	7651 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.988007	-147.988007 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993	133.692993 ± 7.81E-03	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
	·
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0309999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.722
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82299995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	888.947998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-292.006989
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.050999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.061999999
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.93700004
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0850000009
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1024
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	9.61999989
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	26.7999992
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.45499992
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.1830006
/trCtrl_Vecu_Volt_M_f32[0]	16.8080006
/trCtrl_Vecu_Volt_M_f32[1]	19.1679993
/ltrCurrDaxPrevIntg_Volt_M_f32	4.04500008
/ltrCurrDaxRef_Amp_M_f32[0]	24.6130009
/ltrCurrDaxRef_Amp_M_f32[1]	-20.9400005
/ltrCurrQaxCog_Amp_M_f32	19.6149998
/trCurrQaxFinalRef_Amp_M_f32[0]	171.485992
/trCurrQaxFinalRef_Amp_M_f32[1]	163.787003
/trCurrQaxPrevIntg_Volt_M_f32	16.8353996
/trCurrQaxRef_Amp_M_f32[0]	-76.8769989
MtrCurrQaxRef_Amp_M_f32[1]	-153.238998
/trCurrQaxRpl_Amp_M_f32[0]	171.485992
ltrCurrQaxRpl_Amp_M_f32[1]	163.787003
htrPosComputationDelay_Rad_M_f32[0]	3.05200005
MtrPosComputationDelay_Rad_M_f32[1]	-4.14699984
/trVoltDax_Volt_M_f32[0]	3.45499992
MtrVoltDax_Volt_M_f32[1]	-19.1830006
//trVoltQax_Volt_M_f32[0]	3.45499992
MtrVoltQax_Volt_M_f32[1]	-19.1830006

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.463999987 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.409000009 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.794099987 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.640999973 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -717.299988 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 267.119995 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 20241.6992 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$ 0.221200004 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -717.299988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 267 119995 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 20241.6992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.221200004 SlowDataAccessBufIndex_Cnt_M_u16 0 897 588013 k CLOAFdbackSignalSclFacSlew UlspS f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 1232.52002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.182999998 k_MtrCtrlVirualResQax_Ohm_f32 0.193000004 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 17.7555008 k_MtrVoltDaxIntegHiLim_Volt_f32 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -12.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 22.5324993 k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 -3.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -20.6259995 k_VoltSatQaxPolyCoeff_Uls_f32 1.32299995 k_deadtimeVScale_Uls_f32 0.981000006 t CommOffsetTblX Uls u3p13[0] 2556 t_CommOffsetTblX_Uls_u3p13[1] 4316 t CommOffsetTblY Cnt u16[0] 15 t_CommOffsetTblY_Cnt_u16[1] 40 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 48.8400002 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3024 -30.7789993 target MtrCntrl Read MtrCurrQax Amp f32 Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Actual Value Name **Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 40 40 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 64290 64290 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 -1.71320462 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -1.71314001 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 16.3994045 16.3994007 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 30748 ± 1.52588E-05 30748 0 ± 0.0625 MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 + 7 81F-03

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

163.787003

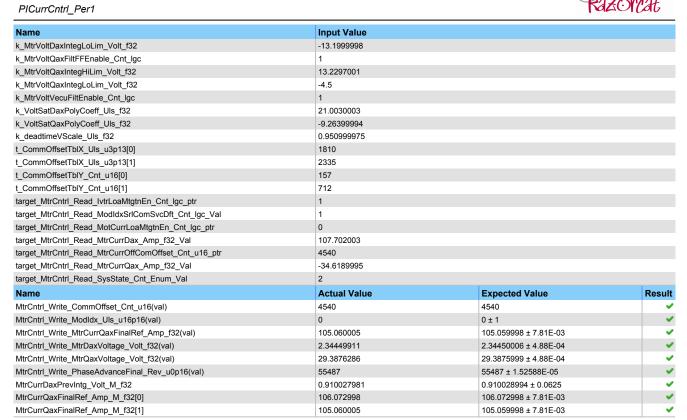
163.787003 ± 7.81E-03





Test Step 2.25 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-133.947006 75.7020035
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0320000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.057999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.34800005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.11099994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-942.771973
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-380.85199
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.093999968
MtrCtrl_MtrlmpedOax_Ohm_M_f32[1]	0.0879999995 0.096000008
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.35000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.74900013
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.3530006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.3040009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.4720001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.099999
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg Volt M f32	7.92799997 15.0869999
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004
MtrCurrDaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxCog_Amp_M_f32	114.531998
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxPrevIntg_Volt_M_f32	5.42920017
MtrCurrQaxRef_Amp_M_f32[0]	191.369003
MtrCurrQaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxRpl_Amp_M_f32[0]	106.072998
MtrCurrQaxRpl_Amp_M_f32[1] MtrDocComputationDology_Red_M_f23[0]	-112.455002 2.61800003
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1]	-1.04299998
MtrVoltDax Volt M f32[0]	26.4720001
MtrVoltDax_Volt_M_f32[1]	6.099999
MtrVoltQax_Volt_M_f32[0]	26.4720001
MtrVoltQax_Volt_M_f32[1]	6.099999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.600000024
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.89999976
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.179199994
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.257999986
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl MtrVecuFilt M str.TermN UIs f32	404.899994 46120.5
PICurrCntri_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.578299999
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	1118
PlCurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	404.899994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.578299999
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2048.80005
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3548.88989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResOax_Ohm_f32	0.079000036
k_MtrCtrlVirualResQax_Ohm_f32 k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.177000001
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	2.39529991

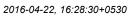




Test Step Call Trace	Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	•	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•	

Test Step 2.26 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.108000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0979999974
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.266000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.08399999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-150.298996

PICurrCntrl_Per1





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-165.235001
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.91199994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.56400001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	0
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-6.06799984
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-7.83199978
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	14.4589996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.13000011
MtrCtrl_Vecu_Volt_M_f32[0]	17.9899998
MtrCtrl_Vecu_Volt_M_f32[1]	20.3500004
	-9.29100037
MtrCurrDaxPrevIntg_Volt_M_f32	140.289001
MtrCurrDaxRef_Amp_M_f32[0]	
MtrCurrDaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxCog_Amp_M_f32	34.7879982
MtrCurrQaxFinalRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxFinalRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxPrevIntg_Volt_M_f32	26.5946007
MtrCurrQaxRef_Amp_M_f32[0]	-147.343002
MtrCurrQaxRef_Amp_M_f32[1]	127.972
MtrCurrQaxRpl_Amp_M_f32[0]	24.6130009
MtrCurrQaxRpl_Amp_M_f32[1]	-20.9400005
MtrPosComputationDelay_Rad_M_f32[0]	2.3440001
MtrPosComputationDelay_Rad_M_f32[1]	-4.34200001
MtrVoltDax_Volt_M_f32[0]	14.4589996
MtrVoltDax_Volt_M_f32[1]	-5.13000011
MtrVoltQax_Volt_M_f32[0]	14.4589996
MtrVoltQax_Volt_M_f32[1]	-5.13000011
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.509000003
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.446999997
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.8125
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.331999987
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	865.320007
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0255999994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	865.320007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	40399.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.0255999994
SlowDataAccessBufIndex Cnt M u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3449.11011
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7870.1001
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.164000005
k_MtrCtrlVirualResQax_Ohm_f32	0.0610000007
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	17.9570999
k_MtrVoltDaxIntegLoLim_Volt_f32	-14.1999998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	16.1431007
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-13.4589996
k_VoltSatQaxPolyCoeff_Uls_f32	12.7139997
k_deadtimeVScale_Uls_f32	0.981999993
t_CommOffsetTblX_Uls_u3p13[0]	1360
t_CommOffsetTblX_Uls_u3p13[1]	5743
t_CommOffsetTblY_Cnt_u16[0]	1436
t_CommOffsetTbIY_Cnt_u16[1]	1891
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	5./2399998
	5.72399998 808
	808
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	

PICurrCntrl_Per1



Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1891	1891	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64356	64356 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-206.744003	-206.744003 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.6647816	-17.6648006 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.222200543	0.222200006 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8196	8196 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-206.744003	-206.744003 ± 7.81E-03	✓
MtrCurrQaxFinalRef Amp M f32[1]	-20.9400005	-20.9400005 ± 7.81E-03	✓

est Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.27 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.153
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69299996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-43.257
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.920013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0099999978
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.363999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65900004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-185.072998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-920.171997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.2709999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	20.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	23.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	21.4680004
MtrCurrDaxRef_Amp_M_f32[0]	91.8850021
MtrCurrDaxRef_Amp_M_f32[1]	182.261002
MtrCurrQaxCog_Amp_M_f32	91.9309998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.065002
MtrCurrQaxPrevIntg_Volt_M_f32	18.1345997

2016-04-22, 16:28:30+0530



PICurrCntrl Per1 Input Value MtrCurrQaxRef_Amp_M_f32[0] 6.18900013 MtrCurrQaxRef_Amp_M_f32[1] 83.0540009 MtrCurrQaxRpl_Amp_M_f32[0] -166.035004 MtrCurrQaxRpl_Amp_M_f32[1] 183.065002 MtrPosComputationDelay_Rad_M_f32[0] -1.79400003 MtrPosComputationDelay_Rad_M_f32[1] -1.37800002 MtrVoltDax_Volt_M_f32[0] -7.3130002 MtrVoltDax_Volt_M_f32[1] 3.05299997 MtrVoltQax_Volt_M_f32[0] -7.3130002 $MtrVoltQax_Volt_M_f32[1]$ 3 05299997 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.256999999 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.933000028 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.0346999988 0.460000008 PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -194.190002 PICurrCntrl MtrVecuFilt_M_str.PrevOutput_Uls_f32 -657.099976 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 25640.4004 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.40000006 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -657.099976 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32$ 25640.4004 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.400000006 SlowDataAccessBufIndex_Cnt_M_u16 Λ k_CLOAFdbackSignalSclFacSlew_UlspS_f32 876.684998 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2879.57007 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.00200000009 k MtrCtrlVirualResQax Ohm f32 0.0710000023 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k MtrCurrQaxRefModifRplEn Cnt lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 3 07459998 k MtrVoltDaxIntegLoLim Volt f32 -15.1999998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 5.46850014 k MtrVoltQaxIntegLoLim Volt f32 -4.5999999 k_MtrVoltVecuFiltEnable_Cnt_lgc k VoltSatDaxPolyCoeff_Uls_f32 10.7320004 k_VoltSatQaxPolyCoeff_Uls_f32 3.59299994 k deadtimeVScale Uls f32 0.959999979 t_CommOffsetTbIX_Uls_u3p13[0] 4701 t CommOffsetTblX Uls u3p13[1] 5063 t_CommOffsetTblY_Cnt_u16[0] 155 t_CommOffsetTblY_Cnt_u16[1] 873 0 $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 650 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9 31999969 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 650 650 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 80.2930069 80.2929993 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 0.738990366 0.738990009 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.74277258 -4.7427702 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 12444 12444 ± 1.52588E-05 $MtrCurrDaxPrevIntg_Volt_M_f32$ 0 ± 0.0625

80.2930069

183.065002

MtrCurrQaxFinalRef_Amp_M_f32[0]

MtrCurrQaxFinalRef_Amp_M_f32[1]

80.2929993 ± 7.81E-03

183.065002 ± 7.81E-03



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(var)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_NtsCtnt_axntip_ioz_val
/trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-212.632996
//wti-ctri_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
	0.068000037
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] MtrCtrl MtrDampTermDax Ohm M f32[1]	0.063000001
	0.071999969
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0289999992
/trCtrl_MtrDawpTermQax_Ohm_M_f32[1]	1.75100005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.246999994
	292.062988
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	292.U02988 884.252991
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0860000029
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.74000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.890999973
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	260.899994
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	994.27301
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.32400036
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-26.5540009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992
/trCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
/trCurrDaxPrevIntg_Volt_M_f32	24.8929996
/trCurrDaxRef_Amp_M_f32[0]	-218.035004
/trCurrDaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxCog_Amp_M_f32	21.4759998
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.289001
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.235992
/trCurrQaxPrevIntg_Volt_M_f32	28.968399
/trCurrQaxRef_Amp_M_f32[0]	-105.246002
/trCurrQaxRef_Amp_M_f32[1]	41.6290016
/trCurrQaxRpl_Amp_M_f32[0]	140.289001
/trCurrQaxRpl_Amp_M_f32[1]	178.235992
MtrPosComputationDelay_Rad_M_f32[0]	-0.916000009
MtrPosComputationDelay_Rad_M_f32[1]	-6.00099993
MtrVoltDax_Volt_M_f32[0]	-8.79500008
MtrVoltDax_Volt_M_f32[1]	27.5049992

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-8.79500008		
MtrVoltQax_Volt_M_f32[1]	27.5049992		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.950999975		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.626999974		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.899699986		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.944000006		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	35039		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.588		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.588		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5931.81982		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3685.94995		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.0540000014		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.05350006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-16.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.8121996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	2.45499992		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9769993		
k_deadtimeVScale_Uls_f32	0.961000025		
t_CommOffsetTbIX_UIs_u3p13[0]	2294		
t_CommOffsetTbIX_UIs_u3p13[1]	6390		
t_CommOffsetTblY_Cnt_u16[0]	11		
t_CommOffsetTblY_Cnt_u16[1]	125		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	20.6149998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	631		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	125	125	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62980	62980 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-5.06428909	-5.06429005 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-12.7523785	-12.7524004 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27157	27157 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	~

Test Step Call Trace	est Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

178.235992

178.235992 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.29 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0480000004 0.0930000022
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0209999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.153
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69299996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-43.257
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.920013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0099999978
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.363999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65900004
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	185.072998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-920.171997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991
MtrCtrl MtrVoltDaxFF Volt M f32[1]	13.2709999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	22.3540001
MtrCtrl_Vecu_Volt_M_f32[1]	24.7140007
MtrCurrDaxPrevIntg_Volt_M_f32	21.4680004
MtrCurrDaxRef_Amp_M_f32[0]	91.8850021
MtrCurrDaxRef_Amp_M_f32[1]	182.261002
MtrCurrQaxCog_Amp_M_f32	91.9309998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.065002
MtrCurrQaxPrevIntg_Volt_M_f32	9.05210018
MtrCurrQaxRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxRpl_Amp_M_f32[0]	-166.035004
MtrCurrQaxRpl_Amp_M_f32[1]	183.065002
MtrPosComputationDelay_Rad_M_f32[0]	-6.28318977
MtrPosComputationDelay_Rad_M_f32[1]	-6.28318977
MtrVoltDax_Volt_M_f32[0]	-7.3130002
MtrVoltDax_Volt_M_f32[1]	3.05299997
MtrVoltQax_Volt_M_f32[0]	-7.3130002
MtrVoltQax_Volt_M_f32[1]	3.05299997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.256999999
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.933000028
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.786800027
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.460000008
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	36325.3984
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.365999997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005 -
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	36325.3984
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.365999997
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	876.684998
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2879.57007
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.00200000009
k_MtrCtrlVirualResQax_Ohm_f32	0.0710000023
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1

MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)

 $MtrCurrDaxPrevIntg_Volt_M_f32$

MtrCurrQaxFinalRef_Amp_M_f32[0]

MtrCurrQaxFinalRef_Amp_M_f32[1]

MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



4.78353024 ± 4.88E-04

80.2929993 ± 7.81E-03

183.065002 ± 7.81E-03

864

0 ± 0.0625

Input Value $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 29.9500999 k_MtrVoltDaxIntegLoLim_Volt_f32 -17.5 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ k MtrVoltQaxIntegHiLim_Volt_f32 27.7511997 $k_MtrVoltQaxIntegLoLim_Volt_f32$ -6.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 10.7320004 $k_VoltSatDaxPolyCoeff_Uls_f32$ k_VoltSatQaxPolyCoeff_Uls_f32 3.59299994 k_deadtimeVScale_Uls_f32 0.959999979 t_CommOffsetTblX_Uls_u3p13[0] 4701 t_CommOffsetTblX_Uls_u3p13[1] 5063 t_CommOffsetTblY_Cnt_u16[0] 155 t CommOffsetTblY Cnt u16[1] 873 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 650 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 650 650 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 80.2930069 80.2929993 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 0.397277474 0.397278011 ± 4.88E-04

4.78353119

80.2930069

183.065002

864

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	-
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 2.30 (Repeat Count = 1)		
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0680000037	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.063000001	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0719999969	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75100005	





Picurchin_Peri	MACIA
Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.246999994
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	292.062988
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	884.252991
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.063000001
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.114
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0860000029
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.74000001
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.890999973
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-260.899994
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-994.27301
/trCtrl MtrVoltDaxFF Volt M f32[0]	-8.32400036
MtrCtrl MtrVoltDaxFF Volt M f32[1]	-26.5540009
//dtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992
/trCtrl_Vecu_Volt_M_f32[0]	14.2779999
/trCtrl_Vecu_Volt_M_f32[1]	16.6380005
/trCurrDaxPrevIntg_Volt_M_f32	24.8929996
AtrCurrDaxRef Amp M f32[0]	-218.035004
	11.6370001
/trCurrDaxRef_Amp_M_f32[1]	
MtrCurrQaxCog_Amp_M_f32	21.4759998
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.289001
/trCurrQaxFinalRef_Amp_M_f32[1]	178.235992
/trCurrQaxPrevIntg_Volt_M_f32	16.3929005
/trCurrQaxRef_Amp_M_f32[0]	-105.246002
/trCurrQaxRef_Amp_M_f32[1]	41.6290016
/trCurrQaxRpl_Amp_M_f32[0]	140.289001
/trCurrQaxRpI_Amp_M_f32[1]	178.235992
/trPosComputationDelay_Rad_M_f32[0]	6.28318977
/trPosComputationDelay_Rad_M_f32[1]	6.28318977
/trVoltDax_Volt_M_f32[0]	-8.79500008
//dtrVoltDax_Volt_M_f32[1]	27.5049992
//trVoltQax_Volt_M_f32[0]	-8.79500008
//trVoltQax_Volt_M_f32[1]	27.5049992
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.950999975
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.626999974
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0658000037
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.944000006
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	10763.7002
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.228200004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	10763.7002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.228200004
SlowDataAccessBufIndex Cnt M u16	0
CLOAFdbackSignalSclFacSlew_UlspS_f32	5931.81982
LLOAFdbackSignalSclFacSlew_UlspS_f32	3685.94995
MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
_MtrCtrlVirualResDax_Ohm_f32	0.112000003
_MtrCtrlVirualResQax_Ohm_f32	0.0540000014
_MtrCurrQaxRefModifDsb_Cnt_lgc	1
_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0
_MtrVoltDaxIntegHiLim_Volt_f32	14.7652998
_MtrVoltDaxIntegLoLim_Volt_f32	-18.5
_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
_MtrVoltQaxIntegHiLim_Volt_f32	14.1267004
_MtrVoltQaxIntegLoLim_Volt_f32	-7.5
_MtrVoltVecuFiltEnable_Cnt_lgc	0
_VoltSatDaxPolyCoeff_Uls_f32	2.45499992
_VoltSatQaxPolyCoeff_Uls_f32	16.9769993
_deadtimeVScale_Uls_f32	0.961000025
_CommOffsetTbIX_Uls_u3p13[0]	2294
CommOffsetTbIX_UIs_u3p13[1]	6390
_CommOffsetTblY_Cnt_u16[0]	11
_CommOffsetTblY_Cnt_u16[1]	125
	0
arget MtrCntrl Dead lytrl coMtataEn Cat los ats	U
	0
arget_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
arget_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr arget_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val arget_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr arget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	0 1 20.6149998

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	125	125	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62980	62980 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-5.08662462	-5.08662987 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.7434855	12.7434998 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61575	61575 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.235992	178.235992 ± 7.81E-03	~

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Test Step 2.31 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.898999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23500001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-113.670998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-827.208008
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.53100002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.80499995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-277.385986
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-517.232971
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	3.87299991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2730007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_Vecu_Volt_M_f32[0]	20.2549992
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998
MtrCurrDaxPrevIntg_Volt_M_f32	-29.7089996
MtrCurrDaxRef_Amp_M_f32[0]	-216.921997
MtrCurrDaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxCog_Amp_M_f32	-124.709999

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	91.8850021		
MtrCurrQaxFinalRef_Amp_M_f32[1]	182.261002		
MtrCurrQaxPrevIntg_Volt_M_f32	3.45029998		
MtrCurrQaxRef_Amp_M_f32[0]	-213.026993 -66.7229996		
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl Amp M f32[0]	91.8850021		
	182.261002		
MtrCurrQaxRpI_Amp_M_f32[1] MtrPosComputationDelay_Rad_M_f32[0]	0		
MtrPosComputationDelay_Rad_M_f32[1]	0		
MtrVoltDax Volt M f32[0]	-22.3460007		
MtrVoltDax_Volt_M_f32[1]	-17.8169994		
MtrVoltQax_Volt_M_f32[0]	-22.3460007		
MtrVoltQax_Volt_M_f32[1]	-17.8169994		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.735000014		
PICurrCntrl InverterFailSclFac Uls M f32	0.151999995		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.19990001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.30399999		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13385.9004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.58950001		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	13385.9004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.58950001		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1279.38		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4881.5498		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.075000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.0209999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.2935009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.1322002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.7220001		
k_VoltSatQaxPolyCoeff_Uls_f32	-13.0010004		
k_deadtimeVScale_Uls_f32	0.992999971		
t_CommOffsetTblX_Uls_u3p13[0]	1827		
t_CommOffsetTblX_Uls_u3p13[1]	5226		
t_CommOffsetTblY_Cnt_u16[0]	1326		
t_CommOffsetTblY_Cnt_u16[1]	1829		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	79.6729965		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target MtrCntrl Read MtrCurrQax Amp f32 Val	416 -205.514999		
target_MtrCntrl_Read_MtrCurrQax_Amp_rsz_val target MtrCntrl Read SysState Cnt Enum Val	-205.514999		
		Eupopted Value	Decut
Name MtrCotrl Write CommOffset Cot u16(val)	Actual Value 1829	Expected Value 1829	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl Write Modldx Uls u16p16(val)			
, , ,	65077	65077 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-180.201996	-180.201996 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.96159673 -20.0173283	-1.96159995 ± 4.88E-04 -20.0172997 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	33787	-20.0172997 ± 4.88E-04 33787 ± 1.52588E-05	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32	0		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-180.201996	0 ± 0.0625 -180.201996 ± 7.81E-03	
MtrCurrQaxFinalRef_Amp_M_f32[1]	182.261002	182.261002 ± 7.81E-03	
ma can saxi mantor_mnp_m_loz[1]	102.201002	102.201002 1 7.01L=03	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
VtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-69.0940018
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	161.973007
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0179999992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.046000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0759999976
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0240000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.43599999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.893999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	497.348999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-685.572998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.123999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0790000036
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00700000022
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0839999989
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.68299997
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0769999996
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-594.544983
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	215.455994
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.1380005
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.920000017
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.448
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.9260006
htrCtrl_Vecu_Volt_M_f32[0]	13.085
ftrCtrl_Vecu_Volt_M_f32[1]	15.4449997
ltrCurrDaxPrevIntg_Volt_M_f32	-9.7670002
/trCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
/trCurrQaxCog_Amp_M_f32	-185.608994
/trCurrQaxFinalRef_Amp_M_f32[0]	-218.035004
htrCurrQaxFinalRef_Amp_M_f32[1]	11.6370001
/trCurrQaxPrevIntg_Volt_M_f32	20.1585007
ItrCurrQaxRef_Amp_M_f32[0]	-212.632996
trCurrQaxRef_Amp_M_f32[1]	-205.085007
ItrCurrQaxRpI Amp M f32[0]	-218.035004
ItrCurrQaxRpl Amp M f32[1]	11.6370001
ItrPosComputationDelay_Rad_M_f32[0]	-3.05999994
ItrPosComputationDelay Rad M f32[1]	-4.93499994
/trVoltDax_Volt_M_f32[0]	-23.448
/trVoltDax Volt M f32[1]	-24.9260006
/trVoltQax_Volt_M_f32[0]	-23.448
/trVoltQax_Volt_M_f32[1]	-24.9260006

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.989000022		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.867999971		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.249599993		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.578000009		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	39404.3984		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.423099995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	39404.3984		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.423099995		
SlowDataAccessBufIndex Cnt M u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	7139.7998		
k ILOAFdbackSignalSclFacSlew UlspS f32	5357.10986		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0689999983		
k MtrCtrlVirualResQax Ohm f32	0.063000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	11.7138004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.5		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	7.54269981		
k_MtrVoltQaxIntegLoLim_Volt_f32	3.5		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-7.60300016		
k_VoltSatQaxPolyCoeff_Uls_f32	-11.1459999		
k_deadtimeVScale_Uls_f32	0.94999988		
t_CommOffsetTblX_Uls_u3p13[0]	2440		
t CommOffsetTblX Uls u3p13[1]	3744		
t CommOffsetTblY Cnt u16[0]	695		
t CommOffsetTblY Cnt u16[1]	1480		
target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	0.486999989		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1059		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	1059	1059	•
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-31.1130066	-31.1130009 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-27.2355175	-27.2355003 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	11.2039785	11.2040005 ± 4.88E-04	~
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	1749	1749 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	11.7138004	11.7138004 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-218.035004	-218.035004 ± 7.81E-03	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

-31.1130066

-31.1130009 ± 7.81E-03

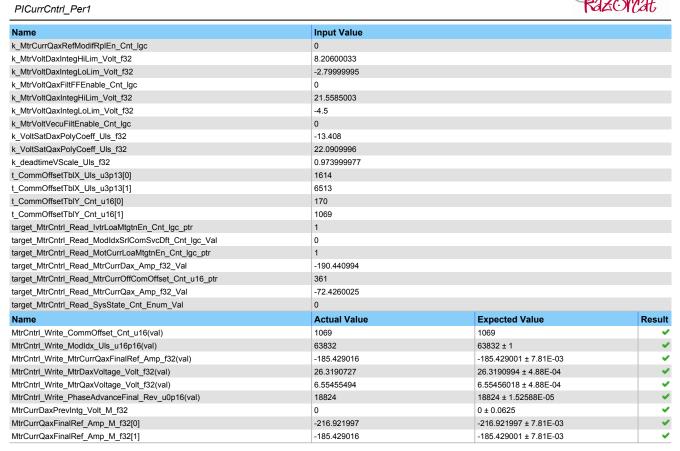
MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.33 (Repeat Count = 1)	Input Value	
Name EastDataAccessRufindey Cot M u16	Input Value	
FastDataAccessBufIndex_Cnt_M_u16 MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.66400003 1.54200006	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	133.104004	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	671.512024	
MtrCtrl MtrlmpedDax Ohm M f32[0]	0.0329999998	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0140000004	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.123000003	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.118000001	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.107000001	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.104000002	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	831.671021	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	382.882996	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.5469999	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-22.8390007	
MtrCtrl_Vecu_Volt_M_f32[0]	25.4869995	
MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001	
MtrCurrDaxPrevIntg_Volt_M_f32	10.5640001	
MtrCurrDaxRef_Amp_M_f32[0]	160.044006	
MtrCurrDaxRef_Amp_M_f32[1]	165.242004	
MtrCurrQaxCog_Amp_M_f32	163.561005	
MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.921997 -184.923996	
MtrCurrQaxPrevIntg_Volt_M_f32	15.6246996	
MtrCurrQaxRef Amp M f32[0]	205.820999	
MtrCurrQaxRef_Amp_M_f32[1]	-206.792007	
MtrCurrQaxRpl_Amp_M_f32[0]	-216.921997	
MtrCurrQaxRpl Amp M f32[1]	-184.923996	
MtrPosComputationDelay_Rad_M_f32[0]	4.51300001	
MtrPosComputationDelay_Rad_M_f32[1]	0.477999985	
MtrVoltDax_Volt_M_f32[0]	15.5469999	
MtrVoltDax_Volt_M_f32[1]	-22.8390007	
MtrVoltQax_Volt_M_f32[0]	15.5469999	
MtrVoltQax_Volt_M_f32[1]	-22.8390007	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.418000013	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.85199998	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.066699966	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.039999991	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-784.130005	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_LUls_f32	-717.299988 41423.8984	
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.516300023	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	41423.8984	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.516300023	
SlowDataAccessBufIndex_Cnt_M_u16	1	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1592.43005	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5074.45996	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0	
k_MtrCtrlVirualResDax_Ohm_f32	0.137999997	
k_MtrCtrlVirualResQax_Ohm_f32	0.172000006	
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0	

2016-04-22, 16:28:30+0530





Fest Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.34 (Repeat Count = 1)	🗸
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.148
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-987.179016

© Report created by TESSY V3.1.11, report template V2.1





MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 9. MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0. MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2. MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3. MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3. MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7. MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7. MtrCtrl_Vecu_Volt_M_f32[0] -7. MtrCtrl_Vecu_Volt_M_f32[0] -6. MtrCurrDaxPrevintg_Volt_M_f32[0] -6. MtrCurrDaxPrevintg_Volt_M_f32[0] -6. MtrCurrDaxRef_Amp_M_f32[0] -7. MtrCurrQaxFinalRef_Amp_M_f32[0] -1. MtrCurrQaxFinalRef_Amp_M_f32[0] -1. MtrCurrQaxFinalRef_Amp_M_f32[0] -1. MtrCurrQaxRef_Amp_M_f32[0] -6. MtrCurrQaxRef_Amp_M_f32[0] -7. MtrCurrQaxRef_Amp_M_f32[0] -7. <th>nput Value 952.34198 9.0.549999997 9.0.0979999974 9.1.25 9.0.00999999978 1.4090004 1.56299996 247.07298 40.618 31 31 7.3130002 8.05299997 16.808006 19.167993 21.3630009 65.1900024 216.972 20.7061996 68.0940018 161.973007 166.035004 183.065002 20.7061996 68.0940018 161.973007 166.035004 183.065002 1.1.5100002 9.337000012 7.3130002 8.05299997 7.3130002 8.05299997 7.3130002 8.05299997 7.3130002 8.05299997 7.3130002 9.0839999989 9.0887000024</th>	nput Value 952.34198 9.0.549999997 9.0.0979999974 9.1.25 9.0.00999999978 1.4090004 1.56299996 247.07298 40.618 31 31 7.3130002 8.05299997 16.808006 19.167993 21.3630009 65.1900024 216.972 20.7061996 68.0940018 161.973007 166.035004 183.065002 20.7061996 68.0940018 161.973007 166.035004 183.065002 1.1.5100002 9.337000012 7.3130002 8.05299997 7.3130002 8.05299997 7.3130002 8.05299997 7.3130002 8.05299997 7.3130002 9.0839999989 9.0887000024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0. MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[0] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2. MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3. MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3. MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7. MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 3. MtrCtrl_Vecu_Volt_M_f32[1] 1. MtrCtrl_Vecu_Volt_M_f32[1] 1. MtrCurrDaxPrevIntg_Volt_M_f32 -2. MtrCurrDaxRef_Amp_M_f32[0] -6. MtrCurrDaxRef_Amp_M_f32[1] -2. MtrCurrQaxCog_Amp_M_f32 9. MtrCurrQaxFinalRef_Amp_M_f32[0] -1. MtrCurrQaxFinalRef_Amp_M_f32[0] -1. MtrCurrQaxFinalRef_Amp_M_f32[0] -6. MtrCurrQaxRef_Amp_M_f32[0] -6. MtrCurrQaxRef_Amp_M_f32[0] -1. MtrCurrQaxRef_Amp_M_f32[0] -1.	0.054999997 0.0979999974 0.125 0.0099999998 1.4090004 1.56299996 247.072998 40.618 31 31 7.313002 0.0529997 16.808006 19.1679993 21.363009 65.1900024 216.972 201.9309998 166.035004 183.065002 0.07661996 69.0940018 161.973007 166.035004 183.065002 1.15100002 0.33700012 7.3130002 0.0529997 0.083999998 0.867000024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0. MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[0] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2. MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3. MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3. MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7. MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 3. MtrCtrl_Vecu_Volt_M_f32[1] 1. MtrCtrl_Vecu_Volt_M_f32[1] 1. MtrCurrDaxPrevIntg_Volt_M_f32 -2. MtrCurrDaxRef_Amp_M_f32[0] -6. MtrCurrDaxRef_Amp_M_f32[1] -2. MtrCurrQaxCog_Amp_M_f32 9. MtrCurrQaxFinalRef_Amp_M_f32[0] -1. MtrCurrQaxFinalRef_Amp_M_f32[0] -1. MtrCurrQaxFinalRef_Amp_M_f32[0] -6. MtrCurrQaxRef_Amp_M_f32[0] -6. MtrCurrQaxRef_Amp_M_f32[0] -1. MtrCurrQaxRef_Amp_M_f32[0] -1.	0.054999997 0.0979999974 0.125 0.0099999998 1.4090004 1.56299996 247.072998 40.618 31 31 7.313002 0.0529997 16.808006 19.1679993 21.363009 65.1900024 216.972 291.9309998 166.035004 183.065002 0.07661996 69.0940018 161.973007 166.035004 183.065002 1.15100002 0.33700012 7.3130002 0.0529997 0.083999998 0.867000024
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[0] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 1. MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -4 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_Vecu_Volt_M_f32[0] -7 MtrCtrl_Vecu_Volt_M_f32[0] -1 MtrCtrl_Vecu_Volt_M_f32[1] -1 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[1] -1 MtrCurrQaxRef_Amp_M_f32[1] -1 MtrCurrQaxRef_Amp_M_f32[1] -1 MtrPosComputationDelay_Rad_M_f32[0] -1	0.0979999974 0.125 0.0099999998 1.4090004 1.5629996 2247.07298 40.618 31 31 31 7.313002 3.0529997 16.808006 19.167993 221.363009 65.190024 216.972 29.1939998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.5100002 0.337000012 7.3130002 0.3037000012 7.3130002 0.083999997 0.0839999989 0.887000024
MtrCtri_MtrImpedQax_Ohm_M_f32[0] 0. MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 1. MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -4 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_Vecu_Volt_M_f32[0] -7 MtrCtrl_Vecu_Volt_M_f32[0] -1 MtrCtrl_Vecu_Volt_M_f32[0] -1 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxPer_Amp_M_f32[0] -6 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[0] -1	0.125 0.0099999998 1.4090004 1.56299996 247.07298 40.618 31 31 31 7.3130002 3.0529997 16.8080006 19.1679993 21.3630009 65.1900024 216.972 20.1930998 1166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.15100002 0.337000012 7.3130002 0.30529997 7.3130002 0.05299997 0.0839999989 0.887000024
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 1. MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -6 MtrCtrl_Vecu_Volt_M_f32[0] -6 MtrCtrl_Vecu_Volt_M_f32[1] -2 MtrCurrDaxPrevintg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3	0.0099999998 1.4090004 1.5629996 247.07298 40.618 31 31 31 7.313002 3.0529997 16.808006 19.167993 21.363009 65.190024 216.972 21.930998 166.035004 183.065002 20.7061996 69.094018 161.973007 166.035004 183.065002 20.337000012 7.3130002 0.337000012 7.3130002 0.083999989 0.887000024
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1. MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 1. MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -7 MtrCtrl_Vecu_Volt_M_f32[0] -1 MtrCtrl_Vecu_Volt_M_f32[1] -1 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrQaxCog_Amp_M_f32 9 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1]	1.40900004 1.56299996 247.072998 40.618 31 31 31 31 7.3130002 3.0529997 16.8080006 19.167993 21.3630009 65.1900024 216.972 21.39300998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 20.337000012 7.3130002 0.337000012 7.3130002 0.033999997 7.3130002 0.0839999989 0.0887000024
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 1. MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -7 MtrCtrl_Vecu_Volt_M_f32[0] -1 MtrCtrl_Vecu_Volt_M_f32[1] -1 MtrCtrl_Vecu_Volt_M_f32[0] -6 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 9 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32	1.56299996 247.072998 40.618 31 31 31 7.3130002 8.05299997 16.8080006 19.1679993 21.3630009 65.1900024 216.972 21.930998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.15100002 2.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.0839999989 0.887000024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -2 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -4 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 3 MtrCtrl_Vecu_Volt_M_f32[0] 16 MtrCtrl_Vecu_Volt_M_f32[1] 19 MtrCurrDaxPevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[0] -1 MtrCurrQaxCog_Amp_M_f32 9 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PiCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PiCurrCntrl_MtrCurrDaxSatFlu	247.072998 40.618 31 31 7.3130002 3.05299997 16.8080006 19.1679993 21.3630009 65.1900024 216.972 21.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.15100002 2.337000012 7.3130002 3.05299997 7.3130002 3.05299997 0.0839999989 0.887000024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 44 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -3 MtrCtrl_Vecu_Volt_M_f32[0] -16 MtrCtrl_Vecu_Volt_M_f32[1] -1 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxFenalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -6 MtrCurrQaxFinalRef_Amp_M_f32[0] -6 MtrCurrQaxFinalRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 0 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PICurrCntrl_InverterFailSclFac_Uls_M_f3	40.618 31 31 7.3130002 8.05299997 16.8080006 19.1679993 21.3630009 65.1900024 216.972 21.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1510002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.0839999989 0.887000024
MtrCtrl_MtrVoltDaxFF_Volt_M_32[0] -3 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -8 MtrCtrl_Vecu_Volt_M_f32[0] -1 MtrCtrl_Vecu_Volt_M_f32[1] -1 MtrCurrDaxPervIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFere/Intg_Volt_M_f32 26 MtrCurrQaxPrevIntg_Volt_M_f32 26 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrCorrQaxRpl_Amp_M_f32[1] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] -1 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] -7 MtrVoltQax_Volt_M_f32[1] -7 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 -7 PICurrCntrl_MtrVecuFiit_M_str.Pre	31 31 31 31 31 32 305299997 36.808006 39.1679993 21.3630009 65.1900024 216.972 31.9309998 31.96.035004 31.96.035004 31.96.035004 31.973007 31.96.035004 31.96.035004 31.973007 31.973007 31.973007 31.973007 31.97300012 31.97300012 31.97300012 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002 31.973130002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -3 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 3 MtrCtrl_Vecu_Volt_M_f32[0] 16 MtrCtrl_Vecu_Volt_M_f32[1] 15 MtrCurrDaxPevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 9 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxPrevIntg_Volt_M_f32 26 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrCurrQaxRpl_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[0] -7 Mtr	31 7.3130002 3.05299997 16.8080006 19.1679993 21.3630009 65.1900024 216.972 21.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.15100002 3.37000012 7.3130002 3.05299997 7.3130002 3.05299997 0.0839999989 0.887000024
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 3 MtrCtrl_Vecu_Volt_M_f32[0] 16 MtrCtrl_Vecu_Volt_M_f32[1] 15 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 9 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Ul	7.3130002 3.05299997 16.8080006 19.1679993 21.3630009 65.1900024 216.972 211.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 1.3.37000012 7.3130002 3.05299997 7.3130002 3.05299997 0.0839999989 0.887000024
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -7 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 3 MtrCtrl_Vecu_Volt_M_f32[0] 16 MtrCtrl_Vecu_Volt_M_f32[1] 15 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 9 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Ul	7.3130002 3.05299997 16.8080006 19.1679993 21.3630009 65.1900024 216.972 211.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 1.3.37000012 7.3130002 3.05299997 7.3130002 3.05299997 0.0839999989 0.887000024
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 3. MtrCtrl_Vecu_Volt_M_f32[0] 16 MtrCtrl_Vecu_Volt_M_f32[1] 15 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 9 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[1] 18 MtrCurrQaxPrevIntg_Volt_M_f32 2 MtrCurrQaxPrevIntg_Volt_M_f32 2 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRpl_Amp_M_f32[1] 17 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrVottDax_Volt_M_f32[0] -7 MtrVottDax_Volt_M_f32[1] 0 MtrVottDax_Volt_M_f32[0] -7 MtrVottQax_Volt_M_f32[1] 3 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 1 PlCurrCntrl_MtrVecuFiit_M_	3.05299997 16.8080006 19.1679993 21.3630009 65.1900024 216.972 21.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 3.337000012 7.3130002 3.05299997 7.3130002 3.05299997 0.0839999989 0.887000024
MtrCtrl_Vecu_Volt_M_f32[0] 16 MtrCtrl_Vecu_Volt_M_f32[1] 19 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 9° MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[1] 18 MtrCurrQaxPerlntg_Volt_M_f32 2 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.Prevoluput_Uls_f32 1 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 1 PlCurrCntrl_M	16.8080006 19.1679993 21.3630009 65.1900024 216.972 201.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 0.05299997 7.3130002 0.0839999989 0.0887000024
MtrCtrl_Vecu_Volt_M_f32[1] 15 MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 95 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[1] 16 MtrCurrQaxPevIntg_Volt_M_f32 25 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0. MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3. MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1. PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 1. PlCur	19.1679993 21.3630009 65.1900024 216.972 201.9309998 166.035004 183.065002 20.7061996 66.9940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 0.05299997 7.3130002 0.0839999989 0.0887000024
MtrCurrDaxPrevIntg_Volt_M_f32 -2 MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 93 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[1] 18 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRpl_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 1 PICurrCntrl_MtrVecuFilt_M_str.Previoutput_Uls_f32 1 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0	21.3630009 65.1900024 216.972 91.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.0839999989 0.887000024
MtrCurrDaxRef_Amp_M_f32[0] -6 MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 93 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[1] 18 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRpl_Amp_M_f32[1] 17 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0. MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3. MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0. PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrVecuFilt_M_str.Prevoluput_Uls_f32 1. PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1. PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0. PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32	65.1900024 216.972 21.9309998 166.035004 183.065002 20.7061996 69.9940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 3.05299997 7.3130002 3.05299997 0.0839999989 0.887000024
MtrCurrDaxRef_Amp_M_f32[1] -2 MtrCurrQaxCog_Amp_M_f32 95 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[1] 18 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[0] -1 MtrCurrQaxRpl_Amp_M_f32[1] 18 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0. MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PICurrCntrl_InverterFailSclFac_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFilt_M_str.Previoutput_Uls_f32 1. PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1. PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0. PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	216.972 21.9309998 31.66.035004 183.065002 20.7061996 69.0940018 161.973007 1166.035004 183.065002 1.1.15100002 20.337000012 7.3130002 3.05299997 7.3130002 3.05299997 0.0839999989 0.887000024
MtrCurrQaxCog_Amp_M_f32 99 MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[1] 18 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRpl_Amp_M_f32[1] 18 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0. MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PICurrCntrl_InverterFailSclFac_Uls_M_f32 0. PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFilt_M_str.Previoutput_Uls_f32 1. PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1. PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0. PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	91.9309998 166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.0839999989 0.887000024
MtrCurrQaxFinalRef_Amp_M_f32[0] -1 MtrCurrQaxFinalRef_Amp_M_f32[1] 18 MtrCurrQaxPrevIntg_Volt_M_f32 20 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRpl_Amp_M_f32[0] -1 MtrCurrQaxRpl_Amp_M_f32[1] 18 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFilt_M_str.Prevolut_Uls_f32 1 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 1 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0	166.035004 183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.0839999989 0.887000024
MtrCurrQaxFinalRef_Amp_M_f32[1] 18 MtrCurrQaxPrevIntg_Volt_M_f32 26 MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRpl_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 3 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 1 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0	183.065002 20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.083999989 0.887000024
MtrCurrQaxPrevIntg_Volt_M_f32 2t MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 1t MtrCurrQaxRpl_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0. MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PICurrCntrl_InverterFailSclFac_Uls_M_f32 0. PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 0. PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1. PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 1. PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0. PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.083999989 0.887000024
MtrCurrQaxPrevIntg_Volt_M_f32 2t MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 1t MtrCurrQaxRpl_Amp_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0. MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[1] 3. PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PICurrCntrl_InverterFailSclFac_Uls_M_f32 0. PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 0. PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1. PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 1. PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0. PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	20.7061996 69.0940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.083999989 0.887000024
MtrCurrQaxRef_Amp_M_f32[0] -6 MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRpl_Amp_M_f32[0] -1 MtrCurrQaxRpl_Amp_M_f32[1] 18 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 36 PlCurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 17 PlCurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 42 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0	69.0940018 161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.083999989 0.887000024
MtrCurrQaxRef_Amp_M_f32[1] 16 MtrCurrQaxRp_Amp_M_f32[0] -1 MtrCurrQaxRpl_Amp_M_f32[1] 18 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFit_M_str.PrevOutput_Uls_f32 36 PICurrCntrl_MtrVecuFit_M_str.PrevOutput_Uls_f32 1 PICurrCntrl_MtrVecuFit_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0	161.973007 166.035004 183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.083999989 0.887000024
MtrCurrQaxRpl_Amp_M_f32[0] -1 MtrCurrQaxRpl_Amp_M_f32[1] 18 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[1] 3 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFit_M_str.PrevInput_Uls_f32 36 PlCurrCntrl_MtrVecuFit_M_str.PrevOutput_Uls_f32 1 PlCurrCntrl_MtrVecuFit_M_str.TermN_Uls_f32 42 PlCurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0	.166.035004 .183.065002 .1.15100002 .0.337000012 .7.3130002 .8.05299997 .7.3130002 .8.05299997 .0.083999989 .0.887000024
MtrCurrQaxRpI_Amp_M_f32[1] 18 MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0 MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[1] 3 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 36 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 11 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0	183.065002 1.1.15100002 0.337000012 7.3130002 8.05299997 7.3130002 8.05299997 0.083999989 0.887000024
MtrPosComputationDelay_Rad_M_f32[0] -1 MtrPosComputationDelay_Rad_M_f32[1] 0. MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[1] 3. PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PICurrCntrl_InverterFailSclFac_Uls_M_f32 0. PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFit_M_str.PrevInput_Uls_f32 36 PICurrCntrl_MtrVecuFit_M_str.PrevOutput_Uls_f32 11 PICurrCntrl_MtrVecuFit_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0. PICurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0.	1.15100002 0.33700012 7.3130002 8.05299997 7.3130002 8.05299997 0.083999989 0.887000024
MtrPosComputationDelay_Rad_M_f32[1] 0. MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[1] 3. PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PICurrCntrl_InverterFailSclFac_Uls_M_f32 0. PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 36 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 11 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	0.33700012 7.3130002 8.05299997 7.3130002 8.05299997 0.083999989 0.887000024
MtrVoltDax_Volt_M_f32[0] -7 MtrVoltDax_Volt_M_f32[1] 3 MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[1] 3 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 36 PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1 PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42 PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0	7.3130002 3.05299997 7.3130002 3.05299997 0.083999989 0.887000024
MtrVoltDax_Volt_M_f32[1] 3. MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[1] 3. PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0. PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 36 PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 11 PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42 PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	3.05299997 .7.3130002 3.05299997 0.083999989 0.887000024
MtrVoltQax_Volt_M_f32[0] -7 MtrVoltQax_Volt_M_f32[1] 3 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 36 PlCurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 1 PlCurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 42 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0	7.3130002 8.05299997 0.083999989 0.887000024
MtrVoltQax_Volt_M_f32[1] 3. PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0. PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 38 PlCurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 11 PlCurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 42 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0.	3.05299997 0.0839999989 0.887000024
MtrVoltQax_Volt_M_f32[1] 3. PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0. PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PlCurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 38 PlCurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 11 PlCurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 42 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0.	3.05299997 0.0839999989 0.887000024
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0. PICurrCntrl_InverterFailSclFac_Uls_M_f32 0. PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 36 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 17 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0.	0.0839999989 0.087000024
PICurrCntrl_InverterFailSclFac_Uls_M_f32 0. PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 38 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 11 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	0.887000024
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 36 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 11 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0. PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 38 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 11 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	J.2353UUUU4
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 0.	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 11 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 42 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.	0.460000008
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 42 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 0.	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 0.	1118
	1 218.1001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 38	0.665600002
	386.220001
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	1118
	4218.1001
2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	0.665600002
SlowDataAccessBufIndex_Cnt_M_u16	1
	7047 47004
	5847.47021
	1025.58997
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1	1
k_MtrCtrlVirualResDax_Ohm_f32 0.	0.0179999992
k_MtrCtrlVirualResQax_Ohm_f32 0.	0.0780000016
k_MtrCurrQaxRefModifDsb_Cnt_lgc 0	
k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0	
- '	12.6604004
	3.5
_	22.7973995
	6.5999999
k_MtrVoltVecuFiltEnable_Cnt_lgc 0	
k_VoltSatDaxPolyCoeff_UIs_f32 -4	4.08099985
k_VoltSatQaxPolyCoeff_Uls_f32 7.	7.89599991
k_deadtimeVScale_Uls_f32 0.	0.961000025
	1147
	4096
	189
	988
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 1	
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0	
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	33.9489975
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 30	3069
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2	33.9489975





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	988	988	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62980	62980 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-113.022995	-113.023003 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	18.2308731	18.2308998 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.63592601	2.63593006 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18401	18401 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-3.5	-3.5 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-166.035004	-166.035004 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-113.022995	-113.023003 ± 7.81E-03	~

est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.35 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.588999987
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.73000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-655.848999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-834.401001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.720000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65400004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	868.789001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-349.798004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997
MtrCurrDaxPrevIntg_Volt_M_f32	3.15400004
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	21.4759998
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.289001
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.235992
MtrCurrQaxPrevIntg_Volt_M_f32	28.3425999

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[0]	-132.813004		
MtrCurrQaxRef_Amp_M_f32[1]	-9.14299965		
MtrCurrQaxRpl_Amp_M_f32[0]	140.289001		
MtrCurrQaxRpl_Amp_M_f32[1]	178.235992		
MtrPosComputationDelay_Rad_M_f32[0]	-4.09700012		
MtrPosComputationDelay_Rad_M_f32[1]	-1.01900005		
MtrVoltDax_Volt_M_f32[0]	-8.79500008		
MtrVoltDax_Volt_M_f32[1]	27.5049992		
MtrVoltQax_Volt_M_f32[0]	-8.79500008		
MtrVoltQax_Volt_M_f32[1]	27.5049992		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.499000013		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.825999975		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.904100001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.944000006		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21877.4004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.363599986		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	21877.4004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.363599986		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1867.13		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	506.598999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.180999994		
k_MtrCtrlVirualResQax_Ohm_f32	0.0680000037		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.13619995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.474		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	7.38500023		
k_VoltSatQaxPolyCoeff_Uls_f32	-23.0559998		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	3464		
t_CommOffsetTblX_Uls_u3p13[1]	6250		
t_CommOffsetTblY_Cnt_u16[0]	1218		
t_CommOffsetTblY_Cnt_u16[1]	1360		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3103		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1360	1360	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63700	63700 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-208.854996	-208.854996 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-7.57722521	-7.57722998 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.40297616	1.40297997 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40433	40433 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.289001	140.289001 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-208.854996	-208.854996 ± 7.81E-03	'



Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Name	Input Value
	o input value
FastDataAccessBufIndex_Cnt_M_u16	-
VtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
VtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120000001
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.47299999
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-143.399002
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-302.690002
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0920000002
htrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0960000008
ItrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.386999995
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.847
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-611.046997
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	804.908997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0
//dtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
/trCtrl Vecu Volt M f32[0]	17.9899998
/trCtrl Vecu Volt M f32[1]	20.3500004
htrCurrDaxPrevIntg Volt M f32	17.5130005
/trCurrDaxRef Amp M f32[0]	-208.287994
/trCurrDaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxCog Amp M f32	-124.709999
/trCurrQaxFinalRef_Amp_M_f32[0]	91.8850021
trCurrQaxFinalRef_Amp_M_f32[1]	182.261002
AtrCurrQaxPrevIntg Volt M f32	22.6252003
/trCurrQaxRef_Amp_M_f32[0]	-146.173996
//trCurrQaxRef_Amp_M_f32[1]	-213.335007
	91.8850021
htrCurrQaxRpl_Amp_M_f32[0]	182.261002
ItrCurrQaxRpl_Amp_M_f32[1]	
ItrPosComputationDelay_Rad_M_f32[0]	0.372999996
htrPosComputationDelay_Rad_M_f32[1]	1.69299996
/trVoltDax_Volt_M_f32[0]	-22.3460007
MtrVoltDax_Volt_M_f32[1]	-17.8169994
/trVoltQax_Volt_M_f32[0]	-22.3460007
//dtrVoltQax_Volt_M_f32[1]	-17.8169994

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



		•	
Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.959999979		
PICurrCntrl InverterFailSclFac Uls M f32	0.0370000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.339300007		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.30399999		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	33920.5		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.583899975		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	33920.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.583899975		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	211.854996		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1900.18005		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.156000003		
k MtrCtrlVirualResQax Ohm f32	0.142000005		
k MtrCurrQaxRefModifDsb Cnt Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.5790997		
k MtrVoltDaxIntegLoLim Volt f32	-3.5999999		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	21.0468998		
k MtrVoltQaxIntegLoLim Volt f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_UIs_f32	-10.6560001		
k VoltSatQaxPolyCoeff Uls f32	6.90999985		
k deadtimeVScale Uls f32	0.958999991		
t CommOffsetTbIX UIs u3p13[0]	6160		
t_CommOffsetTblX_Uls_u3p13[1]	6291		
t CommOffsetTblY Cnt u16[0]	1130		
t CommOffsetTblY Cnt u16[1]	1422		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0		
*	-184.522003		
target_MtrCntrl_Read_MtrCurrOffComOffcot_Cnt_u16_ptr	758		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	80.8180008		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	4		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val		l=	
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1422	1422	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62849	62849 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-113.348999	-113.348999 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-9.24689293	-9.24689007 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	14.5650482	14.5649996 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	63526	63526 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-113.348999	-113.348999 ± 7.81E-03	•
MtrCurrOoyEinelDof Amp M f22[1]	102 261002	102 261002 ± 7 01 = 02	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

182.261002

182.261002 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]





Test Step 2.37 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-69.0940018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	161.973007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0179999992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0240000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.53400004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.579999983
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-341.976013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-806.22998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.123999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.079000036
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.083999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.657000005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.386000007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	268.286011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-542.14502 -15.1960001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-2.83699989
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.448
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.9260006
MtrCtrl Vecu Volt M f32[0]	26.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	29.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	-17.0869999
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-185.608994
MtrCurrQaxFinalRef_Amp_M_f32[0]	-218.035004
MtrCurrQaxFinalRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxPrevIntg_Volt_M_f32	15.4982004
MtrCurrQaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxRpl_Amp_M_f32[0]	-218.035004
MtrCurrQaxRpl_Amp_M_f32[1]	11.6370001
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay Rad M f32[1]	-3.875 2.73300004
MtrVoltDax_Volt_M_f32[0]	-23.448
MtrVoltDax Volt M f32[1]	-24.9260006
MtrVoltQax Volt M f32[0]	-23.448
MtrVoltQax_Volt_M_f32[1]	-24.9260006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.63499999
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.81999993
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.874000013
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.578000009
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20769.3008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.513000011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	386.220001
PICurrCotrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	20769.3008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.513000011
SlowDataAccessBufIndex_Cnt_M_u16 k_CLOAFdbackSignalSclFacSlew_UlspS_f32	670.247986
k ILOAFdbackSignalSclFacSlew UlspS f32	2501.06006
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0
k MtrCtrlFeedbackControlDisable Cnt lgc	0
k MtrCtrlVirualResDax Ohm f32	0.188999996
k MtrCtrlVirualResQax Ohm f32	0.138999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	8.58329964

PICurrCntrl_Per1





Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.80000019		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.8449993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	10.2069998		
k_VoltSatQaxPolyCoeff_Uls_f32	0.906000018		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTbIX_Uls_u3p13[0]	1016		
t_CommOffsetTbIX_Uls_u3p13[1]	2286		
t_CommOffsetTblY_Cnt_u16[0]	110		
t_CommOffsetTblY_Cnt_u16[1]	537		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-197.354996		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1734		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1734	1734	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.38547489	-0.38547501 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	30.2225418	30.2224998 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24985	24985 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	11.6370001	11.6370001 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.38 (Repeat Count = 1)		V
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.92999995	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.70000005	





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	524.476013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-993.447021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0329999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0140000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.118000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.713999987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-823.801025
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-751.585022
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	4.9299983
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	14.6809998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.5469999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-22.8390007
MtrCtrl_Vecu_Volt_M_f32[0]	16.882
MtrCtrl_Vecu_Volt_M_f32[1]	19.2420006
MtrCurrDaxPrevIntg_Volt_M_f32	-23.8190002
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxCog_Amp_M_f32	163.561005
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxFinalRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxPrevIntg Volt M f32	8.19719982
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRei_Amp_M_i32[i] MtrCurrQaxRpi Amp_M_f32[0]	
7 - 7 - 7 - 7 - 7 - 7 - 7 - 7 - 7 - 7 -	-216.921997
MtrCurrQaxRpl_Amp_M_f32[1]	-184.923996
MtrPosComputationDelay_Rad_M_f32[0]	0.691999972
MtrPosComputationDelay_Rad_M_f32[1]	1.05700004
MtrVoltDax_Volt_M_f32[0]	15.5469999
MtrVoltDax_Volt_M_f32[1]	-22.8390007
MtrVoltQax_Volt_M_f32[0]	15.5469999
MtrVoltQax_Volt_M_f32[1]	-22.8390007
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.141000003
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0670000017
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.445899993
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.039999991
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	38607.8008
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.001500000001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	38607.8008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.00150000001
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3129.08008
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	51.4420013
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.104999997
k_MtrCtrlVirualResQax_Ohm_f32	0.032999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	1.54809999
k_MtrVoltDaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	14.2641001
k MtrVoltQaxIntegLoLim Volt f32	-7.5
k MtrVoltVecuFiltEnable Cnt lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	10.2329998
k_VoltSatQaxPolyCoeff_Uls_f32	-11.7980003
k_deadtimeVScale_UIs_f32	0.962000012
t_CommOffsetTbIX_UIs_u3p13[0]	1408
t_CommOffsetTblX_Uls_u3p13[1]	3505
t_CommOffsetTbIY_Cnt_u16[0]	100
t_CommOffsetTblY_Cnt_u16[1]	429
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	152.016006
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4045
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-40.9220009

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	429	429	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63045	63045 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	185.149994	185.149994 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	10.0092726	10.0093002 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-15.5712671	-15.5712996 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37834	37834 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-7.5	-7.5 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997	-216.921997 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	185.149994	185.149994 ± 7.81E-03	✓

Test Step Call Trace	est Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.39 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.173996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-213.335007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80900002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.64300001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-495.540985
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-831.38501
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0869999975
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.71700001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.625
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	799.594971
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	445.729004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.9510002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.6159992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5.70200014
MtrCtrl_Vecu_Volt_M_f32[1]	8.06200027
MtrCurrDaxPrevIntg_Volt_M_f32	5.15299988
MtrCurrDaxRef_Amp_M_f32[0]	-105.246002
MtrCurrDaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxCog_Amp_M_f32	-209.716003
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxFinalRef_Amp_M_f32[1] 46.8180008 MtrCurrQaxPrevIntg_Volt_M_f32 15.6709003 MtrCurrQaxRef_Amp_M_f32[0] 106.072998 MtrCurrQaxRef_Amp_M_f32[1] -112.455002 MtrCurrQaxRpl_Amp_M_f32[0] -82.2979965 MtrCurrQaxRpl_Amp_M_f32[1] 46.8180008 MtrPosComputationDelay_Rad_M_f32[0] 3.39899993 MtrPosComputationDelay_Rad_M_f32[1] 2.32800007 MtrVoltDax_Volt_M_f32[0] -31 MtrVoltDax_Volt_M_f32[1] -31 MtrVoltQax_Volt_M_f32[0] -31 MtrVoltQax_Volt_M_f32[1] -31 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.699000001 0.828000009 PICurrCntrl InverterFailSclFac Uls M f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.369500011 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.476000011 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -194.190002 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 947.73999 $PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32$ 21678.8008 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.862100005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 947.73999 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 21678.8008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.862100005 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3473.06006 k ILOAFdbackSignalSclFacSlew UlspS f32 466.734985 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k MtrCtrlFeedbackControlDisable Cnt lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.0130000003 k MtrCtrlVirualResQax Ohm f32 0.189999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k MtrCurrQaxRefModifRplEn Cnt lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 13 2995005 k_MtrVoltDaxIntegLoLim_Volt_f32 -2.5999999 k MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 17.0296993 -2.5999999 $k_MtrVoltQaxIntegLoLim_Volt_f32$ k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k VoltSatDaxPolyCoeff Uls f32 22.2140007 k_VoltSatQaxPolyCoeff_Uls_f32 -4.26499987 k deadtimeVScale Uls f32 0.958000004 t_CommOffsetTblX_Uls_u3p13[0] 1556 t_CommOffsetTblX_Uls_u3p13[1] 5071 t_CommOffsetTblY_Cnt_u16[0] 718 t_CommOffsetTbIY_Cnt_u16[1] 721 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 54.1119995 target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr 1747 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 75.0830002 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 721 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 62783 62783 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 220 220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 1.23833871 1.23834002 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 5.32030106 5.3203001 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 37838 37838 ± 1.52588E-05 $MtrCurrDaxPrevIntg_Volt_M_f32$ -2.5999999 -2.5999999 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] 220 220 ± 7.81E-03

46.8180008

MtrCurrQaxFinalRef_Amp_M_f32[1]

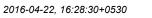
46.8180008 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

lausa .	Innut Value
lame	Input Value
astDataAccessBufIndex_Cnt_M_u16	1
/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
ItrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
ItrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.122000001
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123999998
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.103
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10899997
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.992999971
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-192.371002
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	695.664001
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00800000038
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0189999994
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.611000001
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.84599996
trCtrl MtrQaxPropotionalGain Ohm M f32[0]	875.080017
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-275.667999
ItrCtrl MtrVoltDaxFF Volt M f32[0]	29.0550003
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.6779995
trCtrl MtrVoltQaxFF Volt M f32[0]	31
trCtrl MtrVoltQaxFF Volt M f32[1]	31
trCtrl Vecu Volt M f32[0]	18.3600006
trCtrl_Vecu_Volt_M_f32[1]	20.7199993
trCurrDaxPrevIntg Volt M f32	-18.4589996
trCurrDaxRef_Amp_M_f32[0]	-213.026993
trCurrDaxRef Amp M f32[1]	-66.7229996
ltrCurrQaxCog_Amp_M_f32	77.189003
trCurrQaxFinalRef Amp M f32[0]	160.044006
	165.242004
trCurrQaxFinalRef_Amp_M_f32[1] trCurrQaxPrevIntg_Volt_M_f32	15.4617996
	
trCurrQaxRef_Amp_M_f32[0]	24.6130009 -20.9400005
trCurrQaxRef_Amp_M_f32[1]	
trCurrQaxRpl_Amp_M_f32[0]	160.044006
trCurrQaxRpl_Amp_M_f32[1]	165.242004
trPosComputationDelay_Rad_M_f32[0]	-3.51399994
trPosComputationDelay_Rad_M_f32[1]	-2.17799997
ItrVoltDax_Volt_M_f32[0]	31
ItrVoltDax_Volt_M_f32[1]	31
ItrVoltQax_Volt_M_f32[0] ItrVoltQax Volt M f32[1]	31 31

PICurrCntrl_Per1





Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.495000005		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.50999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.244599998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.736000001		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	18254.6992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.245199993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18254.6992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.245199993		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1865.18005		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7841.00977		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.171000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.0909999982		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	4.52769995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	14.1113997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-15.96		
k VoltSatQaxPolyCoeff Uls f32	16.2980003		
k_deadtimeVScale_UIs_f32	0.972000003		
t CommOffsetTblX Uls u3p13[0]	401		
t_CommOffsetTblX_Uls_u3p13[1]	1457		
t CommOffsetTblY Cnt u16[0]	1020		
t CommOffsetTblY Cnt u16[1]	1562		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	-17.6900005		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	951		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	74.0660019		
target MtrCntrl Read SysState Cnt Enum Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	951	951	resuit
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-220	-220 ± 7.81E-03	
MtrCntrl Write MtrDaxVoltage Volt f32(val)	-14.9265556	-14.9266005 ± 4.88E-04	
MtrCntrl Write MtrQaxVoltage Volt f32(val)	26.1750889	26.1751003 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37413	37413 ± 1.52588E-05	
will Onlin_write_rnaseAuvancermai_Rev_uup ro(vai)			
MtrCurrDayProvinta Volt M f32			
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]	4.52769995 160.044006	4.52769995 ± 0.0625 160.044006 ± 7.81E-03	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.41 (Repeat Count = 1) Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0769999996
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.029999993
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0480000004
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.11199999
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.02100003
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	210.968002
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-929.856018
/ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.075000003
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0649999976
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
/ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.40199995
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0790000036
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	203.302002
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	608.874023
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.2709999
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0
/trCtrl_Vecu_Volt_M_f32[0]	22.7800007
/trCtrl_Vecu_Volt_M_f32[1]	25.1399994
/trCurrDaxPrevIntg_Volt_M_f32	-4.07800007
MtrCurrDaxRef_Amp_M_f32[0]	-212.632996
MtrCurrDaxRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxCog_Amp_M_f32	-145.169006
MtrCurrQaxFinalRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972
MtrCurrQaxPrevIntg_Volt_M_f32	30.3006992
MtrCurrQaxRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxRpl_Amp_M_f32[0]	-65.1900024
/trCurrQaxRpl_Amp_M_f32[1]	-216.972
MtrPosComputationDelay_Rad_M_f32[0]	4.01200008
MtrPosComputationDelay_Rad_M_f32[1]	-3.62100005
//trVoltDax_Volt_M_f32[0]	0
//trVoltDax_Volt_M_f32[1]	0
/trVoltQax_Volt_M_f32[0]	0
//dtrVoltQax_Volt_M_f32[1]	0
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.264999986
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.444000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.477999985
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.499000013
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	20.7000008
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23863
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404900014
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	20.7000008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	23863
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.404900014
SlowDataAccessBufIndex_Cnt_M_u16	0
CLOAFdbackSignalSclFacSlew_UlspS_f32	4784.52979
_ILOAFdbackSignalSclFacSlew_UlspS_f32	1499.40002
<_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
<_MtrCtrlVirualResDax_Ohm_f32	0.0649999976
<_MtrCtrlVirualResQax_Ohm_f32	0.0179999992
<pre>c_MtrCurrQaxRefModifDsb_Cnt_lgc</pre>	0

PICurrCntrl_Per1



Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.72659993		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.3562002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-21.0760002		
k_VoltSatQaxPolyCoeff_Uls_f32	22.4570007		
k_deadtimeVScale_Uls_f32	0.961000025		
t_CommOffsetTblX_Uls_u3p13[0]	868		
t_CommOffsetTbIX_Uls_u3p13[1]	1049		
t_CommOffsetTblY_Cnt_u16[0]	1020		
t_CommOffsetTblY_Cnt_u16[1]	1034		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-214.828995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1164		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-149.003006		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1034	1034	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62980	62980 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	44.3240051	44.3240013 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	13.7315779	13.7315998 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	17.04949	17.0494995 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	48919	48919 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	44.3240051	44.3240013 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972	-216.972 ± 7.81E-03	✓

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.42 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.101999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0430000015
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.120999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.802999973
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.56299996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-946.299988





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-752.830994
	0.0579999983
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.944999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.273
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-629.994019
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	916.687988
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.32400036
MtrCtrl MtrVoltDaxFF Volt M f32[1]	-26.5540009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-15.9300003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.7889996
MtrCtrl_Vecu_Volt_M_f32[0]	21.2910004
MtrCtrl_Vecu_Volt_M_f32[1]	23.6509991
MtrCurrDaxPrevIntg_Volt_M_f32	0.981999993
MtrCurrDaxRef_Amp_M_f32[0]	205.820999
MtrCurrDaxRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxCog_Amp_M_f32	-100.035004
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxFinalRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxPrevIntg_Volt_M_f32	12.4246998
MtrCurrQaxRef_Amp_M_f32[0]	140.289001
MtrCurrQaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxRpl_Amp_M_f32[0]	-146.723007
MtrCurrQaxRpl_Amp_M_f32[1]	-121.943001
MtrPosComputationDelay_Rad_M_f32[0]	-3.15400004
	2.54299998
MtrPosComputationDelay_Rad_M_f32[1]	
MtrVoltDax_Volt_M_f32[0]	-15.9300003
MtrVoltDax_Volt_M_f32[1]	-19.7889996
MtrVoltQax_Volt_M_f32[0]	-15.9300003
MtrVoltQax_Volt_M_f32[1]	-19.7889996
PICurrCntrl CurrSensFailSclFac Uls M f32	0.592999995
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.638999999
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0196000002
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0350000001
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	1838.12
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1611
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	1838.12
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.1611
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6671
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3327.94995
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k MtrCtrlFeedbackControlDisable Cnt lgc	0
k MtrCtrlVirualResDax Ohm f32	0.165999994
k_MtrCtrlVirualResQax_Ohm_f32	0.196999997
k MtrCurrQaxRefModifDsb Cnt Igc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	13.8471003
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.6999981
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	4.8657999
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999
k MtrVoltVecuFiltEnable Cnt lgc	0
k VoltSatDaxPolyCoeff Uls f32	12.7969999
k_VoltSatQaxPolyCoeff_Uls_f32	-5.26999998
k_deadtimeVScale_Uls_f32	0.968999982
t_CommOffsetTbIX_UIs_u3p13[0]	3006
t_CommOffsetTblX_Uls_u3p13[1]	6971
t_CommOffsetTblY_Cnt_u16[0]	136
	136 593
t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1]	593
t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	593 0
t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	593 0 0
t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	593 0 0 0
t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	593 0 0 0 0 -96.3310013
t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	593 0 0 0 -96.3310013 3660
t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	593 0 0 0 0 -96.3310013





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	593	593	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63504	63504 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.7191525	-13.7192001 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-15.4085112	-15.4084997 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	7458	7458 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	13.8471003	13.8471003 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-121.943001	-121.943001 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.43 (Repeat Count = 1)		
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00800000038	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0989999995	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.064000003	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0869999975	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.925000012	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.28699994	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-467.540985	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	559.55603	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0729999989	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0450000018	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0130000003	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0970000029	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.53100002	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.621999979	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	363.421997	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-896.711975	
MtrCtrl MtrVoltDaxFF Volt M f32[0]	3.87299991	
MtrCtrl MtrVoltDaxFF Volt M f32[1]	19.2730007	
MtrCtrl MtrVoltQaxFF Volt M f32[0]	18.8899994	
MtrCtrl MtrVoltQaxFF Volt M f32[1]	6.02799988	
MtrCtrl Vecu Volt M f32[0]	12.1129999	
MtrCtrl Vecu Volt M f32[1]	14.4729996	
MtrCurrDaxPrevIntg Volt M f32	22.4890003	
MtrCurrDaxRef_Amp_M_f32[0]	-69.0940018	
MtrCurrDaxRef Amp M f32[1]	161.973007	
MtrCurrQaxCog_Amp_M_f32	-184.522003	
MtrCurrQaxFinalRef Amp M f32[0]	-208.287994	
MtrCurrQaxFinalRef Amp M f32[1]	-27.9839993	
MtrCurrQaxPrevIntg Volt M f32	24.3127995	

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxRef_Amp_M_f32[0] 91.8850021 MtrCurrQaxRef_Amp_M_f32[1] 182.261002 MtrCurrQaxRpl_Amp_M_f32[0] -208.287994 MtrCurrQaxRpl_Amp_M_f32[1] -27.9839993 MtrPosComputationDelay_Rad_M_f32[0] -3.4059999 MtrPosComputationDelay_Rad_M_f32[1] 1.83000004 MtrVoltDax_Volt_M_f32[0] 18.8899994 MtrVoltDax_Volt_M_f32[1] 6.02799988 MtrVoltQax_Volt_M_f32[0] 18.8899994 $MtrVoltQax_Volt_M_f32[1]$ 6 02799988 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.287 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.202999994 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.0443000011 PICurrCntrl MtrCurrQaxSatFluxRatio_Uls_M_f32 0.128999993 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 22.2399998 PICurrCntrl MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 30983.1992 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.328200012 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 22.2399998 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 386.220001 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32$ 30983.1992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.328200012 SlowDataAccessBufIndex_Cnt_M_u16 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 1324.80005 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 754.981018 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.0610000007 k MtrCtrlVirualResQax Ohm f32 0.112000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k MtrCurrQaxRefModifRplEn Cnt lgc 0 14 0340996 k_MtrVoltDaxIntegHiLim_Volt_f32 k MtrVoltDaxIntegLoLim Volt f32 -4.80000019 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 26.7854996 k MtrVoltQaxIntegLoLim Volt f32 -4.5999999 k_MtrVoltVecuFiltEnable_Cnt_lgc k VoltSatDaxPolyCoeff_Uls_f32 0.541999996 k_VoltSatQaxPolyCoeff_Uls_f32 5.81099987 k deadtimeVScale Uls f32 0.957000017 t_CommOffsetTbIX_Uls_u3p13[0] 205 t CommOffsetTblX Uls u3p13[1] 4096 t_CommOffsetTblY_Cnt_u16[0] 34 t_CommOffsetTblY_Cnt_u16[1] 96 0 $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -168.113007 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2573 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ 0 **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) 62717 62717 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 220 220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -9.07771492 -9.07771015 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 7.2092185 7.20921993 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 20627 20627 ± 1.52588E-05 $MtrCurrDaxPrevIntg_Volt_M_f32$ Λ 0 + 0.0625MtrCurrQaxFinalRef_Amp_M_f32[0] 220 220 ± 7.81E-03 -27.9839993 -27.9839993 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.44 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0049999989
MtrCtrl MtrDampTermDax Ohm M f32[1]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl MtrDampTermQax Ohm M f32[1]	0.0710000023
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.869000018
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.84599996
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	181.75
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	166.714005
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.10400002
MtrCtrl MtrImpedDax Ohm M f32[1]	0.00600000005
MtrCtrl MtrImpedQax Ohm M f32[0]	0.107000001
MtrCtrl MtrlmpedQax Ohm M f32[1]	0.0109999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.98500001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.287999988
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-837.336975
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-656.465027
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-30.1380005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.920000017
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-8.16100025
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	24.6310005
MtrCtrl_Vecu_Volt_M_f32[0]	26.3600006
MtrCtrl Vecu Volt M f32[1]	28.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-4.96700001
MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
MtrCurrDaxRef Amp M f32[1]	-9.14299965
MtrCurrQaxCog Amp M f32	-197.354996
MtrCurrQaxFinalRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxFinalRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxPrevIntg_Volt_M_f32	18.0771999
MtrCurrQaxRef_Amp_M_f32[0]	-218.035004
MtrCurrQaxRef Amp M f32[1]	11.6370001
MtrCurrQaxRpl_Amp_M_f32[0]	31.5869999
MtrCurrQaxRpl_Amp_M_f32[1]	-186.395996
MtrPosComputationDelay_Rad_M_f32[0]	-2.08400011
MtrPosComputationDelay_Rad_M_f32[1]	2.81699991
MtrVoltDax_Volt_M_f32[0]	-8.16100025
MtrVoltDax Volt M f32[1]	24.6310005
MtrVoltQax_Volt_M_i32[i]	-8.16100025
	24.6310005
MtrVoltQax_Volt_M_f32[1]	24.0310003

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.602999985 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.887000024 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.169799998 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.649999976 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -627.179993 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 8419.69043 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$ 0.0151000004 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -627 179993 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 8419.69043 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0151000004 SlowDataAccessBufIndex_Cnt_M_u16 0 771 372986 k CLOAFdbackSignalSclFacSlew UlspS f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 798.940002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0700000003 $k_MtrCtrlVirualResQax_Ohm_f32$ 0.0270000007 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 18.6986008 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -4.9000001 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 10.9334002 k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 -5.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -20.1189995 k_VoltSatQaxPolyCoeff_Uls_f32 -7.09100008 k_deadtimeVScale_Uls_f32 0.995999992 t CommOffsetTblX Uls u3p13[0] 2212 t_CommOffsetTblX_Uls_u3p13[1] 4742 t CommOffsetTblY Cnt u16[0] 109 t_CommOffsetTblY_Cnt_u16[1] 367 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -5.66300011 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1243 -207.917999 target MtrCntrl Read MtrCurrQax Amp f32 Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name Actual Value **Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 367 367 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 65273 65273 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -52.2670135 -52.2669983 ± 7.81E-03 0.133214787 0.133214995 ± 4.88E-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -26.254221 -26.2542 ± 4.88E-04 10978 + 1 52588F-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 10978 0 ± 0.0625 MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0] -52 2670135 -52 2669983 + 7 81F-03

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

-186.395996

-186.395996 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]





Test Step 2.45 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0370000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80200005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.740999997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-489.436005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	938.341003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.019999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10899997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.479999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	916.997009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1002.97998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.1960001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-2.83699989
MtrCtrl_Vecu_Volt_M_f32[0]	5.33099985
MtrCtrl_Vecu_Volt_M_f32[1]	7.69099998
MtrCurrDaxPrevIntg_Volt_M_f32	6.17600012
MtrCurrDaxRef_Amp_M_f32[0]	-146.173996
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxCog_Amp_M_f32	152.016006
MtrCurrQaxFinalRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxPrevIntg Volt M f32	1.08770001
MtrCurrQaxRef Amp M f32[0]	-216.921997
MtrCurrQaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxRpl Amp M f32[0]	-133.947006
MtrCurrQaxRpl Amp M f32[1]	75.7020035
MtrPosComputationDelay_Rad_M_f32[0]	-6.23799992
MtrPosComputationDelay_Rad_M_f32[1]	2.11599994
MtrVoltDax_Volt_M_f32[0]	15.1960001
MtrVoltDax_Volt_M_f32[1]	-2.83699989
MtrVoltQax Volt M f32[0]	15.1960001
MtrVoltQax_Volt_M_f32[1]	-2.83699989
PICurrCntrl CurrSensFailSclFac Uls M f32	0.43299998
PICurrCntrl InverterFailSclFac Uls M f32	0.0109999999
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.335599989
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.85199998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-43.1699982
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-10.21
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	12079.9004
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.620700002
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-43.1699982
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-10.21
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12079.9004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.620700002
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2475.81006
k ILOAFdbackSignalSclFacSlew UlspS f32	2475.81006
K_ILOAI ubackolgilalocii acolew_Ulspo_loz	2645.06006
k MtrCtrlCurrl oonSecOrTranEcEnable Cat Ica	1
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0 47000005
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32	0.179000005
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32	0.179000005 0.0120000001
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32	0.179000005

PICurrCntrl_Per1





Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.0999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.614899993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.26999998		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9449997		
k_deadtimeVScale_Uls_f32	0.962000012		
t_CommOffsetTblX_Uls_u3p13[0]	4809		
t_CommOffsetTblX_Uls_u3p13[1]	5553		
t_CommOffsetTblY_Cnt_u16[0]	663		
t_CommOffsetTblY_Cnt_u16[1]	905		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	114.946999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1956		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1956	1956	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.381894678	0.381895989 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.79481602	-4.79481983 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32410	32410 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	-
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035	75.7020035 ± 7.81E-03	✓

Actual Function	Count	Expected Function	Count	Result
MtrCntrl Read MtrCurrQax Amp f32	1	MtrCntrl Read MtrCurrQax Amp f32	1	~
MtrCntrl Read MtrCurrDax Amp f32	1	MtrCntrl Read MtrCurrDax Amp f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.46 (Repeat Count = 1)		~
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.116999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0860000029	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.00600000005	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.579	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.407999992	





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-297.562012
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	435.532013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0189999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.878000021
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.247999996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	791.299988
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-595.505981
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	10.0620003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	10.7410002
	-4.92999983
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	14.6809998
MtrCtrl_Vecu_Volt_M_f32[0]	18.2229996
MtrCtrl_Vecu_Volt_M_f32[1]	20.5830002
MtrCurrDaxPrevIntg_Volt_M_f32	2.41400003
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	54.1119995
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevintg Volt M f32	
	11.5314999
MtrCurrQaxRef_Amp_M_f32[0]	138.595001
MtrCurrQaxRef_Amp_M_f32[1]	-157.388
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	5.76999998
MtrPosComputationDelay_Rad_M_f32[1]	4.8210001
MtrVoltDax_Volt_M_f32[0]	-4.92999983
MtrVoltDax_Volt_M_f32[1]	14.6809998
MtrVoltQax_Volt_M_f32[0]	-4.92999983
MtrVoltQax_Volt_M_f32[1]	14.6809998
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.890999973
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.76700002
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.976100028
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.837000012
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47476.6016
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.878300011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	47476.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.878300011
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4075.97998
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1135.18994
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0430000015
k_MtrCtrlVirualResQax_Ohm_f32	0.0289999992
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k MtrCurrQaxRefModifRplEn Cnt Igc	0
k MtrVoltDaxIntegHiLim Volt f32	6.86969995
k_MtrVoltDaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	0.205699995
k_MtrVoltQaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-11.4569998
k_VoltSatQaxPolyCoeff_Uls_f32	0.670000017
k_deadtimeVScale_Uls_f32	0.958000004
t_CommOffsetTbIX_UIs_u3p13[0]	4424
t_CommOffsetTbIX_UIs_u3p13[1]	7552
t_CommOffsetTblY_Cnt_u16[0]	1052
t_CommOffsetTblY_Cnt_u16[1]	1891
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	3.89299989
target_witrontin_read_witroutrbax_Amp_toz_var	
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1889
	1889 136.341003

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1889	1889	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.32646561	7.32645988 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	28.7801018	28.7800999 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	52885	52885 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.47 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	24.6130009
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-20.9400005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.270000011
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.89100003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	898.598999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	416.613007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0199999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.699000001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	378.188995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	157.612
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	26.9950008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	7.13500023
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	30.9510002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-20.6159992
MtrCtrl_Vecu_Volt_M_f32[0]	28.0200005
MtrCtrl_Vecu_Volt_M_f32[1]	30.3799992
MtrCurrDaxPrevIntg_Volt_M_f32	17.8910007
MtrCurrDaxRef_Amp_M_f32[0]	171.485992
MtrCurrDaxRef_Amp_M_f32[1]	163.787003

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxCog_Amp_M_f32 -17.6900005 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[1] -66.7229996 $MtrCurrQaxPrevIntg_Volt_M_f32$ 10.2707996 MtrCurrQaxRef_Amp_M_f32[0] -100.282997 MtrCurrQaxRef_Amp_M_f32[1] -120.248001 MtrCurrQaxRpl_Amp_M_f32[0] -213.026993 $MtrCurrQaxRpl_Amp_M_f32[1]$ -66.7229996 MtrPosComputationDelay_Rad_M_f32[0] 5.76399994 MtrPosComputationDelay_Rad_M_f32[1] 5 0999999 MtrVoltDax_Volt_M_f32[0] 30.9510002 MtrVoltDax_Volt_M_f32[1] -20 6159992 MtrVoltQax_Volt_M_f32[0] 30.9510002 MtrVoltQax Volt M f32[1] -20 6159992 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.978999972 PICurrCntrl InverterFailSclFac Uls M f32 0.845000029 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.128399998 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.428000003 $PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32$ 267.119995 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -38.7999992 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 3431.37012 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.436399996 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 267.119995 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -38.7999992 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32$ 3431.37012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.436399996 SlowDataAccessBufIndex_Cnt_M_u16 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6201.14014 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 194.557007 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k MtrCtrlFeedbackControlDisable Cnt lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0469999984 k MtrCtrlVirualResQax Ohm f32 0.164000005 k MtrCurrQaxRefModifDsb Cnt Igo 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k MtrVoltDaxIntegHiLim Volt f32 26.1525002 k_MtrVoltDaxIntegLoLim_Volt_f32 -2.5999999 k MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 4.69950008 k MtrVoltQaxIntegLoLim Volt f32 -2.5999999 k_MtrVoltVecuFiltEnable_Cnt_lgc k VoltSatDaxPolyCoeff Uls f32 -12.8179998 k_VoltSatQaxPolyCoeff_Uls_f32 12.3579998 k_deadtimeVScale_Uls_f32 0.972000003 t_CommOffsetTblX_Uls_u3p13[0] 705 t_CommOffsetTblX_Uls_u3p13[1] 4996 t_CommOffsetTblY_Cnt_u16[0] 1077 t_CommOffsetTblY_Cnt_u16[1] 1690 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 45.3779984 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 4409 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value** Name **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1690 1690 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63700 63700 ± 1 -35 8349991 -35.8349991 ± 7.81E-03 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.6776619 -28.6777 ± 4.88E-04 -7.04092979 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -7.04093504 34300 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 34300 0 ± 0.0625 MtrCurrDaxPrevIntg Volt M f32 0 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 -213.026993 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[1] -35.8349991 -35.8349991 ± 7.81E-03



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value	
FastDataAccessBufIndex Cnt M u16	1	
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0560000017	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0469999984	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70599997	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.759000003	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-535.288025	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-568.218018	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0160000008	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0549999997	
ftrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0979999974	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.624000013	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.85000002	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-327.623993	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	577.963989	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	21.0270004	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-7.53299999	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	29.0550003	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.6779995	
/trCtrl_Vecu_Volt_M_f32[0]	6.83099985	
1trCtrl_Vecu_Volt_M_f32[1]	9.19099998	
1trCurrDaxPrevIntg Volt M f32	15.6529999	
/trCurrDaxRef Amp M f32[0]	106.072998	
/trCurrDaxRef_Amp_M_f32[1]	-112.455002	
/trCurrQaxCog_Amp_M_f32	-214.828995	
htrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996	
htrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007	
ItrCurrQaxPrevIntg Volt M f32	5.7888999	
1trCurrQaxRef_Amp_M_f32[0]	-68.6760025	
ItrCurrQaxRef_Amp_M_f32[1]	-96.776001	
ItrCurrQaxRpl Amp M f32[0]	-212.632996	
ItrCurrQaxRpl Amp M f32[1]	-205.085007	
htrPosComputationDelay_Rad_M_f32[0]	-0.913999975	
/trPosComputationDelay Rad M f32[1]	3.25399995	
/trVoltDax_Volt_M_f32[0]	29.0550003	
/trVoltDax_Volt_M_102[0]	-17.6779995	
MtrVoltQax Volt M f32[0]	29.0550003	
MtrVoltQax_Volt_M_f32[1]	-17.6779995	

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.610000014 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.959999979 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.593599975 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.291000009 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 404.899994 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 13842.5 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$ 0.35710001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 404.899994 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -194 190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 13842.5 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.35710001 SlowDataAccessBufIndex_Cnt_M_u16 4211 16016 k CLOAFdbackSignalSclFacSlew UlspS f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3085.33008 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.0430000015 k_MtrCtrlVirualResQax_Ohm_f32 0.119000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 8.83409977 k_MtrVoltDaxIntegHiLim_Volt_f32 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -12.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 13.2000999 k_MtrVoltQaxIntegLoLim_Volt_f32 -12.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3659992 k_VoltSatQaxPolyCoeff_Uls_f32 8.98099995 k_deadtimeVScale_Uls_f32 0.990999997 t CommOffsetTblX Uls u3p13[0] 1580 t_CommOffsetTblX_Uls_u3p13[1] 2671 t CommOffsetTblY Cnt u16[0] 161 t_CommOffsetTblY_Cnt_u16[1] 1743 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 103.652 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4487 target MtrCntrl Read MtrCurrQax Amp f32 Val 1.62199998 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Actual Value **Expected Value** Name Result MtrCntrl Write CommOffset Cnt u16(val) 1743 1743 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 64946 64946 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 220 220 ± 7.81E-03 3.61272049 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 3.61272001 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 8.36116123 8.36116028 ± 4.88E-04 38195 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 38195 0 ± 0.0625

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

-212.632996

220

-212.632996 ± 7.81E-03

220 ± 7.81E-03

MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]

MtrCurrQaxFinalRef_Amp_M_f32[1]





Test Step 2.49 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	140.289001 178.235992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0430000015
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.829
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.39300001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-515.534973
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.975006
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.120999999
MtrCtrl_MtrlmpedOay_Ohm_M_f32[1] MtrCtrl_MtrlmpedOay_Ohm_M_f32[0]	0.090999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.954
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.512000024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	305.28299
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-513.950012
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.9390001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-24.3929996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.0189991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	13.2709999
MtrCtrl_Vecu_Volt_M_f32[0]	8.61200047
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg Volt M f32	10.9720001 -19.3589993
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	-96.3310013
MtrCurrQaxFinalRef_Amp_M_f32[0]	205.820999
MtrCurrQaxFinalRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxPrevIntg_Volt_M_f32	14.2783003
MtrCurrQaxRef_Amp_M_f32[0]	-139.906998
MtrCurrQaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxRpl_Amp_M_f32[0]	205.820999
MtrCurrQaxRpl_Amp_M_f32[1] MtrDocComputationDology Rad, M_f23[0]	-206.792007 2.22900009
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay Rad M f32[1]	-0.579999983
MtrVoltDax Volt M f32[0]	-22.0189991
MtrVoltDax_Volt_M_f32[1]	13.2709999
MtrVoltQax_Volt_M_f32[0]	-22.0189991
MtrVoltQax_Volt_M_f32[1]	13.2709999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.791999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0799999982
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.619400024
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.783999979
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	865.320007
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl MtrVecuFilt M str.TermN Uls f32	-194.190002 46503.6992
PICurrCntri_MtrVecuFilt_M_str.TermN_UIS_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIS_f32	0.198599994
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	865.320007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46503.6992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.198599994
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7813.02979
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6623.8501
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.12099999
k_MtrCtrlVirualResQax_Ohm_f32 k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.100000001
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	30.1000996
_	

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	5.77519989		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.5739994		
k_VoltSatQaxPolyCoeff_Uls_f32	-11.3669996		
k_deadtimeVScale_Uls_f32	0.952000022		
t_CommOffsetTblX_Uls_u3p13[0]	908		
t_CommOffsetTblX_Uls_u3p13[1]	5956		
t_CommOffsetTblY_Cnt_u16[0]	578		
t_CommOffsetTblY_Cnt_u16[1]	1247		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	99.348999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2730		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2730	2730	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.1812501	-4.1812501 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.27480721	2.27481008 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	48299	48299 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	205.820999	205.820999 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	•	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓	

Test Step 2.50 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	91.8850021
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	182.261002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0309999995
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.545000017
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.884000003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-366.040009





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-870.554993
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0189999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.690999985
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.344000012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	592.877014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	559.130005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	5.68100023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.06599998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.32400036 -26.5540009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0]	24.4610004
MtrCtrl_Vecu_Volt_M_f32[1]	26.8209991
MtrCurrDaxPrevIntg_Volt_M_f32	29.6800003
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	-168.113007
MtrCurrQaxFinalRef_Amp_M_f32[0]	-69.0940018
MtrCurrQaxFinalRef_Amp_M_f32[1]	161.973007
MtrCurrQaxPrevIntg_Volt_M_f32	18.2201996
MtrCurrQaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxRpl_Amp_M_f32[0]	-69.0940018
MtrCurrQaxRpl_Amp_M_f32[1]	161.973007
MtrPosComputationDelay_Rad_M_f32[0]	4.2750001
MtrPosComputationDelay_Rad_M_f32[1]	-2.16300011
MtrVoltDax_Volt_M_f32[0]	-8.32400036
MtrVoltDax_Volt_M_f32[1]	-26.5540009
MtrVoltQax_Volt_M_f32[0]	-8.32400036
MtrVoltQax_Volt_M_f32[1]	-26.5540009
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.785000026
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.55400002
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.91960001
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.871999979
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	20.7000008
PICurrCotrl_MtrVeouFilt_M_str.TermN_UIs_f32	42029.6016 0.0784000009
PICurrCotrl_MtrVecuFilt_M_str.TermD_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	20.700008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	42029.6016
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.0784000009
SlowDataAccessBufIndex Cnt M u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	712.458008
k ILOAFdbackSignalSclFacSlew UlspS f32	303.729004
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k MtrCtrlFeedbackControlDisable Cnt Igc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.100000001
k_MtrCtrlVirualResQax_Ohm_f32	0.079999982
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	8.17039967
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	12.5521002
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-24.3269997
k_VoltSatQaxPolyCoeff_Uls_f32	-18.0820007
k_deadtimeVScale_UIs_f32	0.976999998
t_CommOffsetTbIX_UIs_u3p13[0]	7
t_CommOffsetTbIX_UIs_u3p13[1]	179
t_CommOffsetTbIY_Cnt_u16[0]	128
t_CommOffsetTblY_Cnt_u16[1]	452
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	0
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	0 1 80.5459976
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	0





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	452	452	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64028	64028 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	154.908997	154.908997 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.3200226	-17.3199997 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	16.4666386	16.4666004 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	36134	36134 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	154.908997	154.908997 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	161.973007	161.973007 ± 7.81E-03	~

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Test Step 2.51 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-218.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	11.6370001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0489999987
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.17900002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46399999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-613.749023
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-825.028992
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0199999996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.202000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.88499999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	635.659973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-88.5709991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	19.6130009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.3180008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.87299991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	19.2730007
MtrCtrl_Vecu_Volt_M_f32[0]	21.3409996
MtrCtrl_Vecu_Volt_M_f32[1]	23.7010002
MtrCurrDaxPrevIntg_Volt_M_f32	-25.5009995
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	-5.66300011
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxPrevIntg_Volt_M_f32	16.0422993

2016-04-22, 16:28:30+0530



PICurrCntri_Per1		(W	acitato
Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[0]	160.044006		
MtrCurrQaxRef_Amp_M_f32[1]	165.242004		
MtrCurrQaxRpl_Amp_M_f32[0]	-132.813004		
MtrCurrQaxRpl_Amp_M_f32[1]	-9.14299965		
MtrPosComputationDelay Rad M f32[0]	-1.83500004		
MtrPosComputationDelay_Rad_M_f32[1]	0.768999994		
MtrVoltDax_Volt_M_f32[0]	3.87299991		
MtrVoltDax_Volt_M_f32[1]	19.2730007		
MtrVoltQax Volt M f32[0]	3.87299991		
MtrVoltQax_Volt_M_f32[1]	19.2730007		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.472000003		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.796000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.4287		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.926999986		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17234.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0538000017		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17234.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0538000017		
SlowDataAccessBufIndex_Cnt_M_u16	0		
CLOAFdbackSignalSclFacSlew UlspS f32	642.700012		
LOAFdbackSignalSclFacSlew UlspS f32	7742.27002		
<pre>c_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc</pre>	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0280000009		
k_MtrCtrlVirualResQax_Ohm_f32	0.174999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.6893997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.8882008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
<pre><_VoltSatDaxPolyCoeff_Uls_f32</pre>	6.35500002		
k_VoltSatQaxPolyCoeff_Uls_f32	21.1189995		
k_deadtimeVScale_Uls_f32	0.967999995		
_CommOffsetTblX_Uls_u3p13[0]	4506		
t_CommOffsetTblX_Uls_u3p13[1]	5381		
CommOffsetTblY_Cnt_u16[0]	1282		
t_CommOffsetTblY_Cnt_u16[1]	1346		
arget MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	0		
arget MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	1		
rarget_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
rarget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	41.1769981		
rarget_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3770		
arget_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Resu
MtrCntrl Write CommOffset Cnt u16(val)	3770	3770	
MtrCntrl Write ModIdx Uls u16p16(val)	0	0 ± 1	
VtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
VtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.727851868	0.727851987 ± 4.88E-04	
VtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.78495884	4.78495979 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	47971	47971 ± 1.52588E-05	
		0 ± 0.0625	
MtrCurrDaxPrevIntg Volt M f32	0		
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	



est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

ame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	0	
ItrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt lgc ptr	
ItrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val	
ItrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	
triCntrl_Read_MotourrDax Amp_f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
ItrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-216.921997	
trCtrl MtrCurrDaxMaxVal Amp M f32[1]	-184.923996	
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0979999974	
trCtrl MtrDampTermDax_Ohm M f32[1]	0.0850000009	
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003	
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.082000003	
trCtrl MtrDaxIntegralGain Ohm M f32[0]	1.671	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.986000001	
trCtrl MtrDaxPropotionalGain Ohm M f32[0]	129.369003	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-141.128998	
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.065999996	
trCtrl_MtrImpedDax_Onm_M_i32[i]	0.0120000001	
trCtrl_MtrImpedDax_Onm_M_i32[i]	0.0970000029	
: :	0.0160000029	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.111000001	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.83399999	
ItrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	972.747009	
ItrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	150.199997	
ItrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]		
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	28.1019993	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.381000012	
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-30.1380005	
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.920000017	
trCtrl_Vecu_Volt_M_f32[0]	28.2360001	
trCtrl_Vecu_Volt_M_f32[1]	30.5960007	
trCurrDaxPrevIntg_Volt_M_f32	28.816	
ItrCurrDaxRef_Amp_M_f32[0]	-146.723007	
trCurrDaxRef_Amp_M_f32[1]	-121.943001	
trCurrQaxCog_Amp_M_f32	114.946999	
htrCurrQaxFinalRef_Amp_M_f32[0]	-146.173996	
ItrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007	
trCurrQaxPrevIntg_Volt_M_f32	22.5016003	
trCurrQaxRef_Amp_M_f32[0]	-65.1900024	
trCurrQaxRef_Amp_M_f32[1]	-216.972	
trCurrQaxRpl_Amp_M_f32[0]	-146.173996	
trCurrQaxRpl_Amp_M_f32[1]	-213.335007	
ttrPosComputationDelay_Rad_M_f32[0]	5.08300018	
ltrPosComputationDelay_Rad_M_f32[1]	3.41400003	
ltrVoltDax_Volt_M_f32[0] ltrVoltDax_Volt_M_f32[1]	-30.1380005 0.920000017	

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-30.1380005		
MtrVoltQax_Volt_M_f32[1]	0.920000017		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.56000002		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.125		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.716899991		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.54400003		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20241.6992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.124300003		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-43.1699982		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	20241.6992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.124300003		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	4748.89014		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5014.08008		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.109999999		
k MtrCtrlVirualResQax Ohm f32	0.0350000001		
- -	0		
k_MtrCurrQaxRefModifDsb_Cnt_lgc k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0		
	8.6864996		
k_MtrVoltDaxIntegHiLim_Volt_f32 k_MtrVoltDaxIntegLoLim_Volt_f32			
	-30.2000008 0		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	30.9398003		
k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
	0		
k_MtrVoltVecuFiltEnable_Cnt_lgc	-5.84200001		
k_VoltSatDaxPolyCoeff_Uls_f32			
k_VoltSatQaxPolyCoeff_Uls_f32	-16.993		
k_deadtimeVScale_UIs_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	3030		
t_CommOffsetTblX_Uls_u3p13[1]	5366		
t_CommOffsetTblY_Cnt_u16[0]	554		
t_CommOffsetTblY_Cnt_u16[1]	778		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-30.7789993		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4190		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	48.8400002		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3	1_	
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	778	778	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63111	63111 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-33.9630051	-33.9630013 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	18.543644	18.5436001 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-19.8871384	-19.8871002 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12422	12422 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-33.9630051	-33.9630013 ± 7.81E-03	•
MtrCurrOavEinalDef Amp M f32[1]	213 335007	213 335007 ± 7 815 03	

est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

-213.335007

-213.335007 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.53 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.50699997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.64999998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	491.182007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	987.453979
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0520000011
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00800000038
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.82099998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.995999992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-73.2539978
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-688.901978
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	4.64300013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.7069998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	4.36100006
MtrCtrl_Vecu_Volt_M_f32[0]	28.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	30.2600002
MtrCurrDaxPrevIntg_Volt_M_f32	-11.5699997
MtrCurrDaxRef_Amp_M_f32[0]	-208.287994
MtrCurrDaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxCog_Amp_M_f32	3.89299989
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993
MtrCurrQaxPrevIntg_Volt_M_f32	25.7052002
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRpl_Amp_M_f32[1]	133.692993
MtrPosComputationDelay_Rad_M_f32[0]	4.79699993
MtrPosComputationDelay_Rad_M_f32[1]	-0.542999983
MtrVoltDax_Volt_M_f32[0]	-26.5079994
MtrVoltDax_Volt_M_f32[1]	4.36100006
MtrVoltQax_Volt_M_f32[0]	-26.5079994
MtrVoltQax_Volt_M_f32[1]	4.36100006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.745000005
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.305999994
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.720300019
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0160000008
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	46120.5
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.107100002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.107100002
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5850.64014
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2794.15991
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0399999991
k_MtrCtrlVirualResQax_Ohm_f32	0.150999993
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0

PICurrCntrl_Per1



Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.24860001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.75800037		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-12.8660002		
k_VoltSatQaxPolyCoeff_Uls_f32	6.20200014		
k_deadtimeVScale_Uls_f32	0.986999989		
t_CommOffsetTbIX_Uls_u3p13[0]	4850		
t_CommOffsetTblX_Uls_u3p13[1]	6241		
t_CommOffsetTblY_Cnt_u16[0]	1044		
t_CommOffsetTblY_Cnt_u16[1]	1978		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	23		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	107.702003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1044	1044	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	27056	27056 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-11.7069998	-11.7069998 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.36100006	4.36100006 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	47208	47208 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013	-91.4420013 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	~

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Test Step 2.54 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	160.044006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	165.242004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.029999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.81200004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.92799997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-519.974976





Name	
The state of the s	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-32.9770012
	0.050999999
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.061999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.952000022
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.88499999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-957.802979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	641.666016
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	25.9820004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-23.0480003
MtrCtrl MtrVoltQaxFF Volt M f32[0]	10.0620003
	10.7410002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	
MtrCtrl_Vecu_Volt_M_f32[0]	22.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	24.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	20.1009998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	45.3779984
MtrCurrQaxFinalRef Amp M f32[0]	171.485992
MtrCurrQaxFinalRef Amp M f32[1]	163.787003
MtrCurrQaxPrevIntg Volt M f32	28.1571999
	-208.287994
MtrCurrQaxRef_Amp_M_f32[0]	
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	171.485992
MtrCurrQaxRpl_Amp_M_f32[1]	163.787003
MtrPosComputationDelay_Rad_M_f32[0]	-3.4230001
MtrPosComputationDelay_Rad_M_f32[1]	-2.05100012
MtrVoltDax_Volt_M_f32[0]	10.0620003
MtrVoltDax_Volt_M_f32[1]	10.7410002
MtrVoltQax_Volt_M_f32[0]	10.0620003
MtrVoltQax_Volt_M_f32[1]	10.7410002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.476999998
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.254999995
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0443000011
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.142000005
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.742399991
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.742399991
	0.742399991
SlowDataAccessBufIndex_Cnt_M_u16	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3971.34009
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5639.2998
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.00300000003
k_MtrCtrlVirualResQax_Ohm_f32	0.00899999961
k MtrCurrQaxRefModifDsb Cnt lgc	1
k MtrCurrQaxRefModifRpIEn Cnt lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	17.7175007
k MtrVoltDaxIntegLoLim Volt f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	4.11920023
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	24.934
k_VoltSatQaxPolyCoeff_Uls_f32	-16.5429993
k_deadtimeVScale_Uls_f32	0.959999979
t_CommOffsetTbIX_UIs_u3p13[0]	2114
t_CommOffsetTblX_Uls_u3p13[1]	4735
t_CommOffsetTbIX_Cnt_u16[0]	153
t_CommOffsetTblY_Cnt_u16[1]	914
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
torget MtrCntrl Dood MedidyCrlComCyaDft Cnt Inc. Vol	1
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_ModidxSrlComSvcDtt_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	•
	177.046997
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	177.046997





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4147	4147	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.35074472	-4.35073996 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.02756619	2.02757001 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32308	32308 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	171.485992	171.485992 ± 7.81E-03	✓
MtrCurrQaxFinalRef Amp M f32[1]	-220	-220 ± 7.81E-03	✓

Fest Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.55 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0839999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.683000028
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.86699998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-334.098999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	800.172974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.119999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.77999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.85699999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	130.878998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-255.671997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	14.9390001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.4060001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.9950008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	7.13500023
MtrCtrl_Vecu_Volt_M_f32[0]	18.7189999
MtrCtrl_Vecu_Volt_M_f32[1]	21.0790005
MtrCurrDaxPrevIntg_Volt_M_f32	-24.684
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxCog_Amp_M_f32	103.652
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxFinalRef_Amp_M_f32[1] -112.455002 MtrCurrQaxPrevIntg_Volt_M_f32 18.5097008 MtrCurrQaxRef_Amp_M_f32[0] 31.5869999 MtrCurrQaxRef_Amp_M_f32[1] -186.395996 MtrCurrQaxRpl_Amp_M_f32[0] 106.072998 MtrCurrQaxRpl_Amp_M_f32[1] -112.455002 MtrPosComputationDelay_Rad_M_f32[0] 1.03199995 MtrPosComputationDelay_Rad_M_f32[1] 4.79300022 MtrVoltDax_Volt_M_f32[0] 26.9950008 MtrVoltDax_Volt_M_f32[1] 7 13500023 MtrVoltQax_Volt_M_f32[0] 26.9950008 MtrVoltQax_Volt_M_f32[1] 7 13500023 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.0390000008 0.84799999 PICurrCntrl InverterFailSclFac Uls M f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.611599982 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.144999996 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -1118 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 $PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32$ 25640.4004 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.971499979 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 25640.4004 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.971499979 SlowDataAccessBufIndex_Cnt_M_u16 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 4766.68994 k ILOAFdbackSignalSclFacSlew UlspS f32 7056.62988 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k MtrCtrlFeedbackControlDisable Cnt lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0 114 k MtrCtrlVirualResQax Ohm f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k MtrCurrQaxRefModifRplEn Cnt lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 2 74440002 k_MtrVoltDaxIntegLoLim_Volt_f32 -8.68999958 k MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 9.79839993 -8.68999958 $k_MtrVoltQaxIntegLoLim_Volt_f32$ k_MtrVoltVecuFiltEnable_Cnt_lgc k VoltSatDaxPolyCoeff Uls f32 -14.3280001 k_VoltSatQaxPolyCoeff_Uls_f32 -7.07999992 k deadtimeVScale Uls f32 0.99000001 t_CommOffsetTblX_Uls_u3p13[0] 1498 t_CommOffsetTblX_Uls_u3p13[1] 4940 t_CommOffsetTblY_Cnt_u16[0] 125 t_CommOffsetTbIY_Cnt_u16[1] 898 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr 155 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) 64880 64880 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -177.592987 -177.593002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -20.2797813 -20.2798004 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.92063665 4.92063999 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 36092 36092 ± 1.52588E-05 $MtrCurrDaxPrevIntg_Volt_M_f32$ 0 ± 0.0625 106.072998 106.072998 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1] -177.592987 -177.593002 ± 7.81E-03



Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
VtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.118000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.26400006
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.94200003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-771.768005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	61.4269981
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0489999987
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0579999983
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.657999992
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.64900005
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-279.015015
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-333.037994
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.6959991
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-27.8540001
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	21.0270004
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-7.53299999
/trCtrl_Vecu_Volt_M_f32[0]	22.3540001
/trCtrl_Vecu_Volt_M_f32[1]	24.7140007
/ltrCurrDaxPrevIntg_Volt_M_f32	-18.9759998
/ltrCurrDaxRef_Amp_M_f32[0]	209.052002
/ltrCurrDaxRef_Amp_M_f32[1]	-124.994003
ltrCurrQaxCog_Amp_M_f32	99.348999
htrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
/trCurrQaxFinalRef_Amp_M_f32[1]	133.692993
/trCurrQaxPrevIntg_Volt_M_f32	0.0860000029
/trCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
/trCurrQaxRpl_Amp_M_f32[0]	-91.4420013
ltrCurrQaxRpl_Amp_M_f32[1]	133.692993
htrPosComputationDelay_Rad_M_f32[0]	1.44299996
MtrPosComputationDelay_Rad_M_f32[1]	4.22300005
/trVoltDax_Volt_M_f32[0]	21.0270004
//dtrVoltDax_Volt_M_f32[1]	-7.53299999
//trVoltQax_Volt_M_f32[0]	21.0270004
MtrVoltQax_Volt_M_f32[1]	-7.53299999

2016-04-22, 16:28:30+0530



Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.941999972		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.619000018		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0478000008		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.801999986		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.570299983		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.570299983		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7521.91016		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5032.43018		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.079999982		
k_MtrCtrlVirualResQax_Ohm_f32	0.0060000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	17.1720009		
k MtrVoltDaxIntegLoLim Volt f32	-4.57000017		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	21.2935009		
k MtrVoltQaxIntegLoLim Volt f32	-4.57000017		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.3940001		
k VoltSatQaxPolyCoeff Uls f32	10.5640001		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTbIX_UIs_u3p13[0]	6110		
t CommOffsetTbIX UIs u3p13[1]	7324		
t_CommOffsetTblY_Cnt_u16[0]	940		
t CommOffsetTblY Cnt u16[1]	1216		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0		
target MtrCntrl Read MtrCurrDax Amp f32 Val	-161.352005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1115		
target MtrCntrl Read MtrCurrQax Amp f32 Val	20.6149998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
		Even atod Value	Daaul
Name Marchel Weite Commoffeet Cet (146(191))	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1216	1216	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	63438	63438 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-157.339996	-157.339996 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-23.0935135	-23.0935001 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.24554586	-6.24555016 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24909	24909 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	,
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013	-91.4420013 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-157.339996	-157.339996 ± 7.81E-03	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~





Test Step 2.57 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.070000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.199
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.528
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-96.7659988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-485.93399
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrlmpedOax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0489999987
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.114 1.79999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[t]	0.797999978
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	839.791992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-829.577026
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.9399996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	29.1739998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-14.9390001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.3929996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	1.27900004
MtrCurrDaxRef_Amp_M_f32[0]	-200.556
MtrCurrDaxRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxCog_Amp_M_f32	80.5459976
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1] MtrCurrQaxPrevIntg_Volt_M_f32	119.721001 10.5852003
MtrCurrQaxRef_Amp_M_f32[0]	209.052002
MtrCurrQaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxRpl Amp M f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-3.0940001
MtrPosComputationDelay_Rad_M_f32[1]	1.25100005
MtrVoltDax_Volt_M_f32[0]	-14.9390001
MtrVoltDax_Volt_M_f32[1]	-24.3929996
MtrVoltQax_Volt_M_f32[0]	-14.9390001
MtrVoltQax_Volt_M_f32[1]	-24.3929996
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.374000013
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.744000018
PICurrCotrl_MtrCurrOavSatFluxRatio_Uls_M_f32	0.797999978
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0.699000001 570.700012
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	1118
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	36325.3984
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.287999988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	36325.3984
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.287999988
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6822.06006
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5157.0498
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrl/FeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.163000003 0.043999998
k_MtrCtrlVirualResQax_Ohm_f32 k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.0439999998
k MtrCurrQaxRefModifRplEn Cnt Igc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	6.49800014

2016-04-22, 16:28:30+0530



Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.0617008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	11.9960003		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.5869999		
k_deadtimeVScale_Uls_f32	0.998000026		
t_CommOffsetTblX_Uls_u3p13[0]	220		
t_CommOffsetTblX_Uls_u3p13[1]	5037		
t_CommOffsetTblY_Cnt_u16[0]	980		
t_CommOffsetTblY_Cnt_u16[1]	1528		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1366		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	79.6729965		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1528	1528	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65404	65404 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	61.0160065	61.0159988 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.96529174	-3.96529007 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-13.6866035	-13.6865997 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3438	3438 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	61.0160065	61.0159988 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.58 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	0
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10599995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.44500005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	308.303009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-313.46701
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007





trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-12.816	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-15.0170002 5.68100023	
trCtrl_MtrVoltQaxFF_Volt_M_f32[0] trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.06599998	
trCtrl_Vecu_Volt_M_f32[0]	20.2549992	
trCtrl_Vecu_Volt_M_f32[1]	22.6149998	
trCurrDaxPrevIntg_Volt_M_f32	-9.55599976	
trCurrDaxRef_Amp_M_f32[0]	67.4899979	
trCurrDaxRef_Amp_M_f32[1]	119.721001	
trCurrQaxCog_Amp_M_f32	41.1769981	
trCurrQaxFinalRef_Amp_M_f32[0]	-146.173996	
trCurrQaxFinalRef_Amp_M_f32[1]	-213.335007	
trCurrQaxPrevIntg_Volt_M_f32	13.0853004	
trCurrQaxRef_Amp_M_f32[0]	-200.556	
trCurrQaxRef_Amp_M_f32[1]	-98.4449997	
trCurrQaxRpI_Amp_M_f32[0]	-146.173996	
trCurrQaxRpl_Amp_M_f32[1]	-213.335007	
trPosComputationDelay_Rad_M_f32[0]	5.96099997	
trPosComputationDelay_Rad_M_f32[1]	2.0480001	
trVoltDax_Volt_M_f32[0]	5.68100023	
trVoltDax_Volt_M_f32[1]	2.06599998	
trVoltQax_Volt_M_f32[0] trVoltQax_Volt_M_f32[1]	5.68100023 2.06599998	
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.44400006	
CurrCntrl_InverterFailSclFac_Uls_M_f32	0.815999985	
CurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.891499996	
CurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0939999968	
CurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	0	
CurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988	
CurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	10763.7002	
CurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.852599978	
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0	
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988	
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	10763.7002	
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.852599978	
owDataAccessBufIndex_Cnt_M_u16 CLOAEdbackSignalSclEacSlaw_UlspS_f32	0 1549.32996	
_CLOAFdbackSignalSclFacSlew_UlspS_f32 _ILOAFdbackSignalSclFacSlew_UlspS_f32	6542.3501	
MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0	
MtrCtrlFeedbackControlDisable Cnt lgc	0	
MtrCtrlVirualResDax_Ohm_f32	0.119000003	
MtrCtrlVirualResQax Ohm f32	0.0289999992	
MtrCurrQaxRefModifDsb Cnt lgc	0	
MtrCurrQaxRefModifRplEn_Cnt_lgc	0	
_MtrVoltDaxIntegHiLim_Volt_f32	17.6065006	
_MtrVoltDaxIntegLoLim_Volt_f32	-10.5	
_MtrVoltQaxFiltFFEnable_Cnt_lgc	0	
_MtrVoltQaxIntegHiLimVolt_f32	14.5948	
_MtrVoltQaxIntegLoLim_Volt_f32	-10.5	
_MtrVoltVecuFiltEnable_Cnt_lgc	0	
_VoltSatDaxPolyCoeff_Uls_f32	7.44799995	
_VoltSatQaxPolyCoeff_Uls_f32	0.351999998	
_deadtimeVScale_Uls_f32	0.971000016	
CommOffsetTbIX_Uls_u3p13[0] CommOffsetTbIX_Uls_u3p13[1]	573 7569	
	556	
CommOffsetTblY_Cnt_u16[0] CommOffsetTblY_Cnt_u16[1]	934	
rget_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0	
rget_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0	
	0	
rget_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr rget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848	
rget_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	-118.848 3490	
rget_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr rget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val		
rget_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr rget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val rget_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3490	





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63635	63635 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-95.5590057	-95.5589981 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	10.777771	10.7777996 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-16.4515743	-16.4515991 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	23358	23358 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-95.5590057	-95.5589981 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007	-213.335007 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.59 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-115.696999	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-141.417007	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.107000001	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0109999999	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0529999994	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0939999968	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.980000019	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.497000009	
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	550.754028	
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-584.435974	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0529999994	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0939999968	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0270000007	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0149999997	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.45700002	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.78799999	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	966.106995	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	858.828003	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	27.9379997	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.50300026	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	19.6130009	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.3180008	
MtrCtrl_Vecu_Volt_M_f32[0]	13.085	
MtrCtrl_Vecu_Volt_M_f32[1]	15.4449997	
MtrCurrDaxPrevIntg_Volt_M_f32	23.0559998	
MtrCurrDaxRef_Amp_M_f32[0]	37.4550018	
MtrCurrDaxRef_Amp_M_f32[1]	-2.84500003	
MtrCurrQaxCog_Amp_M_f32	-30.7789993	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013	
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993	
MtrCurrQaxPrevIntg_Volt_M_f32	13.2370005	
MtrCurrQaxRef_Amp_M_f32[0]	67.4899979	

PICurrCntrl_Per1



Input Value		
119.721001		
-91.4420013		
133.692993		
2.34599996		
1.27100003		
19.6130009		
-29.3180008		
19.6130009		
-29.3180008		
0.208000004		
0.451000005		
0.251300007		
0.681999981		
-627.179993		
1118		
68.5733032		
0.957700014		
-627.179993		
1118		
68.5733032		
0.957700014		
1		
2384.97998		
3626.42993		
0		
0		
0.108999997		
0.108999997		
1		
0		
25.8696003		
-11.6000004		
1		
26.0412998		
26.0412998		
26.0412998 -11.6000004		
26.0412998 -11.6000004		
26.0412998 -11.6000004 1 -12.5480003		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 0		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 0 -72.4260025		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326 -190.440994		
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326 -190.440994 4	Expected Value	Daguit
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value	Expected Value	Result
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value 2326	2326	•
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value 2326 0	2326 0 ± 1	•
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value 2326 0 16.8070068	2326 0 ± 1 16.8069992 ± 7.81E-03	•
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value 2326 0 16.8070068 8.37593937	2326 0 ± 1 16.8069992 ± 7.81E-03 8.37594032 ± 4.88E-04	
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value 2326 0 16.8070068 8.37593937 -28.8799	2326 0 ± 1 16.8069992 ± 7.81E-03 8.37594032 ± 4.88E-04 -28.8799 ± 4.88E-04	•
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value 2326 0 16.8070068 8.37593937 -28.8799 43081	2326 0 ± 1 16.8069992 ± 7.81E-03 8.37594032 ± 4.88E-04 -28.8799 ± 4.88E-04 43081 ± 1.52588E-05	
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value 2326 0 16.8070068 8.37593937 -28.8799 43081 0	2326 0 ± 1 16.8069992 ± 7.81E-03 8.37594032 ± 4.88E-04 -28.8799 ± 4.88E-04 43081 ± 1.52588E-05 0 ± 0.0625	•
26.0412998 -11.6000004 1 -12.5480003 -9.86100006 0.970000029 1154 5284 39 93 1 1 1 0 -72.4260025 2326 -190.440994 4 Actual Value 2326 0 16.8070068 8.37593937 -28.8799 43081	2326 0 ± 1 16.8069992 ± 7.81E-03 8.37594032 ± 4.88E-04 -28.8799 ± 4.88E-04 43081 ± 1.52588E-05	
	119.721001 -91.4420013 133.692993 2.34599996 1.27100003 19.6130009 -29.3180008 19.6130009 -29.3180008 0.208000004 0.451000005 0.251300007 0.681999981 -627.179993 1118 68.5733032 0.957700014 -627.179993 1118 68.5733032 0.957700014 1 2384.97998 3626.42993 0 0 0.108999997 0.108999997 1 0 25.8696003 -11.6000004	119.721001 -91.4420013 133.692993 2.34599996 1.2710003 19.6130009 -29.3180008 19.6130009 -29.3180008 0.20800004 0.45100005 0.251300007 0.681999981 -627.179993 1118 68.5733032 0.957700014 -627.179993 1118 68.5733032 0.957700014 1 2384.97998 3626.42993 0 0 0.108999997 0.108999997 11 0 25.8696003 -11.6000004



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

ame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	0	
trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	
trCntrl Read ModIdxSrlComSvcDft Cnt lgc(Val)	target_MtrCntrl_Read_ModidxSrlComSvcDft Cnt_lgc_Val	
trCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
trCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
rCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	140.470001	
rCtrl MtrCurrDaxMaxVal_Amp_M_132[0]	93.5790024	
rCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120000001	
rCtrl_MtrDampTermDax_Ohm_M_i32[0]	0.0560000017	
rCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.017000009	
rCtrl_MtrDawpTermQax_Ohm_M_f32[1]	0.0410000011	
rCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.93900001	
rCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.79499996	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-876.190002	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	798.229004	
rCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0170000009	
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011	
rCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0769999996	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.029999993	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.93000007	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.875	
rCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	737.640991	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-190.210999	
rCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-9.61999989	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-0.206	
rCtrl_MtrVoltQaxFF_Volt_M_f32[0]	28.1019993	
rCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.381000012	
rCtrl_Vecu_Volt_M_f32[0]	25.4869995	
rCtrl_Vecu_Volt_M_f32[1]	27.8470001	
trCurrDaxPrevIntg_Volt_M_f32	15.3000002	
trCurrDaxRef_Amp_M_f32[0]	94.3150024	
rCurrDaxRef_Amp_M_f32[1]	37.4959984	
trCurrQaxCog_Amp_M_f32	-34.6189995	
trCurrQaxFinalRef_Amp_M_f32[0]	171.485992	
trCurrQaxFinalRef_Amp_M_f32[1]	163.787003	
trCurrQaxPrevIntg_Volt_M_f32	1.81389999	
rCurrQaxRef_Amp_M_f32[0]	37.4550018	
rCurrQaxRef_Amp_M_f32[1]	-2.84500003	
rCurrQaxRpl_Amp_M_f32[0]	171.485992	
rCurrQaxRpl_Amp_M_f32[1]	163.787003	
trPosComputationDelay_Rad_M_f32[0]	-0.107000001	
trPosComputationDelay_Rad_M_f32[1]	-1.71000004	
trVoltDax_Volt_M_f32[0]	28.1019993	
trVoltDax Volt M f32[1]	0.381000012	

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value Name MtrVoltQax_Volt_M_f32[0] 28.1019993 MtrVoltQax_Volt_M_f32[1] 0.381000012 0.442000002 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.184 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.864700019 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.991999984 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -43.1699982 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ -657.130005 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 50.7543983 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.536199987 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -657 130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 50.7543983 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.536199987 SlowDataAccessBufIndex_Cnt_M_u16 2035.48999 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5391.29004 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ 0 k_MtrCtrlVirualResDax_Ohm_f32 0.133000001 $k_MtrCtrlVirualResQax_Ohm_f32$ 0.159999996 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ Λ k_MtrVoltDaxIntegHiLim_Volt_f32 19.5212002 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -11.1000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 26.7397003 k_MtrVoltQaxIntegLoLim_Volt_f32 -30.2000008 k MtrVoltVecuFiltEnable Cnt lgc k_VoltSatDaxPolyCoeff_Uls_f32 -13.3400002 k VoltSatQaxPolyCoeff Uls f32 12.0819998 k_deadtimeVScale_Uls_f32 0.990999997 t CommOffsetTblX Uls u3p13[0] 5022 t_CommOffsetTblX_Uls_u3p13[1] 7003 t_CommOffsetTblY_Cnt_u16[0] 1896 t CommOffsetTblY Cnt u16[1] 1952 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target MtrCntrl Read MtrCurrDax Amp f32 Val 83.9489975 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2340 target MtrCntrl Read MtrCurrQax Amp f32 Val 83.9489975 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 2340 2340 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -99.4119873 -99.4120026 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -0.531391025 -0.531391025 + 4.88F-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.92642355 -4.92642021 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 32773 32773 + 1 52588F-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 + 0.0625-99.4119873 -99.4120026 ± 7.81E-03 MtrCurrQaxFinalRef Amp M f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1] 163.787003 163.787003 ± 7.81E-03



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

lame	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
AtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	
	target_MttCritrl_Read_NotIdxSrlComSvcDft Cnt lqc Val	
<pre>//trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val) //trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)</pre>	target_MtrCntrl_Read_ModitxSncomSvcbit_Gnt_gtc_var	
trCntrl_Read_MotCurrDax_Amp_f32(Val)	· · · ·	
htrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MrCntrl Read MtrCurrQax Amp f32(Val)		
	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002	
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.093999968	
ItrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0879999995	
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0130000003	
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0970000029	
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.07099998	
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.27900004	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	650.622009	
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	557.583984	
ItrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0130000003	
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0970000029	
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998	
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.79900002	
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.624000013	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-125.525002	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	839.142029	
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-12.2150002	
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.874000013	
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	4.64300013	
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-11.7069998	
ltrCtrl_Vecu_Volt_M_f32[0]	16.8080006	
ltrCtrl_Vecu_Volt_M_f32[1]	19.1679993	
ltrCurrDaxPrevIntg_Volt_M_f32	-18.566	
ltrCurrDaxRef_Amp_M_f32[0]	212.455994	
ltrCurrDaxRef_Amp_M_f32[1]	89.8619995	
ltrCurrQaxCog_Amp_M_f32	177.046997	
ltrCurrQaxFinalRef_Amp_M_f32[0]	-220	
ItrCurrQaxFinalRef_Amp_M_f32[1]	-220	
ltrCurrQaxPrevIntg_Volt_M_f32	27.2450008	
ItrCurrQaxRef_Amp_M_f32[0]	94.3150024	
ItrCurrQaxRef_Amp_M_f32[1]	37.4959984	
ItrCurrQaxRpl_Amp_M_f32[0]	-220	
ltrCurrQaxRpl_Amp_M_f32[1]	-220	
ItrPosComputationDelay_Rad_M_f32[0]	4.64699984	
ItrPosComputationDelay Rad M f32[1]	2.27200007	
/trVoltDax_Volt_M_f32[0]	4.64300013	
/trVoltDax Volt M f32[1]	-11.7069998	

2016-04-22, 16:28:30+0530



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	4.64300013		
MtrVoltQax Volt M f32[1]	-11.7069998		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.375999987		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0529999994		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.636099994		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.49000001		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	33.7361984		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.666299999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	33.7361984		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.666299999		
SlowDataAccessBufIndex Cnt M u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	1046.93005		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1066.56006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.063000001		
k MtrCtrlVirualResQax Ohm f32	0.119000003		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.9659004		
k MtrVoltDaxIntegLoLim Volt f32	-11.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.7127991		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-0.66900003		
k_VoltSatQaxPolyCoeff_Uls_f32	13.8260002		
k_deadtimeVScale_Uls_f32	0.959999979		
t CommOffsetTbIX Uls u3p13[0]	4611		
	5579		
t_CommOffsetTbIX_UIs_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0]	730		
t_CommOffsetTblY_Cnt_u16[1]	1388		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr			
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3783 -144.667007		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val			
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2	l=	1
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3783	3783	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	80.4490051	80.4489975 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.357355237	0.357354999 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.78667927	-4.78668022 ± 4.88E-04	,
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55689	55689 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	13.9659004	13.9659004 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	80.4490051	80.4489975 ± 7.81E-03	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

ame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	0	
trCntrl Read lvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	
trCntrl Read ModIdxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
trCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target_MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
rCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013	
rCtrl MtrCurrDaxMaxVal Amp M f32[1]	133.692993	
rCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.070000003	
rCtrl MtrDampTermDax_Ohm M f32[1]	0.0130000003	
rCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.107000001	
	0.0109999999	
rCtrl_MtrDampTermQax_Ohm_M_f32[1] rCtrl MtrDaxIntegralGain Ohm M f32[0]	1.75300002	
trCtrI_MtrDaxIntegralGain_Onm_M_132[0] trCtrI_MtrDaxIntegralGain_Ohm_M_f32[1]	0.11299998	
rCtrl MtrDaxPropotionalGain Ohm M f32[0]	524.809998	
	-986.283997	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	0.107000001	
rCtrl_MtrlmpedDax_Ohm_M_f32[0]		
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.010999999	
rCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0850000009	
trCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.112999998	
rCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.02900004	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.546000004	
rCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	613.835999	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	556.35498	
rCtrl_MtrVoltDaxFF_Volt_M_f32[0]	22.1809998	
rCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.0150003	
rCtrl_MtrVoltQaxFF_Volt_M_f32[0]	25.9820004	
rCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.0480003	
rCtrl_Vecu_Volt_M_f32[0]	5.56799984	
trCtrl_Vecu_Volt_M_f32[1]	7.92799997	
rCurrDaxPrevIntg_Volt_M_f32	-15.7600002	
trCurrDaxRef_Amp_M_f32[0]	-108.124001	
rCurrDaxRef_Amp_M_f32[1]	178.639008	
rCurrQaxCog_Amp_M_f32	-9.31999969	
trCurrQaxFinalRef_Amp_M_f32[0]	220	
rCurrQaxFinalRef_Amp_M_f32[1]	220	
rCurrQaxPrevIntg_Volt_M_f32	11.2662001	
rCurrQaxRef_Amp_M_f32[0]	212.455994	
rCurrQaxRef_Amp_M_f32[1]	89.8619995	
rCurrQaxRpl_Amp_M_f32[0]	220	
trCurrQaxRpl_Amp_M_f32[1]	220	
rPosComputationDelay_Rad_M_f32[0]	2.65499997	
trPosComputationDelay_Rad_M_f32[1]	-5.77899981	
trVoltDax_Volt_M_f32[0]	25.9820004	
trVoltDax_Volt_M_f32[1]	-23.0480003	

2016-04-22, 16:28:30+0530



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	25.9820004		
MtrVoltQax_Volt_M_f32[1]	-23.0480003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.37999995		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.151999995		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.621800005		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.887000024		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	65.2260971		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.8046		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	65.2260971		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.8046		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	6758.08008		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7037.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.0520000011		
k MtrCtrlVirualResQax Ohm f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.4785004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.3000002		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.1450005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.6620007		
k_VoltSatQaxPolyCoeff_Uls_f32	-24.7110004		
k_deadtimeVScale_Uls_f32	0.986000001		
t CommOffsetTbIX UIs u3p13[0]	459		
	5775		
t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0]	24		
	47		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	80.8180008		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val			
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4523		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2	I=	1
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4523	4523	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	1.77600098	1.77600002 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	19.8459988	19.8460007 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	23.2468662	23.2469006 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	35063	35063 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	-11.3000002	-11.3000002 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	1.77600098	1.77600002 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	•



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Name	Input Value
	o input value
FastDataAccessBufIndex_Cnt_M_u16	·
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.412
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.523
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-834.685974
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-788.218994
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.50899994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	881.109009
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	1005.21997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-23.6089993
MtrCtrl MtrVoltDaxFF Volt M f32[1]	-14.04
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	14,9390001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.4060001
MtrCtrl Vecu Volt M f32[0]	17.9899998
VtrCtrl Vecu Volt M f32[1]	20.3500004
MtrCurrDaxPrevIntg Volt M f32	-14.0459995
WtrCurrDaxRef_Amp_M_f32[0]	-76.8769989
VtrCurrDaxRef Amp M f32[1]	-153,238998
WtrCurrQaxCog Amp M f32	-161.352005
92 12 2	0
MtrCurrQaxFinalRef_Amp_M_f32[0]	0
MtrCurrQaxFinalRef_Amp_M_f32[1]	
VtrCurrQaxPrevIntg_Volt_M_f32	29.0646
/trCurrQaxRef_Amp_M_f32[0]	-108.124001
MtrCurrQaxRef_Amp_M_f32[1]	178.639008
MtrCurrQaxRpl_Amp_M_f32[0]	0
MtrCurrQaxRpl_Amp_M_f32[1]	0
MtrPosComputationDelay_Rad_M_f32[0]	-3.5199998
MtrPosComputationDelay_Rad_M_f32[1]	3.33299994
MtrVoltDax_Volt_M_f32[0]	14.9390001
MtrVoltDax_Volt_M_f32[1]	-29.4060001

PICurrCntrl_Per1



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	14.9390001		
MtrVoltQax_Volt_M_f32[1]	-29.4060001		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.635999978		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.791999996		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.847599983		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.493999988		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.400299996		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	25.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.400299996		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3945.78003		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7691.68994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.172999993		
k MtrCtrlVirualResQax Ohm f32	0.0810000002		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.3655996		
k MtrVoltDaxIntegLoLim Volt f32	-11.3999996		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	0.514699996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.2639999		
k VoltSatQaxPolyCoeff Uls f32	11.283		
k_deadtimeVScale_Uls_f32	0.968999982		
t CommOffsetTbIX UIs u3p13[0]	2638		
t_CommOffsetTblX_Uls_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	310		
t_CommOffsetTblY_Cnt_u16[1]	1418		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	-44.2579994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	129		
target MtrCntrl Read MtrCurrQax Amp f32 Val	121.994003		
target MtrCntrl Read SysState Cnt Enum Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1418	1418	result
MtrCntrl Write Modldx Uls u16p16(val)	63504	63504 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	53.2280045	53.2280006 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.5495453	-16.5494995 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-5.47703886	-5.47703981 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	9103	9103 ± 1.52588E-05	•
INITIONITE_VITE_FITASEAUVATICEFITIAI_REV_UUPTO(VAI)			•
MtrCurrDayProvInta Volt M f32			
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef Amp M f32[0]	0 53.2280045	0 ± 0.0625 53.2280006 ± 7.81E-03	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.64 (Repeat Count = 1) Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0529999994
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.093999968
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.093999968
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.087999995
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.30700004
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.85500002 620.015015
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	715.487
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] /trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.093999968
htrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.087999995
htrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.089999995
AtrCtrl MtrImpedQax_Onm_M_r32[1]	0.0170000009
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.987999976
MtrCtrl MtrQaxIntegralGain_Ohm M f32[1]	0.42899999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	811.825012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-796.757996
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-21.1959991
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-4.78100014
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.6959991
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-27.8540001
/trCtrl_Vecu_Volt_M_f32[0]	26.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	29.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	9.64799976
MtrCurrDaxRef_Amp_M_f32[0]	191.369003
MtrCurrDaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxCog_Amp_M_f32	-205.514999
MtrCurrQaxFinalRef_Amp_M_f32[0]	-115.696999
MtrCurrQaxFinalRef_Amp_M_f32[1]	-141.417007
MtrCurrQaxPrevIntg_Volt_M_f32	2.89910007
MtrCurrQaxRef_Amp_M_f32[0]	-76.8769989
MtrCurrQaxRef_Amp_M_f32[1]	-153.238998
MtrCurrQaxRpl_Amp_M_f32[0]	-115.696999
MtrCurrQaxRpl_Amp_M_f32[1]	-141.417007
/trPosComputationDelay_Rad_M_f32[0]	3.77699995
/trPosComputationDelay_Rad_M_f32[1]	4.99499989
//trVoltDax_Volt_M_f32[0]	-17.6959991
MtrVoltDax_Volt_M_f32[1]	-27.8540001
MtrVoltQax_Volt_M_f32[0]	-17.6959991
MtrVoltQax_Volt_M_f32[1]	-27.8540001
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.713
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.726000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.289799988
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.129999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	70.1131973
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.631200016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-38.7999992 947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	70.1131973
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	0.631200016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 SlowDataAccessBufIndex Cnt M u16	0.631200016
SIOWDataAccessBuffindex_Crit_M_u16 C CLOAFdbackSignalSclFacSlew UlspS f32	1611.48999
C_CLOAFdbackSignalSciFacSiew_UispS_f32	5394.18018
<pre><_ILOAF abackSignalSciFacSiew_OispS_i32 <_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc</pre>	1
<pre><_MinCtricurtcoopsecorranrcEnable_Cnt_lgc <_MtrCtrlFeedbackControlDisable_Cnt_lgc</pre>	1
<pre><_MitCtrlVirualResDax_Ohm_f32</pre>	0.0710000023
k MtrCtrlVirualResQax Ohm f32	0.1010000025
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0





Name	Input Value			
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0			
k_MtrVoltDaxIntegHiLim_Volt_f32	2.98559999			
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004			
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0			
k_MtrVoltQaxIntegHiLim_Volt_f32	16.9648991			
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017			
k_MtrVoltVecuFiltEnable_Cnt_lgc	0			
k_VoltSatDaxPolyCoeff_Uls_f32	-22.1690006			
k_VoltSatQaxPolyCoeff_Uls_f32	-5.51499987			
k_deadtimeVScale_Uls_f32	0.995000005			
t_CommOffsetTblX_Uls_u3p13[0]	1212			
t_CommOffsetTblX_Uls_u3p13[1]	1704			
t_CommOffsetTblY_Cnt_u16[0]	1257			
t_CommOffsetTblY_Cnt_u16[1]	1842			
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1			
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-40.9220009			
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	662			
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-41.5750008			
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1			
Name	Actual Value	Expected Value	Result	
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1842	1842	~	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65208	65208 ± 1	✓	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	193.693008	193.692993 ± 7.81E-03	✓	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	15.6482735	15.6483002 ± 4.88E-04	✓	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-24.2919445	-24.2919006 ± 4.88E-04	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	13363	13363 ± 1.52588E-05	~	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-115.696999	-115.696999 ± 7.81E-03	~	
MtrCurrQaxFinalRef_Amp_M_f32[1]	193.693008	193.692993 ± 7.81E-03	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓	

Test Step 2.65 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.526000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53100002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	555.133972





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	919.028015
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0560000017 0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.662
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.31599998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-876.906982
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-215.744003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.7129993
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.4500008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-11.9399996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	29.1739998
MtrCtrl_Vecu_Volt_M_f32[0]	16.882
MtrCtrl_Vecu_Volt_M_f32[1]	19.2420006
MtrCurrDaxPrevIntg_Volt_M_f32	6.12300014
MtrCurrDaxRef_Amp_M_f32[0]	-147.343002
MtrCurrOavCog Amp_M_f32[1]	127.972
MtrCurrQaxCog_Amp_M_f32	-118.848 140.470001
MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1]	93.5790024
MtrCurrQaxPrevIntg_Volt_M_f32	24.7549992
MtrCurrQaxRef_Amp_M_f32[0]	191.369003
MtrCurrQaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxRpl_Amp_M_f32[0]	140.470001
MtrCurrQaxRpl_Amp_M_f32[1]	93.5790024
MtrPosComputationDelay_Rad_M_f32[0]	3.8440001
MtrPosComputationDelay_Rad_M_f32[1]	3.79399991
MtrVoltDax_Volt_M_f32[0]	-11.9399996
MtrVoltDax_Volt_M_f32[1]	29.1739998
MtrVoltQax_Volt_M_f32[0]	-11.9399996
MtrVoltQax_Volt_M_f32[1]	29.1739998
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.47299999
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00899999961
PICurrCotrl_MtrCurrOavCatFluxRatio_Uls_M_f32	0.0659999996 0.665000021
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8.62930012
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.434899986
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	8.62930012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.434899986
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2358.21997
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5388.91992
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.072999989
k_MtrCtrlVirualResQax_Ohm_f32 k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.023
k MtrCurrQaxRefModifRplEn Cnt Igc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	8.81120014
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	25.0259991
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	22.6620007
k_VoltSatQaxPolyCoeff_Uls_f32	12.3109999
k_deadtimeVScale_Uls_f32	0.985000014
t_CommOffsetTblX_Uls_u3p13[0]	3808
t_CommOffsetTblX_Uls_u3p13[1]	7298
t_CommOffsetTblY_Cnt_u16[0]	181
t_CommOffsetTblY_Cnt_u16[1]	812
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	75.0830002
J	
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	917
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	48.840002





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	917	917	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	169.747009	169.746994 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.90829039	4.90828991 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-30.1379299	-30.1378994 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	5642	5642 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	169.747009	169.746994 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	93.5790024	93.5790024 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.66 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	24.6130009
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-20.9400005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.44099998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.26900005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-818.776001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-274.428986
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.976000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-737.580994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	408.726013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	18.2380009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.3910007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-12.816
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-15.0170002
MtrCtrl_Vecu_Volt_M_f32[0]	5.70200014
MtrCtrl_Vecu_Volt_M_f32[1]	8.06200027
MtrCurrDaxPrevIntg_Volt_M_f32	15.0279999
MtrCurrDaxRef_Amp_M_f32[0]	6.18900013
MtrCurrDaxRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxCog_Amp_M_f32	-220

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-184.923996		
MtrCurrQaxPrevIntg_Volt_M_f32	1.02610004		
MtrCurrQaxRef Amp M f32[0]	-147.343002		
MtrCurrQaxRef_Amp_M_f32[1]	127.972		
MtrCurrQaxRpl_Amp_M_f32[0]	-216.921997		
MtrCurrQaxRpl_Amp_M_f32[1]	-184.923996		
MtrPosComputationDelay_Rad_M_f32[0]	4.13000011		
MtrPosComputationDelay Rad M f32[1]	-3.80699992		
MtrVoltDax_Volt_M_f32[0]	-12.816		
MtrVoltDax_Volt_M_f32[1]	-15.0170002		
MtrVoltQax Volt M f32[0]	-12.816		
MtrVoltQax_Volt_M_f32[1]	-15.0170002		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.657000005		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.43299998		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.185100004		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.515999973		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-340.130005		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	12.6120005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.790099978		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
	12.6120005		
PICurrCotrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.790099978		
SlowDataAccessBufIndex_Cnt_M_u16			
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3275.26001		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2711.1499		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.165000007		
k_MtrCtrlVirualResQax_Ohm_f32	0.200000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	25.7908001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.1938992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	19.0259991		
k_VoltSatQaxPolyCoeff_Uls_f32	1.51499999		
k_deadtimeVScale_Uls_f32	1		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	174		
t_CommOffsetTblY_Cnt_u16[1]	589		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	74.0660019		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3897		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	107.702003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	589	589	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65535	65536 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.64351988	2.64352012 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-5.05218792	-5.05218983 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	5281	5281 ± 1.52588E-05	
MtrCurrDaxPrevIntg Volt M f32	-10.5	-10.5 ± 0.0625	
ca ca.i toting_voi_m_ioz			
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
	-
VtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.010999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.165999994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.68499994
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	41.1699982
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	456.949005
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0799999982
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.57700002
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.83099997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-771.507996
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	920.502991
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.2689991
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	15.2200003
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	27.9379997
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	8.50300026
/trCtrl_Vecu_Volt_M_f32[0]	28.3600006
/trCtrl_Vecu_Volt_M_f32[1]	30.7199993
/ltrCurrDaxPrevIntg_Volt_M_f32	-1.19400001
/trCurrDaxRef_Amp_M_f32[0]	-105.246002
/trCurrDaxRef_Amp_M_f32[1]	41.6290016
/ltrCurrQaxCog_Amp_M_f32	220
/trCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965
/trCurrQaxFinalRef_Amp_M_f32[1]	46.8180008
/trCurrQaxPrevIntg_Volt_M_f32	11.6451998
/trCurrQaxRef_Amp_M_f32[0]	6.18900013
/trCurrQaxRef_Amp_M_f32[1]	83.0540009
/trCurrQaxRpl_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008
/trPosComputationDelay_Rad_M_f32[0]	1.53499997
/trPosComputationDelay_Rad_M_f32[1]	2.22300005
/trVoltDax_Volt_M_f32[0]	27.9379997
MtrVoltDax_Volt_M_f32[1]	8.50300026
MtrVoltQax_Volt_M_f32[0]	27.9379997
MtrVoltQax_Volt_M_f32[1]	8.50300026

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



		•	
Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.930999994		
PICurrCntrl InverterFailSclFac Uls M f32	0.893000007		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.76819998		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.670000017		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	42.0777016		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.450700015		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	42.0777016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.450700015		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	220.787994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5103.45996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0370000005		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k MtrCurrQaxRefModifDsb Cnt Igc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.48790002		
k MtrVoltDaxIntegLoLim Volt f32	-11.6000004		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	18.9482002		
k MtrVoltQaxIntegLoLim Volt f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	7.15899992		
k VoltSatQaxPolyCoeff Uls f32	6.94099998		
k deadtimeVScale Uls f32	0.968999982		
t CommOffsetTbIX Uls u3p13[0]	1532		
t_CommOffsetTbIX_UIs_u3p13[1]	2851		
t CommOffsetTblY Cnt u16[0]	158		
t CommOffsetTblY Cnt u16[1]	544		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	1		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-149.003006		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4983		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	5.72399998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
	4983	4983	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0 ± 1	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)			
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-131.513	-131.513 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.275402665	0.275402993 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.83716631	4.83717012 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	16604	16604 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	<u> </u>
MtrCurrQaxFinalRef_Amp_M_f32[0]	-131.513	-131.513 ± 7.81E-03	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

46.8180008

46.8180008 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.68 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.598999977
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.00399995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	324.985992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-932.651978
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.528999984
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.03900003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	139.416
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-482.338013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.91699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.9359999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-9.61999989
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-0.206
MtrCtrl_Vecu_Volt_M_f32[0]	22.7800007
MtrCtrl_Vecu_Volt_M_f32[1]	25.1399994
MtrCurrDaxPrevIntg_Volt_M_f32	-18.1529999
MtrCurrDaxRef_Amp_M_f32[0]	-213.026993
MtrCurrDaxRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxCog_Amp_M_f32	0
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004
MtrCurrQaxPrevIntg_Volt_M_f32	29.9225006
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxRpl_Amp_M_f32[0]	160.044006
MtrCurrQaxRpl_Amp_M_f32[1]	165.242004
MtrPosComputationDelay_Rad_M_f32[0]	0.574999988
MtrPosComputationDelay_Rad_M_f32[1]	-5.59200001
MtrVoltDax_Volt_M_f32[0]	-9.61999989
MtrVoltDax_Volt_M_f32[1]	-0.206
MtrVoltQax_Volt_M_f32[0]	-9.61999989
MtrVoltQax_Volt_M_f32[1]	-0.206
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.907999992
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.222000003
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.957899988
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.731999993
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	42.2845001
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.870100021
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	42.2845001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.870100021
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1457.72998
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4901.47021
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0680000037
k_MtrCtrlVirualResQax_Ohm_f32	0.029999993
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0





Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.7781		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.2348995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	13.868		
k_VoltSatQaxPolyCoeff_Uls_f32	24.8209991		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTbIX_Uls_u3p13[0]	4162		
t_CommOffsetTbIX_Uls_u3p13[1]	8053		
t_CommOffsetTblY_Cnt_u16[0]	565		
t_CommOffsetTblY_Cnt_u16[1]	1207		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-124.758003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3029		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1207	1207	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	65339	65339 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	16.376791	16.3768005 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-15.7359533	-15.7360001 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	30365	30365 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004	165.242004 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 2.69 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	138.595001	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-157.388	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0759999976	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0500000007	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.169	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.234999999	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	643.937012	





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-774.807983
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.076999996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.34399998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.00300002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	144.895996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	675.440002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-10.6440001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.8400002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-12.2150002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.874000013
MtrCtrl_Vecu_Volt_M_f32[0]	21.2910004
MtrCtrl_Vecu_Volt_M_f32[1]	23.6509991
MtrCurrDaxPrevIntg_Volt_M_f32	-6.90899992
MtrCurrDaxRef_Amp_M_f32[0]	-212.632996
MtrCurrDaxRef_Amp_M_f32[1]	-205.085007
	70.6559982
MtrCurrQaxCog_Amp_M_f32	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972
MtrCurrQaxPrevIntg_Volt_M_f32	24.0646
MtrCurrQaxRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxRpl_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRpl_Amp_M_f32[1]	-216.972
	4.14599991
MtrPosComputationDelay_Rad_M_f32[0]	
MtrPosComputationDelay_Rad_M_f32[1]	4.31799984
MtrVoltDax_Volt_M_f32[0]	-12.2150002
MtrVoltDax_Volt_M_f32[1]	0.874000013
MtrVoltQax_Volt_M_f32[0]	-12.2150002
MtrVoltQax_Volt_M_f32[1]	0.874000013
PICurrCntrl CurrSensFailSclFac Uls M f32	0.0250000004
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.723999977
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.785399973
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0450000018
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	43.7542992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.50029999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
	43.7542992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	
PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.50029999
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5655.9502
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4407.62012
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k MtrCtrlFeedbackControlDisable Cnt lgc	1
k MtrCtrlVirualResDax Ohm f32	0.0579999983
k_MtrCtrlVirualResQax_Ohm_f32	0.191
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	10.6548996
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	13.8830004
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
	-9.04999902 0
k_MtrVoltVecuFiltEnable_Cnt_lgc	
k_VoltSatDaxPolyCoeff_Uls_f32	-5.80900002
k_VoltSatQaxPolyCoeff_Uls_f32	1.51699996
k_deadtimeVScale_Uls_f32	0.986000001
t_CommOffsetTblX_Uls_u3p13[0]	5153
t_CommOffsetTblX_Uls_u3p13[1]	8027
t_CommOffsetTblY_Cnt_u16[0]	164
	921
t_CommOffsetTblY_Cnt_u16[1]	
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2959
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	20.6149998
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1
target_mitroritit_read_oysotate_ont_Enum_val	· · · · · · · · · · · · · · · · · · ·





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	921	921	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64618	64618 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	79.5930099	79.5930023 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	9.86156178	9.86155987 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	21.132122	21.1320992 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49593	49593 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-65.1900024	-65.1900024 ± 7.81E-03	✓
MtrCurrQaxFinalRef Amp M f32[1]	79.5930099	79.5930023 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.70 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-100.282997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-120.248001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.41799998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.275000006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	747.85199
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-144.074005
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.05900002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.105999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	675.771973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1006.70001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-12.7250004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-6.00099993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	22.1809998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-11.0150003
MtrCtrl_Vecu_Volt_M_f32[0]	12.1129999
MtrCtrl_Vecu_Volt_M_f32[1]	14.4729996
MtrCurrDaxPrevIntg_Volt_M_f32	-14.8070002
MtrCurrDaxRef_Amp_M_f32[0]	205.820999
MtrCurrDaxRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxCog_Amp_M_f32	-111.970001
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxFinalRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxPrevIntg_Volt_M_f32	11.6198997

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxRef_Amp_M_f32[0] -212.632996 MtrCurrQaxRef_Amp_M_f32[1] -205.085007 MtrCurrQaxRpl_Amp_M_f32[0] -216.921997 MtrCurrQaxRpl_Amp_M_f32[1] -184.923996 MtrPosComputationDelay_Rad_M_f32[0] -1.23300004 MtrPosComputationDelay_Rad_M_f32[1] 5.02600002 MtrVoltDax_Volt_M_f32[0] 22.1809998 MtrVoltDax_Volt_M_f32[1] -11.0150003 MtrVoltQax_Volt_M_f32[0] 22.1809998 $MtrVoltQax_Volt_M_f32[1]$ -11 0150003 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.0820000023 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.128999993 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.530900002 PICurrCntrl MtrCurrQaxSatFluxRatio_Uls_M_f32 0.677999973 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 1118 PICurrCntrl MtrVecuFilt_M_str.PrevOutput_Uls_f32 -10.21 16.2423992 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.400099993 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -10.21 16.2423992 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32$ PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.400099993 SlowDataAccessBufIndex_Cnt_M_u16 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 7814.10986 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3404.45996 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.0659999996 k MtrCtrlVirualResQax Ohm f32 0.0670000017 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k MtrCurrQaxRefModifRplEn Cnt lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 23 7416 k MtrVoltDaxIntegLoLim Volt f32 -22.4099998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 30.2787991 k MtrVoltQaxIntegLoLim Volt f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc k VoltSatDaxPolyCoeff_Uls_f32 -21.8419991 k_VoltSatQaxPolyCoeff_Uls_f32 -0.100000001 k deadtimeVScale Uls f32 0.978999972 t_CommOffsetTbIX_Uls_u3p13[0] 2802 t CommOffsetTblX Uls u3p13[1] 3899 t_CommOffsetTblY_Cnt_u16[0] 12 t_CommOffsetTblY_Cnt_u16[1] 15 $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1011 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 79 6729965 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) 56799 56799 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 91.8089752 91.8089981 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -6.00099993 -6.00099993 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -11.0150003 -11.0150003 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24858 24858 ± 1.52588E-05 $MtrCurrDaxPrevIntg_Volt_M_f32$ 0 + 0.0625-216.921997 -216.921997 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1] 91.8089752 91.8089981 ± 7.81E-03



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
	-
VtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-68.6760025
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-96.776001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.023
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.14400005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.58899999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	734.922974
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	615.338989
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.123000003
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0460000001
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.216999993
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.522000015
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-153.945007
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-664.44397
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0.779999971
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-18.3120003
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.6089993
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-14.04
/trCtrl_Vecu_Volt_M_f32[0]	20.3600006
/trCtrl_Vecu_Volt_M_f32[1]	22.7199993
/trCurrDaxPrevIntg_Volt_M_f32	-19.6690006
/trCurrDaxRef_Amp_M_f32[0]	-69.0940018
/ltrCurrDaxRef_Amp_M_f32[1]	161.973007
/trCurrQaxCog_Amp_M_f32	-72.4260025
/trCurrQaxFinalRef_Amp_M_f32[0]	138.595001
MtrCurrQaxFinalRef_Amp_M_f32[1]	-157.388
/trCurrQaxPrevIntg_Volt_M_f32	28.9094009
/trCurrQaxRef_Amp_M_f32[0]	-132.813004
1trCurrQaxRef_Amp_M_f32[1]	-9.14299965
ltrCurrQaxRpl_Amp_M_f32[0]	138.595001
/trCurrQaxRpl_Amp_M_f32[1]	-157.388
MtrPosComputationDelay_Rad_M_f32[0]	1.02199996
htrPosComputationDelay_Rad_M_f32[1]	-3.79699993
//trVoltDax_Volt_M_f32[0]	-23.6089993
//dtrVoltDax_Volt_M_f32[1]	-14.04
/trVoltQax_Volt_M_f32[0]	-23.6089993
MtrVoltQax_Volt_M_f32[1]	-14.04

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
	Input Value 0.261000007		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.261000007		
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.726999972		
	0.386200011		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32			
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012 83.4807968		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.745700002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl MtrVoltQaxFFFiit M str.TermN Uls f32	83.4807968		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.745700002		
SlowDataAccessBufIndex Cnt M u16	0.743700002		
k CLOAFdbackSignalSclFacSlew UlspS f32	2013.56995		
k ILOAFdbackSignalSclFacSlew UlspS f32	944.638977		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k MtrCtrlFeedbackControlDisable Cnt Igc	0		
k MtrCtrlVirualResDax Ohm f32	0.126000002		
k_MtrCtrlVirualResQax_Ohm_f32	0.0670000017		
k MtrCurrQaxRefModifDsb Cnt Igc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	0.694800019		
k MtrVoltDaxIntegLoLim Volt f32	-8.68999958		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	27.6896992		
k MtrVoltQaxIntegLoLim Volt f32	-8.68999958		
k MtrVoltVecuFiltEnable Cnt Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-8.64500046		
k VoltSatQaxPolyCoeff Uls f32	-10.3520002		
k deadtimeVScale Uls f32	0.987999976		
t CommOffsetTbIX Uls u3p13[0]	4686		
t_CommOffsetTblX_Uls_u3p13[1]	6119		
t_CommOffsetTblY_Cnt_u16[0]	557		
t CommOffsetTblY Cnt u16[1]	678		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3142		
target MtrCntrl Read MtrCurrQax Amp f32 Val	0.486999989		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	678	678	~
MtrCntrl Write Modldx Uls u16p16(val)	64749	64749 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-198.981995	-198.981995 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	20.0663643	20.0664005 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.40769327	1.40769005 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	26313	26313 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0.694800019	0.694800019 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-198.981995	-198.981995 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-157.388	-157.388 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-198.981995	-198.981995 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~





Test Step 2.72 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrChtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-139.906998 115.814003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.10999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0540000014
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.88999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.33099997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-329.475006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-304.359985
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] MtrCtrl MtrImpedQax Ohm M f32[0]	0.0710000023 0.0340000018
MtrCtrl_MtrImpedQax_Onm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.38400003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.620999992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	794.978027
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-414.11499
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.04099989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-21.3549995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-21.1959991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-4.78100014
MtrCtrl_Vecu_Volt_M_f32[0]	5.33099985
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg Volt M f32	7.69099998 24.066
MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
MtrCurrDaxRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxCog_Amp_M_f32	83.9489975
MtrCurrQaxFinalRef_Amp_M_f32[0]	-100.282997
MtrCurrQaxFinalRef_Amp_M_f32[1]	-120.248001
MtrCurrQaxPrevIntg_Volt_M_f32	19.3868999
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32[0]	-100.282997
MtrCurrQaxRpl_Amp_M_f32[1] MtrPosComputationDelay_Rad_M_f32[0]	-120.248001 -4.38800001
MtrPosComputationDelay_Rad_M_f32[1]	-3.3640008
MtrVoltDax Volt M f32[0]	-21.1959991
MtrVoltDax_Volt_M_f32[1]	-4.78100014
MtrVoltQax_Volt_M_f32[0]	-21.1959991
MtrVoltQax_Volt_M_f32[1]	-4.78100014
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.479000002
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.882000029
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.258100003
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	1118 -38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Ois_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-38.7999992 65.649498
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.843400002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	65.649498
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.843400002
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7558.6001
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3548.41992
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlVirualPosDay_Ohm_f32	0 0 12600002
k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32	0.126000002 0.179000005
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.179000005
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	24.4853992

PICurrCntrl_Per1





Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.72780037		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-4.80100012		
k_VoltSatQaxPolyCoeff_Uls_f32	19.4750004		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTbIX_UIs_u3p13[0]	1139		
t_CommOffsetTbIX_UIs_u3p13[1]	7438		
t_CommOffsetTblY_Cnt_u16[0]	268		
t_CommOffsetTblY_Cnt_u16[1]	1844		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4704		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1844	1844	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64552	64552 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-177.036011	-177.035995 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.15903664	7.15903997 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.47758746	-2.47759008 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50307	50307 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-100.282997	-100.282997 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-177.036011	-177.035995 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.73 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0410000011
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.03900003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.26999998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	964.854004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-233.382004
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998





MtrPosComputationDelay_Rad_M_f32[1]	2.41899991		
MtrVoltDax_Volt_M_f32[0]	-22.7129993		
MtrVoltDax_Volt_M_f32[1]	-20.4500008		
MtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[1]	-22.7129993 -20.4500008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.324000001		
PICurrCotrl_InverterFailSclFac_UIs_M_f32	0.540000021		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.153999999 0.930000007		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-657.130005		
PICurrCotrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	95.354599 0.241500005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	-194.190002 05.354500		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	95.354599 0.241500005		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2668.02002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	791.747986		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0839999989		
k_MtrCtrlVirualResQax_Ohm_f32	0.128999993		
k_MtrCurrQaxRefModifDsb_Cnt_Igc k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.6718006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1 13 4462006		
k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	13.4462996 -25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	1.19500005		
k_VoltSatQaxPolyCoeff_Uls_f32	-18.6650009		
k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0]	0.963999987 2449		
t_CommOffsetTbIX_UIs_u3p13[1]	2875		
t_CommOffsetTblY_Cnt_u16[0]	135		
t_CommOffsetTblY_Cnt_u16[1]	1455		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_val	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	654		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
	0		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name	Actual Value	Expected Value	Resul





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1.64366305	1.64365995 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.53108931	4.53108978 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	28861	28861 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-68.6760025	-68.6760025 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.74 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl Read SysState Cnt Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	160.044006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	165.242004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.967999995
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.12600005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-115.790001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	183.574997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.093999968
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.62800002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.66900003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-689.698975
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	753.629028
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	18.1280003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.3299999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.2380009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.3910007
MtrCtrl_Vecu_Volt_M_f32[0]	25.7329998
MtrCtrl_Vecu_Volt_M_f32[1]	28.0930004
MtrCurrDaxPrevIntg_Volt_M_f32	-14.8500004
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	80.8180008
MtrCurrQaxFinalRef_Amp_M_f32[0]	-139.906998
MtrCurrQaxFinalRef_Amp_M_f32[1]	115.814003





Name	Input Value		
MtrCurrQaxPrevIntg_Volt_M_f32	3.95619988		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32[0]	-139.906998		
MtrCurrQaxRpl_Amp_M_f32[1]	115.814003		
MtrPosComputationDelay_Rad_M_f32[0]	3.92700005		
MtrPosComputationDelay_Rad_M_f32[1]	5.99399996		
MtrVoltDax_Volt_M_f32[0]	18.2380009		
MtrVoltDax_Volt_M_f32[1]	27.3910007		
MtrVoltQax_Volt_M_f32[0]	18.2380009		
MtrVoltQax_Volt_M_f32[1]	27.3910007		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.538999975		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.671000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.321999997		
PICurrCotrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-194.190002 -194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	68.892601		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.91109997		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-194.190002		
PICurrCntrl MtrVoltQaxFFFiit M str.PrevOutput Uls f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	68.892601		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.91109997		
SlowDataAccessBufIndex Cnt M u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	6474.75		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5821.56006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.187999994		
k_MtrCtrlVirualResQax_Ohm_f32	0.0480000004		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	28.8398991		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.0121994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.8379993		
k_VoltSatQaxPolyCoeff_Uls_f32	-10.9659996		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	4153		
t_CommOffsetTblX_Uls_u3p13[1]	8176		
t_CommOffsetTblY_Cnt_u16[0]	434		
t_CommOffsetTblY_Cnt_u16[1]	1438		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1246		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007 2		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val			1_
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1246	1246	,
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-32.8450012	-32.8450012 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.55194139	-3.55193996 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.31712341	3.31712008 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	53971	53971 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]	28.8398991	28.8398991 ± 0.0625 -139.906998 ± 7.81E-03	
MITCUITCUAXEINAIRET AMD M 132101	1-139.906998	-139.90b998 ± 7.81E-03	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(var)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	
	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-65.1900024
	-216.972
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.938000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-751.672974
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	758.984985
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87199998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.86800003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	881.539001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	971.434998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-1.92999995
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.432000011
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.2689991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.2200003
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-21.5599995
MtrCurrDaxRef_Amp_M_f32[0]	171.485992
MtrCurrDaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxCog_Amp_M_f32	-44.2579994
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxPrevIntg_Volt_M_f32	15.5335999
MtrCurrQaxRef_Amp_M_f32[0]	106.072998
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxRpl_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008
MtrPosComputationDelay_Rad_M_f32[0]	0.128000006
MtrPosComputationDelay_Rad_M_f32[1]	-2.62299991
MtrVoltDax Volt M f32[0]	-17.2689991
MtrVoltDax Volt M f32[1]	15.2200003

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrVoltQax Volt M f32[0] -17.2689991 MtrVoltQax_Volt_M_f32[1] 15.2200003 PICurrCntrl_CurrSensFailSclFac_Uls_M f32 0.47299999 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.0410000011 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.142000005 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 386.220001 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ 20.7000008 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 82.1283035 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$ 0.396600008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 20 7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 82.1283035 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.396600008 SlowDataAccessBufIndex_Cnt_M_u16 6937.75977 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3932.20996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ 0 k_MtrCtrlVirualResDax_Ohm_f32 0.0869999975 $k_MtrCtrlVirualResQax_Ohm_f32$ 0.175999999 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ Λ 2.12940001 k_MtrVoltDaxIntegHiLim_Volt_f32 -12.6000004 $k_MtrVoltDaxIntegLoLim_Volt_f32$ k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 28.5435009 k_MtrVoltQaxIntegLoLim_Volt_f32 -11.6000004 k MtrVoltVecuFiltEnable Cnt lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 14.7320004 k VoltSatQaxPolyCoeff Uls f32 9.23999977 k_deadtimeVScale_Uls_f32 0.967999995 t CommOffsetTblX Uls u3p13[0] 4776 t_CommOffsetTblX_Uls_u3p13[1] 7741 t_CommOffsetTblY_Cnt_u16[0] 1756 t CommOffsetTblY Cnt u16[1] 1670 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target MtrCntrl Read MtrCurrDax Amp f32 Val -126.640999 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1865 -44.2579994 target MtrCntrl Read MtrCurrQax Amp f32 Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1715 1715 49741 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 49741 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -115.014999 -115.014999 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 0.432000011 0.432000011 + 4.88F-04 15.2200003 15.2200003 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 38473 38473 + 1 52588F-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32 0 ± 0.0625 -82.2979965 -82.2979965 ± 7.81E-03 MtrCurrQaxFinalRef Amp M f32[0]

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

-115.014999

-115.014999 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.76 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.723007
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-121.943001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.68200004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.48800004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-133.520004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-739.294006
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.337000012
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.964999974
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	481.321014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	577.322998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.4989996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.1389999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-4.91699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.9359999
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	1.12
MtrCurrDaxRef_Amp_M_f32[0]	106.072998
MtrCurrDaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxCog_Amp_M_f32	-40.9220009
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004
MtrCurrQaxPrevIntg_Volt_M_f32	28.4825993
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpl_Amp_M_f32[0]	160.044006
MtrCurrQaxRpl_Amp_M_f32[1]	165.242004
MtrPosComputationDelay_Rad_M_f32[0]	3.76399994
MtrPosComputationDelay_Rad_M_f32[1]	3.68400002
MtrVoltDax_Volt_M_f32[0]	-4.91699982
MtrVoltDax_Volt_M_f32[1]	-13.9359999
MtrVoltQax_Volt_M_f32[0]	-4.91699982
MtrVoltQax_Volt_M_f32[1]	-13.9359999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.134000003
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0390000008
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.5352
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.524999976
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.8788986
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.45570001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.8788986
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.45570001
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1146.66003
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4987.56982
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.109999999
k_MtrCtrlVirualResQax_Ohm_f32	0.0340000018
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0

PICurrCntrl_Per1



Name	Input Value		
	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	9.01119995		
k_MtrVoltDaxIntegHiLim_Volt_f32	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	20.6303005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	20.9039993		
k_VoltSatQaxPolyCoeff_Uls_f32	-4.77099991		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTblX_Uls_u3p13[0]	492		
t_CommOffsetTblX_Uls_u3p13[1]	7840		
t_CommOffsetTblY_Cnt_u16[0]	589		
t_CommOffsetTblY_Cnt_u16[1]	1202		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	121.994003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	812		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-40.9220009		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	812	812	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-94.5090103	-94.5090027 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.26297998	-4.26297998 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.46629858	-2.46630001 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	17405	17405 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-94.5090103	-94.5090027 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004	165.242004 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.77 (Repeat Count = 1)		V
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.04700005	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.331999987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	965.18103
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	180.692001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.8890006
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.802999973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-940.226013
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-976.195007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.9390001 18.1280003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.3299999
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-22.2029991
MtrCurrDaxRef Amp M f32[0]	24.6130009
MtrCurrDaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxCog_Amp_M_f32	-207.917999
MtrCurrQaxFinalRef_Amp_M_f32[0]	209.052002
MtrCurrQaxFinalRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxPrevIntg_Volt_M_f32	-31
MtrCurrQaxRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxRpl_Amp_M_f32[0]	209.052002
MtrCurrQaxRpl_Amp_M_f32[1]	-124.994003
MtrPosComputationDelay_Rad_M_f32[0]	4.39099979
MtrPosComputationDelay_Rad_M_f32[1]	1.33599997
MtrVoltDax_Volt_M_f32[0]	18.1280003
MtrVoltDax_Volt_M_f32[1]	-29.3299999
MtrVoltQax_Volt_M_f32[0]	18.1280003
MtrVoltQax_Volt_M_f32[1]	-29.3299999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.216000006
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.023
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.945299983
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.112999998
PICurrCotrl_MtrVeouFilt_M_str.PrevInput_UIs_f32	947.73999 -717.299988
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	94.2040024
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.323000014
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	94.2040024
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.323000014
SlowDataAccessBufIndex_Cnt_M_u16	0
k CLOAFdbackSignalSclFacSlew UlspS f32	7138.00977
k ILOAFdbackSignalSclFacSlew UlspS f32	7847.91016
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0930000022
k_MtrCtrlVirualResQax_Ohm_f32	0.050999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	3.55049992
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	5.07770014
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-5.18900013
k_VoltSatQaxPolyCoeff_Uls_f32	-7.39099979
k_deadtimeVScale_Uls_f32	0.986000001
t_CommOffsetTbIX_UIs_u3p13[0]	2834
t_CommOffsetTblX_Uls_u3p13[1]	3595
	13366
t_CommOffsetTblY_Cnt_u16[0]	1165
t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1]	1651
t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1651 0
t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1651 0 1
t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1651 0

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	75.0830002		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	744	744	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-167.169006	-167.169006 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.38360167	-3.3836 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.58554578	3.58555007 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37910	37910 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	3.55049992	3.55049992 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-167.169006	-167.169006 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-124.994003	-124.994003 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.78 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	37.4550018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-2.84500003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.41499996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.848999977
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-824.46698
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	454.670013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.196999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.954
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-729.622009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	640.599976
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-1.92999995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.432000011
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	26.6639996
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004

PICurrCntrl_Per1



Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	183.065002		
MtrCurrQaxCog_Amp_M_f32	-198.285995		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-98.4449997		
MtrCurrQaxPrevIntg_Volt_M_f32	31		
MtrCurrQaxRef_Amp_M_f32[0]	140.289001		
MtrCurrQaxRef_Amp_M_f32[1]	178.235992		
MtrCurrQaxRpl_Amp_M_f32[0]	-200.556		
MtrCurrQaxRpl_Amp_M_f32[1]	-98.4449997		
MtrPosComputationDelay_Rad_M_f32[0]	-5.37699986		
MtrPosComputationDelay_Rad_M_f32[1]	-2.19300008 -1.92999995		
MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[1]	0.432000011		
MtrVoltDax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[0]	-1.92999995		
MtrVoltQax_Volt_M_f32[1]	0.432000011		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.810000002		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0489999987		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.932299972		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	6.96400023		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.331699997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.96400023		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.331699997		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1416.70996		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5107.18018		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.057		
k_MtrCtrlVirualResQax_Ohm_f32	0.179000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.02270031		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998 0		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32	24.8586998		
k MtrVoltQaxIntegLoLim Volt f32	-22.409998		
k MtrVoltVecuFiltEnable Cnt Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-11.7589998		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.63000011		
k_deadtimeVScale_Uls_f32	0.984000027		
t_CommOffsetTbIX_UIs_u3p13[0]	2154		
t CommOffsetTbIX Uls u3p13[1]	6783		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	253		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1110	1110	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64487	64487 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.35975122	7.35974979 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.151220575	0.151221007 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	58832	58832 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	V
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556	-200.556 ± 7.81E-03	V
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
######################################	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)		
	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)		
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
AtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 94.3150024	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]		
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	37.4959984	
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125	
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0820000023	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.896000028	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.76800001	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-286.584991	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	831.651001	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.927999973	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-140.283005	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	380.562012	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-28.2420006	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	12.6160002	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.4989996	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	13.1389999	
/trCtrl_Vecu_Volt_M_f32[0]	18.9510002	
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008	
/trCurrDaxPrevIntg_Volt_M_f32	-18.7919998	
/trCurrDaxRef_Amp_M_f32[0]	140.289001	
/trCurrDaxRef_Amp_M_f32[1]	178.235992	
MtrCurrQaxCog_Amp_M_f32	136.341003	
/trCurrQaxFinalRef_Amp_M_f32[0]	67.4899979	
/trCurrQaxFinalRef_Amp_M_f32[1]	119.721001	
MtrCurrQaxPrevIntg_Volt_M_f32	0	
/trCurrQaxRef_Amp_M_f32[0]	91.8850021	
/trCurrQaxRef_Amp_M_f32[1]	182.261002	
/trCurrQaxRpl_Amp_M_f32[0]	67.4899979	
/trCurrQaxRpl_Amp_M_f32[1]	119.721001	
/trPosComputationDelay_Rad_M_f32[0]	3.58100009	
MtrPosComputationDelay_Rad_M_f32[1]	4.95800018	
MtrVoltDax_Volt_M_f32[0]	-8.4989996	
/trVoltDax_Volt_M_f32[1]	13.1389999	
MtrVoltQax_Volt_M_f32[0]	-8.4989996	
MtrVoltQax_Volt_M_f32[1]	13.1389999	

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.93599999		
PICurrCntrl InverterFailSclFac Uls M f32	0.967000008		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.99089998		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.30399999		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-826.23999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	12.8741999		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.75819999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-826.23999		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	12.8741999		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD UIs f32	0.75819999		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	5043.06982		
k ILOAFdbackSignalSclFacSlew UlspS f32	1949.64001		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.181999996		
k MtrCtrlVirualResQax Ohm f32	0.018999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k MtrCurrQaxRefModifRpIEn Cnt Igc	0		
k MtrVoltDaxIntegHiLim Volt f32	21.3099003		
k MtrVoltDaxIntegLoLim Volt f32	-8.68999958		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	5.74840021		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k VoltSatDaxPolyCoeff Uls f32	19.0599995		
k_VoltSatQaxPolyCoeff_Uls_f32	14.7340002		
k_deadtimeVScale_UIs_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	418		
t_CommOffsetTblX_Uls_u3p13[1]	4570		
t CommOffsetTbIY Cnt u16[0]	23		
t CommOffsetTblY Cnt u16[1]	212		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4760		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	4760	4760	~
MtrCntrl Write Modldx Uls u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-111.945992	-111.945999 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.95347309	-1.95346999 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.43919373	4.43918991 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33027	33027 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef Amp M f32[0]	-111.945992	-111.945999 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

119.721001

119.721001 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.80 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	212.455994
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	89.8619995
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20599997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90699995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-304.572998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	299.334991
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.052999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.85399997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.3999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	778.853027
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-658.843994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-9.60900021
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	26.3260002
MtrCtrl_Vecu_Volt_M_f32[0]	14.243
MtrCtrl_Vecu_Volt_M_f32[1]	16.6030006
MtrCurrDaxPrevIntg_Volt_M_f32	-9.86299992
MtrCurrDaxRef_Amp_M_f32[0]	91.8850021
MtrCurrDaxRef_Amp_M_f32[1]	182.261002
MtrCurrQaxCog_Amp_M_f32	59.7319984
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	13.4132004
MtrCurrQaxRef_Amp_M_f32[0]	-218.035004
MtrCurrQaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	0.112999998
MtrPosComputationDelay_Rad_M_f32[1]	-5.44500017
MtrVoltDax_Volt_M_f32[0]	-9.60900021
MtrVoltDax_Volt_M_f32[1]	26.3260002
MtrVoltQax_Volt_M_f32[0]	-9.60900021
MtrVoltQax_Volt_M_f32[1]	26.3260002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.144999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.995999992
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.702400029
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.208000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	5.05210018
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.2227
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	5.05210018
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.2227
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3857.37988
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2438.91992
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0970000029
k_MtrCtrlVirualResQax_Ohm_f32	0.0659999996
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0

PICurrCntrl_Per1





Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.79429996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.95400047		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-12.4820004		
k_VoltSatQaxPolyCoeff_Uls_f32	10.7770004		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	2163		
t_CommOffsetTblX_Uls_u3p13[1]	5439		
t_CommOffsetTblY_Cnt_u16[0]	237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1062		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63635	63635 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-45.2499962	-45.25 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.6555309	-10.6555004 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.0980501	12.0980997 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	1211	1211 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-45.2499962	-45.25 ± 7.81E-03	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓	

Test Step 2.81 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-108.124001
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	178.639008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.029999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.880999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.54700005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-747.278992





	(10-10
Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-161.845993
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.050999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.836000025
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1011.37
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	886.40802
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.61900043
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.1560001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	23.4519997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-14.2340002
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6060009
MtrCurrDaxRef_Amp_M_f32[0]	-218.035004
MtrCurrDaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxCog_Amp_M_f32	1.62199998
MtrCurrQaxFinalRef_Amp_M_f32[0]	94.3150024 37.4959984
MtrCurrQaxFinalRef_Amp_M_f32[1] MtrCurrQaxFroyInta_Volt_M_f32	
MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0]	-5.69140005 -216.921997
MtrCurrQaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxRel_Amp_M_f32[0]	94.3150024
MtrCurrQaxRpl_Amp_M_f32[1]	37.4959984
MtrPosComputationDelay_Rad_M_f32[0]	-5.296
MtrPosComputationDelay Rad M f32[1]	-0.995999992
MtrVoltDax_Volt_M_f32[0]	23.4519997
MtrVoltDax_Volt_M_f32[1]	-14.2340002
MtrVoltQax_Volt_M_f32[0]	23.4519997
MtrVoltQax_Volt_M_f32[1]	-14.2340002
PICurrCntrl CurrSensFailSclFac Uls M f32	0.354999989
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.851999998
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.345699996
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0850000009
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	69.3029022
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.383899987
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	69.3029022
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.383899987
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3045.54004
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4138.33984
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.063000001
k_MtrCtrlVirualResQax_Ohm_f32	0.101999998
k_MtrCurrQayRefModifDsb_Cnt_lgc	1 0
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc k MtrVoltDaxIntegHiLim Volt f32	18.9549007
k MtrVoltDaxIntegLoLim Volt_132	-25.600004
k MtrVoltQaxFiltFFEnable Cnt lgc	-23.0000004
k MtrVoltQaxIntegHiLim Volt f32	16.6681995
k MtrVoltQaxIntegLoLim Volt f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k VoltSatDaxPolyCoeff Uls f32	-2.83899999
k_VoltSatQaxPolyCoeff_Uls_f32	-14.1759996
k_deadtimeVScale_Uls_f32	0.99099997
t_CommOffsetTbIX_UIs_u3p13[0]	1162
t_CommOffsetTbIX_UIs_u3p13[1]	1932
t_CommOffsetTblY_Cnt_u16[0]	71
t_CommOffsetTblY_Cnt_u16[1]	676
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4	4	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1.26505959	1.26505995 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.7907877	-4.79079008 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40372	40372 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	37.4959984	37.4959984 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.82 (Repeat Count = 1)	<u> </u>
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-76.8769989
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-153.238998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70299995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.44000006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	317.347992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-582.065002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.21300006
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.16400003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	72.6969986
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-560.289978
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.3199997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	18.0170002
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-31
MtrCurrDaxRef_Amp_M_f32[0]	-216.921997
MtrCurrDaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxCog_Amp_M_f32	-126.640999

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxFinalRef_Amp_M_f32[0] 212.455994 MtrCurrQaxFinalRef_Amp_M_f32[1] 89.8619995 MtrCurrQaxPrevIntg_Volt_M_f32 0.200000003 MtrCurrQaxRef_Amp_M_f32[0] -82.2979965 MtrCurrQaxRef_Amp_M_f32[1] 46.8180008 MtrCurrQaxRpl_Amp_M_f32[0] 212.455994 MtrCurrQaxRpl_Amp_M_f32[1] 89.8619995 MtrPosComputationDelay_Rad_M_f32[0] 0.537999988 MtrPosComputationDelay_Rad_M_f32[1] 0.358999997 MtrVoltDax_Volt_M_f32[0] 26 3199997 MtrVoltDax_Volt_M_f32[1] 18.0170002 MtrVoltQax_Volt_M_f32[0] 26 3199997 MtrVoltQax_Volt_M_f32[1] 18.0170002 PICurrCntrl CurrSensFailSclFac Uls M f32 0.661000013 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.606999993 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.201399997 $PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32$ 0.564999998 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -627.179993 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 56.7700005 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.200399995 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -627.179993 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 56.7700005 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.200399995 SlowDataAccessBufIndex_Cnt_M_u16 k CLOAFdbackSignalSclFacSlew UlspS f32 874.497986 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 1758.53003 k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k MtrCtrlVirualResDax Ohm f32 0.156000003 k_MtrCtrlVirualResQax_Ohm_f32 0.0890000015 k MtrCurrQaxRefModifDsb Cnt lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.24860001 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 18.9195995 k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 13.8400002 k VoltSatQaxPolyCoeff_Uls_f32 7.34399986 k_deadtimeVScale_Uls_f32 0.964999974 t_CommOffsetTblX_Uls_u3p13[0] 1892 $t_CommOffsetTblX_Uls_u3p13[1]$ 4832 t_CommOffsetTblY_Cnt_u16[0] 912 t CommOffsetTblY_Cnt_u16[1] 1211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target MtrCntrl Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2780 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1211 1211 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63242 63242 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -168.112991 -168.113007 ± 7.81E-03 -18.3108006 ± 4.88E-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -18.3107929 15.4814291 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 15.4813995 ± 4.88E-04 62084 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 62084 -10.5 ± 0.0625 MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -168.112991 MtrCurrQaxFinalRef_Amp_M_f32[0] -168.113007 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[1] 89.8619995 89.8619995 ± 7.81E-03



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
/dtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(vtr)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	191.369003
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	107.137001
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0560000017
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0130000003
htrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.174999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.27099998
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	317.493011
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	653.375977
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.107000001
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.89100003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.17499995
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	345.561005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	325.127991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-24.052
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.3250008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.35299969
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.7590008
/trCtrl_Vecu_Volt_M_f32[0]	29.0240002
ftrCtrl_Vecu_Volt_M_f32[1]	30.2299995
ftrCurrDaxPrevIntg_Volt_M_f32	31
/trCurrDaxRef_Amp_M_f32[0]	138.595001
/trCurrDaxRef_Amp_M_f32[1]	-157.388
ltrCurrQaxCog_Amp_M_f32	121.994003
ftrCurrQaxFinalRef_Amp_M_f32[0]	-108.124001
/ltrCurrQaxFinalRef_Amp_M_f32[1]	178.639008
/ltrCurrQaxPrevIntg_Volt_M_f32	27.8124008
ltrCurrQaxRef_Amp_M_f32[0]	160.044006
ltrCurrQaxRef_Amp_M_f32[1]	165.242004
ltrCurrQaxRpl_Amp_M_f32[0]	-108.124001
MtrCurrQaxRpl_Amp_M_f32[1]	178.639008
htrPosComputationDelay_Rad_M_f32[0]	-4.69399977
htrPosComputationDelay_Rad_M_f32[1]	0.0949999988
htrVoltDax_Volt_M_f32[0]	-8.35299969
//dtrVoltDax_Volt_M_f32[1]	-29.7590008
//trVoltQax_Volt_M_f32[0]	-8.35299969
//dtrVoltQax_Volt_M_f32[1]	-29.7590008

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.261999995		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.382999986		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.419499993		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.226999998		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-304.940002		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	95.180397		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.65170002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-304.940002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	95.180397		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.65170002		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	6833.22998		
k ILOAFdbackSignalScIFacSlew UlspS f32	3531.6499		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.030999995		
k MtrCtrlVirualResQax Ohm f32	0.131999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	23.7777004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	27.8603001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-19.1399994		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.7269993		
k_deadtimeVScale_UIs_f32	0.981999993		
t_CommOffsetTbIX_UIs_u3p13[0]	6349		
t CommOffsetTbIX UIs u3p13[1]	7225		
t CommOffsetTbIY Cnt u16[0]	49		
t CommOffsetTblY Cnt u16[1]	735		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3088		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	735	735	- Rooult
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64356	64356 ± 1	_
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	146.174011	146.173996 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	23.6980801	23.6980991 ± 4.88E-04	_
MtrCntrl Write MtrQaxVoltage Volt f32(val)	15.8347845	15.8347998 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	26816	26816 ± 1.52588E-05	_
MtrCurrDaxPrevIntg_Volt_M_f32	23.7777004	23.7777004 ± 0.0625	
MtrCurrQaxFinalRef Amp M f32[0]	146.174011	146.173996 ± 7.81E-03	
mil our gazi man tei_zimp_m_roz[o]	170.177011	170.170000 17.01L-00	_

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

178.639008

178.639008 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]





Test Step 2.84 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -147.343002
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl MtrDampTermDax Ohm M f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.26699996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-645.427979
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	733.924988
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] AttrCtrl_MtrOcylptogralCaip_Ohm_M_f32[0]	0.0560000017 1.03900003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.03900003 1.528
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	643.85498
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	55.9690018
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	21.2989998
MtrCtrl_Vecu_Volt_M_f32[1]	23.6590004
MtrCurrDaxPrevIntg_Volt_M_f32	0
MtrCurrDaxRef_Amp_M_f32[0]	-100.282997
MtrCurrDaxRef_Amp_M_f32[1]	-120.248001
MtrCurrQaxCog_Amp_M_f32	-41.5750008
MtrCurrQaxFinalRef_Amp_M_f32[0]	-76.8769989
MtrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998 22.0902996
MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxRpl Amp M f32[0]	-76.8769989
MtrCurrQaxRpl Amp M f32[1]	-153.238998
MtrPosComputationDelay_Rad_M_f32[0]	-1.20700002
MtrPosComputationDelay_Rad_M_f32[1]	-2.58599997
MtrVoltDax_Volt_M_f32[0]	-29.3959999
MtrVoltDax_Volt_M_f32[1]	-1.94000006
MtrVoltQax_Volt_M_f32[0]	-29.3959999
MtrVoltQax_Volt_M_f32[1]	-1.94000006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.181999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.34999994
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.615199983
PICurrCotrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.521000028
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl MtrVecuFilt M str.TermN Uls f32	-38.7999992 78.1542969
PICurrCntrl_mtrvecuFilt_M_str.TermN_UIs_f32 PICurrCntrl MtrVecuFilt M str.TermD UIs f32	0.47240001
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	570.700012
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	78.1542969
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.47240001
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7388.58984
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4638.1499
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
	0.123000003
k_MtrCtrlVirualResDax_Ohm_f32	
k_MtrCtrlVirualResQax_Ohm_f32	0.0130000003





Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	10.2995996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-11.0579996		
k_VoltSatQaxPolyCoeff_Uls_f32	14.974		
k_deadtimeVScale_Uls_f32	0.981999993		
t_CommOffsetTblX_Uls_u3p13[0]	3351		
t_CommOffsetTblX_Uls_u3p13[1]	5291		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4554		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4554	4554	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	53.2619934	53.262001 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.13420677	4.13421011 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.64885473	-2.64884996 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	9738	9738 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	53.2619934	53.262001 ± 7.81E-03	-
MtrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998	-153.238998 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

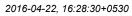
Name	Input Value	
FastDataAccessBufIndex Cnt M u16	0	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.582000017	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.196999997	





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	847.179993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-586.309021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.351999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83099997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	808.513977
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.0500031 -3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl MtrVoltDaxFF Volt M f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_Vecu_Volt_M_f32[0]	21.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	23.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	17.9769993
MtrCurrDaxRef_Amp_M_f32[0]	-68.6760025
MtrCurrDaxRef_Amp_M_f32[1]	-96.776001
MtrCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001
MtrCurrQaxPrevIntg_Volt_M_f32	24.0972004
MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1]	-146.723007 -121.943001
MtrCurrQaxRpl Amp M f32[0]	191.369003
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001
MtrPosComputationDelay_Rad_M_f32[0]	-5.93400002
MtrPosComputationDelay_Rad_M_f32[1]	5.86899996
MtrVoltDax_Volt_M_f32[0]	-17.1070004
MtrVoltDax_Volt_M_f32[1]	15.9390001
MtrVoltQax_Volt_M_f32[0]	-17.1070004
MtrVoltQax_Volt_M_f32[1]	15.9390001
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.512000024
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.275000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.342900008
PICurrCotrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977 -38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	-36.7999992 -43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	68.4229965
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.445100009
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	68.4229965
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.445100009
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	329.425995
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4506.12012
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32	0.0189999994 0.18999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.109999990
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	29.8101997
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	30.8836002
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	17.5830002
k_VoltSatQaxPolyCoeff_Uls_f32	3.3670001
k_deadtimeVScale_Uls_f32	0.986999989
t_CommOffsetTbIX_UIs_u3p13[0] t_CommOffsetTbIX_UIs_u3p13[1]	1450 4529
t_CommOffsetTblY_Cnt_u16[0]	4329 889
t_CommOffsetTblY_Cnt_u16[1]	1543
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	3203 59.7319984

PICurrCntrl_Per1





Name	Input Value		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1543	1543	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64684	64684 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	10.0439243	10.0438995 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-18.5360146	-18.5359993 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	31231	31231 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.86 (Repeat Count = 1) ✓		
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.239999995	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.128999993	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-873.200012	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-251.832993	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.123000003	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0700000003	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0130000003	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.69299996	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.763999999	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-639.518982	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	659.557007	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996	
MtrCtrl_Vecu_Volt_M_f32[0]	22.3600006	
MtrCtrl_Vecu_Volt_M_f32[1]	24.7199993	
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4580002	
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998	
MtrCurrDaxRef_Amp_M_f32[1]	115.814003	
MtrCurrQaxCog_Amp_M_f32	107.702003	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002	

PICurrCntrl_Per1



Name	Input Value	
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972	
MtrCurrQaxPrevIntg_Volt_M_f32	25.1226997	
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994	
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993	
MtrCurrQaxRpl_Amp_M_f32[0]	-147.343002	
MtrCurrQaxRpl_Amp_M_f32[1]	127.972	
MtrPosComputationDelay_Rad_M_f32[0]	-1.05700004	
MtrPosComputationDelay_Rad_M_f32[1]	4.92199993	
MtrVoltDax_Volt_M_f32[0]	0.908999979	
MtrVoltDax_Volt_M_f32[1]	21.0249996	
MtrVoltQax_Volt_M_f32[0]	0.908999979	
MtrVoltQax_Volt_M_f32[1]	21.0249996 0.84799999	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.224000007	
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.133100003	
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.143000007	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1048.76001	
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	0.987200022	
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.676100016	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002	
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-1048.76001	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	0.987200022	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.676100016	
SlowDataAccessBufIndex Cnt M u16	1	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6782.70996	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1518.89001	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0	
k_MtrCtrlVirualResDax_Ohm_f32	0.180999994	
k_MtrCtrlVirualResQax_Ohm_f32	0.171000004	
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0	
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0	
k MtrVoltDayIntegHil im Volt f32	24.4330006	
k_MtrVoltDaxIntegHiLim_Volt_f32		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	-22.4099998 0	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32	-22.4099998 0 24.4650993	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998 0 24.4650993 -22.4099998	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc	-22.4099998 0 24.4650993 -22.4099998	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0]	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1]	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0]	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1]	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MoltCurrLoaMtgtnEn_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MoldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_Ivrt.oaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOgx_Amp_f32_Val	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998	alue Result
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_VvrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOgx_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998 3	alue Result
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998 3 Actual Value Expected Va	alue Result
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrOgx_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val)	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998 3 Actual Value Expected Value 1636	· ·
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_MotlQurs_ComSvcDft_Cnt_lgc_val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val)	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998 3 Actual Value 1636 63700 Expected Value 1636 63700)3
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtghtEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998 3 Actual Value 1636 63700 63700 ± 1 -220 -220 ± 7.81E-0	03 - 4.88E-04
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotGursLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrOgax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998 3 Actual Value 1636 63700 63700 ± 1 -220 -7.63010979 -7.63010979	03 - 4.88E-04 - 4.88E-04
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotGurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998 3 Actual Value 1636 63700 63700 ± 1 -220 -7.63010979 -7.63010979 -22.7841721 -22.7842007 ±	203 24.88E-04 24.88E-04
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotlCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	-22.4099998 0 24.4650993 -22.4099998 0 10.5150003 -6.57200003 0.972000003 1663 5979 771 1636 1 0 0 -144.667007 1472 1.62199998 3 Actual Value 1636 63700 63700 ± 1 -220 -7.63010979 -22.7841721 21941 21941 21941 ± 1.525	23 2 4.88E-04 2 4.88E-04 2 88E-05



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
VtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
VitrCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target_MtrCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_val
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.035999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.25300002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0610000007
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-237.227005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	186.412003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.61699998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.86600006
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	896.210999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-75.5360031
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
/ltrCtrl_Vecu_Volt_M_f32[0]	20.3600006
/trCtrl_Vecu_Volt_M_f32[1]	22.7199993
/ltrCurrDaxPrevIntg_Volt_M_f32	-22.4869995
/ltrCurrDaxRef_Amp_M_f32[0]	-82.2979965
/ltrCurrDaxRef_Amp_M_f32[1]	46.8180008
/ltrCurrQaxCog_Amp_M_f32	5.72399998
/trCurrQaxFinalRef_Amp_M_f32[0]	6.18900013
/trCurrQaxFinalRef_Amp_M_f32[1]	83.0540009
/ltrCurrQaxPrevIntg_Volt_M_f32	25.5816994
ltrCurrQaxRef_Amp_M_f32[0]	31.5869999
/ltrCurrQaxRef_Amp_M_f32[1]	-186.395996
htrCurrQaxRpl_Amp_M_f32[0]	6.18900013
/ltrCurrQaxRpl_Amp_M_f32[1]	83.0540009
htrPosComputationDelay_Rad_M_f32[0]	-3.98799992
htrPosComputationDelay_Rad_M_f32[1]	2.56999993
/trVoltDax_Volt_M_f32[0]	-28.2420006
//dtrVoltDax_Volt_M_f32[1]	12.6160002
//dtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.768999994		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.68599999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.566999972		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.098999995		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	412.23999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	64.4531021		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.970099986		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	412.23999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	64.4531021		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.970099986		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2259.41992		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6958.5498		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0219999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.0149999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	14.9607		
k MtrVoltDaxIntegLoLim Volt f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	0.550499976		
k MtrVoltQaxIntegLoLim Volt f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.7080002		
k VoltSatQaxPolyCoeff Uls f32	7.73099995		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTbIX_UIs_u3p13[0]	2195		
t CommOffsetTbIX UIs u3p13[1]	6013		
t_CommOffsetTblY_Cnt_u16[0]	365		
t CommOffsetTblY Cnt u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read MtrCurrDax Amp f32 Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1694		
target MtrCntrl Read MtrCurrQax Amp f32 Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
	Actual Value	Expected Value	Resul
Name		Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1371	1371	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	43962	43962 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03 8.55099964 ± 4.88E-04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33019	33019 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013	6.18900013 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•





Test Step 2.88 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-212.632996 -205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16100001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	600.401001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-510.458008
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrlmpedOay_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0529999994 0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.709999979
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.37
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-807.60199
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-536.44397
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	14.243
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg Volt M f32	16.6030006 29.1970005
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	3.13030005
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002 41.6290016
MtrCurrQaxRpl_Amp_M_f32[1] MtrPosComputationDelay_Rad_M_f32[0]	6.22800016
MtrPosComputationDelay_Rad_M_f32[1]	-2.81399989
MtrVoltDax Volt M f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.602999985
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0560999997
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.046999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 PICurrCntrl MtrVecuFilt M str.PrevOutput UIs f32	20.7000008 -43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uis_f32 PICurrCntrl MtrVecuFilt M str.TermN Uls f32	-43.1699982 4.45230007
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.590499997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	20.7000008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	4.45230007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.590499997
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3135.04004
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6729.4502
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlVirualPosDay_Ohm_f32	1 0.0460000001
k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32	0.196999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.190999997
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	18.1431999

PICurrCntrl_Per1





Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.7143993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-15.9820004		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.8069992		
k_deadtimeVScale_Uls_f32	0.981000006		
t_CommOffsetTbIX_UIs_u3p13[0]	3023		
t_CommOffsetTbIX_UIs_u3p13[1]	3703		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4486		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1236	1236	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64290	64290 ± 1	·
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-88.0050049	-88.0049973 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.58217335	4.58217001 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-13.199666	-13.1997004 ± 4.88E-04	·
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	28707	28707 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-88.0050049	-88.0049973 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.89 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.09300005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	2.95000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-590.848999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009





MicCle (Michinesediaca, Chem. M. (2019) MicCle (Michinesediacian) Chem. (M. (2019) Michinesediacian) Chem. (M. (2019) Michinesediac	k_VoltSatQaxPolyCoeff_Uls_f32		
Mich. J. Minimediac, Ohm. M. 1930	o.	24.8470001	
Mich. J. Minimediac, Ohm. M. 1930	k VoltSatDaxPolyCoeff Llls f32		
Michit J.Minreediac, Ohm J. (1921)	k_MtrVoltVecuFiltEnable_Cnt_lgc		
MicCle Minimercolacian Chim M. 182(1) MicCle Minimercolacian M. 182(1) MicCle Minimercolacian M. 182(1) MicCle Minimercolacian Chim M. 182(1) Minimercolacian Chi	k_MtrVoltQaxIntegLoLim_Volt_f32		
MicCl, Mi	k_MtrVoltQaxIntegHiLim_Volt_f32	23.5613003	
MitCell MitrimeedSac Ohm M. 192(1) 0.416000011 MitCell Mid-admisspra(Gan, Ohm M. 192(1) 0.48699997 MitCell Mid-admisspra(Gan, Ohm M. 192(1) 0.48699997 MitCell Mid-admisspra(Gan, Ohm M. 192(1) -802,944971 MitCell Mid-Call Mid-admisspra(Gan, Ohm M. 192(1) -802,944971 MitCell Mid-Call Mid-Call Mid-admisspra(F. Voll, M. 192(1) -802,944971 MitCell Mid-Call M	k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0	
MicCell, Microlangel Gain, Ohm, M. (201) MicCell, MicCell, MicColangel Gain, Ohm, M. (201) MicCell, Mi	k_MtrVoltDaxIntegLoLim_Volt_f32		
MicCirl, MicCardingral Gain, Ohm, M, 52(1) MicCirl, MicCardingral Cardingral Gain, Ohm, M, 52(1) MicCirl, M	k MtrVoltDaxIntegHiLim Volt f32		
MicCarl, Microanger John, M. (52(1) 0.410000011 MicCarl, MicCarley John, M. (52(1) 0.44899997 MicCarl, MicCarley John, M. (52(1) 0.48899997 MicCarl, MicCarley John, M. (52(1) 0.48999997 MicCarl, MicCarley John, M. (52(1) 0.48999999999999999999999999999999999999	k MtrCurrQaxRefModifRpIEn Cnt Igc		
MicColf, Mic	k MtrCurrQaxRefModifDsb Cnt lgc		
MicCrit MicCri			
MicCiri MirchardingeralGain, Ohm M, 132(1) MirCiri, Mirchardinger, York, M, 132(1) MirCiri, Yeau, Yea, M, 132(1) MirCiri, Yeau, M, 132(1) MirCiri, MirCiri, M, 132(1) MirCiri, M, 132(1) MirCiri, MirCiri, M, 132(1) MirCiri, M, 132(1)	:		
MicCirl MirCoxintegralGam, Ohm, M. 32(1) MirCirl MirCoxintegralGam, Ohm, M. 52(0) MirCirl MirCoxintegralGam, Ohm, M. 52(0) MirCirl MirCoxintegralGam, Ohm, M. 52(0) A 46999997 MirCirl MirCoxintegralGam, Ohm, M. 52(0) A 6999997 MirCirl MirCoxintegralGam, Ohm, M. 52(1) A 5999994 MirCirl MirCirl MirCoxintegralGam, Ohm, M. 52(1) A 5999994 MirCirl MirCirl MirCoxintegralGam, Ohm, M. 52(1) A 5999994 MirCirl MirCirl MirCirl MirCoxintegral Coxintegral Cox			
MicCirl, Mitricavindegai Cain, Drin M. 152(1) 0.0410000011 MitCirl, Micoavindegai Cain, Drin M. 152(1) 0.44899997 MitCirl, Micoavindegai Cain, Drin M. 152(1) 0.44899997 MitCirl, Micoavindegai Cain, Drin, M. 152(1) 20.189999 MitCirl, Michoavindegai Cain, Drin, M. 152(1) 4.6599994 MitCirl, Michoavinde, F., Volt, M. 152(1) 4.6599994 MitCirl, Micholarde, F., Volt, M. 152(1) 4.65999994 MitCirl, Micholarde, F., Volt, M. 152(1) 8.81900043 MitCirl, Micholarde, F., Volt, M. 152(1) 1.3169001 MitCirl, Veou, Volt, M. 152(1) 13.362999 MitCirl, Leve, Volt, M. 152(1) 15.7229996 MitCirl, Microavinde, J. Volt, M. 152 2.31609993 MitCirl, Say, D. M. 152(1) 65.1900024 MitCirl, Say, D. M. 152(1) 65.1900024 MitCirl, Say, D. M. 152(1) 20.6149999 MitCirl, Carda, D. M. 152(1) 20.6149999 MitCirl, Carda, Dr. M. 152(1) 4.609993 MitCirl, MitC	k_ILOAFdbackSignalSclFacSlew_UlspS_f32		
MicCirl, Mirchardingan, Ohm, M. (321) 0.0410000011 MirCirl, Mirchardingian, Ohm, M. (321) 0.216999993 MirCirl, Mirchardingian, Ohm, M. (321) 0.46899997 MirCirl, Mirchardingian, Ohm, M. (321) 4.06899997 MirCirl, Mirchardingian, Ohm, M. (321) 4.02244671 MirCirl, Mirviorophonicalian, Ohm, M. (321) 4.05999994 Mircirl, Vecu, Volt, M. (321) 4.05899999 Mircirl, Vecu, Volt, M. (321) 1.059999 Mircirl, Ohm, M. (321) 6.0729996 Mircirl, Curbarket, Ann, M. (320) 6.051900024 Mircirl, Curbarket, Ann, M. (321) 2.16 672 Mircirl, Curbarket, Ann, M. (321) 6.0722996 Mircirl, Curbarket, Ann, M. (321) 6.07229996 Mircirl, Ann, M. (321) 6.07229996 M			
MicClin, MircoakintegralGain, Ohm, M. (520) 0.218999983 MirClir, MircoakintegralGain, Ohm, M. (520) 0.218999997 MirClir, MircoakintegralGain, Ohm, M. (520) 20.6189995 MirClir, MircoakintegralGain, Ohm, M. (520) 40.6189999 MirClir, MircoakintegralGain, Ohm, M. (520) 40.6599994 MirClir, Mircollaber, F., Volt, M. (520) 4.55999994 MirClir, Mircollaber, F., Volt, M. (520) 4.55999994 MirClir, Mircollaber, F., Volt, M. (520) 4.55999994 MirClir, Mircollaber, F., Volt, M. (520) 13.650001 MirClir, Mircollaber, F., Volt, M. (520) 13.5590001 MirClir, Veou, Volt, M. (520) 13.559099 MirClir, Veou, Volt, M. (520) 13.559999 MirClir, Veou, Volt, M. (520) 45.190993 MirClir, Deap M. (520) 45.190993 MirClir, Carbon, M. (520) 46.190993 MirClir, Carbon, M. (520) 47.08799 MirClir, Carbon, M. (520) 47.08799 MirClir, Carbon, M. (520) 47.08799 MirClir, Carbon, M. (520) 47.087999 MirClir, Carbon, M. (520) 47.0829996 MirClir, Carbon, M. (520) 47.0829996	SlowDataAccessBufIndex_Cnt_M_u16		
MicCirl Mircoaknegra(Gan_Ohm_M_S2(1) 0.410000011 MirCirl Mircoaknegra(Gan_Ohm_M_S2(1) 0.21699993 MirCirl Mircoaknegra(Gan_Ohm_M_S2(1) 0.44699997 MirCirl Mircoaknegra(Gan_Ohm_M_S2(1)) 2.0 6199995 MirCirl MircoakpropotionalGan_Ohm_M_S2(1) 802 844971 MirCirl MirvoliDaxFF_Volt_M_S2(1) 4.55999994 MirCirl MirvoliDaxFF_Volt_M_S2(1) 2.0 830002 MirCirl MirvoliDaxFF_Volt_M_S2(1) 1.3 1560001 MirCirl MirvoliDaxFF_Volt_M_S2(1) 1.3 1560001 MirCirl MirvoliDaxFF_Volt_M_S2(1) 1.3 1560001 MirCirl Mirvoll MaxFF_Volt_M_S2(1) 1.5 7220996 Mircirl Mirvoll MaxFF_Volt_M_S2(1) 1.5 7220996 Mircirl Mirvoll Mircirl Mirvoll MaxFP_Volt_M_S2(1) 2.3 1609993 Mircirl Mirvoll Mirvoll Mirvoll M_S2(1) 2.5 672 Mircirl Mirvoll	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32		
MicCirl Mircoaknegra(Gan_Ohm_M_S2(1) 0.410000011 MirCirl Mircoaknegra(Gan_Ohm_M_S2(1) 0.21699993 MirCirl Mircoaknegra(Gan_Ohm_M_S2(1) 0.44699997 MirCirl Mircoaknegra(Gan_Ohm_M_S2(1)) 2.0 6199995 MirCirl MircoakpropotionalGan_Ohm_M_S2(1) 802 844971 MirCirl MirvoliDaxFF_Volt_M_S2(1) 4.55999994 MirCirl MirvoliDaxFF_Volt_M_S2(1) 2.0 830002 MirCirl MirvoliDaxFF_Volt_M_S2(1) 1.3 1560001 MirCirl MirvoliDaxFF_Volt_M_S2(1) 1.3 1560001 MirCirl MirvoliDaxFF_Volt_M_S2(1) 1.3 1560001 MirCirl Mirvoll MaxFF_Volt_M_S2(1) 1.5 7220996 Mircirl Mirvoll MaxFF_Volt_M_S2(1) 1.5 7220996 Mircirl Mirvoll Mircirl Mirvoll MaxFP_Volt_M_S2(1) 2.3 1609993 Mircirl Mirvoll Mirvoll Mirvoll M_S2(1) 2.5 672 Mircirl Mirvoll	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32		
MicCiri MircoxintegraGax, Ohm M, 152(1) 0.410000011 MirCirl MircoxintegraGain, Ohm M, 152(1) 0.46699997 MirCirl MircoxintegraGain, Ohm M, 152(1) 0.46699997 MirCirl MircoxintegraGain, Ohm M, 152(1) 802 644971 MirCirl MircoxintegraGain, Ohm M, 152(1) 802 644971 MirCirl Mircir MircoxintegraGain, Ohm M, 152(1) 4.55999994 MirCirl MircoxintegraGain, Ohm M, 152(1) 4.55999994 MirCirl MircoxintegraGain, Ohm M, 152(1) 8.9300003 MirCirl MircoxintegraGain, Ohm M, 152(1) 8.9300003 MirCirl MircoxintegraGain, Ohm M, 152(1) 13.1580001 MirCirl MircoxintegraGain, Ohm M, 152(1) 13.262999 MirCirl MircoxintegraGain, Ohm M, 152(1) 157229996 MircoxintegraGain, Ohm M, 152(1) 157229996 MircoxintegraGain, Ohm M, 152(1) 216.972 MircoxintegraGain, Ohm M, 152(1) 213.069993 MircoxintegraGain, Ohm M, 152(1) 213.069993 MircoxintegraGain, Ohm M, 152(1) 213.069993 MircoxintegraGain, Ohm M, 152(1) 213.06993 MircoxintegraGain, Ohm M, 152(1) 213.06993 MircoxintegraGain, Ohm M, 152(1) 14680001	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32		
MICCH_MirtingedQax_Ohm_M_132(1) MICCH_Mirtinger(Can_Ohm_M_152(1) MICCH_Mirtinger(Can_Ohm_M_152(1) 0.21699993 MICCH_Mirtinger(Can_Ohm_M_152(1) 0.44699997 MICCH_Mirtinger(Can_Ohm_M_152(1) MICCH_Mirtinger(Can_Ohm_M_152(1) MICCH_Mirtinger(Can_Ohm_M_152(1) 4.5599994 MICCH_Mirtinger(Can_Ohm_M_152(1) 4.5150001 MICCH_Mirtinger(Can_Ohm_M_152(1) 4.3150001 MICCH_Mirtinger(Can_Ohm_M_152(1) 4.3150001 MICCH_Mirtinger(Can_Ohm_M_152(1) 4.51500993 MICCH_Mirtinger(Can_Ohm_M_152(1) 4.51500993 MICCH_Mirtinger(Can_Ohm_M_152(1) 4.51500933 MICCH_Mirtinger(Can_Ohm_M_152(1) 4.51500933 MICCH_Mirtinger(Can_Ohm_M_152(1) MICCH_Mirtinger(Can_Ohm_M_152	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32		
MICCIL MirringedGax, Ohm M, 132(1) MICCIL Mirricawintegra[Gain_Ohm_M, 152(0) MICCIL Mirricawintegra[Gain_Ohm_M, 15	PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32		
MITCH_MitropackQax_Ohm_M_[32[1] 0.410000011 MITCH_MitropackpregralGain_Ohm_M_[32[1] 0.21699993 MITCH_MitropackpregralGain_Ohm_M_[32[1] 0.4699997 MITCH_MitropackpropolionalGain_Ohm_M_[32[1] 20.6189995 MITCH_MitropackpropolionalGain_Ohm_M_[32[1] -802.844971 MITCH_MitropackpropolionalGain_Ohm_M_[32[1] -802.844971 MITCH_MitropackpropolionalGain_Ohm_M_[32[1] -802.844971 MITCH_MitropackpregralGain_Ohm_M_[32[1] -20.8330002 MITCH_MitropackpregralGain_Ohm_M_[32[1] -20.8330002 MITCH_MitropackpregralGain_Ohm_M_[32[1] -20.8330002 MITCH_MitropackpregralGain_Ohm_M_[32[1] -31.560001 MITCH_MitropackpregralGain_Ohm_M_[32[1] -31.560001 MITCH_MitropackpregralGain_Ohm_M_[32[0] -31.3629999 MITCH_MITCH_MitropackpregralGain_Ohm_M_[32[0] -55.900024 MITCH_MITCH_MitropackpregralGain_Ohm_M_[32[0] -55.900024 MITCH_MITCH_MitropackpregralGain_Ohm_M_[32[0] -56.900024 MITCH_MITCH_MitropackpregralGain_Ohm_M_[32[0] -21.3026993 MITCH_MITCH_MitropackpregralGain_Ohm_M_[32[0] -21.3026993 MITCH_MITCH_MITCH_MitropackpregralGain_Ohm_M_[32[0] -21.3026993 MITCH_MIT			
MitCirl_MirlmpedQax_Ohm_M_[32[1] 0.0410000011 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 0.216999933 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 2.06189995 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 2.06189995 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 3.0284971 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 3.0284971 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 3.0284971 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 4.0599994 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 2.08330002 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 2.08330002 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 3.61800043 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 3.3629999 MitCirl_MirdcaintegralGain_Ohm_M_[32[1] 3.3629999 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 3.3629999 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 3.3629999 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 3.3629993 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 3.06189998 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 3.069993 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 3.069999999 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 3.069999999 MitCirl_MirdcaintegralGain_Ohm_M_[32[0] 3.06999999999999999999999999999999999999	PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		
MitCirl MirlimpedQax Ohm M_152[1] 0.410000111 MitCirl MirdixelsrilegralGain, Ohm M_152[0] 0.21699993 MitCirl MirdixelsrilegralGain, Ohm M_152[1] 0.446999997 MitCirl MirdixelsripgralGain, Ohm M_152[0] 20.6189995 MitCirl MirdixelsripoptionalGain, Ohm M_152[0] 4.6599994 MitCirl MirdixelproptionalGain, Ohm M_152[0] 4.5599994 MitCirl MirdixelbaxFF_Volt_M_152[0] 4.5599994 MitCirl MirdixelbaxFF_Volt_M_152[0] 4.5599994 MitCirl MirdixelbaxFF_Volt_M_152[0] 4.5599994 MitCirl MirdixelbaxFF_Volt_M_152[0] 4.55900043 MitCirl MirdixelbaxFF_Volt_M_152[0] 4.31.550001 MitCirl MirdixelbaxFF_Volt_M_152[0] 4.32.59999 MitCirl MirdixelbaxFF_Volt_M_152[0] 4.32.59999 MitCirl MirdixelbaxFF_Volt_M_152[0] 4.57.229996 MirdixelbaxFF_Volt_M_152[0] 4.57.229999999999999999999999999999999999	PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32		
MitCri_MirimpedQax_Ohm_M_[52[1] 0.0410000011 MitCri_MircaxintegraGain_Ohm_M_[52[0] 0.216999933 MitCri_MircaxintegraGain_Ohm_M_[52[1] 0.446999997 MitCri_MircaxintegraGain_Ohm_M_[52[0] 20.8189995 MitCri_MircaxintegraGain_Ohm_M_[52[0] 4.8599994 MitCri_MircintegraF_volt_M_[52[0] 4.5599994 MitCri_MircintegraF_volt_M_[52[0] 4.5599994 MitCri_MircintegraF_volt_M_[52[0] 4.81900043 MitCri_MircintegraF_volt_M_[52[0] 4.81900043 MitCri_MircintegraF_volt_M_[52[0] 13.3629999 MitCri_MircintegraF_volt_M_[52[0] 13.3629999 MitCri_Vecu_Volt_M_[52[0] 13.3629999 MitCri_Vecu_Volt_M_[52[0] 13.3629999 MitCri_DavEe_Amp_M_[52[0] 45.792996 MitCurDavRef_Amp_M_[52[0] 65.1900024 MitCurDavRef_Amp_M_[52[0] 45.1900024 MitCurDavRef_Amp_M_[52[0] 213.026993 MitCurDavEe_Amp_M_[52[0] 213.026993 MitCurDavEe_Amp_M_[52[0] 41.4630003 MitCurDavEe_Amp_M_[52[0] 41.620016 MitCurDavRef_Amp_M_[52[0] 41.620016 MitCurDavRef_Amp_M_[52[0]			
MtrCtrl_MtrImpedQax_Ohm_M_[32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_[32[0] 0.21699993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_[32[1] 0.469999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[32[1] 20.6189995 MtrCtrl_MtrVoltDaxFE_Volt_M_[32[0] 4.55999994 MtrCtrl_MtrVoltDaxFE_Volt_M_[32[0] 4.55999994 MtrCtrl_MtrVoltDaxFE_Volt_M_[32[0] 8.61900043 MtrCtrl_MtrVoltQaxFE_Volt_M_[32[0] 13.3660001 MtrCtrl_MtrVoltQaxFE_Volt_M_[32[1] 13.3660001 MtrCtrl_MtrVoltQaxFE_Volt_M_[32[1] 15.722996 MtrCurlaxPrevintg_Volt_M_[32] 23.166993 MtrCurlaxPrevintg_Volt_M_[32] 23.166993 MtrCurlaxPrevintg_Volt_M_[32[0] 46.1900024 MtrCurlaxPrevintg_Volt_M_[32[1] 216.972 MtrCurlaxRef_Amp_M_[32[1] 213.026993 MtrCurlaxPrevintg_Volt_M_[32[0] 41.4630003 MtrCurrQaxFinalRef_Amp_M_[32[1] 46.7229996 MtrCurrQaxRef_Amp_M_[32[0] 105.246002 MtrCurrQaxRef_Amp_M_[32[0] 41.629016 MtrCurrQaxRef_Amp_M_[32[0] 3.50099993 MtrPosComputationDelay, Rad_M_[32[1] 46.722996 <tr< td=""><td>PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32</td><td></td><td></td></tr<>	PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrCaxIntegralGain_Ohm_M_f32[0] 0.21699993 MtrCtrl_MtrCaxIntegralGain_Ohm_M_f32[1] 0.469999997 MtrCtrl_MtrCaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -8.5999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -8.61800043 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -8.61800043 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] 13.3629999 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229966 MtrCurDaxFet_Amp_M_f32[1] 15.7229963 MtrCurDaxFet_Amp_M_f32[0] -65.1900024 MtrCurDaxFet_Amp_M_f32[0] -65.1900024 MtrCurDaxFet_Amp_M_f32[0] -210.6972 MtrCurDaxFet_Amp_M_f32[0] -213.026993 MtrCurDaxFet_Amp_M_f32[0] -213.026993 MtrCurDaxFet_Amp_M_f32[1] -66.722996 MtrCurDaxFet_Amp_M_f32[1] -105.246002 MtrCurDaxRet_Amp_M_f32[1] -105.246002 MtrCurDaxRet_Amp_M_f32[1] -105.246002 MtrCur			
MtrCtrl_MtrimpedQax_Ohm_M_[32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_[32[1] 0.216999993 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[32[1] 0.46999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[32[1] 20.618995 MtrCtrl_MtrVoltDaxFP_volt_M_[32[0] 4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[0] 4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[0] 8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_[32[0] 13.359999 MtrCtrl_Volt_MTs2[0] 13.359999 MtrCtrl_Volt_MTs2[0] 13.359999 MtrCtrl_Volt_MTs2[0] 13.359999 MtrCtrl_Volt_MTs2[0] 15.722996 MtrCurlDaxPrevintg_Volt_M_[32[1] 15.722996 MtrCurlDaxRef_Amp_M_[32[0] 65.1900024 MtrCurrDaxRef_Amp_M_[32[0] 65.1900024 MtrCurrDaxRef_Amp_M_[32[1] 216.972 MtrCurrDaxFrienaRef_Amp_M_[32[0] 213.026993 MtrCurrQaxFrienaRef_Amp_M_[32[0] 213.026993 MtrCurrQaxFrienaRef_Amp_M_[32[0] 105.246002 MtrCurrQaxRef_Amp_M_[32[0] 105.246002 MtrCurrQaxRef_Amp_M_[32[0] 213.026993 MtrCurrQaxRef_Amp_M_[32[0]	PICurrCntrl_CurrSensFailSclFac_Uls_M_f32		
MtrCtrl_MtrimpedQax_Ohm_M_[52[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_[52[0] 0.216999993 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[52[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[52[0] 20.6189995 MtrCtrl_MtrVoltDaxFr_Votl_M_[52[0] 4.5599994 MtrCtrl_MtrVoltDaxFr_Votl_M_[52[0] 4.55999994 MtrCtrl_MtrVoltDaxFr_Votl_M_[52[0] 8.61900043 MtrCtrl_MtrVoltQaxFr_Votl_M_[52[0] 13.1560001 MtrCtrl_MtrVoltQaxFr_Votl_M_[52[1] 13.1560001 MtrCtrl_Vecu_Votl_M_[52[1] 15.7229996 MtrCtrl_Vecu_Votl_M_[52[1] 15.7229996 MtrCurrDaxFrevIntg_Votl_M_[52] 23.160993 MtrCurrDaxFrevIntg_Votl_M_[52[1] 216.972 MtrCurrDaxFrevIntg_Votl_M_[52[1] 216.972 MtrCurrQaxCog_Amp_M_[52] 20.6149998 MtrCurrQaxFinalRef_Amp_M_[52[0] 213.026993 MtrCurrQaxFinalRef_Amp_M_[52[0] 210.026993 MtrCurrQaxFinalRef_Amp_M_[52[0] 213.026993 MtrCurrQaxFinalRef_Amp_M_[52[0] 105.246002 MtrCurrQaxRef_Amp_M_[52[0] 213.026993 MtrCurrQaxRef_Amp_M_[52[1] 41.6290016	MtrVoltQax_Volt_M_f32[1]		
MtrCtrl_MtrimpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrCaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrCaxPropotionalGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.618995 MtrCtrl_MtrVoxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] 4.5599994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[1] 13.362999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCurrDaxPereIntg_Volt_M_f32[1] -65.1900024 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[1] -216.972 MtrCurrQaxCog_Amp_M_f32 20.614998 MtrCurrQaxCog_Amp_M_f32[1] -66.7229996 MtrCurrQaxFinalRef_Amp_M_f32[1] -66.7229996 MtrCurrQaxRef_Amp_M_f32[1] 41.6290016 MtrCurrQaxRef_Amp_M_f32[1] 41.6290016 MtrCurrQaxRef_Amp_M_f32[1] 41.6290016 MtrCurrQaxRef_Amp_M_f32[1] 46.7229996 <td< td=""><td>MtrVoltQax_Volt_M_f32[0]</td><td>-8.61900043</td><td></td></td<>	MtrVoltQax_Volt_M_f32[0]	-8.61900043	
MtrCtrl_MtrimpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrCaxIntegralGain_Ohm_M_f32[1] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.618995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] 4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] 13.362999 MtrCtrl_Vocu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vocu_Volt_M_f32[0] 15.7229996 MtrCurrDaxPrevIntg_Volt_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[1] -216.972 MtrCurrQaxCog_Amp_M_f32 20.614998 MtrCurrQaxFinalRef_Amp_M_f32[1] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[1] -66.7229996 MtrCurrQaxFeyIntg_Volt_M_f32 1.44630003 MtrCurrQaxFe_Amp_M_f32[0] -105.246002 MtrCurrQaxRef_Amp_M_f32[1] 41.6290016 MtrCurrQaxRef_Amp_M_f32[1] 466.7229996	MtrVoltDax_Volt_M_f32[1]	-13.1560001	
MtrCtrl_MtrlampedQax_Ohm_M_[32[1] 0.0410000011 MtrCtrl_MtrCaxIntegralGain_Ohm_M_[32[1] 0.216999993 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[32[0] 20.6189995 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[0] 4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[1] -20.8330002 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[0] 48.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_[32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_[32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_[32[0] 15.7229996 MtrCurrDaxPrevintg_Volt_M_[32[0] -65.1900024 MtrCurrDaxRef_Amp_M_[32[0] -65.1900024 MtrCurrDaxRef_Amp_M_[32[0] -65.1900024 MtrCurrQaxFinalRef_Amp_M_[32[0] -210.026993 MtrCurrQaxFinalRef_Amp_M_[32[0] -210.026993 MtrCurrQaxFinalRef_Amp_M_[32[0] -66.7229996 MtrCurrQaxRef_Amp_M_[32[0] -105.246002 MtrCurrQaxRef_Amp_M_[32[0] -105.246002 MtrCurrQaxRef_Amp_M_[32[1] -66.722996 MtrCurrQaxRef_Amp_M_[32[0] -213.026993 MtrCurrQaxRef_Amp_M_[32[0] -210.026993 MtrCurrQaxRef_Amp_M_[32[0] -210.026993 <td>MtrVoltDax_Volt_M_f32[0]</td> <td>-8.61900043</td> <td></td>	MtrVoltDax_Volt_M_f32[0]	-8.61900043	
MtrCtrl_MtrlmpedQax_Ohm_M_[32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_[32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_[32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[32[0] 20.6189995 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[0] 4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[0] 4.55999994 MtrCtrl_MtrVoltQaxFF_Volt_M_[32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_[32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_[32[1] -13.1560001 MtrCtrl_MtrVolt_Mr_[32[0] 13.362999 MtrCtrl_Vecu_Volt_M_[32[1] 15.722996 MtrCtrl_DaxPervIntg_Volt_M_[32] -23.160993 MtrCurrDaxPerser_Amp_M_[32[0] -85.190024 MtrCurrDaxRef_Amp_M_[32[1] -216.972 MtrCurrQaxCog_Amp_M_[32] 20.614998 MtrCurrQaxFer_Amp_M_[32[0] -213.026993 MtrCurrQaxFer_Amp_M_[32[0] -213.026993 MtrCurrQaxRef_Amp_M_[32[0] -105.246002 MtrCurrQaxRef_Amp_M_[32[1] 41.6290016 MtrCurrQaxRef_Amp_M_[32[1] 41.6290016 MtrCurrQaxRef_Amp_M_[32[1] 66.7229996	MtrPosComputationDelay_Rad_M_f32[1]	-3.37899995	
MtrCtrl_MtrlandeQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 0.446999995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] 4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.6190043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 13.362999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCurrDaxPrevIntg_Volt_M_f32 -23.1609993 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[1] -216.972 MtrCurrQaxCog_Amp_M_f32 20.6149998 MtrCurrQaxFerAnlaRef_Amp_M_f32[0] -213.026993 MtrCurrQaxPrevIntg_Volt_M_f32 1.44630003 MtrCurrQaxRef_Amp_M_f32[0] -105.246002 MtrCurrQaxRef_Amp_M_f32[0] -105.246002 MtrCurrQaxRef_Amp_M_f32[0] -213.026993	MtrPosComputationDelay_Rad_M_f32[0]		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.5599994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.833002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] 13.3620999 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 13.7229996 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCtrl_DaxPrevintg_Volt_M_f32 -23.1609993 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrQaxFinalRef_Amp_M_f32[1] -216.972 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[1] -66.7229996 MtrCurrQaxFrevintg_Volt_M_f32 1.44630003 MtrCurrQaxRef_Amp_M_f32[0] -105.246002 MtrCurrQaxRef_Amp_M_f32[1] 41.6290016	MtrCurrQaxRpl Amp M f32[1]		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.21699993 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 0.44699997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.618995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -80.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[0] 15.7229996 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrQaxCog_Amp_M_f32 20.6149998 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxRef_Amp_M_f32[0] -210.2640003 MtrCurrQaxRef_Amp_M_f32[0] -105.246002			
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[0] 15.7229996 MtrCurrDaxPrevIntg_Volt_M_f32 -23.1609993 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[1] -216.972 MtrCurrQaxCog_Amp_M_f32 20.6149998 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[1] -66.7229996 MtrCurrQaxPrevIntg_Volt_M_f32 1.44630003			
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[1] -216.972 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993 MtrCurrQaxFinalRef_Amp_M_f32[1] -66.7229996			
MtrCtrl_MtrlmpedQax_Ohm[f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -80.2.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.5599994 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[0] 13.7229996 MtrCtrl_DaxPrevintg_Volt_M_f32 -23.1609993 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[1] -216.972 MtrCurrQaxCog_Amp_M_f32 20.6149998 MtrCurrQaxFinalRef_Amp_M_f32[0] -213.026993			
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCurrDaxPrevIntg_Volt_M_f32 -23.1609993 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[1] -216.972 MtrCurrQaxCog_Amp_M_f32 20.6149998			
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCurrDaxPrevIntg_Volt_M_f32 -23.1609993 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024 MtrCurrDaxRef_Amp_M_f32[1] -216.972			
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCtrl_Vecu_Volt_M_f32[0] -23.1609993 MtrCurrDaxRef_Amp_M_f32[0] -65.1900024			
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996 MtrCtrl_Vecu_Volt_M_f32 -23.1609993			
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001 MtrCtrl_Vecu_Volt_M_f32[0] 13.3629999 MtrCtrl_Vecu_Volt_M_f32[1] 15.7229996	MtrCurrDaxPrevIntg_Volt_M_f32		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -13.1560001	MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -8.61900043	MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -20.8330002	MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.1560001	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -4.55999994	MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.61900043	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -802.844971	MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.8330002	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 20.6189995	MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.55999994	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.446999997	MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.216999993	MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	20.6189995	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.0410000011	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.446999997	
	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.216999993	
	MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011	
MtrCtrl MtrImpedQax Ohm M f32[0] 0.0170000009		0.0170000009	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.112999998	MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998	

PICurrCntrl_Per1



Name	Actual Value	Expected Value	Result
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63045	63045 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	87.7369995	87.7369995 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-12.7889309	-12.7889004 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-8.07618523	-8.07618999 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8033	8033 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✓
MtrCurrQaxFinalRef Amp M f32[1]	87 7369995	87 7369995 + 7 81F-03	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 2.90 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	79.6729965
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxPrevIntg_Volt_M_f32	21.3169994
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007		
MtrCurrQaxRpl_Amp_M_f32[0]	-212.632996		
MtrCurrQaxRpl_Amp_M_f32[1]	-205.085007		
MtrPosComputationDelay_Rad_M_f32[0]	1.52600002		
MtrPosComputationDelay_Rad_M_f32[1]	-2.68400002		
MtrVoltDax_Volt_M_f32[0]	-27.0669994		
MtrVoltDax_Volt_M_f32[1]	28.1070004		
MtrVoltQax_Volt_M_f32[0]	-27.0669994		
MtrVoltQax Volt M f32[1]	28.1070004		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.40000006		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0590000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.231900007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	16.5851002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	16.5851002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.887899995		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5194.8999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.0219999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.7327003		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.5590992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.368		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTbIX_UIs_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrOffCamOffcat_Cat_u16_atr	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0.21000060		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969 3		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	Actual Value	Evposted Value	Darrel
Name		Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	
	0	0 ± 1	
, , ,	0 -87 0220065	_87 0220065 ± 7 94E 02	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.9229965	-87.9229965 ± 7.81E-03	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-87.9229965 2.70515442	2.70514989 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-87.9229965 2.70515442 3.95906138	2.70514989 ± 4.88E-04 3.95905995 ± 4.88E-04	
MtrCntr_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	-87.9229965 2.70515442 3.95906138 43793	2.70514989 ± 4.88E-04 3.95905995 ± 4.88E-04 43793 ± 1.52588E-05	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-87.9229965 2.70515442 3.95906138	2.70514989 ± 4.88E-04 3.95905995 ± 4.88E-04	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

ame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	1	
	·	
ItrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
ItrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
ItrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
ItrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
ItrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-133.947006	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	75.7020035	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0949999988	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0179999992	
ltrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998	
ltrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125	
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.558000028	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.505	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-763.603027	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	830.864014	
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0989999995	
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0170000009	
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.115999997	
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.115999997	
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0810000002	
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.354999989	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	657.155029	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-284.454987	
ItrCtrl MtrVoltDaxFF Volt M f32[0]	22.7639999	
ItrCtrl MtrVoltDaxFF Volt M f32[1]	9.54300022	
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-24.052	
ItrCtrl MtrVoltQaxFF Volt M f32[1]	-25.3250008	
trCtrl Vecu Volt M f32[0]	14.243	
trCtrl Vecu Volt M f32[1]	16.6030006	
trCurrDaxPrevIntg Volt M f32	-10.9969997	
ltrCurrDaxRef_Amp_M_f32[0]	-208.287994	
ItrCurrDaxRef Amp M f32[1]	-27.9839993	
ltrCurrQaxCog_Amp_M_f32	0.48699989	
ltrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004	
ItrCurrQaxFinalRef Amp M f32[1]	-9.14299965	
ltrCurrQaxPrevIntg_Volt_M_f32	26.5330009	
ItrCurrQaxRef Amp M f32[0]	-91.4420013	
trCurrQaxRef_Amp_M_f32[1]	133.692993	
trCurrQaxRel_Amp_M_i32[i] trCurrQaxRpl Amp M f32[0]	-132.813004	
trCurrQaxRpl_Amp_M_isz[0] trCurrQaxRpl Amp_M_f32[1]	-9.14299965	
	-9.14299905 -2.81100011	
ItrPosComputationDelay_Rad_M_f32[0]		
ItrPosComputationDelay_Rad_M_f32[1]	-3.06399989	
ltrVoltDax_Volt_M_f32[0] ltrVoltDax Volt M f32[1]	-24.052 -25.3250008	

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Ficuncini_Feri		(2	ALC TUGIO
Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-24.052		
MtrVoltQax_Volt_M_f32[1]	-25.3250008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.533999979		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0460000001		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.339700013		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.492000014		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	51.8735008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.139899999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	51.8735008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.139899999		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4664.1001		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0250000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.151999995		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	26.3267994		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.5105991		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	11.9359999		
k_VoltSatQaxPolyCoeff_Uls_f32	-16.2380009		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTbIX_Uls_u3p13[0]	1106		
t_CommOffsetTbIX_Uls_u3p13[1]	4701		
t_CommOffsetTblY_Cnt_u16[0]	363		
t_CommOffsetTblY_Cnt_u16[1]	989		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3229		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	989	989	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62324	62324 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	142.348999	142.348999 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	14.6598969	14.6598997 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-5.86465836	-5.86465979 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	53931	53931 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004	-132.813004 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

142.348999

142.348999 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.92 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	209.052002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-124.994003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.093999968
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.0399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-632.612
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-39.875
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrlmpedOax_Ohm_M_f32[1] MtrCtrl_MtrlmpedOax_Ohm_M_f32[0]	0.013000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.465999991 1.62100005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	-201.291
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-817.749023 -14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002 -25.6930008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.00099902 2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[0]	15.7229996
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32	-17.9279995
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-190.440994
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556
MtrCurrQaxFinalRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxPrevIntg_Volt_M_f32	26.2782993
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	-200.556
MtrCurrQaxRpl Amp M f32[1]	-98.4449997
MtrPosComputationDelay Rad M f32[0]	1.08399999
MtrPosComputationDelay Rad M f32[1]	4.23000002
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl CurrSensFailSclFac Uls M f32	0.090000036
PICurrCntrl InverterFailSclFac UIs M f32	0.88499999
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.660399973
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.535000026
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-43.1699982
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-826.23999
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	15.0881996
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.349099994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-826.23999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	15.0881996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.349099994
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5272.3999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.179000005
k_MtrCtrlVirualResQax_Ohm_f32	0.0209999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0





		• "	
Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.5879993		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	20.5517998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6410007		
k_VoltSatQaxPolyCoeff_Uls_f32	20.0030003		
k_deadtimeVScale_UIs_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	687		
t_CommOffsetTblX_Uls_u3p13[1]	7234		
t_CommOffsetTblY_Cnt_u16[0]	341		
t_CommOffsetTblY_Cnt_u16[1]	370		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	389		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	389	389	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.112415425	-0.112415001 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.83869457	-4.8386898 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	11595	11595 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	-
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556	-200.556 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.93 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.495
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.398999989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	184.223999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-915.817017
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0850000009 0.11299998
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.58099997
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.776000023
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-189.419998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	896.187988
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1]	24.8479996 27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxCog_Amp_M_f32	83.9489975
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	9.36159992
MtrCurrQaxRef_Amp_M_f32[0]	106.072998
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32[0]	-112.455002 67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-2.69099998
MtrPosComputationDelay_Rad_M_f32[1]	-1.99899995
MtrVoltDax_Volt_M_f32[0]	-3.59500003
MtrVoltDax_Volt_M_f32[1]	-28.4209995
MtrVoltQax_Volt_M_f32[0]	-3.59500003
MtrVoltQax_Volt_M_f32[1]	-28.4209995
PICurrCottrl_CurrSensFailSclFac_UIs_M_f32	0.155000001
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.158000007 0.76899994
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.565999985
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	58.6325989
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.559199989
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	58.6325989
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 SlowDataAccessBufIndex Cnt M u16	0.559199989
k CLOAFdbackSignalSclFacSlew UlspS f32	1848.06995
k ILOAFdbackSignalSclFacSlew UlspS f32	6831.5
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0219999999
k_MtrCtrlVirualResQax_Ohm_f32	0.0419999994
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	2.43009996
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	-9.64999962 1
k MtrVoltQaxIntegHiLim Volt f32	24.5324001
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	2.3499999
k_VoltSatQaxPolyCoeff_Uls_f32	21.7280006
k_deadtimeVScale_Uls_f32	0.957000017
t_CommOffsetTbiX_Uls_u3p13[0]	474
t_CommOffsetTblX_Uls_u3p13[1]	6954
t_CommOffsetTblY_Cnt_u16[0]	434
t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1438
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1

PICurrCntrl_Per1





Name	Input Value		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2090		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2090	2090	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-45.3659973	-45.3660011 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.73769665	-4.73769999 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-0.671159685	-0.671159983 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	19616	19616 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-45.3659973	-45.3660011 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	~

Test Step Call Trace ✓					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Test Step 2.94 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.495
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.398999989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	184.223999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-915.817017
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.58099997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.776000023
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-189.419998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	896.187988
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011

PICurrCntrl_Per1





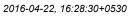
Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxCog_Amp_M_f32	83.9489975		
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979 119.721001		
MtrCurrQaxFinalRef_Amp_M_f32[1] MtrCurrQaxPrevIntg Volt M f32	15.9664001		
MtrCurrQaxRef_Amp_M_f32[0]	106.072998		
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002		
MtrCurrQaxRpl Amp M f32[0]	67.4899979		
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001		
MtrPosComputationDelay Rad M f32[0]	-2.69099998		
MtrPosComputationDelay_Rad_M_f32[1]	-1.99899995		
MtrVoltDax_Volt_M_f32[0]	-3.59500003		
MtrVoltDax_Volt_M_f32[1]	-28.4209995		
MtrVoltQax_Volt_M_f32[0]	-3.59500003		
MtrVoltQax_Volt_M_f32[1]	-28.4209995		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.155000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.158000007		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.147200003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.565999985		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0.44299989		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.57999983		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	0.44299989		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.579999983		
SlowDataAccessBufIndex_Cnt_M_u16	0 100		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6831.5		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.021999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.041999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.578599989		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.73509979		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	2.3499999		
k_VoltSatQaxPolyCoeff_Uls_f32	21.7280006		
k_deadtimeVScale_Uls_f32	0.957000017		
t_CommOffsetTbIX_UIs_u3p13[0]	474		
t_CommOffsetTbIX_UIs_u3p13[1]	6954		
t_CommOffsetTblY_Cnt_u16[0]	434		
t_CommOffsetTblY_Cnt_u16[1]	1438		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	1		
target_MtrCntrl_Read_MotCurrLoamtgtnEn_Cnt_igc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	2090		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target MtrCntrl Read SysState Cnt Enum Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	2090	2090	Kesult
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	J
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-45.3659973	-45.3660011 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-25.3770008	-25.3770008 ± 4.88E-04	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.59500003	-3.59500003 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	19616	19616 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-45.3659973	-45.3660011 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	~



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓	

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lqc Val
VtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ytr)	target_witchtil_read_wiodidxShconisvebit_cht_gc_vari
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl Read_MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 67.4899979
	119.721001
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0.0109999999
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.017000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.56099999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.21300006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	907.228027
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-851.888
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0930000022
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.467999995
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-286.428009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	784.336975
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3880005
/trCtrl_Vecu_Volt_M_f32[0]	25.3600006
/trCtrl_Vecu_Volt_M_f32[1]	27.7199993
/trCurrDaxPrevIntg_Volt_M_f32	-27.3339996
/trCurrDaxRef_Amp_M_f32[0]	209.052002
/trCurrDaxRef_Amp_M_f32[1]	-124.994003
/ltrCurrQaxCog_Amp_M_f32	-144.667007
/trCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
/trCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
/trCurrQaxPrevIntg_Volt_M_f32	21.0373001
/trCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
//trCurrQaxRpl_Amp_M_f32[0]	37.4550018
//trCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-2.78399992
MtrPosComputationDelay_Rad_M_f32[1]	-3.74300003
MtrVoltDax_Volt_M_f32[0]	-25.3770008
/trVoltDax Volt M f32[1]	21.3880005

PICurrCntrl_Per1





Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-25.3770008		
MtrVoltQax_Volt_M_f32[1]	21.3880005		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0		
PICurrCntrl InverterFailSclFac UIs M f32	0.178000003		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.0496999994		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	404.899994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	17.1812992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.386400014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17.1812992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.386400014		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	4653.20996		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1635.59998		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.171000004		
k MtrCtrlVirualResQax Ohm f32	0.180000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.0632		
k MtrVoltDaxIntegLoLim Volt_162	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.61609995		
k MtrVoltQaxIntegLoLim Volt f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.6819992		
k_VoltSatQaxPolyCoeff_Uls_f32	2.70700002		
k_deadtimeVScale_UIs_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	4506		
t_CommOffsetTblX_Uls_u3p13[1]	5381		
t_CommOffsetTblY_Cnt_u16[0]	156		
t_CommOffsetTblY_Cnt_u16[1]	1570		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read MtrCurrDax Amp f32 Val	59.3040009		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	4809		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
		•	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4809	4809	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.825012	131.824997 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	15.7353363	15.7353001 ± 4.88E-04	· ·
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-25.6242886	-25.6243 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	63522	63522 ± 1.52588E-05	· ·
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.825012	131.824997 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
astDataAccessBufIndex_Cnt_M_u16	1
ItrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
ItrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc Val
ItrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
ItrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
ItrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
ItrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-147.343002
trCtrl_MtrCurrDaxMaxVal_Amp_M_i32[1]	127.972
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
trCtrl_MtrDampTermDax_Onm_M_f32[0]	0.0419999994
	0.0280000009
trCtrl_MtrDawpTermQax_Ohm_M_f32[1]	0.0280000009
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.24800003
	-552.150024
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-568.89502
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.035999985
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.921000004
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.173999995
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1018.71997
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-471.221985
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
trCtrl_Vecu_Volt_M_f32[0]	17.7010002
trCtrl_Vecu_Volt_M_f32[1]	20.0610008
trCurrDaxPrevIntg_Volt_M_f32	20.9669991
trCurrDaxRef_Amp_M_f32[0]	-139.906998
trCurrDaxRef_Amp_M_f32[1]	115.814003
trCurrQaxCog_Amp_M_f32	-41.5750008
ltrCurrQaxFinalRef_Amp_M_f32[0]	191.369003
trCurrQaxFinalRef_Amp_M_f32[1]	107.137001
trCurrQaxPrevIntg_Volt_M_f32	23.1735001
trCurrQaxRef_Amp_M_f32[0]	-65.1900024
trCurrQaxRef_Amp_M_f32[1]	-216.972
trCurrQaxRpl_Amp_M_f32[0]	191.369003
ltrCurrQaxRpl_Amp_M_f32[1]	107.137001
trPosComputationDelay_Rad_M_f32[0]	-0.188999996
ItrPosComputationDelay_Rad_M_f32[1]	1.03799999
ltrVoltDax_Volt_M_f32[0]	-29.3959999
ItrVoltDax Volt M f32[1]	-1.94000006

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.9400006		
PICurrCntrl CurrSensFailSclFac Uls M f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.662999988		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.952600002		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.686999977		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	865.320007		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	67.9733963		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.439500004		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	865.320007		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	67.9733963		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.439500004		
SlowDataAccessBufIndex Cnt M u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	1265.93005		
	5888.85986		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	0		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc			
k_MtrCtrlVirualResDax_Ohm_f32	0.151999995		
k_MtrCtrlVirualResQax_Ohm_f32	0.032999998		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	11.7454004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.04580021		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.3470001		
k_VoltSatQaxPolyCoeff_Uls_f32	8.97500038		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	3030		
t_CommOffsetTblX_Uls_u3p13[1]	5366		
t_CommOffsetTblY_Cnt_u16[0]	589		
t_CommOffsetTblY_Cnt_u16[1]	1202		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4196		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	589	589	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	10634	10634 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.61400008	2.61400008 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.94000006	-1.94000006 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33870	33870 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	11.7454004	11.7454004 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003	191.369003 ± 7.81E-03	•
MtrCurrOavEinalDef Amn M f32[1]	220	220 ± 7 81E 03	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

-220

-220 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.97 (Repeat Count = 1)	Innut Value
lame	Input Value
astDataAccessBufIndex_Cnt_M_u16	0
<pre>/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr) /trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)</pre>	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(var)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70099998
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.287999988
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1014.57001
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	639.960022 0.0099999978
htrCtrl_MtrImpedDax_Ohm_M_f32[0] htrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
AtrCtrl MtrImpedDax_Onni_M_i32[i]	0.093999968
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.42200005
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.612999976
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-811.013
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-317.71701
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
/trCtrl_Vecu_Volt_M_f32[0]	18.5559998
/trCtrl_Vecu_Volt_M_f32[1]	20.9160004
/trCurrDaxPrevIntg_Volt_M_f32	10.2959995
/trCurrDaxRef_Amp_M_f32[0]	-82.2979965
/trCurrDaxRef_Amp_M_f32[1] /trCurrQaxCog_Amp_M_f32	46.8180008 48.8400002
MtrCurrQaxFinalRef Amp M f32[0]	-147.343002
/trCurrQaxFinalRef_Amp_M_f32[1]	127.972
/trCurrQaxPrevIntg_Volt_M_f32	12.8893003
/trCurrQaxRef_Amp_M_f32[0]	-146.723007
/trCurrQaxRef_Amp_M_f32[1]	-121.943001
/trCurrQaxRpl_Amp_M_f32[0]	-147.343002
/ltrCurrQaxRpl_Amp_M_f32[1]	127.972
/ltrPosComputationDelay_Rad_M_f32[0]	0.750999987
/trPosComputationDelay_Rad_M_f32[1]	-1.45000005
/trVoltDax_Volt_M_f32[0]	-17.1070004
MtrVoltDax_Volt_M_f32[1]	15.9390001
MtVoltQax_Volt_M_f32[0]	-17.1070004 45.000004
MtrVoltQax_Volt_M_f32[1]	15.9390001
PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 PlCurrCntrl InverterFailSclFac Uls M f32	0.640999973 0.88499999
PICurrCntrl_InverterFallSciFac_Ois_M_f32	0.88499999
CurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.143000007
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	267.119995
CurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-38.7999992
CICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	74.1108017
ICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.654399991
ICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
ICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992
ICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	74.1108017
ICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.654399991
lowDataAccessBufIndex_Cnt_M_u16	0
	7251.52002
	5070.0000
_CLOAFdbackSignalSclFacSlew_UlspS_f32 _ILOAFdbackSignalSclFacSlew_UlspS_f32	5272.3999
_ILOAFdbackSignalSclFacSlew_UlspS_f32 _MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
_ILOAFdbackSignalSclFacSlew_UlspS_f32 _MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc _MtrCtrlFeedbackControlDisable_Cnt_lgc	0 0
_ILOAFdbackSignalSclFacSlew_UlspS_f32 _MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0





Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.9356995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.26469994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.3450003		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.0529995		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	4850		
t_CommOffsetTblX_Uls_u3p13[1]	6241		
t_CommOffsetTblY_Cnt_u16[0]	165		
t_CommOffsetTblY_Cnt_u16[1]	651		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3061		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3061	3061	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-48.2200012	-48.2200012 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.995374382	-0.995374024 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.73654222	-4.73653984 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42762	42762 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-48.2200012	-48.2200012 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972	127.972 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.98 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.74000001	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.391000003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	382.878998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-891.598022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.207000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.145999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-192.985992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	708.689026
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979 21.0249996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	28.440005
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxPrevIntg_Volt_M_f32	9.35029984
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	6.18900013
MtrCurrQaxRpI_Amp_M_f32[1]	83.0540009
MtrPosComputationDelay_Rad_M_f32[0]	-3.47099996
MtrPosComputationDelay_Rad_M_f32[1]	4.87400007
MtrVoltDax_Volt_M_f32[0]	0.908999979
MtrVoltDax_Volt_M_f32[1]	21.0249996
MtrVoltQax_Volt_M_f32[0]	0.908999979
MtrVoltQax_Volt_M_f32[1]	21.0249996
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.986999989
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.370000005
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.382499993
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.098999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	79.4266968 0.757300019
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	-657.130005
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	79.4266968
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.757300019
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5252.41016
k ILOAFdbackSignalSclFacSlew UlspS f32	10
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1
k MtrCtrlFeedbackControlDisable Cnt lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.192000002
k_MtrCtrlVirualResQax_Ohm_f32	0.145999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	9.65880013
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	23.7485008
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	8.97799969
k_VoltSatQaxPolyCoeff_Uls_f32	18.2439995
k_deadtimeVScale_Uls_f32	0.975000024
t_CommOffsetTblX_Uls_u3p13[0]	2114
t_CommOffsetTblX_Uls_u3p13[1]	4735
t_CommOffsetTblY_Cnt_u16[0]	297
t_CommOffsetTblY_Cnt_u16[1]	1110
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1110	1110	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63897	63897 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	19.6874123	19.6874008 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	6.8305316	6.83053017 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42233	42233 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✓
MtrCurrOayFinalRef Amn M f32[1]	83 0540009	83 0540009 ± 7 81F-03	_

Fest Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.99 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.15499997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.71800005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	953.320984
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-556.945007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.34200001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0759999976
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	41.5550003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	935.856018
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	1.82700002
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxFinalRef_Amp_M_f32[0] -105.246002 MtrCurrQaxFinalRef_Amp_M_f32[1] 41.6290016 MtrCurrQaxPrevIntg_Volt_M_f32 7.8980999 MtrCurrQaxRef_Amp_M_f32[0] 31.5869999 MtrCurrQaxRef_Amp_M_f32[1] -186.395996 MtrCurrQaxRpl_Amp_M_f32[0] -105.246002 MtrCurrQaxRpl_Amp_M_f32[1] 41.6290016 6.27600002 MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1] 1.47300005 MtrVoltDax_Volt_M_f32[0] -28 2420006 MtrVoltDax_Volt_M_f32[1] 12.6160002 MtrVoltQax_Volt_M_f32[0] -28 2420006 MtrVoltQax_Volt_M_f32[1] 12.6160002 PICurrCntrl CurrSensFailSclFac Uls M f32 0.818000019 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.61500001 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.0132999998 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.0469999984 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -43.1699982 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 97.2235031 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.703100026 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43.1699982 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 97.2235031 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.703100026 SlowDataAccessBufIndex_Cnt_M_u16 k CLOAFdbackSignalSclFacSlew UlspS f32 18.2099991 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 8000 k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc k MtrCtrlVirualResDax Ohm f32 0.0500000007 k_MtrCtrlVirualResQax_Ohm_f32 0.0469999984 k MtrCurrQaxRefModifDsb Cnt lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 16.8875999 k_MtrVoltDaxIntegLoLim_Volt_f32 -11.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 29.7059002 k_MtrVoltQaxIntegLoLim_Volt_f32 -11.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 -14.7080002 k VoltSatQaxPolyCoeff_Uls_f32 3.90300012 k_deadtimeVScale_Uls_f32 0.957000017 t_CommOffsetTblX_Uls_u3p13[0] 1498 $t_CommOffsetTblX_Uls_u3p13[1]$ 4940 t_CommOffsetTblY_Cnt_u16[0] 623 t CommOffsetTblY_Cnt_u16[1] 1212 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target MtrCntrl Read_MtrCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1116 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -126.640999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value** Name **Expected Value** Result 1116 1116 MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -2.39211178 -2.39211011 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.1441555 -4.14415979 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 38153 38153 ± 1.52588E-05 0 ± 0.0625 MtrCurrDaxPrevIntg_Volt_M_f32 131.109009 131.108994 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1] 41.6290016 41.6290016 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Name	Input Value	
	1	
FastDataAccessBufIndex_Cnt_M_u16	•	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961	
ltrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
ltrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982	
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.335000008	
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.61000001	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	676.015015	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.322021	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998	
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125	
ItrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994	
ItrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968	
1trCtrl MtrQaxIntegralGain Ohm M f32[0]	0.521000028	
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.65699995	
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	40.1660004	
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	649.921021	
ItrCtrl MtrVoltDaxFF Volt M f32[0]	-0.736000001	
htrCtrl MtrVoltDaxFF Volt M f32[1]	-13.6160002	
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005	
ItrCtrl MtrVoltQaxFF Volt M f32[1]	-23.1870003	
ItrCtrl Vecu Volt M f32[0]	18.9510002	
trCtrl Vecu Volt M f32[1]	21.3110008	
htrCurrDaxPrevIntg Volt M f32	-11.698	
htrCurrDaxRef_Amp_M_f32[0]	-146.723007	
ItrCurrDaxRef Amp M f32[1]	-121.943001	
ItrCurrQaxCog_Amp_M_f32	59.3040009	
	-213.026993	
ttrCurrQaxFinalRef_Amp_M_f32[0] ttrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	
	3.4605999	
ItrCurrQaxPrevIntg_Volt_M_f32	-133,947006	
htrCurrQaxRef_Amp_M_f32[0]		
htrCurrQaxRef_Amp_M_f32[1]	75.7020035	
htrCurrQaxRpl_Amp_M_f32[0]	-213.026993	
/trCurrQaxRpl_Amp_M_f32[1]	-66.7229996	
/trPosComputationDelay_Rad_M_f32[0]	-1.23399997	
MtrPosComputationDelay_Rad_M_f32[1]	-1.25100005	
/trVoltDax_Volt_M_f32[0]	18.6380005	
MtrVoltDax_Volt_M_f32[1]	-23.1870003	

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrVoltQax Volt M f32[0] 18.6380005 MtrVoltQax_Volt_M_f32[1] -23.1870003 PICurrCntrl_CurrSensFailSclFac_Uls_M f32 0.810000002 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.551999986 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.397899985 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.725000024 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 1118 -38.7999992 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 18.5506001 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$ 0.689499974 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -38 7999992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 18.5506001 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.689499974 SlowDataAccessBufIndex_Cnt_M_u16 5675.16992 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7779.18994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ 0 k_MtrCtrlVirualResDax_Ohm_f32 0.174999997 $k_MtrCtrlVirualResQax_Ohm_f32$ 0.0280000009 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ Λ 4.64909983 k_MtrVoltDaxIntegHiLim_Volt_f32 -30.2000008 $k_MtrVoltDaxIntegLoLim_Volt_f32$ k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 9.52950001 k_MtrVoltQaxIntegLoLim_Volt_f32 -30.2000008 k MtrVoltVecuFiltEnable Cnt lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -2.73900008 k VoltSatQaxPolyCoeff Uls f32 -7.03700018 k_deadtimeVScale_Uls_f32 0.958000004 t CommOffsetTblX Uls u3p13[0] 6110 t_CommOffsetTblX_Uls_u3p13[1] 7324 t_CommOffsetTblY_Cnt_u16[0] 237 t CommOffsetTblY Cnt u16[1] 383 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2039 -34.6189995 target MtrCntrl Read MtrCurrQax Amp f32 Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 383 383 62783 + 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 62783 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 83.1210022 83.1210022 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -10 3380833 -10 3381004 + 4 88F-04 -17.6049595 -17.6049995 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 25258 + 1 52588F-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 25258 MtrCurrDaxPrevIntg_Volt_M_f32 -30.2000008 -30.2000008 ± 0.0625 -213.026993 ± 7.81E-03

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

-213.026993

83.1210022

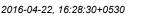
83.1210022 ± 7.81E-03

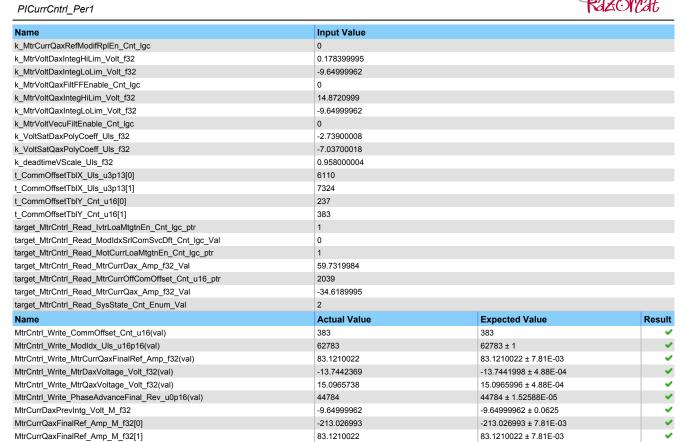
MtrCurrQaxFinalRef Amp M f32[0]

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.101 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.335000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.61000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	676.015015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.322021
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.521000028
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65699995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	40.1660004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	649.921021
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-11.698
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	10.2080002
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-1.23399997
MtrPosComputationDelay_Rad_M_f32[1]	-1.25100005
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.810000002
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.551999986
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.338800013
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-304.940002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21.2028008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0865999982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-304.940002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	21.2028008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0865999982
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5675.16992
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	100
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.174999997
k_MtrCtrlVirualResQax_Ohm_f32	0.0280000009
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.102 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005





March Michael Approximation Chem M. (20) 0.44 1000000000000000000000000000000000		
MATCH_MINISTER_POIN_P_CST[]	Name	Input Value
MACH_Mirepedits_Chm_M_S281 MCLMirepedits_Chm_M_S281 MCLMirepedits_Ch		
MINCH, Minriesday, Drim, M. 2821 0.0780000000000000000000000000000000000		
MACH J. Michael M. Com J. M. (2001) MICH J. Michael M. (2007) MICHAEl M.		
Michi Michaelega Chen M. (28] Michi J. Mosariega Chen M. (28] Michi J. Mosariega Chen M. (28] Michi J. Mosariega Chen M. (28) Mi		
MacChi MacChambergolian, Chem. M. 10201		
Michit Michaelmegra Sear Chem M. 25(1) 55,85000	MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.123000003
Mich (Michaer Propotentials Comm. M. 2010) 5.5 828989 Michaer Michael Michaer Michae	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
Michael Markael Post (M. M. 1920) 50219991 Michael Markael Post (M. 1920) 50219991 Michael Markael Post (M. 1921) 1924999 Michael Markael Post (M. 1921) 19249999 Michael Markael Post (M. 1921) 19249999 Michael Markael Markael Post (M. 1921) 19249999 Michael Markael Mark	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MICHAMPORDARY, VOLM, 1930 90.216991 90.246999 90.0000000000000000000000000000000	MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
Michael Mich	MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MacCharle Workschaff- Verl M. 1920] 27 066994 Michic Mevo Volum M. 1921] 28 100004 Michic Mevo Volum M. 1921] 29 110008 Michic West Volum M. 1921] 29 12 12 12 12 12 12 12 12 12 12 12 12 12	MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
MacCharle Workschaff- Verl M. 1920] 27 066994 Michic Mevo Volum M. 1921] 28 100004 Michic Mevo Volum M. 1921] 29 110008 Michic West Volum M. 1921] 29 12 12 12 12 12 12 12 12 12 12 12 12 12	MtrCtrl MtrVoltDaxFF Volt M f32[1]	19.2049999
MICH. MICH. Vol. Vol. M. D30		-27.0669994
Michic Vest, Vol. M, 1920 18,0510002 Michic 20,0000000000000000000000000000000000		
Michael News, Vol. M. 1920 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73007 148,73008		
MICLOFFERD M. 1921		
Michael Agent Michael Agen		
McCurraker Mr. M. 1951 19.0001		
Michael Robert Mich		
McCurcasFriedRef, Amp. M. 5201 20.065807 McCurcasFriedRef Amp. M. 5201 20.065807 McCurcasFredRef Amp. M. 5201 21.335007 McCurcasFed. Amp. M. 5201 20.065807 McCurcasFed. M. 5201 20.065807	MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MICHADENFINATER APPL, M. 1921 MICHADENFINATER APPL, M. 1920 MICHADENFINATER M. 1920 MI	MtrCurrQaxCog_Amp_M_f32	79.6729965
McCurdaxPeving, Vot. M. 32	MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
McCurdaxPeving, Vot. M. 32	MtrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
Michard Ref. Amp. M. 12(1)		0.771099985
McCurdaxRef Amp, M. 2921 McCurdaxRef Amp, M. 2920 McCurdax Vet, M. 1920 McVoribax, Vot, M. 1920 McCurdax, McVoribax, McCurdax, McCu		
MicCurdoxRef J.Amp. M. 28701 MicPosComputationDelay, Rad, M. 28701 MicPosComputation, Vol. M. 182701 MicPosComputation, Vol. M. 182701 MicPosComputation, M. 18270 MicPosComputation, MicPosComputation, M. 18270 MicPosComputation, MicRosComputation, MicRos		
Michael Angle J. Am. M. (221) 200 0000000000000000000000000000000		
MPPsGcomputationDelay Rad M, 132(0) 1,5200002 2,2088994 MinVoilDax, Voil, M, 132(1) 2,200002 2,2088994 MinVoilDax, Voil, M, 132(1) 2,200004 2,2088994 MinVoilDax, Voil, M, 132(1) 28,1070004 27,068994 MinVoilDax, Voil, M, 132(1) 28,1070004 27,068994 MinVoilDax, Voil, M, 132(1) 28,1070004 27,0689994 MinVoilDax, Voil, M, 132(1) 28,1070004 28,1070004 28,1070004 28,1070004 28,1070004 28,1070004 28,1070004 28,1070004 28,1070004 28,1070004 28,1070004 28,10700006 28,10700006 28,10700006 28,10700006 28,107000006 28,107000006 28,107000006 28,107000006 28,107000006 28,107000006 28,1070000006 28,1070000006 28,10700000006 28,10700000000000000000000000000000000000		
McPrescompationbeley, Read, M. (1921) 2. 8440002 MrtVollDax (Volt, M. (1921) 28 1070004 PlCurCortini, Lowisens'alisel'ace, Us. M. (1922) 0.400000000 PlCurCortini, Mrcurdosas'aliur'Alto, Us. M. (1922) 0.37500003 PlCurCortini, Mrcurdosas'aliur'Alto, Us. M. (1922) 0.37000012 PlCurCortini, Mrcurdosas'aliur'Alto, Us. M. (1922) 0.37000012 PlCurCortini, Mrcurdosas'aliur'Alto, Us. M. (1922) 2. 2399998 PlCurCortini, Mrcurdosifi, M. ast Termi, Us. (1922) 1.5 8891002 PlCurCortini, Mrcurdosifi, M. ast Termi, Us. (1922) 4.5 8891002 PlCurCortini, Mrcurdosastefferi, M. ast Termi, Us. (1922) 4.5 889998 PlCurCortini, Mrcurdosastefferi, M. ast Termi, Us. (1922) 4.5 889908 PlCurCortini, Mrcurdosastefferi, M. ast Termi, Us. (1922) 4.5 889998 PlCurCortini, Mrcurdosastefferi, M. ast Termi, Us. (1922) 1.6 8851002 PlCurCortini, Mrcurdosastefferi, M. ast Termi, Us. (1922) 1.6 8851002 PlCurCortini, Mrcurdosastefferi, M. ast Termi, Us. (1922) 1.6 8891002 K. McCurdo		
MM/Viciliza, Vol. M. G2(1)		
MrVolDax, Volt, M, D3(1) 28.1070004 MrVolDax, Volt, M, D3(1) 27.0669994 MrVolDax, Volt, M, D3(1) 28.1070004 PlCurCnort, Journal Carrisme FallsGFac, Uls, M, D3 0.400000006 PlCurCnort, Journal Carrisme FallsGFac, Uls, M, D3 0.375200003 PlCurCnort, Journal Carrisme FallsGFac, Uls, M, D3 0.375000003 PlCurCnort, Journal Carrisme, Justine Falls Justine, Justi		-2.68400002
Mr.Voliciax_Velt_M.52[0]	MtrVoltDax_Volt_M_f32[0]	-27.0669994
Methodicax_Voit_M_32[1] 28.1070004 PiCurrCnti_CurrSenFailsClFac_Uis_M_52	MtrVoltDax_Volt_M_f32[1]	28.1070004
PICUTCONT Universification U.S. M. 152 0.40000006 PICUTCONT Universification U.S. M. 152 0.37520003 PICUTCONT Universifial Stelface U.S. M. 152 0.37520003 PICUTCONT Universifial Stelface U.S. M. 152 0.375000012 PICUTCONT MIVEQUEFIE M. SEPREVOLIDUL U.S. 152 2.239998 PICUTCONT MIVEQUEFIE M. SEPREVOLIDUL U.S. 152 15.8851002 PICUTCONT MIVEQUEFIE M. SEPREVOLIDUL U.S. 152 0.887899995 PICUTCONT MIVEQUEFIE M. SEPREVOLIDUL U.S. 152 0.9889999 PICUTCONT MIVEQUEFIE M. SEPREVOLIDUL U.S. 152 0.9899999 PICUTCONT MIVEQUEFIE MIVEQUEFIE U.S. 152 0.9899999 PICUTCONT MIVEQUEFIE	MtrVoltQax_Volt_M_f32[0]	-27.0669994
PiCurrCntri CurrSensFailsClFac_Uls_M_132	MtrVoltQax Volt M f32[1]	28.1070004
PICUMONTH_INVENTEASSAFFIJUKBAID_UIS_M_132 0.375200003 PICUMONTH_MITCUTDASSAFFIJUKBAID_UIS_M_132 0.375200003 PICUMONTH_MITCUTDASSAFFIJUKBAID_UIS_M_132 0.375000012 PICUMONTH_MITCUTDASSAFFIJUKBAID_UIS_M_132 2.2399998 PICUMONTH_MITCUELFIJI_M_SET_PERVIDUT_UIS_M_132 4.1899982 PICUMONTH_MITCUELFIJI_M_SET_PERVIDUT_UIS_M_132 4.1899982 PICUMONTH_MITCUELFIJI_M_SET_TERM_UIS_M_23 0.887899995 PICUMONTH_MITCUELFIJI_M_SET_TERM_UIS_M_23 2.2399998 PICUMONTH_MITCUELFIJI_M_SET_TERM_UIS_M_23 2.2399998 PICUMONTH_MITCUELFIJI_M_SET_TERM_UIS_M_23 4.1899982 PICUMONTH_MITCUELFIJI_M_SET_TERM_UIS_M_23 4.1899982 PICUMONTH_MITCUELFIJI_M_SET_TERM_UIS_M_23 6.8851002 PICUMONTHETHEAUT_MUIS_M_23 6.8851002 PICUMONTHETHEAUT_MUIS_M_23 6.8851002 PICUMONTHETHEAUT_MUIS_M_23 6.82851004 PICUMONTHETHEAUT_MUIS_M_23 6.28851004 PICUMONTHETHEAUT_MUIS_M_23 6.88899		0.40000006
PICUTECINEL_MITCUTPDASSAIFLUXRAID_UIS_MI_22		
PICUTCINTI_MITVECUFIL_M_str.Previoup_UI_SS2 22.239998 PICUTCINTI_MITVECUFIL_M_str.Previoup_UI_SS2 43.1699982 PICUTCINTI_MITVECUFIL_M_str.Previoup_UI_SS2 16.5851002 PICUTCINTI_MITVECUFIL_M_str.Previoup_UI_SS2 0.87899995 PICUTCINTI_MITVECUFIL_M_str.Previoup_UI_SS2 0.87899998 PICUTCINTI_MITVECUFIL_M_str.Previoup_UI_SS2 22.239998 PICUTCINTI_MITVOIDASFFFIL_M_str.Previoup_UI_SS2 22.239998 PICUTCINT_MITVOIDASFFFIL_M_str.Previoup_UI_SS2 43.1699982 PICUTCINT_MITVOIDASFFFIL_M_str.Previoup_UI_SS2 0.887899995 PICUTCINT_MITVOIDASFFFIL_M_str.Previoup_UI_SS2 0.887899995 PICUTCINT_MITVOIDASFFIL_M_str.Previoup_UI_SS2 0.98899995 PICUTCINT_MITVOIDASFFIL_M_str.Previoup_UI_SS2 0.98899995 PICUTCINT_MITVOIDASPFIL_M_str.Previoup_UI_SS2 0.98899995 PICUTCINT_MITVOIDASPFIL_M_str.Previoup_UI_SS2 0.98899999 PICUTCINT_MITVOIDASPFIL_M_str.Previoup_UI_SS2 0.98899999 PICUTCINT_MITVOIDASPFIL_M_str.Previoup_UI_SS2 0.98899999 PICUTCINT_MITVOIDASPFIL_M_str.Previoup_UI_SS2 0.98899999 PICUTCINT_MITVOIDASPFIL_M_str.Previoup_UI_SS2 0.98899999 PICUTCINT_MITTOID_UI_SS2 0.98899999 PICUTCINT_MITTOID_UI_SS2 0.98899999 PICUTCINT_MITTOID_UI_SS2 0.98899999 PICUTCINT_UI_UI_SS2 0.98899999 PICUTCINT_UI_UI_UI_SS2 0.98899999 PICUTCINT_UI_UI_UI_UI_UI_UI_UI_UI_UI_UI_UI_UI_UI_		
PiCurrChit MirVecuFit M. str Previnput Uis 132 43.1699982 PiCurrChit MirVecuFit M. str Term Uis 162 16.5861002 PiCurrChit MirVecuFit M. str Term Uis 162 16.5861002 PiCurrChit MirVecuFit M. str Term Uis 162 22.2399998 PiCurrChit MirVecuFit M. str Term Uis 162 22.2399998 PiCurrChit MirVecuFit M. str Previnput Uis 132 22.2399998 PiCurrChit MirVoliQaxFFFIT M. str Previnput Uis 132 43.169982 PiCurrChit MirVoliQaxFFFIT M. str Prevince Uis 16.5861002 PiCurrChit MirVoliQaxFFFIT M. str Perm Uis 162 16.5861002 PiCurrChit MirVoliQaxFFFIT M. str Term Uis 162 16.5861002 PiCurrChit MirVoliQaxFFFIT M. str Term Uis 162 16.5861002 PiCurrChit MirVoliQaxFFIT M. str Term Uis 162 16.5861002 R. Li ChardbackSignalSci FacSieve Uisps 162 16.5861002 R. Li ChardbackSignalSci FacSieve Uisps 162 16.5861002 R. MirChit Uisps Uisps 162 16.5861002 16.5861002 R. MirChit Uisps Uisps 162 16.5861002 16.5861002 16.5861002 R. MirChit Uisps		
PlCurChtf_Mtr/vecuFit_M_str_PrevOutput_Uis_f32		
PCurrCntf_Mtr/veuFit_M_str.TermN_Uls_f32		
PICurrCntf_MtrVecUFilt_M_str.TermD_UIs_132 0.887899995 PICurrCntf_MtrVolCaxFFFFilt_M_str.PrevOupt_UIs_132 22.2399989 PICurrCntf_MtrVolCaxFFFFilt_M_str.TermN_UIs_132 16.8851002 PICurrCntf_MtrVolCaxFFFIlt_M_str.TermN_UIS_132 16.8851002 PICurrCntf_MtrVolCaxFFFIlt_M_str.TermN_UIS_132 0.88789995 SlowDataAccessBuffindex_Cnt_M_u16 1 k_CLOAFdbackSignalsCiFacSlew_UIspS_132 2663.65991 k_ILOAFdbackSignalsCiFacSlew_UIspS_132 5194.8999 k_MtrCtrifUncopSecOrTranFerEnable_Cnt_Igc 1 k_MtrCtrifVirualResDax_Ohm_132 0.11200003 k_MtrCtrifVirualResDax_Ohm_132 0.11200003 k_MtrCurrCaxRefModifispLe_Cnt_Igc 1 k_MtrCurrCaxRefModifispLe_Cnt_Igc 1 k_MtrVoltDaxinteg-Lill_m_Volt_132 22.4099998 k_MtrVoltDaxinteg-Lill_m_Volt_132 22.24099998 k_MtrVoltQaxrillef-Enable_Cnt_Igc 1 k_MtrVoltQaxrillef-Enable_Cnt_Igc 1 k_MtrVoltQaxrillef-Enable_Cnt_Igc 22.4099998 k_MtrVoltQaxrillef-Enable_Cnt_Igc 2.430001 k_VoltSatDaxPolyCoef_UIs_132 2.430001 k_VoltSatDaxPolyCoef_UIs_132 2		
PICurrCntf_MtrVotIQaxFFFit_M_str.Prevlotput_Uis_f32 43.1699982 PICurrCntf_MtrVotIQaxFFFit_M_str.Prevlotput_Uis_f32 43.1699982 PICurrCntf_MtrVotIQaxFFFit_M_str.TermU_uis_f32 0.88769995 SlowDataAccessBufindex_Cnt_M_u16 1 K_CLOAFdbackSignalScFacSiew_UispS_f32 2663.65991 K_LICAAFdbackSignalScFacSiew_UispS_f32 5194.8999 K_MtrCtrICurt_copsecOrTranFcEnable_Cnt_lgo 1 K_MtrCtrIVriualResDax_Ohm_f32 0.112000003 K_MtrCtrIVriualResDax_Ohm_f32 0.129999999 K_MtrCurrCaxRefModifbs_Cnt_lgo 1 K_MtrCurrCaxRefModifbs_Cnt_lgo 1 K_MtrCurrCaxRefModifbs_Cnt_lgo 1 K_MtrVotIDaxIntepLit_in_Vot_f32 22.409999 K_MtrVotIDaxIntepLit_in_Vot_f32 22.409999 K_MtrVotIDaxIntepLit_in_Vot_f32 8.2833004 K_MtrVotIQaxIntepLit_in_Vot_f32 8.2833004 K_MtrVotIQaxIntepLit_in_Vot_f32 4.2430001 K_VotISataFurpHit_in_Vot_f32 4.2430001 K_VotISataFurpHit_in_Vot_f32 4.2430001 K_VotISataFurpHit_in_Vot_f32 0.58899991 K_VotISataFurpHit_in_Vot_f32 0.58899991 <		
PICurrCntr MtrVoltQaxFFFill M_str. PrevOutput_Uis_r32	PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995
PICurrCntr MtrVoltQaxFFFit M_str.TermD_Uls_f32 0.887899995 SlowDataAccessBuffndex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 2663.65991 k_LILOAFdbackSignalSclFacSlew_UlspS_f32 5194.8999 k_MtCtrICdurtLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtCtrIFedebackControlDisable_Cnt_lgc 0 k_MtCtrIFedebackControlDisable_Cnt_lgc 0.1200003 k_MtrCtrIVirualResDax_Ohm_f32 0.0219999999 k_MtrCurrQaxRefModfiDs_Cnt_lgc 1 k_MtrVoltDaxintegHiLm_Volt_f32 19.6758003 k_MtrVoltDaxintegHiLm_Volt_f32 42.499998 k_MtrVoltQaxFefModfiRpl_Cnt_lgc 1 k_MtrVoltQaxFefModfiRpl_Cnt_lgc 1 k_MtrVoltQaxFefModfiRpl_Cnt_lgc 1 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxFiltFFLable_Cnt_lgc 1 k_MtrVoltQaxFiltFFLable_Cnt_lgc 2.2409998 k_MtrVoltQaxFiltgel_Lim_Volt_f32 2.2409998 k_MtrVoltQaxFiltgel_Lim_Volt_f32 2.423001 k_VoltSatQaxPolyCoeff_Uls_f32 2.423001 k_VoltSatQaxPolyCoeff_Uls_f32 0.5	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MrtVoltQaxFFFItL_Mstr.TermD_Uls_f32 0.887899995 SlowDataAccessBufindex_Cnt_M_u16 1 k_CLOAF dackSignalSclFacslew_UlspS_f32 2663.65991 k_MtrCrtrCurrLoopSecOfTranFcEnable_Cnt_lgc 5194.8999 k_MtrCrtrCurrLoopSecOfTranFcEnable_Cnt_lgc 0 k_MtrCrtrVirualResDax_Ohm_f32 0.112000003 k_MtrCurrQaxReModiffSpla_Cnt_lgc 0 k_MtrCurrQaxReModiffSpla_Cnt_lgc 0 k_MtrCurrQaxReModiffSpla_Cnt_lgc 0 k_MtrVoltDaxIntegHiLm_Volt_f32 22.4099998 k_MtrVoltDaxIntegHiLm_Volt_f32 22.409998 k_MtrVoltQaxIntegHiLm_Volt_f32 22.409998 k_MtrVoltQaxIntegHiLm_Volt_f32 22.409998 k_MtrVoltQaxIntegHiLm_Volt_f32 22.409998 k_MtrVoltQaxIntegHiLm_Volt_f32 22.409998 k_MtrVoltQaxIntegHiLm_Volt_f32 22.4099998 k_MtrVoltQaxIntegHiLm_Volt_f32 22.4099998 k_MtrVoltQaxIntegLoLim_Volt_f32 22.4099998 k_VoltSatQaxPolyCoeff_Uls_f32 25.4099998 k_VoltSatQaxPolyCoeff_Uls_f32 26.509 k_VoltSatQaxPolyCoeff_Uls_f32 26.509 k_CeadtimeVScale_Uls_f32	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
SlowDataAccessBufindex_Cnt_N_u16	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.5851002
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 2663.65991 k_LLOAFdbackSignalSclFacSlew_UlspS_f32 5194.8999 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.112000003 k_MtrCtrlVirualResDax_Ohm_f32 0.0219999999 k_MtrCurrQaxRefModiffSpE_Cnt_lgc 1 k_MtrCurrQaxRefModiffSpE_Cnt_lgc 0 k_MtrVolDaxIntegHilim_Volt_f32 22.4099998 k_MtrVolIDaxIntegHilim_Volt_f32 22.4099998 k_MtrVolIQaxIntegLoLim_Volt_f32 22.4099998 k_MtrVolIQaxIntegHilim_Volt_f32 22.4099998 k_MtrVolIQaxIntegLoLim_Volt_f32 22.4099998 k_MtrVolIQaxIntegLoLim_Volt_f32 22.4099998 k_MtrVolIQaxIntegLoLim_Volt_f32 22.4099998 k_MtrVolIQaxIntegLoLim_Volt_f32 24.230001 k_VoltSalDaxPolyCoeff_Uls_f32 24.230001 k_VoltSalDaxPolyCoeff_Uls_f32 24.230001 k_CeadimeVScale_Uls_J32 0.958999991 t_CommOffsetTblX_Uls_u3p13(1) 8192 t_CommOffsetTblY_Cnt_u16(1) 211 t_arget_MtrCntrl_Read_MtrLuraMtpEn_Cnt_lgc_ptr 1 t_arget_MtrCntrl_Read_MtrCurrLoaMtgin	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.887899995
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 2663.65991 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5194.8999 k_MtrCtrlCurrLoopSecOTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.112000003 k_MtrCtrlVirualResDax_Ohm_f32 0.0219999999 k_MtrCurrAceReModiffSpb_Cnt_lgc 1 k_MtrCurrAceReModiffSpb_Cnt_lgc 0 k_MtrVoltDaxIntegHilim_Volt_f32 22.409998 k_MtrVoltDaxIntegHilim_Volt_f32 22.409998 k_MtrVoltQaxIntegLoLim_Volt_f32 22.409998 k_MtrVoltQaxIntegLoLim_Volt_f32 22.409998 k_MtrVoltQaxIntegLoLim_Volt_f32 22.409998 k_MtrVoltQaxIntegLoLim_Volt_f32 22.409998 k_MtrVoltQaxIntegLoLim_Volt_f32 22.409998 k_MtrVoltQaxIntegLoLim_Volt_f32 24.230001 k_VoltSalDaxPolyCoeff_Uls_f32 24.230001 k_VoltSalDaxPolyCoeff_Uls_f32 24.230001 k_CeadimeVScale_Uls_f32 25.98999991 t_CommOffsetTblx_Uls_u3p13(0) 6528 t_CommOffsetTbly_Cnt_u16(1) 3192 t_CommOffsetTbly_Cnt_u16(1) 3192 t_CommOffsetTbly_Cnt_u16(1) 41 <td>SlowDataAccessBufIndex Cnt M u16</td> <td>1</td>	SlowDataAccessBufIndex Cnt M u16	1
k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5194.8999 k_MtrCth/CurtLoopSecOrTranFcEnable_Ont_lgc 1 k_MtrCth/FeedbackControlDisable_Cnt_lgc 0 k_MtrCth/VirualResDax_Ohm_f32 0.112000003 k_MtrCurt/QaxRefModiffbsb_Cnt_lgc 1 k_MtrCurrQaxRefModiffbsb_Cnt_lgc 1 k_MtrVorlDaxIntegHiLim_Volt_f32 19.6788003 k_MtrVorlDaxIntegHiLim_Volt_f32 22.4099998 k_MtrVollQaxFiltFEnable_Cnt_lgc 1 k_MtrVollQaxFiltFEnable_Cnt_lgc 1 k_MtrVollQaxIntegLoLim_Volt_f32 8.2833004 k_MtrVollQaxIntegLoLim_Volt_f32 8.2833004 k_MtrVollQaxIntegLoLim_Volt_f32 4.22.409998 k_MtrVollQaxPolyCoeff_Uls_f32 4.2430001 k_VoltSalQaxPolyCoeff_Uls_f32 4.2430001 k_Journal CommoffsetTblX_Uls_u3p13(0) 6.528 L_CommoffsetTblX_Uls_u3p13(1) 8192 L_CommoffsetTblY_Cnt_u16(0) 76 L_CommoffsetTblY_Cnt_u16(1) 211 target_MtrCntrl_Read_MotCurt_caMtgnEn_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurt_caMtgnEn_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurt_caMtgnEn_Cnt_lgc_Val <td></td> <td>2663.65991</td>		2663.65991
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.112000003 k_MtrCtrlVirualResQax_Ohm_f32 0.0219999999 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 22.4099998 k_MtrVoltDaxIntegLoLim_Volt_f32 22.4099998 k_MtrVoltQaxIntegLoLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegLoLim_Volt_f32 8.2433004 k_MtrVoltQaxIntegLoLim_Volt_f32 22.4099998 k_MtrVoltQaxIntegLoLim_Volt_f32 2.2499998 k_VoltSatDaxPolyCoeff_Uls_f32 2.24099998 k_VoltSatDaxPolyCoeff_Uls_f32 2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 2.1.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTbX_Uls_u3p13[1] 8192 t_CommOffsetTbY_Cnt_u16[0] 76 t_CommOffsetTbY_Cnt_u16[1] 211 target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_v1r 1 target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_v1r 1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.112000003 k_MtrCtrlVirualResDax_Ohm_f32 0.021999999 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.6758003 k_MtrVoltDaxIntegHiLim_Volt_f32 -22.4099998 k_MtrVoltQaxIntegLoLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegLoLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegLoLim_Volt_f32 4.2499998 k_MtrVoltQaxIntegLoLim_Volt_f32 4.22.4099998 k_MtrVoltQaxIntegLoLim_Volt_f32 4.2430001 k_VoltSatDaxPolyCoeff_Uls_f32 4.2430001 k_VoltSatDaxPolyCoeff_Uls_f32 4.2430001 k_VoltSatQaxPolyCoeff_Uls_f32 4.21368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTblX_Uls_u3p13[0] 6528 t_CommOffsetTblY_Cnt_u16[1] 8192 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_wtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_vtr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val <		
k_MtrCtrlVirualResDax_Ohm_f32 0.112000003 k_MtrCtrlVirualResDax_Ohm_f32 0.0219999999 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCvrQaxRefModiffsplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.6758003 k_MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltQaxFiltFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.283004 k_MtrVoltQaxIntegLoLim_Volt_f32 8.283004 k_MtrVoltQaxIntegLoLim_Volt_f32 22.4099998 k_MtrVoltQaxIntegLoLim_Volt_f32 22.430001 k_VoltSatDaxPolyCoeff_Uls_f32 24.330001 k_VoltSatQaxPolyCoeff_Uls_f32 21.368 k_deadtimeVScale_Uls_f32 9.588999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_ModldxSriComSvcDft_Cnt_lgc_val 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val 9.31999969 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val 9.31999969		
k_MtrCtrVirualResQa_Ohm_f32 0.0219999999 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifSplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.6758003 k_MtrVoltDaxIntegHiLim_Volt_f32 -22.4099998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegHiLim_Volt_f32 8.2833004 k_MtrVoltVccurritilEnable_Cnt_lgc 1 k_MtrVoltQaxIntegDiLim_Volt_f32 -22.4099998 k_MtrVoltVccurritilEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -24.30001 k_VoltSatQaxPolyCoeff_Uls_f32 -24.368 k_deadtimeVScale_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 9.58999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IntrOurrit_OakIgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoakIgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.319	·	
k_MtrCurrQaxRefModifPsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 19.6758003 k_MtrVoltDaxIntegHiLm_Volt_f32 22.4099998 k_MtrVoltDaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.2833004 k_MtrVoltVoltCaxIntegLoLim_Volt_f32 8.2833004 k_MtrVoltVoltPashleg_LoLim_Volt_f32 4.224099998 k_MtrVoltVecuFillEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 2.4230001 k_VoltSatDaxPolyCoeff_Uls_f32 2.430001 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MoldkxSrlcomSvcDft_Cnt_lgc_val 1 target_MtrCntrl_Read_MoldcurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 9.31999969 target_MtrCntrl_Read_MtrCurrOffComffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 9.31999969		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc 19.6758003 k_MtrVoltDaxIntegHiLim_Volt_f32 -22.4099998 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltQaxIntegHoutim_Volt_f32 8.2833004 k_MtrVoltQaxIntegHoutim_Volt_f32 8.2833004 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.95899991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u6[1] 211 target_MtrCntrl_Read_MotCurrLoaMtgInEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgInEn_Cnt_lgc_Val 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
k_MtrVoltDaxIntegHiLim_Volt_f32 19.6758003 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegHiLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVcucFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
k_MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltQaxFiltFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.2833004 k_MtrVoltVecuFiltEnable_Cnt_lgc -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[0] 76 t_commOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_ModldxSriComSvcDft_Cnt_lgc_vtr 1 target_MtrCntrl_Read_ModldxSriComSvcDft_Cnt_lgc_vtr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrOftx_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969	- · · ·	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegLoLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTblX_Uls_u3p13[0] 6528 t_CommOffsetTblX_Uls_u3p13[1] 8192 t_CommOffsetTblY_Cnt_u16[0] 76 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotlcurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotcurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969	k_MtrVoltDaxIntegHiLim_Volt_f32	19.6758003
k_MtrVoltQaxIntegHiLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969	k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxIntegHiLim_Volt_f32 8.2833004 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969	k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		8.2833004
k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTblX_Uls_u3p13[0] 6528 t_CommOffsetTblY_Cnt_u16[0] 76 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
k_VoltSatQaxPolyCoeff_Uls_f32 -21.368 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTblX_Uls_u3p13[0] 6528 t_CommOffsetTblY_Cnt_u16[0] 76 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTblX_Uls_u3p13[0] 6528 t_CommOffsetTblY_Cht_u16[0] 76 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
t_CommOffsetTbIX_UIs_u3p13[0] 6528 t_CommOffsetTbIX_UIs_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
t_CommOffsetTbIX_UIs_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_ivtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrGftComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969	t_CommOffsetTblY_Cnt_u16[0]	76
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969	t_CommOffsetTblY_Cnt_u16[1]	211
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.3199969 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		1
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969	· · · · ·	
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969		
target_introditt_read_oysoidte_orit_entrill_val		
	target_wittOntin_Reau_oysotate_Ont_Enum_val	J





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.9229965	-87.9229965 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70514989 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95905995 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996	-212.632996 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-87.9229965	-87.9229965 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.103 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.73699999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.397000015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	692.312988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	147.145996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.29700005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0260000005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	381.019012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-514.21698
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-1.17400002
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxPrevIntg_Volt_M_f32	23.9027004		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016		
MtrPosComputationDelay_Rad_M_f32[0]	2.648		
MtrPosComputationDelay_Rad_M_f32[1]	-4.26800013		
MtrVoltDax_Volt_M_f32[0]	-28.2420006		
MtrVoltDax_Volt_M_f32[1]	12.6160002		
MtrVoltQax_Volt_M_f32[0]	-28.2420006		
MtrVoltQax_Volt_M_f32[1]	12.6160002		
PICurrCotrl JavatorFailScIFac_UIs_M_f32	0.109999999		
PICurrCotrl_InverterFailSclFac_Uls_M_f32	0.289799988		
PICurrCotrl_MtrCurrOavSatFluxRatio_Uls_M_f32			
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	39.4047012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.869400024		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-38.7999992		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	39.4047012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.869400024		
SlowDataAccessBufIndex Cnt M u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3550.11011		
k ILOAFdbackSignalSclFacSlew UlspS f32	5873.56006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.162		
k_MtrCtrlVirualResQax_Ohm_f32	0.0790000036		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	26.7257996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.8303003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.2010002		
k_VoltSatQaxPolyCoeff_Uls_f32	-9.57699966		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTbIX_UIs_u3p13[0]	573		
t_CommOffsetTblX_Uls_u3p13[1]	7569		
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	410 -126.640999		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999 0		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	· · · · · · · · · · · · · · · · · · ·	E	la .
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	410	410	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.40960884	-2.40961003 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.17446756	-4.17446995 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	312	312 ± 1.52588E-05 0 ± 0.0625	
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef Amp M f32[0]	0 131.109009	131.108994 ± 7.81E-03	•



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~		
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~		
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~		
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•		
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•		
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~		
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~		
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~		
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•		
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~		
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~		
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•		
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~		
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~		

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl Read_MtrCurrOffComOffset Cnt u16 ptr
/trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-205.085007
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982
htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.878000021
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.29400003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-295.479004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-442.687988
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.093999968
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.896000028
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.91700006
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1015.31
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-261.230011
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008
/trCurrDaxPrevIntg_Volt_M_f32	-16.7709999
/trCurrDaxRef_Amp_M_f32[0]	-146.723007
/trCurrDaxRef_Amp_M_f32[1]	-121.943001
/trCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
/trCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
/trCurrQaxPrevIntg_Volt_M_f32	17.1947002
/trCurrQaxRef Amp M f32[0]	-133.947006
/trCurrQaxRef_Amp_M_f32[1]	75.7020035
/trCurrQaxRpl Amp M f32[0]	-213.026993
/trCurrQaxRpl Amp M f32[1]	-66.7229996
/trPosComputationDelay Rad M f32[0]	0.994000018
MtrPosComputationDelay Rad M f32[1]	-0.177000001
MtrVoltDax Volt M f32[0]	18.6380005
MtrVoltDax_Volt_M_132[0]	-23.1870003

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



	1		
Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.658999979		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.203999996		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.49939999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	96.5500031		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	56.0906982		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.676299989		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	96.5500031		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	56.0906982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.676299989		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6301.31982		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3999.36011		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.129999995		
k_MtrCtrlVirualResQax_Ohm_f32	0.182999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.9897995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.6005001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.84200001		
k_VoltSatQaxPolyCoeff_Uls_f32	18.5489998		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	1154		
t_CommOffsetTbIX_UIs_u3p13[1]	5284		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1520		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63176	63176 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.4028301	-10.4027996 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.7152195	-17.7152004 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	36460	36460 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	-4.57000017	-4.57000017 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

83.1210022

83.1210022 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.105 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.76499999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.986999989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-48.4529991
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-844.020996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.22599995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.58099997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	17.7269993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-203.524002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	7.91989994
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	4.15500021
MtrPosComputationDelay_Rad_M_f32[1]	1.90199995
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.944000006
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.294999987
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.936500013
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	5.45760012
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.833899975
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	5.45760012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.833899975
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5388.75
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3076.13989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.123999998
k_MtrCtrlVirualResQax_Ohm_f32	0.163000003
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1

PICurrCntrl_Per1



Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.2125001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.89789963		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-23.9650002		
k_VoltSatQaxPolyCoeff_Uls_f32	-16.2169991		
k_deadtimeVScale_Uls_f32	0.94999988		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3921		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3921	3921	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.21121168	-4.21120977 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.19731569	-2.19731998 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	21938	21938 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.106 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.08099997	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.26699999
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	484.062988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	62.8199997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.40799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.17900002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	623.000977
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-937.359009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	2.18199992
MtrCurrQaxRef Amp M f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	1.20200002
MtrPosComputationDelay_Rad_M_f32[1]	-1.38100004
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.25999999
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.222000003
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.918299973
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	301.089996
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	50.1682014
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.833800018
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	301.089996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50.1682014
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.833800018
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7174.7002
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1940.90002
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0850000009
k_MtrCtrlVirualResQax_Ohm_f32	0.118000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	30.3138008
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	15.4816999
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-14.8479996
k_VoltSatQaxPolyCoeff_Uls_f32	5.72399998
k_deadtimeVScale_Uls_f32	1
t CommOffset DIX UIS 113D13IDI	
t_CommOffsetTbIX_UIs_u3p13[0]	4611
t_CommOffsetTbIX_UIs_u3p13[1]	4611 5579
t_CommOffsetTbIX_UIs_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0]	4611 5579 889
t_CommOffsetTbIX_UIs_u3p13[1]	4611 5579
t_CommOffsetTbIX_UIs_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0]	4611 5579 889
t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1]	4611 5579 889 1543
t_CommOffsetTbIX_UIs_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSriComSvcDft_Cnt_lgc_Val	4611 5579 889 1543
t_CommOffsetTbIX_UIs_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	4611 5579 889 1543 0 1
t_CommOffsetTbIX_UIs_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	4611 5579 889 1543 0

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3001	3001	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	57345	57345 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.107 (Repeat Count = 1)	v v
Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModIdxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0089999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.42499995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.64400005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	980.661987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	771.224976
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.26199996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.75600004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	424.487
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	866.411987
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007

PICurrCntrl_Per1



Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	13.4927998		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	2.78600001		
MtrPosComputationDelay_Rad_M_f32[1]	-0.129999995		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.1870003		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.185000002		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.737999976		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.543500006		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-200.740005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	85.9597015		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.842700005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-200.740005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	85.9597015		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.842700005		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3789.18994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3865.98999 1		
k_MtrCtrlCorrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrl/irus/ResDay_Ohm_f23	0.00700000022		
k_MtrCtrlVirualResDax_Ohm_f32	0.149000004		
k_MtrCtrlVirualResQax_Ohm_f32 k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.9689007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.60000038		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.1835003		
k MtrVoltQaxIntegLoLim Volt f32	-11.6000004		
k MtrVoltVecuFiltEnable Cnt Igc	0		
k VoltSatDaxPolyCoeff Uls f32	-10.0129995		
k VoltSatQaxPolyCoeff Uls f32	-13.5450001		
k_deadtimeVScale_Uls_f32	0.995000005		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t CommOffsetTblY Cnt u16[1]	1636		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4728		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	- Nosuit
MtrCntrl Write ModIdx Uls u16p16(val)	65208	65208 ± 1	_
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.7373619	-10.7374001 ± 4.88E-04	_
MtrCntrl Write MtrQaxVoltage Volt 132(val)	-18.2849007	-18.2849007 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	36950	36950 ± 1.52588E-05	*
MtrCurrDaxPrevIntg_Volt_M_f32	-9.6000038	-9.60000038 ± 0.0625	
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	*
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	_

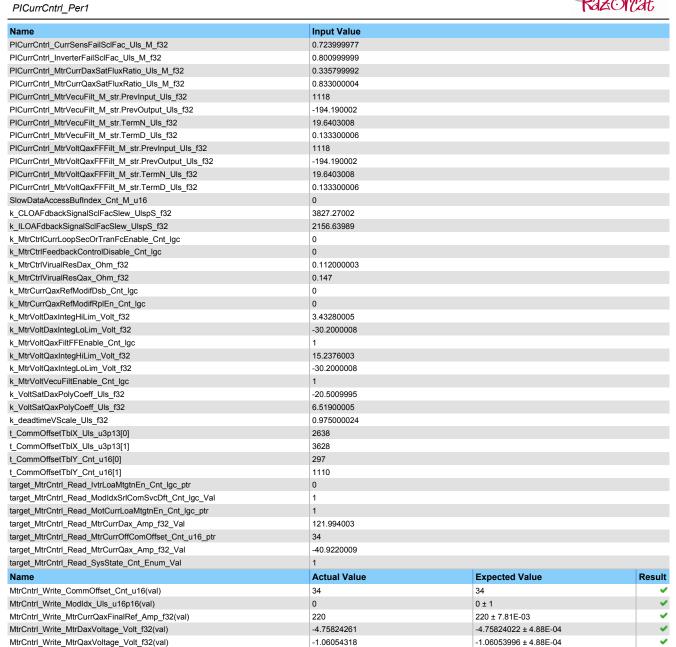


Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
######################################	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)		
	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	· ·	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008	
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.247999996	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.372999996	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	875.137024	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-484.88501	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.29700005	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.26699999	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-49.7849998	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	739.11499	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008	
/trCtrl MtrVoltQaxFF Volt M f32[0]	-7.66699982	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008	
/trCtrl Vecu Volt M f32[0]	18.5559998	
/trCtrl_Vecu_Volt_M_f32[1]	20.9160004	
MtrCurrDaxPrevIntg Volt M f32	-7.71299982	
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999	
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996	
MtrCurrQaxCog_Amp_M_f32	-144.667007	
MtrCurrQaxFinalRef Amp M f32[0]	37.4550018	
_ := = -:-	-2.84500003	
MtrCurrQaxFinalRef_Amp_M_f32[1] MtrCurrQaxPrevIntg Volt M f32	-2.64500003 2.60570002	
	2.60570002 171.485992	
MtrCurrQaxRef_Amp_M_f32[0]	1111	
/trCurrQaxRef_Amp_M_f32[1]	163.787003	
/trCurrQaxRpl_Amp_M_f32[0]	37.4550018	
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003	
/trPosComputationDelay_Rad_M_f32[0]	5.8579998	
/trPosComputationDelay_Rad_M_f32[1]	2.852	
/trVoltDax_Volt_M_f32[0]	-7.66699982	
MtrVoltDax_Volt_M_f32[1]	2.61400008	
MtrVoltQax_Volt_M_f32[0]	-7.66699982	
MtrVoltQax_Volt_M_f32[1]	2.61400008	

2016-04-22, 16:28:30+0530





Actual Function	Count	Expected Function	Count	Result
MtrCntrl Read MtrCurrQax Amp f32	1	MtrCntrl Read MtrCurrQax Amp f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~

MtrCntrl_Write_CommOffset_Cnt_u16

42430

220

-2.84500003

42430 ± 1.52588E-05

-2.84500003 ± 7.81E-03

0 ± 0.0625

220 + 7 81F-03

MtrCntrl_Write_CommOffset_Cnt_u16

 $MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)$

MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.109 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.474999994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.837000012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	673.796997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-165.348999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0149999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.88800001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-32.8989983
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	814.530029
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	6.07289982
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	-2.54200006
MtrPosComputationDelay_Rad_M_f32[1]	4.63800001
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.296000004
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.281399995
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-861.580017
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	88.4244995
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.322100013
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-861.580017
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	88.4244995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.322100013
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	208.033005
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.061999999
k_MtrCtrlVirualResQax_Ohm_f32	0.0989999995
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1





Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.790099978		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.3572006		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-6.70300007		
k_VoltSatQaxPolyCoeff_Uls_f32	-3.44300008		
k_deadtimeVScale_Uls_f32	0.977999985		
t_CommOffsetTblX_Uls_u3p13[0]	1212		
t_CommOffsetTblX_Uls_u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1147		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	75.0830002		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1147	1147	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.88757896	4.88757992 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-0.153855383	-0.153854996 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55734	55734 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0.790099978	0.790099978 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.110 (Repeat Count = 1)		~
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.22500002	

2016-04-22, 16:28:30+0530

PICurrCntrl_Per1



Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.88600004
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	800.210022
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-622.848022
MtrCtrl MtrImpedDax Ohm M f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999990
	0.0529999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] MtrCtrl_MtrOayIntegralGain_Ohm_M_f32[0]	
MtrCtrl_MtrQxvIntegralGain_Ohm_M_f32[0]	1.82700002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.74600005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-301.071014 369.952007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	368.852997 -0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002 18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003 5.12000001
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	11.2777004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	0.674000025
MtrPosComputationDelay_Rad_M_f32[1]	-3.648
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.448000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.565900028
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.700012
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	75.3476028
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.841799974
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	75.3476028
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.841799974
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0460000001
k_MtrCtrlVirualResQax_Ohm_f32	0.101000004
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	0.3583
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	0.993200004
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	23.7310009
k_VoltSatQaxPolyCoeff_Uls_f32	10.2309999
k_deadtimeVScale_Uls_f32	0.986999989
t_CommOffsetTbIX_Uls_u3p13[0]	3808
t_CommOffsetTbIX_UIs_u3p13[1]	7298
t_CommOffsetTbIY_Cnt_u16[0]	1237
	383
t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_lytrl_gaMtgtnEn_Cnt_lgc_ptr	383
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1 0
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1 0 0
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1 0

PICurrCntrl_Per1





Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	~
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64684	64684 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.73893094	-3.73892999 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.36711168	-6.36710978 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	256	256 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.111 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.62300003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23300004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	810.853027
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-988.492981
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.263000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.324999988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-344.360992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	396.108002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg_Volt_M_f32	3.09949994		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003		
MtrPosComputationDelay_Rad_M_f32[0]	-1.17200005		
MtrPosComputationDelay_Rad_M_f32[1]	5.78900003		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.777999997		
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.0450000018 0.95569998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uis_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	77.2248001		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.411900014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	77.2248001		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.411900014		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6667.54004		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7823.27002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.00499999989		
k_MtrCtrlVirualResQax_Ohm_f32	0.098999995		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.7106991		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.35650003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.0270004		
k_VoltSatQaxPolyCoeff_Uls_f32	19.8290005		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTbIX_UIs_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71 676		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target MtrCntrl Read MotCurrLoaMtqtnEn Cnt lqc ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	107.702003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	233		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	220		
target MtrCntrl Read SysState Cnt Enum Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl Write CommOffset Cnt u16(val)	233	233	
MtrCntrl Write ModIdx Uls u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-29.7752876	-29.7752991 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.19967127	4.19966984 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38389	38389 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	
·		<u> </u>	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Test Step 2.112 (Repeat Count = 1)	and the same of th
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/ltrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
/ltrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.859000027
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.911000013
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-309.057007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-788.815002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.00399995
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.43400002
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-19.7409992
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1021.15997
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
/trCtrl_Vecu_Volt_M_f32[0]	14.243
/trCtrl_Vecu_Volt_M_f32[1]	16.6030006
/trCurrDaxPrevIntg_Volt_M_f32	-3.78200006
MtrCurrDaxRef Amp M f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
/trCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
/trCurrQaxFinalRef Amp M f32[1]	-2.84500003
/trCurrQaxPrevIntg Volt M f32	23.843399
/trCurrQaxRef Amp M f32[0]	-133.947006
ItrCurrQaxRef Amp M f32[1]	75.7020035
ItrCurrQaxRpl Amp M f32[0]	37.4550018
/trCurrQaxRpl Amp M f32[1]	-2.84500003
//trPosComputationDelay Rad M f32[0]	3.53800011
MtrPosComputationDelay_Rad_M_f32[1]	-1.18900001
vtrVoltDax_Volt_M_f32[0]	-7.66699982

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrVoltDax_Volt_M_f32[1] 2.61400008 MtrVoltQax_Volt_M_f32[0] -7.66699982 MtrVoltQax_Volt_M_f32[1] 2.61400008 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.851999998 0.398999989 PICurrCntrl InverterFailSclFac Uls M f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.705799997 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -657.099976 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -340.130005 $PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32$ 49 1376991 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.0364000015 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -657 099976 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -340.130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 49 1376991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0364000015 SlowDataAccessBufIndex_Cnt_M_u16 $k_CLOAFdbackSignalSclFacSlew_UlspS_f32$ 7980.1499 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 6489.7002 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.0280000009 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.129999995 k MtrCurrQaxRefModifDsb Cnt Igc Λ k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 14.1104002 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -4.57000017 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 13.7721004 k MtrVoltQaxIntegLoLim Volt f32 -4.57000017 k_MtrVoltVecuFiltEnable_Cnt_lgc k VoltSatDaxPolyCoeff Uls f32 -0.893999994 k_VoltSatQaxPolyCoeff_Uls_f32 -24 9239998 k deadtimeVScale Uls f32 0.966000021 t_CommOffsetTblX_Uls_u3p13[0] 1532 t_CommOffsetTblX_Uls_u3p13[1] 2851 t_CommOffsetTblY_Cnt_u16[0] 912 t_CommOffsetTblY_Cnt_u16[1] 1211 $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 5.72399998 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2264 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 0 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1211 1211 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63307 63307 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 220 + 7 81F-03 220 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -15.956131 -15.9561005 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 1 62337315 1 62337005 + 4 88F-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 37808 37808 ± 1.52588E-05 0 ± 0.0625 MtrCurrDaxPrevIntg Volt M f32 0 MtrCurrQaxFinalRef_Amp_M_f32[0] 37.4550018 37.4550018 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[1] 220 220 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.624000013
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.05799997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	609.603027
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-912.517029
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
htrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
htrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0560000017
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.47599995
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.801999986
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-292.941986
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	762.052002
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
ftrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
ftrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
ftrCtrl_Vecu_Volt_M_f32[0]	13.3629999
ItrCtrl_Vecu_Volt_M_f32[1]	15.7229996
ltrCurrDaxPrevIntg_Volt_M_f32	7.36499977
MtrCurrDaxRef_Amp_M_f32[0]	-100.282997
ltrCurrDaxRef_Amp_M_f32[1]	-120.248001
htrCurrQaxCog_Amp_M_f32	-41.5750008
ltrCurrQaxFinalRef_Amp_M_f32[0]	-76.8769989
ItrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998
htrCurrQaxPrevIntg_Volt_M_f32	8.32960033
ftrCurrQaxRef_Amp_M_f32[0]	-65.1900024
ItrCurrQaxRef_Amp_M_f32[1]	-216.972
ltrCurrQaxRpl_Amp_M_f32[0]	-76.8769989
ItrCurrQaxRpl_Amp_M_f32[1]	-153.238998
ItrPosComputationDelay_Rad_M_f32[0]	1.32799995
htrPosComputationDelay_Rad_M_f32[1]	-0.925000012
/trVoltDax_Volt_M_f32[0]	-29.3959999
/trVoltDax Volt M f32[1]	-1.94000006
/trVoltQax Volt M f32[0]	-29.3959999
MtrVoltQax_Volt_M_f32[1]	-1.9400006

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.349000007 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.111000001 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.824800014 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.521000028 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -657.099976 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 82.828598 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$ 0.797699988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -657 099976 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 82.828598 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.797699988 SlowDataAccessBufIndex_Cnt_M_u16 0 1293 53003 k CLOAFdbackSignalSclFacSlew UlspS f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7361.14014 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.193000004 k_MtrCtrlVirualResQax_Ohm_f32 0.103 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 24.9596996 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 17.6800003 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 8.96500015 k_VoltSatQaxPolyCoeff_Uls_f32 -13.8369999 k_deadtimeVScale_Uls_f32 0.968999982 t CommOffsetTblX Uls u3p13[0] 459 t_CommOffsetTblX_Uls_u3p13[1] 5775 t CommOffsetTblY Cnt u16[0] 771 t_CommOffsetTblY_Cnt_u16[1] 1636 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 1 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3317 50.0610008 target MtrCntrl Read MtrCurrQax Amp f32 Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name Actual Value **Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 3317 3317 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 53.2619934 53.262001 ± 7.81E-03 -1.7370646 -1.73705995 ± 4.88E-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.52290058 -4.5229001 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 50444 50444 ± 1.52588E-05 0 ± 0.0625 MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0] 53 2619934 53 262001 + 7 81F-03

-153.238998

-153.238998 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
htrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ftrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009	
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978	
ItrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982	
1trCtrl MtrDampTermQax Ohm M f32[0]	0.098999995	
ItrCtrl MtrDampTermQax Ohm M f32[1]	0.0170000009	
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.591000021	
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0130000003	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	248.214996	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-29.2189999	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978	
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.079999982	
htrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.093999968	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995	
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	1.46300006	
/trCtrl MtrQaxIntegralGain Ohm M f32[1]	1.03900003	
/trCtrl MtrQaxPropotionalGain Ohm M f32[0]	126.671997	
/trCtrl MtrQaxPropotionalGain Ohm M f32[1]	-963.362976	
/trCtrl MtrVoltDaxFF Volt M f32[0]	-3.59500003	
/trCtrl MtrVoltDaxFF Volt M f32[1]	-28.4209995	
htrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004	
/trCtrl MtrVoltQaxFF Volt M f32[1]	15.9390001	
/trCtrl_Vecu_Volt_M_f32[0]	24.8479996	
/trCtrl_Vecu_Volt_M_f32[1]	27.2080002	
/trCurrDaxPrevIntg_Volt_M_f32	0.851999998	
htrCurrDaxRef Amp M f32[0]	-68.6760025	
	-96.776001	
/trCurrDaxRef_Amp_M_f32[1]	48.8400002	
MtrCurrQaxCog_Amp_M_f32	191.369003	
htrCurrQaxFinalRef_Amp_M_f32[0]	107.137001	
htrCurrQaxFinalRef_Amp_M_f32[1]	28.9717007	
htrCurrQaxPrevIntg_Volt_M_f32	-146.723007	
htrCurrQaxRef_Amp_M_f32[0]	-146.723007	
htrCurrQaxRef_Amp_M_f32[1]	191.369003	
MtrCurrQaxRpl_Amp_M_f32[0]		
/trCurrQaxRpl_Amp_M_f32[1]	107.137001	
/trPosComputationDelay_Rad_M_f32[0]	4.06799984	
MtrPosComputationDelay_Rad_M_f32[1] MtrVoltDax Volt M f32[0]	1.16100001 -17.1070004	

PICurrCntrl_Per1



Min-Vollage, Vol. M. 52(1)	Name	Input Value		
Michael Current Michael Cu		·		
Microline Micr				
Picturo Description Desc				
Picumonia Information In				
Picuriority Microriassial Publish (p. M. 122 0.916199982				
Picumorial Minicural Must Previous July 18, 12 0 0 0 0 0 0 0 0 0		0.916199982		
Picumorial, MirvecuFill, M. str. Previoupul, Uis, 12		0.686999977		
Picumoris Mirveculfil M, str. PrevOupout, Us., 9/2 Picumoris Mirveculfil M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, str. Previous M, Us. 9/2 Picumoris M, Weller M, Str. Previous M, Us. 9/2 Picumoris M, Weller M, Str. Previous M, Us. 9/2 Picumoris M, Weller M, Str. Previous M, Us. 9/2 Picumoris M, Weller M, Str. Previous M, Us. 9/2 Picumoris M, Weller M, Us. 9/2 Picumoris M, Weller M, Weller M, Us. 9/2 Picumoris M, Weller M,				
Picumorish Mivespell M, str. Term N, Uis, 132 95.5231018		-44.2799988		
Picumoris Mir/Vocaffe M. str. TemD_Us. 32 0 2 1 1 1 1 1 1 1 1 1		95.5231018		
PicturCort MrVoliCasFFFIL M, str Previoput Us. 52		0.219099998		
Picturnorni, MirvolitoarFFFE, M, str. Perev. Output, Uis, 152		0		
PicumCruft MirVoliDas/FFII M_str.TermN_Uis_f2		-44.2799988		
Picumornt, MmVoliCas/FFFIz M. str TermD_Us_132 0.21909998		95.5231018		
SlowDataAccessBuffindex, Crt_M_u16 0		0.219099998		
K_ LI CAF ChackSignalSciFaceSiew_UispS_132		0		
k_ILCAFabackSignalSeFacSlew_UspS_132 k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc 0.08100000002 k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc 0.0829999968 k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc 0.k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc 0.k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc 0.c_k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc 0.c_k_MrtCritUrrLoopSeCOTranFcEnable_Cnt_lgc 0.c_k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_MrtVorlDackInteglicLim_Volt_132 k_UrrLoopSeCOTranFcEnable_Cnt_lgc 0.c_k_VorlSalDackPolopGrt_Uls_132 k_DackIntegritUrrLoopSeCOTranFcEnable_Cnt_lgc k_DackIntegritUrrLoopSeCOTranFcEnable_Cnt_lgc k_DackIntegritUrrLoopSeCOTranFcEnable_Cnt_lgc k_DackIntegritUrrLoopSeCOTranFcEnable_Cnt_lgc k_DackIntegritUrrLoopSeCOTranFcEnable_Cnt_lgc k_DackIntegritUrrLoopSeCOTranFcEnable_Cnt_lgc k_DackIntegritUrrLoopSeCOTranFcEnable_Cnt_lgc l_DomnofisetTibl_Uls_ubg13(1) larget_MrtCritIr_Read_IntranFcCotra		6616.02002		
K, MrtCritrCourt-copSecOTrainFeEnable_Cnt_lgc		5777.70996		
k_MtrCtrVirualResDax_Ohm_f32		1		
K_MtrCitrVirualResQax_Ohm_132	k MtrCtrlFeedbackControlDisable Cnt lgc	1		
K_MtrCurrQaxRefModifDsb_Cnt_lgc	k MtrCtrlVirualResDax Ohm f32	0.0810000002		
K_MtrCurrQaxRetModifRplEn_Cnt_lgc	k MtrCtrlVirualResQax Ohm f32	0.0829999968		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	k MtrCurrQaxRefModifDsb Cnt lgc	0		
K_MtrVoltDaxIntegHiLim_Volt_f32		0		
K_MtrVoltDaxIntegLoLim_Volt_132		18.9941998		
K_MtrVottQaxIntegHiLim_Vott_f32	k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxIntegLoLim_Volt_f32	k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
MirVoltVecuFiltEnable_Cnt_lgc 0	k_MtrVoltQaxIntegHiLim_Volt_f32	1.51160002		
MirVoltVecuFiltEnable_Cnt_lgc 0		-10.5		
k_VoltSatQaxPolyCoeff_Uls_f32	k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_deadtimeVScale_Uls_132 0.977999985 t_CommOffsetTblX_Uls_u3p13[0] 2638 t_CommOffsetTblY_Cnt_u16[0] 297 t_CommOffsetTblY_Cnt_u16[1] 1110 target_MtrCntrl_Read_IvtrLoaMtgInEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSriComSvcDtf_Cnt_lgc_Val 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 V MtrCntrl_Write_MtrOaxVoltage_Volt_f32(val) -220 -220 ± 7.81E-03 V MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -23.0419712 -23.0419998 ± 4.88E-04 V MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 V MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-05 V MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -10.5 -10.5 ± 0.0625 V MtrCurrDaxFrevintg_Volt_M_f32(0) -220 <td>k VoltSatDaxPolyCoeff Uls f32</td> <td>19.1259995</td> <td></td> <td></td>	k VoltSatDaxPolyCoeff Uls f32	19.1259995		
t_CommOffsetTbIX_UIs_u3p13[0] 2638 t_CommOffsetTbIX_UIs_u3p13[1] 3628 t_CommOffsetTbIY_Cnt_u16[0] 297 t_CommOffsetTbIY_Cnt_u16[0] 11110 target_MtrCntrl_Read_IvtrLoaMtgInEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_IvtrLoaMtgInEn_Cnt_lgc_Val 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffsent_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrOffsent_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 20.9197999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name	k_VoltSatQaxPolyCoeff_Uls_f32	1.05900002		
t_CommOffsetTbIX_UIs_u3p13[0] 2638 t_CommOffsetTbIX_UIs_u3p13[1] 3628 t_CommOffsetTbIY_Cnt_u16[0] 297 t_CommOffsetTbIY_Cnt_u16[0] 11110 target_MtrCntrl_Read_IvtrLoaMtgInEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_IvtrLoaMtgInEn_Cnt_lgc_Val 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffsent_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrOffsent_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 20.9197999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name		0.977999985		
t_CommOffsetTbIX_Uls_u3p13[1] 3628 t_CommOffsetTbIY_Cnt_u16[0] 297 t_CommOffsetTbIY_Cnt_u16[1] 1110 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MoclcurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MoclcurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffcomOffset_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 20.79.17999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110		2638		
t_CommOffsetTbIY_Cnt_u16[1] 1110 target_MtrCntrl_Read_vtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 20.79.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110	t_CommOffsetTblX_Uls_u3p13[1]	3628		
t_CommOffsetTbIY_Cnt_u16[1] 1110 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 20.79.917999 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 20.79.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110		297		
target_MtrCntrl_Read_NotIdxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 ✓ MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64094 64094 ± 1 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -33.0419712 -23.0419998 ± 4.88E-04 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 ✓ MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03 ✓		1110		
target_MtrCntrl_Read_MotldxSrlComSvcDft_Cnt_lgc_Val		0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 ** MtrCntrl_Write_Modldx_Uls_u16p16(val) 64094 64094 ± 1 ** MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 ** MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 ** MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 ** MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 ** MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03 **	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MtrCurrDax_mp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 70 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 *** MtrCntrl_Write_Modldx_Uls_u16p16(val) 64094 64094 ± 1 *** MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 *** MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 *** MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 *** MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 *** MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03 ***		1		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 1110 *** MtrCntrl_Write_Modidx_Uls_u16p16(val) 64094 64094 ± 1 *** *** MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 *** MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 *** MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 *** MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 *** MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03 ***	target MtrCntrl Read MtrCurrDax Amp f32 Val	20.6149998		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 1110 *** MtrCntrl_Write_Modidx_Uls_u16p16(val) 64094 64094 ± 1 *** *** MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 *** MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 *** MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 *** MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 *** MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03 ***	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	70		
Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 1110 4094 ± 1 4094 ±		-207.917999		
MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64094 64094 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -23.0419712 -23.0419998 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 MtrCntrl_Write_Modldx_Uls_u16p16(val) 64094 64094 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -23.0419712 -23.0419998 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03	Name	Actual Value	Expected Value	Result
MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64094 64094 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -23.0419712 -23.0419998 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03	MtrCntrl Write CommOffset Cnt u16(val)	1110	·	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -23.0419712 -23.0419998 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03	MtrCntrl Write Modldx Uls u16p16(val)	64094	64094 ± 1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03	MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -7.72158337 -7.72159004 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03	MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)			✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 22674 22674 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03	MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)		-7.72159004 ± 4.88E-04	~
MtrCurrDaxPrevIntg_Volt_M_f32 -10.5 -10.5 ± 0.0625 ✓ MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03 ✓	MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)			~
MtrCurrQaxFinalRef_Amp_M_f32[0] -220 -220 ± 7.81E-03 ✓	MtrCurrDaxPrevIntg Volt M f32			
_ (_ (, _ (, _ (, _ (, _ (, _ (, _ (, _	MtrCurrQaxFinalRef_Amp_M_f32[0]			~
	MtrCurrQaxFinalRef_Amp_M_f32[1]			~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
######################################	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)		
	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	· · · · · · · · · · · · · · · · · · ·	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
AtrChtl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]		
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016	
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023	
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0710000023	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.72399998	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	376.216003	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-802.426025	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.070000003	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.60300004	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.42400002	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	18.6140003	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-320.81601	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996	
/trCtrl_Vecu_Volt_M_f32[0]	28.3600006	
/trCtrl_Vecu_Volt_M_f32[1]	30.7199993	
/trCurrDaxPrevIntg_Volt_M_f32	1.579	
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998	
/trCurrDaxRef_Amp_M_f32[1]	115.814003	
MtrCurrQaxCog_Amp_M_f32	107.702003	
/trCurrQaxFinalRef_Amp_M_f32[0]	-147.343002	
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972	
MtrCurrQaxPrevIntg_Volt_M_f32	0.0671999976	
/trCurrQaxRef_Amp_M_f32[0]	-208.287994	
/trCurrQaxRef_Amp_M_f32[1]	-27.9839993	
/trCurrQaxRpl_Amp_M_f32[0]	-147.343002	
/ltrCurrQaxRpl_Amp_M_f32[1]	127.972	
/trPosComputationDelay_Rad_M_f32[0]	-2.94000006	
/trPosComputationDelay_Rad_M_f32[1]	1.80400002	
//dtrVoltDax_Volt_M_f32[0]	0.908999979	
MtrVoltDax_Volt_M_f32[1]	21.0249996	
MtrVoltQax_Volt_M_f32[0]	0.908999979	
/trVoltQax_Volt_M_f32[1]	21.0249996	

PICurrCntrl_Per1



Name	Input Value		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.702000022		
PICurrCntrl InverterFailScIFac Uls M f32	0.702000022		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.99180001		
	0.14300007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32			
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	44.7025986		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.123199999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	44.7025986		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.123199999		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2332.93994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0179999992		
k_MtrCtrlVirualResQax_Ohm_f32	0.050000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.83920002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.60000038		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.530899		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-9.26000023		
k_VoltSatQaxPolyCoeff_Uls_f32	12.5810003		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	1212		
t CommOffsetTblX Uls u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	23		
t CommOffsetTblY Cnt u16[1]	212		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	3905		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	212	212	Result
	65339		
MtrCntrl_Write_ModIdx_UIs_u16p16(val)		65339 ± 1 -168.647003 ± 7.81E-03	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-168.647003	-168.647003 ± 7.81E-03 28.2432995 ± 4.88E-04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	28.2433357		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.33609736	1.33609998 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50762	50762 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	5.83920002	5.83920002 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-168.647003	-168.647003 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972	127.972 ± 7.81E-03	<u> </u>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.116 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-213.026993 -66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.898000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.30599999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	806.749023
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-34.0489998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrlmpedOay_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0970000029 0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.221000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.4900001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	737.367004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	253.417999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	21.2989998
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg Volt M f32	23.6590004 -8.56599998
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxPrevIntg_Volt_M_f32	2.58669996
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	6.18900013
MtrCurrQaxRpl_Amp_M_f32[1]	83.0540009 -2.94300008
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay Rad M f32[1]	0.89800002
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax Volt M f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.499000013
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.1514
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.098999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-43.1699982 -747.200088
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl MtrVecuFilt M str.TermN Uls f32	-717.299988 1.35469997
PICurrCntri_MtrVecuFilt_M_str.TermN_UIS_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIS_f32	0.64999976
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	1.35469997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.649999976
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7083.27002
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	947.890015
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32	0.0540000014 0.172000006
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	27.2376995
· - -	

PICurrCntrl_Per1



Name	Input Value		
k MtrVoltDaxIntegLoLim Volt f32	-30.2000008		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	20.0447998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-13.0120001		
k_VoltSatQaxPolyCoeff_Uls_f32	6.00699997		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	794		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	794	794	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-57.1910019	-57.1910019 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.54663992	-3.54663992 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.29347062	-3.29346991 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	10649	10649 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013	6.18900013 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-57.1910019	-57.1910019 ± 7.81E-03	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	

Test Step 2.117 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.65499997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.88900006

2016-04-22, 16:28:30+0530

PICurrCntrl_Per1



Name		
MacClA, Michael Processor (See Proc. M. 2020)	Name	Input Value
MICHAEL MODERNAME 754 91197 754 9119		
Michic Minimpedia Chm M 1920		
Marcia Minimedata Chin M. 1921		
McCH_MinimpedSac_Ome_M_D3[0] McCH_MichaningsaCan_Ome_M_D3[0] McC		
Michael Michael (Michael Michael Mic		
Michi Midachergesian. Chm M, 1921) 12299988 Michi Michi Midachergesian. Chm M, 1921) 133-90088 Michi Michi Midachergesian. Chm M, 1920) 433-90088 Michi Michi Midachergesian. Chm M, 1920) 491-7800001 Michi Michi Midachergesian. Chm M, 1920) 47-800001 Michi Michi Midachergesian. Chm M, 1920] 47-8000001 Michi Mich	MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
Michi Machaningmickin, Drink M, 1991	MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
Michil Michae Proprient George Com, M. (201) 49:73000001 Michil Michae Proprient George Com, M. (201) 47:3000001 Michil Michae Michae Prov. M. (201) 47:3000001 Michil Michae Michae Prov. M. (201) 19:300000 Michil Michae Michae Prov. M. (201) 19:300000 Michil Michae Michae Prov. M. (201) 19:3000000 Michil Michae Michae Prov. M. (201) 21:3000000 Michil Michae	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.079999982
MICHAEL MICHAELS PLOYAL M. 2021 981,781008 MICHAELS PLOYAL M. 2021 18,816002 MICHAELS PLOYAL M. 2021 18,816002 MICHAELS PLOYAL M. 2021 18,816002 MICHAELS PLOYAL M. 2021 23,179003 MICHAELS PLOYAL M. 2021 23,179003 MICHAELS PLOYAL M. 2021 23,179003 MICHAELS PLOYAL M. 2021 23,719093 MICHAELS PLOYAL M. 2021 MICHAELS PLOYAL M.	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.22399998
MICHAEL MICHAELS PLOYAL M. 2021 981,781008 MICHAELS PLOYAL M. 2021 18,816002 MICHAELS PLOYAL M. 2021 18,816002 MICHAELS PLOYAL M. 2021 18,816002 MICHAELS PLOYAL M. 2021 23,179003 MICHAELS PLOYAL M. 2021 23,179003 MICHAELS PLOYAL M. 2021 23,179003 MICHAELS PLOYAL M. 2021 23,719093 MICHAELS PLOYAL M. 2021 MICHAELS PLOYAL M.		-333,980988
Michan Michan Marchaer F. Volt. M. 2020 0.73000001 Michan Mic		
Michael Marcollaws Vol. M. 1201 18.850005 18.850005 Michael Marcollaws Vol. M. 1201 18.850005 18.850005 Michael Marcollaws Vol. M. 1201 23.1570003 Michael Marcollaws Vol. M. 1201 23.1570003 Michael Marcollaws Vol. M. 1201 23.715999 Michael Marcollaws Vol. M. 1201 23.715999 Michael Marcollaws Vol. M. 1201 18.004409 Michael Marcollaws Vol. M. 1201		
Michil, Marviolianier*, Vol. M. 1201 Michilder Marviolianier*, Vo		
MinCH, Vest, Vot. H., E.791 23.1870003 MinCH, Vest, Vot. H., E.792 23.789983 MinCH, Vest, Vot. H., E.792 23.789983 MinChardsefe, Amp. M., S2(1) 180,044008 MinChardsefe, Amp. M., S2(1) 180,044008 MinChardsefe, Amp. M., S2(1) 180,044009 MinChardsefe, Amp. M., S2(2) 180,044009 MinChardsefe, Amp. M., S2(2) 180,044009 MinChardsefe, Amp. M., S2(2) 180,044009 MinChardsefe, M., M., S2(2) MinChardsefe, M., M., M., M., S2(2) MinChardsefe, M., M., M., M., M., S2(2) MinChardsefe, M., M., M., M., M., M., M., M., M., M.		
McCil Veloc VM M. SSI		
Michael Neal, M. 1921 27,199883 Michael Neal, M. 1921 165,24204 16		
MC-Currobarted_Ampl. M. 1921 165.24004 165.24004 165.24004 165.24004 165.24004 165.24004 165.24004 165.24004 165.24004 165.24004 165.24004 165.24004 165.24008	MtrCtrl_Vecu_Volt_M_f32[0]	21.3600006
MICHAPARED, MICH	MtrCtrl_Vecu_Volt_M_f32[1]	23.7199993
MS-Curto-Africa Amp. M. 521 MS-Curto-Africa Amp. M. 522 59.3040009 MS-Curto-Africa Amp. M. 5210 -105.246002 MS-Curto-Africa Amp. M. 5211 -75.702035 MS-Curto-Africa Amp. M. 5211 -75.702035 MS-Curto-Africa Amp. M. 5210 -105.246002 MS-Curto-Africa Amp. MS-Curto-Africa Amp. MS-Curto-Africa Amp. MS-Curto-Africa Amp. MS-Curto-Africa Amp. MS-Curto-Africa Amp.	MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983
MicCurroax-Engl.Armg.M. [32] MicCurroax-FrailFord Armg.M. [32]0] -105 240002 MicCurroax-FrailFord Armg.M. [32]1] 41 6290016 MicCurroax-FrailFord Armg.M. [32]1] 75 700035 MicCurroax-FrailFord Armg.M. [32]1] 75 700035 MicCurroax-FrailFord Armg.M. [32]1] 75 700035 MicCurroax-FrailFord Armg.M. [32]1] 41 509016 MicCurroax-FrailFord M. [32]1] 43 500002 MicCurroax-FrailFord M. [32]1] 43 500003 MicCurroax-FrailFord M. [32]1] 43 500003 MicCurroax-FrailFord M. [32]1] 43 500006 MicVurroax-FrailFord M. [32]2 0 75 700003 Plourcort, Invertor-FrailFord M. [32]2 0 75 700003 Plourcort, Invertor-FrailFord M. [32]2 0 75 700003 Plourcort, MicVurroax-FrailFord M. [32]2 0 75 700003 Plourcort, MicVurroax-FrailFo	MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MicCurroax-Engl.Armg.M. [32] MicCurroax-FrailFord Armg.M. [32]0] -105 240002 MicCurroax-FrailFord Armg.M. [32]1] 41 6290016 MicCurroax-FrailFord Armg.M. [32]1] 75 700035 MicCurroax-FrailFord Armg.M. [32]1] 75 700035 MicCurroax-FrailFord Armg.M. [32]1] 75 700035 MicCurroax-FrailFord Armg.M. [32]1] 41 509016 MicCurroax-FrailFord M. [32]1] 43 500002 MicCurroax-FrailFord M. [32]1] 43 500003 MicCurroax-FrailFord M. [32]1] 43 500003 MicCurroax-FrailFord M. [32]1] 43 500006 MicVurroax-FrailFord M. [32]2 0 75 700003 Plourcort, Invertor-FrailFord M. [32]2 0 75 700003 Plourcort, Invertor-FrailFord M. [32]2 0 75 700003 Plourcort, MicVurroax-FrailFord M. [32]2 0 75 700003 Plourcort, MicVurroax-FrailFo	MtrCurrDaxRef Amp M f32[1]	165.242004
MicCurdanFinaRed Arry, M. (520) -105,246002 MicCurdanFinaRed Arry, M. (520) 22,5144005 MicCurdanRed Arry, M. (520) 133,947006 MicCurdanRed Arry, M. (520) 133,947006 MicCurdanRed, Arry, M. (520) 105,248002 MicCurdanRed, Arry, M. (520) -105,248002 MicCurdanRed, Arry, M. (520) -3,6190003 MicCurdanRed, Arry, M. (520) -3,6190003 Miry Controlled, R. (520) -4,93400002 Miry Collax, Vol. M. (520) 18,880006 Picur-Corn, Miry Collax, Sale Liva Rato, Us. M. (52) 0,851000011 Picur-Corn, Miry Collax, Sale Liva Rato, Us. M. (52) 0,96800003 Picur-Corn, Miry Collax, Sale Liva Rato, Us. M. (52) 0,968000003 Picur-Corn, Miry Collax, Sale Liva Rato, Us. M. (52) 0,80200000 Picur-Corn, Miry Collax, Fill, M. as Premo, Us. M. (52) 0,8020000		
MicCurdasFinalRot Amp. M. (321)		
MicCurdasPrefug, Vol. M. 192 MrCurdasPrefug, M. 192(1) MrCurdasRef, Amp, M. 192(1) MrDusComputationDelay, Rad, M. 192(1) MrDusComputationDelay, Rad, M. 192(1) MrDusComputationDelay, Rad, M. 192(1) MrVicilbax, Vol. M. 192(1) MrVicilba		
MicCurrClaskPel_Amp_M_132(1)		
Microrraker(J.Amp, M.52(1)		
MicCurdaxRpi_Amp_M_32(1)		
Michards Amp J. 1921] Michael Amp J. 1921 Michael	MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MitPosComputationDelay, Rad M, D2(1) 4,93400002 MitPosComputationDelay, Rad M, D2(1) 4,93400002 MitVoitDax, Voit, M, D2(1) 18,6890005 MitVoitDax, Voit, M, D2(1) 23,1870003 MitVoitDax, Voit, M, D2(1) 18,6890005 MitVoitDax, Voit, M, D2(1) 28,1870003 MitVoitDax, Voit, M, D2(1) 28,1870003 PlCurrCntl, Loursens-failsClase, Uls, M, D2 0,77000029 PlCurrCntl, MitCurrDaxSaffiluxRato, Uls, M, D2 0,77000029 PlCurrCntl, MitCurrDaxSaffiluxRato, Uls, M, D3 0,0469999844 PlCurrCntl, MitVoicurSastifiluxRato, Uls, M, D3 0,0469999884 PlCurrCntl, MitVoicurifil, M, str. Term, Uls, D32 118 PlCurrCntl, MitVoicurifil, M, str. Term, Uls, D32 32,299003 PlCurrCntl, MitVoilCaxFFFFil, M, str. Term, Uls, D32 3134 PlCurrCntl, MitVoilCaxFFFil, M, str. Term, Uls, D32 3134 PlCurrCntl, MitVoilCaxFFFIl, M, str. Term, Uls, D32 30,3134 PlCurrCntl, MitVoilCaxFFFIl, M, str. Term, Uls, D32 30,3134 PlCurrCntl, MitVoilCaxFFFIl, M, str. Term, Uls, D32 30,3134 SowbalaxicessBurindex, Cnt, Mit, D3 30,314 K, L, C.O.A.FribackSignalsCellasSiew, UlspS,	MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtPoScomputationCleby, Rad M. f32[1] 4,93400002 MtVotIDax, Volt, M. f32[0] 18,6380005 MtVotIDax, Volt, M. f32[1] 23,1870003 MtVotIDax, Volt, M. f32[1] 18,6380005 MtVotIDax, Volt, M. f32[1] 23,1870003 PICurchart, MrVotIDax, Volt, M. f32[1] 23,1870003 PICurchart, MrCurDaxSatFluxRatio, Uls, M. f32 0,770000003 PICurchart, MrCurDaxSatFluxRatio, Uls, M. f32 0,0469999844 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 118 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 118 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 53,2500003 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 118 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 118 PICurchart, MrVoulCaxFFFIE, M. str. Previoput, Uls, f32 118 PICurchart, MrVoulCaxFFFIE, M. str. Previoput, Uls, f32 118 PICurchart, MrVoulCaxFFFIE, M. str. Termb, Uls, f32 1334 PICurchart, MrVoulCaxFFFIE, M. str. Termb, Uls, f32 334 PICurchart, MrVoulCaxFFIE, M. str. Termb, Uls, f32 134 SlowbalaccessBurlindex, Crit, M. str. Termb, Uls, f32 0,3134 Slowbala	MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtPoScomputationCleby, Rad M. f32[1] 4,93400002 MtVotIDax, Volt, M. f32[0] 18,6380005 MtVotIDax, Volt, M. f32[1] 23,1870003 MtVotIDax, Volt, M. f32[1] 18,6380005 MtVotIDax, Volt, M. f32[1] 23,1870003 PICurchart, MrVotIDax, Volt, M. f32[1] 23,1870003 PICurchart, MrCurDaxSatFluxRatio, Uls, M. f32 0,770000003 PICurchart, MrCurDaxSatFluxRatio, Uls, M. f32 0,0469999844 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 118 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 118 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 53,2500003 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 118 PICurchart, MrVoucrill, M. str. PrevOutput, Uls, f32 118 PICurchart, MrVoulCaxFFFIE, M. str. Previoput, Uls, f32 118 PICurchart, MrVoulCaxFFFIE, M. str. Previoput, Uls, f32 118 PICurchart, MrVoulCaxFFFIE, M. str. Termb, Uls, f32 1334 PICurchart, MrVoulCaxFFFIE, M. str. Termb, Uls, f32 334 PICurchart, MrVoulCaxFFIE, M. str. Termb, Uls, f32 134 SlowbalaccessBurlindex, Crit, M. str. Termb, Uls, f32 0,3134 Slowbala	MtrPosComputationDelay Rad M f32[0]	-3.61800003
Mrtvolidax_Volt_M_I32[0] 18.6380005 Mrtvolidax_Volt_M_I32[1] 23.1870003 Mrtvolidax_Volt_M_I32[1] 18.5380005 18.6380005 Mrtvolidax_Volt_M_I32[1] 23.1870003 Mrtvolidax_Volt_M_I32[1] 23.1870003 PiCurchit_Mrtsansensalistifiea_Uis_M_I32 0.851000011 PiCurchit_Mrtsansensalistifiea_Uis_M_I32 0.787000029 PiCurchit_MrtvordaxsSaffuxRatio_Uis_M_I32 0.787000029 PiCurchit_MrtvordaxSaffuxRatio_Uis_M_I32 0.0469999984 PiCurchit_MrtvordaxSaffuxRatio_Uis_M_I32 0.0469999984 PiCurchit_Mrtvordix_Hit_M_str_Previoput_Uis_I32 1118 PiCurchit_Mrtvordix_Hit_M_str_Previoput_Uis_I32 32.2500003 PiCurchit_Mrtvocufit_M_str_Previoput_Uis_I32 32.2500003 PiCurchit_Mrtvocufit_M_str_Previoput_Uis_I32 32.2500003 PiCurchit_Mrtvocufit_M_str_Previoput_Uis_I32 33.2500003 PiCurchit_Mrtvocufit_M_str_Previoput_Uis_I32 33.2500003 PiCurchit_Mrtvocufit_M_str_Previoput_Uis_I32 33.2500003 PiCurchit_Mrtvocufit_M_str_Previoput_Uis_I32 33.2500003 PiCurchit_Mrtvocufit_M_str_Previoput_Uis_I32 33.2500003 PiCurchit_MrtvoltdaxFFFIM_str_Previoput_Uis_I32 33.2500003 PiCurchit_MrtvoltdaxFFFIM_str_Previoput_Uis_I32 33.34 S0.0000000 PiCurchit_MrtvoltdaxFFFIM_str_Previoput_Uis_I32 33.34 S0.000000 PiCurchit_MrtvoltdaxFFFIM_str_Previoput_Uis_I32 33.34 S0.000000 PiCurchit_MrtvoltdaxFFFIM_str_Previo_Uis_I32 30.3134 S0.000000 PiCurchit_MrtvoltdaxFFIM_str_Previo_Uis_I32 30.000000 PiCurchit_MrtvoltdaxFFIM_str_Previo_Uis_I32 30.0000000 PiCurchit_MrtvoltdaxFFIM_str_Previo_Uis_I32 30.0000000000000000000000000000000000	, , , , , , , , , , , , , , , , , , , ,	-4.93400002
MtVotIDar, Voll, M. (32[1] 23.1870003 MtVotIDar, Voll, M. (32[0] 18.6880005 MtVotIDar, Voll, M. (32[1] 23.1870003 PICurcTont, CursensFallSelfae, Uls, M. (32) 0.851000011 PICurcTont, MtrourDaxSatFluxRatio, Uls, M. (52) 0.77600002 PICurcTont, MtrourDaxSatFluxRatio, Uls, M. (52) 0.046999984 PICurcTont, MtrourDaxSatFluxRatio, Uls, M. (52) 118 PICurcTont, MtrourEuf, M. str. FremPut, Uls, (52) 118 PICurcTont, MtrourEuf, M. str. FremPut, Uls, (52) 32.259003 PICurcTont, MtrourEuf, M. str. FremPut, Uls, (52) 0.3134 PICurcTont, MtroulCaxFFFIL, M. str. FremPut, Uls, (52) 32.259003 PICurcTont, MtroulCaxFFFIL, M. str. FremPut, Uls, (52) 432.2999 PICurcTont, MtroulCaxFFFIL, M. str. TermD, Uls, (52) 432.2999 PICurcTont, MtroulCaxFFFIL, M. str. TermD, Uls, (52) 432.2999 PICurcTont, MtroulCaxFFFIL, M. str. TermD, Uls, (52) 432.2999 PICurcTont, MtroulCaxFFIL, M. str. TermD, Uls, (52) 432.2999 PICurcTont, MtroulCaxFFIL, M. str. TermD, Uls, (52) 438.70996 K. L. O.AF daxAsignalScife acSiew, UlspS, (52) 488.70996 K. McCurdYausResola, Olm, (52) 488.70996		
Mrtvolicax_VotL_M_52[0] 18.6380005 Mrtvolicax_VotL_M_52[1] 23.187003 PiCurrChtt_CurrSensFailSeFac_Uis_M_52 0.851000011 PiCurrChtt_CurrSensFailSeFac_Uis_M_52 0.757000029 PiCurrChtt_MicrorDaxSafFiurRatio_Uis_M_52 0.767000029 PiCurrChtt_MicrorDaxSafFiurRatio_Uis_M_52 0.76900003 PiCurrChtt_MicrorDaxSafFiurRatio_Uis_M_52 0.466999984 PiCurrChtt_MicrorDaxSafFiurRatio_Uis_M_52 0.466999984 PiCurrChtt_MicrorDaxSafFiurRatio_Uis_M_52 1118 PiCurrChtt_MicrorDaxSafFiurRatio_Uis_M_52 8.26 23999 PiCurrChtt_MicrorDaxSafFiurRatio_Uis_M_52 3.314 PiCurrChtt_MicrorDaxFFiit_M_strTermD_Uis_f32 0.3134 PiCurrChtt_MicrorDaxFFiit_M_strTermD_Uis_f32 0.3134 PiCurrChtt_MicrorDaxFFiit_M_strTermD_Uis_f32 1118 PiCurrChtt_MicrorDaxFFiit_M_strTermD_Uis_f32 3.2590003 PiCurrChtt_MicrorDaxFFiit_M_strTermD_Uis_f32 3.03909995 R_ILOAFdbackSignalScFacSiew_UispS_132 4854.7096 R_INCOTOTOTOTOTOTOTOTOTOTOTOTOTOTOTOTOTOTOT		
MrtVoliCax_Volt_M_152[1] 23.187003 PICurrCntr_CurrSensFallsClFac_Uls_M_152 0.851000011 PICurrCntr_InverterFallsClFac_Uls_M_152 0.757000029 PICurrCntr_InverterFallsClFac_Uls_M_152 0.076800003 PICurrCntr_MitrCurrDaxSafFluxRatio_Uls_M_152 0.076800003 PICurrCntr_MitrVecuFit_M_str_Previnput_Uls_152 1118 PICurrCntr_MitrVecuFit_M_str_Previnput_Uls_152 118 PICurrCntr_MitrVecuFit_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFIT_M_str_Previnput_Uls_152 8-26.29999 PICurrCntr_MitrVelOlaxFIT_M_str_Previnput_Uls_152 8-26.29999 PICURCNTR_MitrVelOlaxFIT_M_str_Previnput_Uls_152 8-26.29999 PICURCNTR_MitrVelOlaxFIT_M_str_Previnput_Uls_152 8-26.29999 PICURCNTR_MitrVelOlaxFIT_M_str_Previnput_Uls_152 8-26.29999 PICURCNTR_MitrVelOlaxFIT_M_str_Previnput_Uls_152 8-26.299		
PICUTCHT CurrSensFailSciFac_Uis_M_R22 0.851000011 PICUTCHT Invester FailSciFac_Uis_M_R32 0.757000029 PICUTCHT InvitroumPaskaFiturAstio_Uis_M_R32 0.757000029 PICUTCHT MirtCurrCansSatFiturAstio_Uis_M_R32 0.469999944 PICUTCHT MirtCurrCansSatFiturAstio_Uis_M_R32 0.469999944 PICUTCHT MirtCurrCansSatFiturAstio_Uis_M_R32 1118 PICUTCHT MirtVecuFit_M_str_PrevOutput_Uis_R32 1118 PICUTCHT MirtVecuFit_M_str_PrevOutput_Uis_R32 53.2599003 PICUTCHT_MIRTVecuFit_M_str_FamD_Uis_R32 0.3134 PICUTCHT_MIRTVecuFit_M_str_FamD_Uis_R32 1118 PICUTCHT_MIRTVecuFit_M_str_FamD_Uis_R32 118 PICUTCHT_MIRTVOIRCANFFFIT_M_str_PrevInput_Uis_R32 118 PICUTCHT_MIRTVOIRCANFFFIT_M_str_FamD_Uis_R32 32.659003 PICUTCHT_MIRTVOIRCANFFFIT_M_str_FamD_Uis_R32 32.659003 PICUTCHT_MIRTVOIRCANFFFIT_M_str_FamD_Uis_R32 3.3134 PICUTCHT_MIRTVOIRCANFFFIT_M_str_FamD_Uis_R32 0.3134 PICUTCHT_MIRTVOIRCANFFFIT_M_str_FamD_Uis_R32 0.3134 PICUTCHT_MIRTVOIRCANFFFIT_M_str_FamD_Uis_R32 0.3134 PICUTCHT_MIRTVOIRCANFFIT_M_str_FamD_Uis_R32 0.3134 PICUTCHT_MIRTVOIRCANFFIT_M_ST_FAMD_UIS_R32 0.35999985 PICUTCHT_MIRTVOIRCANFFIT_M_ST_FAMD_UIS_R32 0.359999985 PICUTCHT_MIRTVOIRCANFFIT_M_ST_FAMD_UIS_R32 0.364999962 PICUTCHT_MIRTVOIRCANFFIT_M_ST_FAMD_UIS_R32 0.364999962 PICUTCHT_MIRTVOIRCANFFIT_M_ST_FAMD_UIS_R32 0.37299999 PICUTCHT_MIRTVOIRCANFFIT_M_ST_FAMD_UIS_R32 0.37299999 PICUTCHT_MIRTVOIRCANFFIT_M_ST_FAMD_UIS_R32 0.364		
PICUTCHI Introumbascaffuxatato Uis M. 32 0.75700029 PICUTCHI MITCUTDASCAFfuxatato Uis M. 152 0.046999994 PICUTCHI MITCUTDASCAFfuxatato Uis M. 152 0.046999994 PICUTCHI MITVECUFIIL M. str. Previnput_Uis 132 1118 PICUTCHI MITVECUFIIL M. str. Previnput_Uis 132 828.23999 PICUTCHI MITVECUFIIL M. str. TermD, Uis 132 53.2500003 PICUTCHI MITVECUFIIL M. str. TermD, Uis 132 0.3134 PICUTCHI MITVECUFIIL M. str. TermD, Uis 132 1118 PICUTCHI MITVORIOAXFFFIIL M. str. Previnput_Uis 132 118 PICUTCHI MITVORIOAXFFFIIL M. str. TermD, Uis 132 826.23999 PICUTCHI MITVORIOAXFFFIIL M. str. TermD, Uis 132 53.2500003 PICUTCHI MITVORIOAXFFFIIL M. str. TermD, Uis 132 53.2500003 PICUTCHI MITVORIOAXFFFIIL M. str. TermD, Uis 132 0.3134 PICUTCHI MITVORIOAXFFFIIL M. str. TermD, Uis 132 0.3134 SlowDataAccessBufindex, Crit. M. Uif		
PICurrCntf MtrCurrDaxSafFiuxRatio_Uls_M_f32 0.07660003 PICurrCntf MtrCurrGaxSafFiuxRatio_Uls_M_f32 0.0466999984 PICurrCntf MtrVeculift_M_str Pervloput_Uls_f32 1118 PICurrCntf MtrVeculift_M_str Pervloput_Uls_f32 32.623999 PICurrCntf MtrVeculift_M_str TermD_Uls_f32 53.259003 PICurrCntf MtrVeculift_M_str TermD_Uls_f32 53.259003 PICurrCntf MtrVeculift_M_str TermD_Uls_f32 0.3134 PICurrCntf MtrVeculift_M_str TermD_Uls_f32 1118 PICurrCntf MtrVelQaxFFFiit_M_str TermD_Uls_f32 826.23999 PICurrCntf MtrVelQaxFFFiit_M_str TermD_Uls_f32 826.23990 PICurrCntf MtrVelQaxFFFiit_M_str TermD_Uls_f32 826.23990 PICurrCntf MtrVelQaxFFFiit_M_str TermD_Uls_f32 83.2509003 PICurrCntf MtrVelQaxFFFiit_M_str TermD_Uls_f32 0.3134 PICurrCntf MtrVelQaxFfiit_M_velt_f32 0.680999985 PICurrCntf MtrVelQaxFfiit_M_velt_f32 0.680999962 PICurrCntf MtrVelQaxFfiit_M_velt_f32 0.3134 PICurrCntf MtrVelQaxFfiit_M_velt_f32 0.3134 PICurrCntf MtrVelQaxFfiit_M_velt_f32 0.3134 PICurrCntf MtrVelQaxFfiit_M_velt_f32 0.3134 PICurrCntf MtrVelQaxFfiit_M_velt_f32 0	PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851000011
PICurrCntr MtrCurrQaxSatFluxRatio_Uls_M_132	PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.757000029
PICurrCntr MtrVecuFilt M_ str. Previnput_Uis_f32	PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.070600003
PICurrCntr Mrt/vecuFilt M_str.PrevOutput_UIs_132 826.23999 PICurrCntr Mrt/vecuFilt M_str.TermN_UIs_132 53.2599003 PICurrCntr Mrt/vecuFilt M_str.PrevIput_UIs_132 1118 PICurrCntr Mrt/voltQaxFFFilt M_str.PrevIput_UIs_132 1118 PICurrCntr Mrt/voltQaxFFFilt M_str.PrevOutput_UIs_132 826.23999 PICurrCntr Mrt/voltQaxFFFilt M_str.PrevOutput_UIs_132 826.23999 PICurrCntr Mrt/voltQaxFFFilt M_str.PrevOutput_UIs_132 53.2599003 PICurrCntr Mrt/voltQaxFFFilt M_str.TermD_UIs_132 0.3134 SlowDataAccessBufindex_Cnt. M_u16 0 C_LOAF AbackSignalSclFesiev_UIspS_132 2409.9495 K_LOAF AbackSignalSclFesiev_UIspS_132 4854.70996 K_MtrCtrICurrLoopSecOfTranFcEnable_Cnt.lgc 1 K_MtrCtrIPeabeAcControlIbsable_Cnt.lgc 1 K_MtrCtrIPeabeAcControlIbsable_Cnt.lgc 1 K_MtrCtrIPeabeAcControlIbsable_Cnt.lgc 0 K_MtrCtrIVirualResDax_Ohm_132 0.163000003 K_MtrCurrQaxRefModifBs_Cnt.lgc 0 K_MtrCvIDaxRefModifBs_Cnt.lgc 0 K_MtrVolDaxIntegHilm_Volt_132 0.6499962 K_MtrVolDaxIntegHilm_Volt_132 0.6499962 K_MtrVolDaxIntegHilm_Volt_132 0.6499962 K_MtrVolQaxFiltFEnable_Cnt.lgc 0 K_MtrVolQaxFiltFEnable_Cnt.lgc 0 K_MtrVolQaxIntegHilm_Volt_132 0.6499962 K_MtrVolQaxIntegHilm_Volt_132 0.64999962 K_MtrVolQaxIntegHilm_Volt_132 0.64999962 K_MtrVolQaxIntegHilm_Volt_132 0.64999962 K_MtrVolQaxIntegHilm_Volt_132 0.9729999 K_UotsaDaxPolyCoeff_UIs_132 0.9729999 K_UotsaDaxPolyCoeff_UIs_132 0.9729999 K_UotsaDaxPolyCoeff_UIs_132 0.9729999 K_CommOffsetTbiX_UIs_usp13[1] 5579 CommOffsetTbiX_UIs_usp13[1] 5579 CommOffsetTbiY_Cnt_u16[0] 2000 CommOffsetTbiY_Cnt_u16[0] 2000 target_MtrCntl_Read_MotlodkSriComSvcDft_Cnt_Igc_Jtr 2000 target_MtrCntl_Read_MotlodkSriComSvcDft_Cnt_Igc_Jtr 2000 target_MtrCntl_Read_MotlodkSriComSvcDft_Cnt_Igc_Jtr 2000 target_MtrCntl_Read_MotlodkSriComSvcDft_Cnt_Igc_Jtr 2000 target_MtrCntl_Read_MotlodkSriComSv	PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MrtVecuFilt_M_str.PrevOutput_Uis_f32 826.23999 PICurrCntrl_MrvecuFilt_M_str.TermN_Uis_f32 0.3134 PICurrCntrl_MrtVecuFilt_M_str.PrevInput_Uis_f32 1118 PICurrCntrl_MrtVoltQaxFFFIIt_M_str.PrevInput_Uis_f32 1118 PICurrCntrl_MrtVoltQaxFFFIIt_M_str.PrevInput_Uis_f32 826.23999 PICurrCntrl_MrtVoltQaxFFFIIt_M_str.TermD_Uis_f32 53.2509003 PICurrCntrl_MrtVoltQaxFFFIIt_M_str.TermD_Uis_f32 0.3134 SlowDataAccessBufindex_Cnt_M_u16 0 K_CLOxFrabackSignalSclFacise_UispS_f32 2409.9495 K_LICAFrabackSignalSclFacise_UispS_f32 4854.70996 K_MrtCritCurrLoxpSecOfTranFcEnable_Cnt_ligc 1 K_MrtCritVirualResDax_Ohm_f32 0.0359999985 K_MrtCritVirualResDax_Ohm_f32 0.163000003 K_MrtCritVirualResDax_Ohm_f32 0.163000003 K_MrtVolDaxIntegHiLm_Volt_f32 20.7133999 K_MrtVolDaxIntegHiLm_Volt_f32 9.64999962 K_MrtVolQaxFiltFFEnable_Cnt_ligc 0 K_MrtVolQaxFiltegLolim_Volt_f32 9.64999962 K_MrtVolQaxFiltegLolim_Volt_f32 9.64999962 K_MrtVolQaxFiltegLolim_Volt_f32 9.64999962 K_VoltSaQax	PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	1118
PICurrCntr MitVecuFilt M str.TermD Uls f32 0.3134 PICurrCntr MitVoltQuAFFFIIT M str.TermD Uls f32 1118 PICurrCntr MitVoltQuAFFFIIT M str.PrevOutput Uls f32 826 2399 PICurrCntr MitVoltQuAFFFIIT M str.PrevOutput Uls f32 826 2399 PICurrCntr MitVoltQuAFFFIIT M str.TermD Uls f32 53 2509003 PICurrCntr MitVoltQuAFFFIIT M str.TermD Uls f32 53 2509003 PICurrCntr MitVoltQuAFFFIIT M str.TermD Uls f32 0.3134 SlowDataAccessButindex Cnt.M U16 0		
PICurrCntri_MtrVeuGriit_M_str.TermD_UIs_f32		
PICurrCntri_MirVoilQaxFFFiit_M_str.PrevOutput_Uis_f32		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32		
PICurrCntrl_MtrVoltQaxFFFit_M_str.TermN_Uls_f32 53.2509003 PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0.3134 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSciFacSlew_UlspS_f32 2409.94995 k_ILOAFdbackSignalSciFacSlew_UlspS_f32 4854.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.359999985 k_MtrCtrlVirualResDax_Ohm_f32 0.163000003 k_MtrCurrQaxRefModiffDsb_Cnt_lgc 0 k_MtrVoltTQaxRefModiffDsb_Cnt_lgc 0 k_MtrVoltDaxInteghLiLim_Volt_f32 20.713399 k_MtrVoltDaxInteghLiLim_Volt_f32 9.64999962 k_MtrVoltQaxInteghLiLim_Volt_f32 9.64999962 k_MtrVoltQaxInteghLotim_Volt_f32 9.64999962 k_MtrVoltQaxInteglotim_Volt_f32 9.64999962 k_MtrVoltQaxInteglotim_Volt_f32 9.64999962 k_VoltSatDaxPolyCoeff_Uls_f32 -10.008004 k_VoltSatDaxPolyCoeff_Uls_f32 -10.008004 k_VoltSatQaxPolyCoeff_Uls_f32 9.64999992 t_CeadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbX_Uls_u3p13[0] 4611 t_CommO		
PICurrCntrl_MtrVoltQaxFFIit_M_str.TermD_Uls_f32 0.3134 SlowDataAccessBufindex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 2409.94995 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 4854.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.035999985 k_MtrCtrlVirualResQax_Ohm_f32 0.163000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifIpIEn_Cnt_lgc 0 k_MtrVolIDaxIntegHiLim_Volt_f32 20.7133999 k_MtrVolIDaxIntegHiLim_Volt_f32 9.64999962 k_MtrVolIQaxIntegLoLim_Volt_f32 12.3408003 k_MtrVolIQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVolIQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVolIQaxIntegLoLim_Volt_f32 0 k_MtrVolIQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVolIQaxIntegLoLim_Volt_f32 0 k_VollSatDaxPolyCoeff_Uls_f32 10.0080004 k_VollSatQaxPolyCoeff_Uls_f32 22.6299992 k_deadtimevScale_Uls_f32 0.972999999 k_CommoffsetTblX_Uls_u3p13[0]		
SlowDataAccessBuflindex_Cnt_M_u16 0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 2409.94995 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 4854.70996 k_MtrCtrlCurrLoopSecOrtranFcEnable_Cnt_lgc 1 k_MtrCtrlYirualResDax_Ohm_f32 0.0359999985 k_MtrCtrlVirualResDax_Ohm_f32 0.163000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHill_m_Volt_f32 20.7133999 k_MtrVoltDaxIntegHill_m_Volt_f32 20.7133999 k_MtrVoltQaxIntegHill_m_Volt_f32 9.64999962 k_MtrVoltQaxIntegHill_m_Volt_f32 12.3408003 k_MtrVoltQaxIntegHill_m_Volt_f32 9.64999962 k_MtrVoltQaxIntegLol_im_Volt_f32 9.64999962 k_MtrVoltQaxIntegLol_im_Volt_f32 9.64999962 k_MtrVoltQaxIntegLol_im_Volt_f32 9.64999962 k_MtrVoltQaxIntegLol_im_Volt_f32 9.64999962 k_VoltSatQaxPolyCoeff_Uls_f32 9.64999962 k_VoltSatQaxPolyCoeff_Uls_f32 9.64999962 k_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[0] <td>PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32</td> <td>0.3134</td>	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.3134
k_ILOAFdbackSignalScIFacSlew_UlspS_f32 4854.70996 k_MtrCtIrCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtIrGreedbackControlIpsable_Cnt_lgc 1 k_MtrCtrIVirualResDax_Ohm_f32 0.0359999985 k_MtrCtrIVirualResQax_Ohm_f32 0.163000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDraxRefModifBspIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_volt_f32 20.7133999 k_MtrVoltDaxIntegLoLim_volt_f32 9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_volt_f32 12.3408003 k_MtrVoltQustIntegLoLim_volt_f32 9.64999962 k_MtrVoltQustIntegLoLim_volt_f32 19.64999962 k_MtrVoltQustIntegLoLim_volt_f32 10.080003 k_MtrVoltQustIntenable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 10.080004 k_VoltSatQaxPolyCoeff_Uls_f32 22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000	SlowDataAccessBufIndex_Cnt_M_u16	0
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.0359999985 k_MtrCtrlVirualResDax_Ohm_f32 0.16300003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 20.7133999 k_MtrVoltDaxIntegHiLim_Volt_f32 9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHolLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltVevoLifIlEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 9.64999962 k_VoltSatQaxPolyCoeff_Uls_f32 10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_uls_ulsp13[0] 4611 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 t_CommOffsetTbIY_Cnt_u16[4] 2000 t_commOffsetTbIY_Cnt_u16[4] 2000 t	k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2409.94995
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.0359999985 k_MtrCtrlVirualResDax_Ohm_f32 0.16300003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 20.7133999 k_MtrVoltDaxIntegHiLim_Volt_f32 9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHolLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltVevoLifIlEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 9.64999962 k_VoltSatQaxPolyCoeff_Uls_f32 10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_uls_ulsp13[0] 4611 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 t_CommOffsetTbIY_Cnt_u16[4] 2000 t_commOffsetTbIY_Cnt_u16[4] 2000 t	k ILOAFdbackSignalSclFacSlew UlspS f32	4854.70996
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.0359999985 k_MtrCtrVirualResQax_Ohm_f32 0.163000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHilim_Volt_f32 20.7133999 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHilim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Solt_f32 10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 22.6299992 k_deadtimeVScale_Uls_f32 9.7299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 t_commOffsetTbIY_Cnt_u16[1] 2000 t_commOffsetTbIY_Cnt_u16[1] 2000 t_co		
k_MtrCtrlVirualResDax_Ohm_f32 0.0359999985 k_MtrCtrlQxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 20.7133999 k_MtrVoltDaxIntegLedLim_Volt_f32 20.7133999 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegHill_m_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32		
k_MtrCtrlVirualResQax_Ohm_f32 0.163000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegLotim_Volt_f32 20.7133999 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.6499962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegLoLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 0 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deattimeVScale_Uls_f32 0.97299999 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_lvtrLoaMtgtmEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 20.7133999 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiftFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVourSittEnable_Cnt_lgc 0 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 20.7133999 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiftFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
k_MtrVoltDaxIntegHiLim_Volt_f32 20.7133999 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFitFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFitFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
k_MtrVoltQaxFittFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0	k_MtrVoltDaxIntegHiLim_Volt_f32	20.7133999
k_MtrVoltQaxIntegHiLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0	k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxIntegHiLim_Volt_f32 12.3408003 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0	k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		12.3408003
k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
k_VoltSatDaxPolyCoeff_Uls_f32 -10.0080004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 2000 t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
k_VoltSatQaxPolyCoeff_Uls_f32 -22.6299992 k_deadtimeVScale_Uls_f32 0.97299999 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
k_deadtimeVScale_UIs_f32 0.97299999 t_CommOffsetTblX_UIs_u3p13[0] 4611 t_CommOffsetTblX_UIs_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
t_CommOffsetTblX_UIs_u3p13[0] 4611 t_CommOffsetTblX_UIs_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
t_CommOffsetTblX_Uls_u3p13[1]		
t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0	t_CommOffsetTblX_Uls_u3p13[1]	5579
t_CommOffsetTbIY_Cnt_u16[1] 2000 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0	t_CommOffsetTblY_Cnt_u16[0]	2000
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0		2000
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
V =		
	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 0		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2515		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2000	2000	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63766	63766 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-88.0050049	-88.0049973 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.0192687921	0.0192687996 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	20.7832718	20.7833004 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27809	27809 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-88.0050049	-88.0049973 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.118 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.703999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-685.018005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-140.973999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.24399996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83899999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	584.664978
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	935.218018
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.55999994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.8330002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.61900043
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.1560001
MtrCtrl_Vecu_Volt_M_f32[0]	17.2600002
MtrCtrl_Vecu_Volt_M_f32[1]	19.6200008
MtrCurrDaxPrevIntg_Volt_M_f32	14.2440004
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	20.6149998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	30.4113998		
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	6.09100008		
MtrPosComputationDelay_Rad_M_f32[1]	3.83599997		
MtrVoltDax_Volt_M_f32[0]	-8.61900043		
MtrVoltDax_Volt_M_f32[1]	-13.1560001		
MtrVoltQax_Volt_M_f32[0]	-8.61900043		
MtrVoltQax_Volt_M_f32[1]	-13.1560001		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.237000003		
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.150000006 0.483900011		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000011		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	50.986599		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.889199972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50.986599		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.889199972		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6110.83008		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7208.8501		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.172999993		
k_MtrCtrlVirualResQax_Ohm_f32	0.189999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.9330006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	23.8327999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	17.1749992		
k_VoltSatQaxPolyCoeff_Uls_f32	8.79699993		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1]	49 735		
t_Commomset1blY_Cnt_u16[1] target MtrCntrl Read IvtrLoaMtgtnEn Cnt lgc ptr	1		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	0		
target MtrCntrl Read MtrCurrDax Amp f32 Val	83.9489975		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	3557		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	- Nosait
MtrCntrl Write ModIdx Uls u16p16(val)	64946	64946 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.4397964	-16.4398003 ± 4.88E-04	_
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-10.3817005	-10.3816996 ± 4.88E-04	
			-
MITCHITI Write PhaseAdvanceFinal Rev uud 16(Val)	17752	17752 ± 1.52588E-05	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg Volt M f32		17752 ± 1.52588E-05 0 ± 0.0625	Ž
	17752		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value	
FastDataAccessBufIndex Cnt M u16	1	
VtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
VitrCntrl_Read_INtribatinigtriEll_Cnt_igc(Vti) VitrCntrl_Read_ModidxSrlComSvcDft_Cnt_igc(Val)		
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.010999999	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.131999999	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.051	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	781.679016	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1013.59998	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
ItrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.495000005	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.11000001	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-786.575989	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	755.47699	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008	
/ltrCtrl_Vecu_Volt_M_f32[0]	16.4099998	
/trCtrl_Vecu_Volt_M_f32[1]	18.7700005	
/ltrCurrDaxPrevIntg_Volt_M_f32	26.7269993	
/ltrCurrDaxRef_Amp_M_f32[0]	31.5869999	
/ltrCurrDaxRef_Amp_M_f32[1]	-186.395996	
/trCurrQaxCog_Amp_M_f32	-144.667007	
ftrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	
/trCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	
/ltrCurrQaxPrevIntg_Volt_M_f32	30.6539001	
ltrCurrQaxRef_Amp_M_f32[0]	171.485992	
ltrCurrQaxRef_Amp_M_f32[1]	163.787003	
htrCurrQaxRpl_Amp_M_f32[0]	37.4550018	
/ltrCurrQaxRpl_Amp_M_f32[1]	-2.84500003	
htrPosComputationDelay_Rad_M_f32[0]	4.46799994	
htrPosComputationDelay_Rad_M_f32[1]	2.6400001	
htrVoltDax_Volt_M_f32[0]	-7.66699982	
//dtrVoltDax_Volt_M_f32[1]	2.61400008	
//trVoltQax_Volt_M_f32[0]	-7.66699982	
MtrVoltQax_Volt_M_f32[1]	2.61400008	

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



		•	
Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.980000019		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.840300024		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	51.9599991		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.70389998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	51.9599991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.70389998		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	754.531982		
k ILOAFdbackSignalSclFacSlew UlspS f32	248.589005		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.179000005		
k MtrCtrlVirualResQax Ohm f32	0.158999994		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.8127003		
k MtrVoltDaxIntegLoLim Volt f32	-8.68999958		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.2922001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-23.691		
k_VoltSatQaxPolyCoeff_Uls_f32	-14.6350002		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	220		
t_CommOffsetTblX_Uls_u3p13[1]	5037		
t_CommOffsetTblY_Cnt_u16[0]	671		
t CommOffsetTblY Cnt u16[1]	876		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	256		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	876	876	
MtrCntrl Write Modldx Uls u16p16(val)	63635	63635 ± 1	✓
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	220	220 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.90307331	8.90305042 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	15.9031553	15.9032001 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32859	32859 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~		
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•		
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•		
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•		
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•		
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•		
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~		
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•		
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~		
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	•		
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~		
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•		

-2.84500003

-2.84500003 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.120 (Repeat Count = 1)	√
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrChtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-213.026993 -66.7229996
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.77100003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.31400001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	4.21999979
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	387.277008
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl MtrImpedDax Ohm M f32[1]	0.115999997 0.115999997
MtrCtrl MtrImpedDax_Onm_M_132[1] MtrCtrl MtrImpedQax Ohm M f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.02700000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.25800002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.36399996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	860.961975
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-245.580002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1]	5
MtrCurrDaxPrevIntg Volt M f32	-1.17400002
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	23.6063004
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0] MtrCurrQaxRpl_Amp_M_f32[1]	-105.246002 41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	4.70499992
MtrPosComputationDelay_Rad_M_f32[1]	-2.8670001
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.10999999
PICurrCotrl_InverterFailSclFac_UIs_M_f32	0.00400000019
PICurrCotrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.703999996
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	0.0469999984 -657.130005
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	107.129997
PICurrCntrl MtrVecuFilt M str.TermN UIs f32	71.2244034
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.953999996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	107.129997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	71.2244034
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.953999996
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3550.11011
k_ILOAFdbackSignalSclFacSlew_UlspS_f32 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	5873.56006 1
k MtrCtrlFeedbackControlDisable Cnt lgc	1
k MtrCtrlVirualResDax Ohm f32	0.0240000002
k_MtrCtrlVirualResQax_Ohm_f32	0.0109999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k MtrVoltDaxIntegHiLim Volt f32	16.5436993

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.1534996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	17.4050007		
k_VoltSatQaxPolyCoeff_Uls_f32	-2.23099995		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTbIX_Uls_u3p13[0]	573		
t_CommOffsetTbIX_Uls_u3p13[1]	7569		
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4516		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1211	1211	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63111	63111 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.00506430957	0.00506431004 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.8149972	4.81500006 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49086	49086 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.121 (Repeat Count = 1)		V
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.975000024	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69599998	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	585.619019	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-791.551025	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998	





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0529999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.456999987		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.693000019		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1016.78003		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	915.791992		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] AttrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-13.6160002 18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl MtrVoltQaxFF Volt M f32[1]	-23.1870003		
MtrCtrl_Vecu_Volt_M_f32[0]	31		
MtrCtrl_Vecu_Volt_M_f32[1]	31		
MtrCurrDaxPrevIntg_Volt_M_f32	-16.7709999		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	4.80830002		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	-2.35100007		
MtrPosComputationDelay_Rad_M_f32[1]	-1.33299994		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.1870003		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.658999979		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006		
PICurrCotrl_MtrCurrCoxSatFluxRatio_Uls_M_f32	0.824599981 0.725000024		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	96.5500031		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	27.4986992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.75029999		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	96.5500031		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	27.4986992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.75029999		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6301.31982		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3999.36011		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.00899999961		
k_MtrCtrlVirualResQax_Ohm_f32	0.0469999984		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.9073009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	21.2607994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	19.5340004		
k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32	10.6110001 0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	1154		
t_CommOffsetTblX_Uls_u3p13[1]	5284		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	153		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	56845	56845 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.6160002	-13.6160002 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-23.1870003	-23.1870003 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24403	24403 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.122 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.533999979	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.231999993	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	322.946991	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	998.633972	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.869000018	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.70599997	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	53.6450005	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-178.399002	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008	
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002	
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008	
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998	
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999	
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996	
MtrCurrQaxCog_Amp_M_f32	-144.667007	
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	
MtrCurrQaxPrevIntg_Volt_M_f32	18.9440002	
MtrCurrQaxRef_Amp_M_f32[0]	171.485992	

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value Name MtrCurrQaxRef_Amp_M_f32[1] 163.787003 MtrCurrQaxRpl_Amp_M_f32[0] 37.4550018 MtrCurrQaxRpl_Amp_M_f32[1] -2.84500003 MtrPosComputationDelay_Rad_M_f32[0] 3.41799998 MtrPosComputationDelay_Rad_M_f32[1] 3.54200006 MtrVoltDax_Volt_M_f32[0] -7.66699982 MtrVoltDax_Volt_M_f32[1] 2.61400008 MtrVoltQax_Volt_M_f32[0] -7.66699982 MtrVoltQax_Volt_M_f32[1] 2.61400008 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.944000006 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.465999991 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.403899997 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 386 220001 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 13.2960997 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.640799999 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 386.220001 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 13.2960997 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.640799999 SlowDataAccessBufIndex_Cnt_M_u16 5388 75 $k_CLOAFdbackSignalSclFacSlew_UlspS_f32$ k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3076.13989 k MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ 0 k MtrCtrlVirualResDax Ohm f32 0.178000003 k_MtrCtrlVirualResQax_Ohm_f32 0.181999996 k MtrCurrQaxRefModifDsb Cnt lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k MtrVoltDaxIntegHiLim Volt f32 19.5997009 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 k MtrVoltQaxFiltFFEnable Cnt lgc k_MtrVoltQaxIntegHiLim_Volt_f32 6.4369998 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 16.1879997 k_VoltSatQaxPolyCoeff_Uls_f32 0.165000007 k_deadtimeVScale_Uls_f32 0.954999983 t_CommOffsetTblX_Uls_u3p13[0] 5022 t_CommOffsetTblX_Uls_u3p13[1] 7003 t CommOffsetTblY_Cnt_u16[0] 63 t_CommOffsetTblY_Cnt_u16[1] 327 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -9 31999969 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 998 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9 31999969 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 998 998 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 220 220 ± 7.81E-03 5.79688215 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.79687977 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -29.0319138 -29.0319004 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 2121 2121 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 ± 0.0625 220 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[0] 220 MtrCurrQaxFinalRef_Amp_M_f32[1] -2.84500003 -2.84500003 ± 7.81E-03



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
AtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl Read_MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
	-66,7229996
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0.112999998
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.076999996
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.035999985
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.31799989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53199995
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-952.169983
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-35.3190002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0149999997
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.43999998
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-452.992004
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	297.122009
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
/ltrCtrl_Vecu_Volt_M_f32[0]	14.243
/trCtrl_Vecu_Volt_M_f32[1]	16.6030006
/ltrCurrDaxPrevIntg_Volt_M_f32	-27.6930008
/trCurrDaxRef_Amp_M_f32[0]	-65.1900024
/trCurrDaxRef_Amp_M_f32[1]	-216.972
/trCurrQaxCog_Amp_M_f32	5.72399998
/trCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
/trCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
/trCurrQaxPrevIntg_Volt_M_f32	2.83229995
/trCurrQaxRef_Amp_M_f32[0]	31.5869999
1trCurrQaxRef_Amp_M_f32[1]	-186.395996
/trCurrQaxRpl_Amp_M_f32[0]	-105.246002
//trCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	-5.51200008
MtrPosComputationDelay_Rad_M_f32[1]	3.42700005
MtrVoltDax_Volt_M_f32[0]	-28.2420006
/trVoltDax Volt M f32[1]	12.6160002

PICurrCntrl_Per1



Input Value		
-28.2420006		
12.6160002		
0.25999999		
0.0450000018		
0.464599997		
0.0469999984		
-784.130005		
-43.1699982		
99.8274994		
0.052099999		
-784.130005		
-43.1699982		
99.8274994		
0.052099999		
1		
7174.7002		
1940.90002		
1		
1		
0.0810000002		
0.159999996		
0		
0		
2.89389992		
-11.6000004		
0		
6.79160023		
-11.6000004		
0		
-22.066		
-1.36500001		
0.970000029		
889		
1		
-161.352005		
926		
	Expected Value	Result
	· ·	- Toodic
8.55099964	8.55099964 ± 4.88E-04	
J.55555555	0.0000000T 1 4.00L-04	_
12 6160002	12 6160002 ± 4 88 = 04	
12.6160002	12.6160002 ± 4.88E-04	
41958	41958 ± 1.52588E-05	· ·
	-28.2420006 12.6160002 0.25999999 0.0450000018 0.464599997 0.0469999984 -784.130005 -43.1699982 99.8274994 0.052099999 -784.130005 -43.1699982 99.8274994 0.052099999 1 7174.7002 1940.90002 1 1 1 0.0810000002 0.159999996 0 0 2.89389992 -11.6000004 0 6.79160023 -11.6000004 0 6.79160023 -11.6000004 0 -22.066 -1.36500001 0.97000029 4611 5579 889 1543 0 0 1 -161.352005 926 -161.352005 1 Actual Value 1543 60159 -220	-28.2420006 12.6160002 0.2599999 0.0450000018 0.646599997 0.046999984 -784.130005 -43.169982 99.8274994 0.05209999 -784.130005 -43.169982 99.8274994 0.05209999 1 7774.7002 1940.90002 1 1 1 0.0810000002 0.159999996 0 0 2.89389992 -11.6000004 0 6.79160023 -11.6000004 0 -22.066 -1.36500001 0.970000029 4611 5579 889 1543 0 0 0 1 1 -161.352005 926 -161.352005 1 Actual Value Expected Value 1543 60159 60159 1-220 +7.81E-03

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.124 (Repeat Count = 1)	Innut Value	·
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16 MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	1 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target_intrCntrl_Read_NotIdxSrlComSvcDft Cnt lgc Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.507000029	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53999996	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	330.04599 -179.259003	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0329999968	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.230000004	
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.53799999	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-818.869995	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	353.450989	
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-0.736000001	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003	
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999	
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996	
MtrCurrDaxPrevIntg_Volt_M_f32	20.066	
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007	
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001	
MtrCurrQaxCog_Amp_M_f32	59.3040009	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	
MtrCurrQaxPrevIntg_Volt_M_f32	25.5028	
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006	
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035 -213.026993	
MtrCurrQaxRpl_Amp_M_f32[0] MtrCurrQaxRpl Amp M f32[1]	-66.7229996	
MtrPosComputationDelay_Rad_M_f32[0]	2.3970007	
MtrPosComputationDelay Rad M f32[1]	2.73799992	
MtrVoltDax Volt M f32[0]	18.6380005	
MtrVoltDax_Volt_M_f32[1]	-23.1870003	
MtrVoltQax_Volt_M_f32[0]	18.6380005	
MtrVoltQax_Volt_M_f32[1]	-23.1870003	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.185000002	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.39899989	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0604999997	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	947.73999	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.48909998	
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.176699996	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	947.73999	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	1.48909998	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.176699996	
SlowDataAccessBufIndex_Cnt_M_u16	0	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3789.18994	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3865.98999 1	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0	
	0.0710000023	
K MtrCtrivirualResDax Onm 132		
k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32	0.048999987	





		•	
Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.557200015		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.7143993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-9.73099995		
k_VoltSatQaxPolyCoeff_Uls_f32	-24.3579998		
k_deadtimeVScale_UIs_f32	0.966000021		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	474		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	474	474	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-8.04187298	-8.04187012 ± 4.88E-04	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	28.8459892	28.8460007 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	25723	25723 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	✓

Actual Function	Count	Expected Function	Count	Popult
	Count	•	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.125 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.822000027
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.41999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-292.269989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-754.054993
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.871999979
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.22000003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	688.346985
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-854.249023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-7.71299982
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	17.9473
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-1.56599998
MtrPosComputationDelay_Rad_M_f32[1]	1.21399999
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.723999977
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.111000001
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0421999991
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	75.4738007
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.306199998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	75.4738007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.306199998
SlowDataAccessBufIndex_Cnt_M_u16	0
k CLOAFdbackSignalSclFacSlew UlspS f32	3827.27002
k ILOAFdbackSignalSclFacSlew UlspS f32	2156.63989
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0
k MtrCtrlFeedbackControlDisable Cnt Igc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.185000002
k_MtrCtrlVirualResQax_Ohm_f32	0.079999982
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	2.9461
k MtrVoltDaxIntegLoLim Volt f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k MtrVoltQaxIntegHiLim Volt f32	19.4281006
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	17.6229992
k_VoltSatQaxPolyCoeff_Uls_f32	-20.6590004
k_deadtimeVScale_Uls_f32	0.968999982
t_CommOffsetTbIX_Uls_u3p13[0]	2638
t_CommOffsetTblX_Uls_u3p13[1]	3628
t_CommOffsetTblY_Cnt_u16[0]	297
t_CommOffsetTblY_Cnt_u16[1]	1110
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848

PICurrCntrl_Per1



Name	Input Value		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	2994		
target MtrCntrl Read MtrCurrQax Amp f32 Val	-118.848		
target MtrCntrl Read SysState Cnt Enum Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2994	2994	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.29543591	-4.2954402 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.24126196	-2.24126005 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27802	27802 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.126 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.90799999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.08500004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-899.770996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	697.859009
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.48399997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.526000023
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-99.262001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	138.542007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	28.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	30.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001





Name	Input Value			
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024	-65.1900024		
MtrCurrDaxRef_Amp_M_f32[1]	-216.972	-216.972		
MtrCurrQaxCog_Amp_M_f32	5.72399998			
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002			
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016			
MtrCurrQaxPrevIntg_Volt_M_f32	7.55770016			
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999			
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl Amp M f32[0]	-186.395996 -105.246002			
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016			
MtrPosComputationDelay_Rad_M_f32[0]	1.403			
MtrPosComputationDelay_Rad_M_f32[1]	-4.98400021			
MtrVoltDax_Volt_M_f32[0]	-28.2420006			
MtrVoltDax_Volt_M_f32[1]	12.6160002			
MtrVoltQax_Volt_M_f32[0]	-28.2420006			
MtrVoltQax_Volt_M_f32[1]	12.6160002			
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977			
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.816999972			
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.848399997			
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984			
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005			
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118			
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	52.3392982			
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404900014			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-340.130005			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	52.3392982			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.404900014			
SlowDataAccessBufIndex_Cnt_M_u16		0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32		208.033005		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5			
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1			
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1			
k_MtrCtrlVirualResDax_Ohm_f32	0.0909999982	0.019999996		
k_MtrCurrOovPotModifDob_Cot_loo	0.0199999996			
k_MtrCurrQaxRefModifDsb_Cnt_lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1			
k_MtrVoltDaxIntegHiLim_Volt_f32	9.36629963			
k MtrVoltDaxIntegLoLim Volt f32	-22.4099998			
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1			
k_MtrVoltQaxIntegHiLim_Volt_f32	10.1091003			
k MtrVoltQaxInteqLoLim Volt f32	-22.4099998			
k MtrVoltVecuFiltEnable Cnt lgc	1			
k_VoltSatDaxPolyCoeff_Uls_f32	2.421			
k_VoltSatQaxPolyCoeff_Uls_f32	-11.9060001			
k_deadtimeVScale_Uls_f32	0.977999985			
t_CommOffsetTblX_Uls_u3p13[0]	1212			
t_CommOffsetTblX_Uls_u3p13[1]	1704			
t_CommOffsetTblY_Cnt_u16[0]	23			
t_CommOffsetTblY_Cnt_u16[1]	212			
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0			
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1			
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0			
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009			
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3747			
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008			
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1			
Name	Actual Value	Expected Value	Result	
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3747	3747	~	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	30.219759	30.2198009 ± 4.88E-04	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.43871307	2.43870997 ± 4.88E-04	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	30178	30178 ± 1.52588E-05	*	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	V	
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	V	
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	<u> </u>	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
AtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
/trCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
/trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl Read_MtrCurrOffComOffset Cnt u16 ptr
/trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
	-205.085007
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0.079999982
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.98000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.097
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-313.263
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	882.630981
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.958000004
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.437000006
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	89.8040009
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	673.749023
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
/trCtrl_Vecu_Volt_M_f32[0]	17.7010002
/trCtrl_Vecu_Volt_M_f32[1]	20.0610008
/trCurrDaxPrevIntg_Volt_M_f32	-9.05200005
/trCurrDaxRef_Amp_M_f32[0]	-146.723007
/trCurrDaxRef_Amp_M_f32[1]	-121.943001
/ltrCurrQaxCog_Amp_M_f32	59.3040009
/trCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
/trCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
/trCurrQaxPrevIntg_Volt_M_f32	-22.7238007
/trCurrQaxRef_Amp_M_f32[0]	-133.947006
/trCurrQaxRef_Amp_M_f32[1]	75.7020035
/trCurrQaxRpl_Amp_M_f32[0]	-213.026993
/trCurrQaxRpl_Amp_M_f32[1]	-66.7229996
/trPosComputationDelay Rad M f32[0]	-2.704
/trPosComputationDelay Rad M f32[1]	-2.66799998
/trVoltDax Volt M f32[0]	18.6380005
MtrVoltDax Volt M f32[1]	-23.1870003



PICurrCntrl_	Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
	-23.1870003		
MtrVoltQax_Volt_M_f32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.657599986		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	84.4263992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404500008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	84.4263992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.404500008		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	6145.56982		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lqc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.050999999		
k MtrCtrlVirualResQax Ohm f32	0.0270000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.1113005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.5658998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6089993		
k_VoltSatQaxPolyCoeff_Uls_f32	1.55499995		
k_deadtimeVScale_UIs_f32	0.996999979		
t_CommOffsetTbIX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4791		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65339	65339 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.0475826	-13.0475998 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-15.1589346	-15.1589003 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12352	12352 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-8.68999958	-8.68999958 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•



Test Step 2.128 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.24300003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98800004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-222.714996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	849.533997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.041999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.584999979
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.638000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-338.394012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	815.107971
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	27.7875996
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	4.24399996
MtrPosComputationDelay_Rad_M_f32[1]	1.44299996
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.777999997
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.499000013
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.389899999
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	76.5533981
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0997999981
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	76.5533981
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0997999981
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6667.54004
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7823.27002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.194999993
k_MtrCtrlVirualResQax_Ohm_f32	0.142000005
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1

PICurrCntrl_Per1



Name	Input Value		
k MtrCurrQaxRefModifRplEn Cnt Igc	0		
k MtrVoltDaxIntegHiLim Volt f32	6.06680012		
k MtrVoltDaxIntegLoLim Volt f32	-4.57000017		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	24.2038002		
k MtrVoltQaxIntegLoLim Volt f32	-4.57000017		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k VoltSatDaxPolyCoeff Uls f32	-23.5930004		
k VoltSatQaxPolyCoeff Uls f32	22.8640003		
k deadtimeVScale Uls f32	0.967999995		
t CommOffsetTblX Uls u3p13[0]	918		
t CommOffsetTblX Uls u3p13[1]	1679		
t CommOffsetTblY Cnt u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	190		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	190	190	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.29100323	-4.29099989 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.2389493	-2.23895001 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22866	22866 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	·

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.129 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.76400006	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.99300003
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	176.421997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-310.208008
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] MtrCtrl_MtrOayIntegralGain_Ohm_M_f32[0]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.71200001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQayProportionalCain_Ohm_M_f32[0]	1.60000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	392.343994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	136.852005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	4.63049984
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	5.38999987
MtrPosComputationDelay_Rad_M_f32[1]	2.17600012
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851999998
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.757000029
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.754400015
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50.1534996
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.319000006
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl MtrVoltQaxFFFilt M str.TermN UIs f32	50.1534996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.319000006
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7980.1499
	6489,7002
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlVirualResDay, Ohm f32	0 0040000010
k_MtrCtrlVirualResDax_Ohm_f32	0.00400000019
k_MtrCtrlVirualResQax_Ohm_f32	0.108999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	7.02110004
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	18.0783997
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	21.5779991
k_VoltSatQaxPolyCoeff_Uls_f32	19.1660004
k_deadtimeVScale_Uls_f32	0.97299999
t_CommOffsetTblX_Uls_u3p13[0]	5022
t_CommOffsetTblX_Uls_u3p13[1]	7003
t_CommOffsetTblY_Cnt_u16[0]	63
t_CommOffsetTblY_Cnt_u16[1]	327
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3399
angor_ma onar_nead_ma oan on on on onect_ont_d to_pa	0000





Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	240	240	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	50820	50820 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-26.7350006	-26.7350006 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	34820	34820 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-26.7350006	-26.7350006 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.130 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.28900003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.70599997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	383.354004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-411.454987
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.432999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	39.0110016
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-717.330017
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	7.36499977
MtrCurrDaxRef_Amp_M_f32[0]	-100.282997
MtrCurrDaxRef_Amp_M_f32[1]	-120.248001
MtrCurrQaxCog_Amp_M_f32	-41.5750008

PICurrCntrl_Per1





Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-76.8769989		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998		
MtrCurrQaxPrevIntg_Volt_M_f32	9.1906004		
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpl_Amp_M_f32[0]	-76.8769989		
MtrCurrQaxRpl_Amp_M_f32[1]	-153.238998		
MtrPosComputationDelay_Rad_M_f32[0]	-0.195999995		
MtrPosComputationDelay_Rad_M_f32[1]	-0.303000003		
MtrVoltDax_Volt_M_f32[0]	-29.3959999		
MtrVoltDax_Volt_M_f32[1]	-1.94000006		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.94000006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.349000007		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.150000006		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.203999996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.521000028		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-10.21		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	51.3003006		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.881699979		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	51.3003006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.881699979		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1293.53003		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7361.14014		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.101000004		
k MtrCtrlVirualResQax Ohm f32	0.107000004		
-	0.107000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	15.2988997		
k_MtrVoltDaxIntegHiLim_Volt_f32			
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5 1		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc			
k_MtrVoltQaxIntegHiLim_Volt_f32	14.7958002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	6.31400013		
k_VoltSatQaxPolyCoeff_Uls_f32	16.4209995		
k_deadtimeVScale_Uls_f32	0.99099997		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4458		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4458	4458	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	129.623993	129.623993 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.25051808	-1.25051999 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.7946043	-4.79460001 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33385	33385 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-76.8769989	-76.8769989 ± 7.81E-03	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
VtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
VtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	6.18900013
VtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl MtrDampTermDax_Ohm M f32[1]	0.079999982
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.098999995
wtrCtrl_MtrDampTermQax_Onm_w_i32[u] WtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
WtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0099999978
wtrCtrl_mtrDaxIntegralGain_Onm_iv_r32[0] MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.00999999978
	757.447021
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-505.596985
VtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	0.0099999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.093999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.087999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.73800004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.727
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	841.114014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	815.677979
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
/trCtrl_Vecu_Volt_M_f32[0]	18.9510002
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008
/trCurrDaxPrevIntg_Volt_M_f32	0.851999998
/trCurrDaxRef_Amp_M_f32[0]	-68.6760025
MtrCurrDaxRef_Amp_M_f32[1]	-96.776001
/trCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003
/trCurrQaxFinalRef_Amp_M_f32[1]	107.137001
MtrCurrQaxPrevIntg_Volt_M_f32	17.5212994
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	191.369003
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001
MtrPosComputationDelay_Rad_M_f32[0]	-4.36899996
MtrPosComputationDelay_Rad_M_f32[1]	-4.83900023
MtrVoltDax_Volt_M_f32[0]	-17.1070004
MtrVoltDax Volt M f32[1]	15.9390001

PICurrCntrl_Per1



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-17.1070004		
MtrVoltQax_Volt_M_f32[1]	15.9390001		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.68629998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	32.4859009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.175400004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	32.4859009		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.175400004		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	6616.02002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt Igc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0020000009		
k_MtrCtrlVirualResQax_Ohm_f32	0.0410000011		
k MtrCurrQaxRefModifDsb Cnt Igc	1		
k MtrCurrQaxRefModifRplEn Cnt Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	28.8642998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.4000001		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxInterFilable_Ont_ige	6.41629982		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
	0		
k_MtrVoltVecuFiltEnable_Cnt_lgc	16.5690002		
k_VoltSatDaxPolyCoeff_Uls_f32	18.1630001		
k_VoltSatQaxPolyCoeff_Uls_f32			
k_deadtimeVScale_UIs_f32	0.971000016 459		
t_CommOffsetTblX_Uls_u3p13[0]			
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1441		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	-
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	63635	63635 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-18.0484562	-18.0485001 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	10.1218939	10.1218996 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	4010	4010 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	-2.4000001	-2.4000001 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓



Test Step 2.132 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.029999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.186000004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	161.147995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	308.806
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.070000003
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.43900001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.87399995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	259.894012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	103.217003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	1.579
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
MtrCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972
MtrCurrQaxPrevIntg_Volt_M_f32	13.7006998
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	-147.343002
MtrCurrQaxRpl_Amp_M_f32[1]	127.972
MtrPosComputationDelay_Rad_M_f32[0]	-5.46899986
MtrPosComputationDelay_Rad_M_f32[1]	-3.09299994
MtrVoltDax_Volt_M_f32[0]	0.908999979
MtrVoltDax_Volt_M_f32[1]	21.0249996
MtrVoltQax_Volt_M_f32[0]	0.90899979
MtrVoltQax_Volt_M_f32[1]	21.0249996
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00400000019
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.35589999
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.143000007
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	79.1921005
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.763000011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.1921005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.763000011
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2332.93994
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.169
k_MtrCtrlVirualResQax_Ohm_f32	0.194000006
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1

PICurrCntrl_Per1



Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.7931995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.1371994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	4.79199982		
k_VoltSatQaxPolyCoeff_Uls_f32	19.2709999		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	2638		
t_CommOffsetTblX_Uls_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3172		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3172	3172	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-168.647003	-168.647003 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	18.6116123	18.6116009 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	23.341156	23.3411999 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	15513	15513 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-168.647003	-168.647003 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972	127.972 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.133 (Repeat Count = 1)		
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985	
MtrCtrl MtrDampTermQax Ohm M f32[1]	0.075000003	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.94200003
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.00600004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-80.9769974
	-53.5390015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.352999985
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.35800004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-261.626007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-882.955017
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
	12.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-8.56599998
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxPrevIntg_Volt_M_f32	26.4118996
MtrCurrQaxRef Amp M f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	6.18900013
MtrCurrQaxRpl_Amp_M_f32[1]	83.0540009
MtrPosComputationDelay_Rad_M_f32[0]	2.9749999
MtrPosComputationDelay_Rad_M_f32[1]	0.486999989
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.116499998
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0989999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	3.26889992
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.282200009
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	3.26889992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.282200009
SlowDataAccessBufIndex_Cnt_M_u16	0
k CLOAFdbackSignalSclFacSlew UlspS f32	7083.27002
k ILOAFdbackSignalSclFacSlew UlspS f32	947.890015
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0
k MtrCtrlFeedbackControlDisable Cnt Igc	0
·	0.133000001
k_MtrCtrlVirualResDax_Ohm_f32	
k_MtrCtrlVirualResQax_Ohm_f32	0.131999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	12.9096003
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	29.3528004
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_UIs_f32	-6.11899996
k_VoltSatQaxPolyCoeff_Uls_f32	19.4669991
k_deadtimeVScale_Uls_f32	0.987999976
t_CommOffsetTbIX_UIs_u3p13[0]	1212
t_CommOffsetTbIX_UIs_u3p13[1]	1704
t_CommOffsetTblY_Cnt_u16[0]	23
t_CommOffsetTblY_Cnt_u16[1]	212
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003

2016-04-22, 16:28:30+0530



Name	Input Value		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	479		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	479	479	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	19.6739998	19.6739998 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.46959901	-2.46959996 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.27839613	-4.27839994 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3723	3723 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	19.6739998	19.6739998 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009	83.0540009 ± 7.81E-03	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.134 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lqc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModIdxSrlComSvcDft Cnt Iqc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-212.632996
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	-205.085007
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.079999982
MtrCtrl MtrDampTermDax Ohm M f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl MtrDampTermQax Ohm M f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20099998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.342000008
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	808.778992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-903.747009
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.101999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.838
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	827.307007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	398.522003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983





Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[0]	160.044006		
MtrCurrDaxRef_Amp_M_f32[1]	165.242004		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxPrevIntg_Volt_M_f32	8.14280033		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016		
MtrPosComputationDelay_Rad_M_f32[0]	4.70800018 -2.68499994		
MtrPosComputationDelay_Rad_M_f32[1]	18.6380005		
MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[1]	-23.1870003		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851000011		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.465999991		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.591700017		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12.2650003		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.361299992		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	12.2650003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.361299992		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2409.94995		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4854.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0390000008		
k_MtrCtrlVirualResQax_Ohm_f32	0.158000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.4475994		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	24.7383003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	20.7450008		
k_VoltSatQaxPolyCoeff_Uls_f32	23.7810001		
k_deadtimeVScale_Uls_f32	0.954999983		
t_CommOffsetTblX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2036		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	62586	62586 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-25.2309952	-25.2310009 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	17.3926353	17.3925991 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-10.5688381	-10.5688 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	59610	59610 ± 1.52588E-05	V
MtrCurrDaxPrevIntg_Volt_M_f32	-8.02652168	-8.02651978 ± 0.0625	
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	7
MtrCurrQaxFinalRef_Amp_M_f32[1]	-25.2309952	-25.2310009 ± 7.81E-03	✓



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
######################################	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)		
	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)		
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.296	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.25800002	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-261.467987	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-308.463013	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0170000009	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.09599996	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.614000022	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-333.243011	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	115.543999	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.55999994	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.8330002	
/trCtrl MtrVoltQaxFF Volt M f32[0]	-8.61900043	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.1560001	
/trCtrl Vecu Volt M f32[0]	18.5559998	
/trCtrl_Vecu_Volt_M_f32[1]	20.9160004	
MtrCurrDaxPrevIntg Volt M f32	14.2440004	
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024	
MtrCurrDaxRef_Amp_M_f32[1]	-216.972	
MtrCurrQaxCog_Amp_M_f32	20.6149998	
MtrCurrQaxFinalRef Amp M f32[0]	-213.026993	
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996 9.33899975	
MtrCurrQaxPrevIntg_Volt_M_f32		
/trCurrQaxRef_Amp_M_f32[0]	-105.246002	
/trCurrQaxRef_Amp_M_f32[1]	41.6290016	
/trCurrQaxRpl_Amp_M_f32[0]	-213.026993	
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996	
/trPosComputationDelay_Rad_M_f32[0]	0.157000005	
/trPosComputationDelay_Rad_M_f32[1]	-5.01100016	
//trVoltDax_Volt_M_f32[0]	-8.61900043	
MtrVoltDax_Volt_M_f32[1]	-13.1560001	
MtrVoltQax_Volt_M_f32[0]	-8.61900043	
/trVoltQax_Volt_M_f32[1]	-13.1560001	

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.237000003		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.00650000013		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	20.7000008		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	66.3365021		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.644699991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	20.7000008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	66.3365021		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.644699991		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6110.83008		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7208.8501		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.187000006		
k_MtrCtrlVirualResQax_Ohm_f32	0.112999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.2907009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	11.4712		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	19.9039993		
k VoltSatQaxPolyCoeff Uls f32	-19.9039993		
k deadtimeVScale Uls f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTbIX_UIs_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71		
t CommOffsetTblY Cnt u16[1]	676		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1357		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1357	1357	~
MtrCntrl Write Modldx Uls u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	87.1659851	87.1660004 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.55999994	-4.55999994 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-8.61900043	-8.61900043 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	39481	39481 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef Amp M f32[0]	87.1659851	87.1660004 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

-66.7229996

-66.7229996 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.136 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.51800001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.48300004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-99.1699982
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-632.38501
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.041999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.074000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.166999996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1003.84003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	15.3310003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	6.73180008
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-2.15700006
MtrPosComputationDelay_Rad_M_f32[1]	4.67700005
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.944000006
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.294999987
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.167500004
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	71.040802
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.209299996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	71.040802
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.209299996
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5388.75
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3076.13989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.145999998
k_MtrCtrlVirualResQax_Ohm_f32	0.0710000023
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0

2016-04-22, 16:28:30+0530



		•	
Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.0139999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.2535		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-4.86499977		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.41699982		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	826		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	676	676	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63569	63569 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-19.3592358	-19.3591995 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.96960425	1.96959996 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33456	33456 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	20.0139999	20.0139999 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 2.137 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.675999999	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.14600003	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	63.882	

2016-04-22, 16:28:30+0530



MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[0] MtrCurDaxPrevintg_Volt_M_f32 MtrCurDaxPrevintg_Volt_M_f32[0] MtrCurDaxFinalRef_Amp_M_f32[0] MtrCurQaxCog_Amp_M_f32 MtrCurQaxFinalRef_Amp_M_f32[0] MtrCurQaxRef_Amp_M_f32[0] MtrCurQaxRef_Amp_M_f32[0] MtrCurQaxRef_Amp_M_f32[0] MtrPosComputationDelay_Rad_M_f32[0] <	Input Value -0.995000005 -0.115999997 -0.115999997 -0.0970000029 -0.0270000007 -0.638999999 -0.104000002 -406.304993 -469.421997 -16.302 -8.55099964 -28.2420006 -12.6160002 -18.5559998 -20.9160004 -27.6930008 -65.1900024 -216.972 -5.72399998 -105.246002 -11.6290016 -14.2131004 -15.246002 -16.395996 -105.246002 -16.395996 -105.246002 -16.39599899999 -16.305999899 -16.3059989999999999999999999999999999999999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[0] MtrCurDaxPrevIntg_Volt_M_f32 MtrCurDaxRef_Amp_M_f32[0] MtrCurDaxRef_Amp_M_f32[0] MtrCurQaxCog_Amp_M_f32 MtrCurQaxFinalRef_Amp_M_f32[0] MtrCurQaxPervIntg_Volt_M_f32 MtrCurQaxFinalRef_Amp_M_f32[0] MtrCurQaxFer_Amp_M_f32[1] MtrCurQaxRef_Amp_M_f32[1] MtrCurQaxRef_Amp_M_f32[1] MtrCurQaxRef_Amp_M_f32[1] MtrCurTQaxRef_Amp_M_f32[1] MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[0] MtrVoltDax_V	0.995000005 0.115999997 0.115999997 0.0970000029 0.027000007 0.038999999 0.104000002 4.06.304993 4.69.421997 -16.302 8.5509964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 41.2131004 31.5869999 -186.395996 -105.246002 41.6290016 22.36999989 28.267000008 -28.2420006 12.6160002 -28.2420006 12.6160002
MtrCtrl_MtrImpedDax_Ohm_M_632[0] MtrCtrl_MtrImpedDax_Ohm_M_632[1] MtrCtrl_MtrImpedQax_Ohm_M_632[1] MtrCtrl_MtrImpedQax_Ohm_M_632[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_632[0] MtrCtrl_MtrQaxIntegralGain_Ohm_M_632[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_632[0] MtrCtrl_MtrVoltDaxFF_Volt_M_632[0] MtrCtrl_MtrVoltDaxFF_Volt_M_632[0] MtrCtrl_MtrVoltQaxFF_Volt_M_632[0] MtrCtrl_MtrVoltQaxFF_Volt_M_632[1] MtrCtrl_MtrVoltQaxFF_Volt_M_632[0] MtrCtrl_Vecu_Volt_M_632[0] MtrCtrl_Vecu_Volt_M_632[0] MtrCurDaxPrevIntg_Volt_M_632 MtrCurDaxRef_Amp_M_632[0] MtrCurrQaxFinalRef_Amp_M_632[0] MtrCurrQaxFinalRef_Amp_M_632[0] MtrCurrQaxFinalRef_Amp_M_632[0] MtrCurrQaxPevIntg_Volt_M_632[1] MtrCurrQaxRef_Amp_M_632[0] MtrCurrQaxRef_Amp_M_632[0] MtrCurrQaxRef_Amp_M_632[0] MtrPosComputationDelay_Rad_M_632[0] MtrPosComputationDelay_Rad_M_632[0] MtrVoltDax_Volt_M_632[0] MtrVoltQax_Volt_M_632[0] MtrVoltQax_Volt_M_632[0] MtrVoltQax_Volt_M_632[0] MtrVoltQax_Volt_M_632[0] <t< td=""><td>0.11599997 0.11599997 0.0970000029 0.0270000007 0.638999999 0.0104000002 4406.304993 4469.421997 -16.302 8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 22.6600008 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006</td></t<>	0.11599997 0.11599997 0.0970000029 0.0270000007 0.638999999 0.0104000002 4406.304993 4469.421997 -16.302 8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 22.6600008 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006
MtrCtri_MtrimpedDax_Ohm_M_f32[1] C MtrCtri_MtrimpedQax_Ohm_M_f32[0] C MtrCtrl_MtrimpedQax_Ohm_M_f32[0] C MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] C MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] C MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] C MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] C MtrCtrl_Vecu_Volt_M_f32[0] C MtrCurrDaxPerVerlntg_Volt_M_f32[0] C MtrCurrDaxPerVerlntg_Volt_M_f32[0] C MtrCurrDaxRef_Amp_M_f32[0] C MtrCurrQaxFerinalRef_Amp_M_f32[0] C MtrCurrQaxFerinalRef_Amp_M_f32[0] C MtrCurrQaxFerinalRef_Amp_M_f32[0] C MtrCurrQaxRef_Amp_M_f32[0] C MtrCurrQaxRef_Amp_M_f32[0] C MtrCurrQaxRef_Amp_M_f32[1] C MtrOotDax_Volt_M_f32[0] C MtrVoltDax_Volt_M_f32[0] C MtrVoltDax_Volt_M_f32[0] C MtrVoltDax_Volt	0.11599997 0.0970000029 0.027000007 0.638999999 0.104000002 4406.304993 4469.421997 -16.302 8.5509964 -28.242006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.190024 -216.972 5.7239998 -105.246002 41.629016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 14.2231004 31.5869999 -186.395996 -105.246002 41.6290016 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] C MtrCtrl_MtrCaxIntegralGain_Ohm_M_f32[0] C MtrCtrl_MtrCaxIntegralGain_Ohm_M_f32[0] C MtrCtrl_MtrCaxIntegralGain_Ohm_M_f32[0] C MtrCtrl_MtrCaxPropotionalGain_Ohm_M_f32[0] C MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] C MtrCtrl_Vecu_Volt_M_f32[0] C MtrCtrl_Vecu_Volt_M_f32[0] C MtrCurDaxPrevIntg_Volt_M_f32 C MtrCurDaxRef_Amp_M_f32[0] C MtrCurDaxRef_Amp_M_f32[1] C MtrCurQaxFinalRef_Amp_M_f32[0] C MtrCurQaxFinalRef_Amp_M_f32[0] C MtrCurQaxFinalRef_Amp_M_f32[0] C MtrCurQaxRef_Amp_M_f32[0] C MtrCurQaxRef_Amp_M_f32[0] C MtrCurQaxRef_Amp_M_f32[1] C MtrCurQaxRef_Amp_M_f32[0] C MtrVoltDax_Volt_M_f32[0] C MtrVoltDax_Volt_M_f32[0]	0.0970000029 0.0270000007 0.638999999 0.104000002 406.304993 4469.421997 416.302 8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 405.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] C MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] C MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] C MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] C MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] C MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] C MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] C MtrCtrl_Vecu_Volt_M_f32[0] C MtrCtrl_Vecu_Volt_M_f32[0] C MtrCtrl_Vecu_Volt_M_f32[0] C MtrCurDaxFed_Amp_M_f32[0] C MtrCurDaxRef_Amp_M_f32[0] C MtrCurDaxRef_Amp_M_f32[0] C MtrCurQaxFinalRef_Amp_M_f32[0] C MtrCurQaxFinalRef_Amp_M_f32[0] C MtrCurQaxFinalRef_Amp_M_f32[0] C MtrCurQaxRef_Amp_M_f32[0] C </td <td>0.027000007 0.638999999 0.10400002 -406.304993 -469.421997 -16.302 8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999</td>	0.027000007 0.638999999 0.10400002 -406.304993 -469.421997 -16.302 8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] C MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] C MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] - MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] - MtrCtrl_Vecu_Volt_M_f32[0] - MtrCtrl_Vecu_Volt_M_f32[1] - MtrCurDaxPereIntg_Volt_M_f32 - MtrCurDaxRef_Amp_M_f32[0] - MtrCurDaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] -	0.638999999 0.104000002 4406.304993 4469.421997 416.302 8.55099964 228.2420006 12.6160002 18.5559998 20.9160004 427.6930008 405.72399998 4105.246002 41.6290016 14.2131004 331.5869999 1-186.395996 4105.246002 41.6290016 2.36999989 2.6700008 228.2420006 12.6160002 228.2420006 12.6160002 0.25999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] (0) MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	0.104000002 -406.304993 -469.421997 -16.302 -8.55099964 -28.2420006 -12.6160002 -18.5559998 -20.9160004 -27.6930008 -65.1900024 -216.972 -5.72399998 -105.246002 -41.6290016 -14.2131004 -31.5869999 -186.395996 -105.246002 -41.6290016 -2.36999989 -2.67000008 -28.2420006 -12.6160002 -28.2420006 -12.6160002 -28.2420006 -12.6160002 -259999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] - MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] - MtrCtrl_Vecu_Volt_M_f32[0] - MtrCtrl_Vecu_Volt_M_f32[1] - MtrCurrDaxPrevIntg_Volt_M_f32 - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrQaxCamp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltQax_Volt_M_f32[0] -	4406.304993 4469.421997 416.302 8.55099964 428.2420006 12.6160002 18.5559998 20.9160004 427.6930008 465.1900024 4216.972 5.72399998 4105.246002 41.6290016 14.2131004 331.5869999 1-105.246002 41.6290016 2.36999989 2.67000008 428.2420006 12.6160002 428.2420006 12.6160002 0.25999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] — MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] — MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 8 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] — MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] — MtrCtrl_Vecu_Volt_M_f32[0] — MtrCtrl_Vecu_Volt_M_f32[1] — MtrCurrDaxPrevIntg_Volt_M_f32[0] — MtrCurrDaxRef_Amp_M_f32[0] — MtrCurrDaxRef_Amp_M_f32[0] — MtrCurrQaxCog_Amp_M_f32 — MtrCurrQaxCog_Amp_M_f32[0] — MtrCurrQaxCog_Amp_M_f32[0] — MtrCurrQaxFinalRef_Amp_M_f32[0] — MtrCurrQaxPrevIntg_Volt_M_f32[0] — MtrCurrQaxRef_Amp_M_f32[0] — MtrCurrQaxRef_Amp_M_f32[1] — MtrCurrQaxRef_Amp_M_f32[1] — MtrVoltDax_Volt_M_f32[0] — MtrVoltDax_Volt_M_f32[0] — MtrVoltDax_Volt_M_f32[1] — MtrVoltQax_Volt_M_f32[1] — MtrVoltQax_Volt_M_f32[1] — MtrVoltQax_Volt_M_f32[1] — MtrVo	469.421997 -16.302 8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.190024 -216.972 5.7239998 -1.05.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999899 2.6700008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCtri_MtrVoltDaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 8 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 1 MtrCtrl_Vecu_Volt_M_f32[0] 1 MtrCtrl_Vecu_Volt_M_f32[1] 2 MtrCurrDaxPrevIntg_Volt_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrQaxCog_Amp_M_f32 5 MtrCurrQaxCog_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxPrevIntg_Volt_M_f32[1] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[1] - MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Vol	-16.302 8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCtri_MtrVoltDaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 8 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 1 MtrCtrl_Vecu_Volt_M_f32[0] 1 MtrCtrl_Vecu_Volt_M_f32[1] 2 MtrCurrDaxPrevIntg_Volt_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrQaxCog_Amp_M_f32 5 MtrCurrQaxCog_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxPrevIntg_Volt_M_f32[1] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[1] - MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Vol	8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 8 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 1 MtrCtrl_Vecu_Volt_M_f32[0] 1 MtrCtrl_Vecu_Volt_M_f32[1] 2 MtrCurrDaxPrevIntg_Volt_M_f32 - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[1] - MtrCurrQaxCog_Amp_M_f32[1] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[1] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[1] - MtrCurrQaxRef_Amp_M_f32[1] - MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - PlCurrCntrl_MtrCurrDaxSatFl	8.55099964 -28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] - MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] - MtrCtrl_Vecu_Volt_M_f32[0] - MtrCtrl_Vecu_Volt_M_f32[1] - MtrCurrDaxPrevIntg_Volt_M_f32 - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[1] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFer_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrDoxComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 - PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -	-28.2420006 12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] fmtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] fmtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 fmtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] fmtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxFinalRef_Amp_M_f32[1] fmtrCurrQaxFinalRef_Amp_M_f32[1] MtrCurrQaxPrevIntg_Volt_M_f32 fmtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] fmtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[0] fmtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] fmtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] fmtrOutGax_Volt_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1] fmtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[0] fmtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[0] fmtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[0] fmf32 PlCurrCntrl_CurrCassatFluxRatio_Uls_M_f32 fmf32 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 fmf32 PlCurrCntrl_MtrVeucFilt_M_str.PrevInput_Uls_f32 fmf2 PlCurrCntrl_MtrVeucFilt_M_str.TermD_Uls_f32 fmf2 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 fmf2	12.6160002 18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -105.246002 41.6290016 2.36999899 2.6700008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCtrl_Vecu_Volt_M_f32[0] 1 MtrCtrl_Vecu_Volt_M_f32[1] 2 MtrCurrDaxPrevIntg_Volt_M_f32 - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[1] - MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[1] - MtrCurrQaxFinalRef_Amp_M_f32[1] - MtrCurrQaxFrevIntg_Volt_M_f32 1 MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[1] - MtrPosComputationDelay_Rad_M_f32[0] - MtrPosComputationDelay_Rad_M_f32[1] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - PiCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 - PiCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 -	18.5559998 20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCtrl_Vecu_Volt_M_f32[1] 2 MtrCurrDaxPrevIntg_Volt_M_f32 - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[1] - MtrCurrQaxCog_Amp_M_f32 5 MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[1] - MtrCurrQaxPrevIntg_Volt_M_f32 1 MtrCurrQaxPrevIntg_Volt_M_f32 1 MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] - MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - PiCurrCntrl_MtrCurrDaxSatFluxRatio_Uis_M_f32 - PiCurrCntrl_MtrCurrDaxSatFluxRatio_Uis_M_f32 - PiCurrCntrl_MtrVecuFitt_M_str.PrevInput_Uis_f32 - PiCurrCntrl_MtrVecuFitt_M_str.PrevInput_Uis_f32 -<	20.9160004 -27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrDaxPrevIntg_Volt_M_f32 - MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[1] - MtrCurrQaxCog_Amp_M_f32 5 MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[1] - MtrCurrQaxPrevIntg_Volt_M_f32 - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] - MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - PiCurrCntrl_NtrCurrDaxSatFluxRatio_UIs_M_f32 - PiCurrCntrl_MtrCurrDaxSatFluxRatio_UIs_M_f32 - PiCurrCntrl_MtrVecuFiit_M_str.PrevInput_UIs_f32 - PiCurrCntrl_MtrVecuFiit_M_str.TermN_UIs_f32 - PiCurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_UIs_f32 - PiCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_UIs_f32 - PiCu	-27.6930008 -65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrDaxRef_Amp_M_f32[0] - MtrCurrDaxRef_Amp_M_f32[1] - MtrCurrQaxCog_Amp_M_f32 5 MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[1] - MtrCurrQaxPevIntg_Volt_M_f32 - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] - MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 - PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 - PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 - PlCurrCntrl_MtrVecuFiit_M_str.Prevlnput_Uls_f32 - PlCurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 - </td <td>-65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999</td>	-65.1900024 -216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrDaxRef_Amp_M_f32[1] - MtrCurrQaxCog_Amp_M_f32 5 MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[1] 4 MtrCurrQaxRef_Amp_M_f32[0] 3 MtrCurrQaxRef_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] 4 MtrPosComputationDelay_Rad_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - PICurrCntrl_InverterFailSclFac_Uls_M_f32 - PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 - PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 - PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 - PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 - PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 - PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 - PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 - PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	216.972 5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrQaxCog_Amp_M_f32 5 MtrCurrQaxFinalRef_Amp_M_f32[0] - MtrCurrQaxFinalRef_Amp_M_f32[1] 4 MtrCurrQaxPevIntg_Volt_M_f32 1 MtrCurrQaxRef_Amp_M_f32[0] 3 MtrCurrQaxRef_Amp_M_f32[1] - MtrCurrQaxRpl_Amp_M_f32[0] - MtrPosComputationDelay_Rad_M_f32[0] 2 MtrVosComputationDelay_Rad_M_f32[1] 2 MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltQax_Volt_M_f32[1] 1 MtrVoltQax_Volt_M_f32[1] 1 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 0 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQ	5.72399998 -105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32[0] MtrCurrQaxRpl_Amp_M_f32[1] MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1] MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	-105.246002 41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrQaxFinalRef_Amp_M_f32[1] 4 MtrCurrQaxPrevIntg_Volt_M_f32 1 MtrCurrQaxRef_Amp_M_f32[0] 3 MtrCurrQaxRef_Amp_M_f32[1] - MtrCurrQaxRpl_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] 4 MtrPosComputationDelay_Rad_M_f32[0] 2 MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltQax_Volt_M_f32[1] 1 MtrVoltQax_Volt_M_f32[1] 1 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFit_M_str.PrevInput_Uls_f32 0 PICurrCntrl_MtrVecuFit_M_str.PrevInput_Uls_f32 0 PICurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFit_M_str.PrevOutput_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 P	41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrQaxFinalRef_Amp_M_f32[1] 4 MtrCurrQaxPrevIntg_Volt_M_f32 1 MtrCurrQaxRef_Amp_M_f32[0] 3 MtrCurrQaxRef_Amp_M_f32[1] - MtrCurrQaxRpl_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] 4 MtrPosComputationDelay_Rad_M_f32[0] 2 MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltQax_Volt_M_f32[1] 1 MtrVoltQax_Volt_M_f32[1] 1 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PICurrCntrl_MtrVecuFit_M_str.PrevInput_Uls_f32 0 PICurrCntrl_MtrVecuFit_M_str.PrevInput_Uls_f32 0 PICurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFit_M_str.PrevOutput_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 P	41.6290016 14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrQaxPrevIntg_Volt_M_f32 1 MtrCurrQaxRef_Amp_M_f32[0] 3 MtrCurrQaxRef_Amp_M_f32[1] - MtrCurrQaxRpl_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] 4 MtrPosComputationDelay_Rad_M_f32[0] 2 MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[0] - MtrVoltQax_Volt_M_f32[1] 1 MtrVoltQax_Volt_M_f32[1] 1 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFit_M_str.PrevInput_Uls_f32 0 PlCurrCntrl_MtrVecuFit_M_str.PrevInput_Uls_f32 0 PlCurrCntrl_MtrVecuFit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFit_M_str.PrevOutput_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFit_M_str.TermD_Uls_f32 0 <t< td=""><td>14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.6700008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999</td></t<>	14.2131004 31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.6700008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrQaxRef_Amp_M_f32[0] 3 MtrCurrQaxRef_Amp_M_f32[1] - MtrCurrQaxRpl_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] 4 MtrPosComputationDelay_Rad_M_f32[0] 2 MtrPosComputationDelay_Rad_M_f32[1] 2 MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] 1 MtrVoltQax_Volt_M_f32[1] 1 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 0 PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 0 PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	31.5869999 -186.395996 -105.246002 41.6290016 2.36999989 2.6700008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrQaxRef_Amp_M_f32[1] - MtrCurrQaxRpl_Amp_M_f32[0] - MtrCurrQaxRpl_Amp_M_f32[1] - MtrPosComputationDelay_Rad_M_f32[0] - MtrPosComputationDelay_Rad_M_f32[1] - MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - MtrVoltQax_Volt_M_f32[1] - PlCurrCntr_CurrSensFailSclFac_Uls_M_f32 - PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 - PlCurrCntrl_InverterFailSclFac_Uls_M_f32 - PlCurrCntrl_NtrCurrDaxSatFluxRatio_Uls_M_f32 - PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 - PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 - PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 - PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 <td>-186.395996 -105.246002 41.6290016 2.36999989 2.6700008 -28.242006 12.6160002 -28.242006 12.6160002 0.25999999</td>	-186.395996 -105.246002 41.6290016 2.36999989 2.6700008 -28.242006 12.6160002 -28.242006 12.6160002 0.25999999
MtrCurrQaxRpI_Amp_M_f32[0] - MtrCurrQaxRpI_Amp_M_f32[1] 4 MtrPosComputationDelay_Rad_M_f32[0] 2 MtrPosComputationDelay_Rad_M_f32[1] 2 MtrVoltDax_Volt_M_f32[0] - MtrVoltDax_Volt_M_f32[1] 1 MtrVoltQax_Volt_M_f32[0] - MtrVoltQax_Volt_M_f32[1] 1 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0 PlCurrCntrl_IntrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0 PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 0 PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 0 PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0 PlCurrCntrl_MtrVoltQaxFFFilt	-105.246002 41.6290016 2.36999989 2.6700008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrCurrQaxRpl_Amp_M_f32[1] MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1] MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	41.6290016 2.36999989 2.6700008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1] MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[1] PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVetcuFilt_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	2.36999989 2.6700008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrPosComputationDelay_Rad_M_f32[1] MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	2.67000008 -28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrVoltDax_Volt_M_f32[0] MtrVoltDax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	-28.2420006 12.6160002 -28.2420006 12.6160002 0.25999999
MtrVoltDax_Volt_M_f32[1] MtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[1] PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 PlCurrCntrl_InverterFailSclFac_Uls_M_f32 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PlCurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PlCurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 PlCurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	12.6160002 -28.2420006 12.6160002 0.25999999
MtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[1] 1 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	-28.2420006 12.6160002 0.25999999
MtrVoltQax_Volt_M_f32[0] MtrVoltQax_Volt_M_f32[1] 1 PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32	12.6160002 0.25999999
MtrVoltQax_Volt_M_[32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_[532 PICurrCntrl_InverterFailSclFac_Uls_M_[532 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[532 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_[532 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_[532 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_[532 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_[532 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_[532 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_[532 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_[532 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_[532 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_[532 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_[532 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_[532 PICurrCntrl_MtrVoltQaxFFIit_MtrVoltQaxFFFiit_MtrVoltQaxFFFiit_MtrVoltQaxFFIit_MtrVoltQaxFFIit_MtrVoltQaxFFIit_MtrVoltQaxFFIit_MtrVoltQaxFFIit_MtrVoltQaxFFIit_MtrVoltQaxFFIIt_MtrVoltQaxFFIIt_MtrVoltQaxFFIIt_MtrVoltQaxFFIIt_MtrVoltQaxFFIIt_MtrVoltQaxFFIIt_MtrVoltQaxF	12.6160002 0.25999999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	0.25999999
PICurrCntrl_InverterFailScIFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVetuFiit_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 SlowDataAccessBufindex_Cnt_M_u16	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 SlowDataAccessBuflndex_Cnt_M_u16	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVettlit_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 SlowDataAccessBufIndex_Cnt_M_u16	0.222000003
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 SlowDataAccessBufIndex_Cnt_M_u16	0.763899982
PICurrCntrl_MtrVecuFitL_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVecuFitL_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFitL_M_str.TermD_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermD_UIs_f32 SlowDataAccessBufIndex_Cnt_M_u16	0.0469999984
PICurrCntrl_MtrVecuFitL_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFitL_M_str.TermD_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermD_UIs_f32 SlowDataAccessBufIndex_Cnt_M_u16	22.2399998
PICurrCntrl_MtrVecuFitL_M_str.TermD_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermD_UIs_f32 PICurrCntrl_MtrVoltQaxFFFitL_M_str.TermD_UIs_f32 SlowDataAccessBufIndex_Cnt_M_u16	-10.21
PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_UIs_f32 ClowDataAccessBufIndex_Cnt_M_u16	44.2909012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 4 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 5 SlowDataAccessBufIndex_Cnt_M_u16 1	0.210999995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 4 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 5 SlowDataAccessBufIndex_Cnt_M_u16 1	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 4 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 ClowDataAccessBufIndex_Cnt_M_u16 1	-10.21
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 ClowDataAccessBufIndex_Cnt_M_u16 1	44.2909012
SlowDataAccessBufIndex_Cnt_M_u16	0.210999995
	0.2.10333333
	1
	7174.7002
	1940.90002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.159999996
k_MtrCtrlVirualResQax_Ohm_f32	0.0109999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k MtrCurrQaxRefModifRpIEn Cnt lgc	0
	14.0096998
	-25.6000004
	1
	19.7115993
	-25.6000004
	1
	14.1599998
k_VoltSatQaxPolyCoeff_Uls_f32	-17.3999996
k_deadtimeVScale_Uls_f32	0.967999995
t_CommOffsetTblX_Uls_u3p13[0]	1532
	2851
	912
	1211
0 = = = 0 = =0 =	1
	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	1 1
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1 1 -207.917999
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1 1
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1 1 -207.917999





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	827	827	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-15.7660007	-15.7659998 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.4196074	-2.41961002 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.19178963	-4.1917901 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	62948	62948 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-15.7660007	-15.7659998 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.138 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.33800006
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.481999993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	406.850006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	57.3230019
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.810000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.185000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	222.291
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1002.88
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	17.0116005		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	2.68400002		
MtrPosComputationDelay_Rad_M_f32[1]	5.81400013		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.1870003		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.185000002		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.737999976		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.89200002		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.9187012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.882499993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.9187012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.882499993		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3789.18994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3865.98999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.159999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.0689999983		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.20769978		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.04409981		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	7.02699995		
k_VoltSatQaxPolyCoeff_Uls_f32	-15.4709997		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTblX_Uls_u3p13[0]	0		
t_CommOffsetTblX_Uls_u3p13[1]	0		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4450		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	
MtrCntrl Write ModIdx Uls u16p16(val)	57194	57194 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	19.7759857	19.7759991 ± 7.81E-03	
	-0.736000001	-0.736000001 ± 4.88E-04	
MtrCntrl Write MtrDaxVoltage Volt f32(val)			
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrCaxVoltage_Volt_f32(val)		18 6380005 + 4 88F-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	18.6380005	18.6380005 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18.6380005 27583	27583 ± 1.52588E-05	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	18.6380005		



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
######################################	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)		
	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10300004	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.991999984	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	841.302979	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-775.062012	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.25199997	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.245	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	582.953003	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	578.156982	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008	
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-7.66699982	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008	
/trCtrl Vecu Volt M f32[0]	5.12099981	
/trCtrl_Vecu_Volt_M_f32[1]	7.48099995	
MtrCurrDaxPrevIntg Volt M f32	-7.71299982	
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999	
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996	
MtrCurrQaxCog_Amp_M_f32	-144.667007	
MtrCurrQaxFinalRef Amp M f32[0]	37.4550018	
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	
MtrCurrQaxPrevIntg Volt M f32	15.2893	
htrCurrQaxRef_Amp_M_f32[0]	171.485992	
MtrCurrQaxRef Amp M f32[1]	163.787003	
	37.4550018	
MtrCurrQaxRpl_Amp_M_f32[0]		
/trCurrQaxRpl_Amp_M_f32[1]	-2.84500003 2.86899996	
/trPosComputationDelay_Rad_M_f32[0]		
MtrPosComputationDelay_Rad_M_f32[1]	0.83999974	
/trVoltDax_Volt_M_f32[0]	-7.66699982	
MtrVoltDax_Volt_M_f32[1]	2.61400008	
MtrVoltQax_Volt_M_f32[0]	-7.66699982	
MtrVoltQax_Volt_M_f32[1]	2.61400008	





Name	Input Value		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.723999977		
PICurrCntrl InverterFailSclFac Uls M f32	0.800999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.752200007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	13.1514997		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.821500003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-38.7999992		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	13.1514997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.821500003		
SlowDataAccessBufIndex Cnt M u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	3827.27002		
k ILOAFdbackSignalSclFacSlew UlspS f32	2156.63989		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.0219999999		
k MtrCtrlVirualResQax Ohm f32	0.096000008		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.5093002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.3999998		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	29.0611992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k VoltSatDaxPolyCoeff Uls f32	20.5559998		
k_VoltSatQaxPolyCoeff_Uls_f32	-5.53700018		
k deadtimeVScale Uls f32	0.957000017		
t_CommOffsetTbIX_UIs_u3p13[0]	8192		
t CommOffsetTblX Uls u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTbIY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1730		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	1730	1730	~
MtrCntrl Write ModIdx UIs u16p16(val)	0	0 ± 1	V
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.7741425	-1.77414 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.44394493	4.44394016 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	25963	25963 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef Amp M f32[0]	220	220 ± 7.81E-03	~
MtrCurrQaxFinalRef Amp M f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.076999996
VtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
VtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.62699997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.41400005 -64.4329987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-04.4329987 243.455002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
wtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl MtrImpedQax_Ohm M f32[1]	0.0270000029
WtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.94700003
vitiCtr_MttQaxintegralGain_Ohm_M_f32[t] VtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.409000009
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	648.445007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-828.104004
WtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
VtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
VtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
VtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
VtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef Amp M f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	14.2027998
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	5.21500015
MtrPosComputationDelay_Rad_M_f32[1]	0.550000012
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.296000004
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.194800004
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	60.8638
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.472600013
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	60.8638
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.472600013
SlowDataAccessBufIndex_Cnt_M_u16	0
<_CLOAFdbackSignalSclFacSlew_UlspS_f32	208.033005
c_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.180000007
k_MtrCtrlVirualResQax_Ohm_f32	0.0839999989
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1

2016-04-22, 16:28:30+0530



Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.5352001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	5.54530001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	24.8269997		
k_VoltSatQaxPolyCoeff_Uls_f32	-17.3369999		
k_deadtimeVScale_Uls_f32	0.958000004		
t_CommOffsetTblX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3235		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1273	1273	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	46868	46868 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-86.8739929	-86.8740005 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	11950	11950 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-30.2000008	-30.2000008 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-86.8739929	-86.8740005 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 2.141 (Repeat Count = 1)		~
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.028	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.828000009	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	670.815002	





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-276.028992
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.136000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.861999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	360.989014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	26.3950005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002 18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	-20.8540993
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-1.46899998
MtrPosComputationDelay_Rad_M_f32[1]	-5.42999983
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.448000014
PICurrCotal MarcurrOavSatFluxRatio_Uls_M_f32	0.0320999995 0.725000024
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	9.46790028
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.962000012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	9.46790028
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.962000012
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.131999999
k_MtrCtrlVirualResQax_Ohm_f32	0.112999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	25.1942005
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	29.8994007
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	6.06099987
k_VoltSatQaxPolyCoeff_Uls_f32	-12.2449999
k_deadtimeVScale_Uls_f32	0.963999987
t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1]	5022 7003
t_CommOffsetTblY_Cnt_u16[0]	0
t_CommOffsetTblY_Cnt_u16[1]	0
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4296
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4296	4296	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-126.528008	-126.528 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.47866297	4.47866011 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.78156698	-1.78156996 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	5011	5011 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-126.528008	-126.528 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.142 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.45799994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.934021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.754000008
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.338013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg_Volt_M_f32	29.6310005		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003		
MtrPosComputationDelay_Rad_M_f32[0]	-0.344999999		
MtrPosComputationDelay_Rad_M_f32[1]	0.467999995		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.777999997		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018		
PICurrCotrl_MtrCurrOaxSatFluxRatio_Uls_M_f32	0.0951000005 0.833000004		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	20.700008		
	10.3985996		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl MtrVecuFilt M str.TermD UIs f32	0.630500019		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	20.700008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	10.3985996		
PlCurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.630500019		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6667.54004		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7823.27002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0820000023		
k_MtrCtrlVirualResQax_Ohm_f32	0.0120000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.4818001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.8833008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-7.19700003		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9090004		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	2000		
t_CommOffsetTblY_Cnt_u16[1]	2000		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	674		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	674	674	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40537	40537 ± 1.52588E-05	•
MtrCurrOavFineIDef Amp M (320)	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl_Read_SysState_Cnt_Enum(Val) /trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 67.4899979
	119.721001
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0.0109999999
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.949
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.935974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.30399999
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.403015
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
/trCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
/trCurrDaxPrevIntg_Volt_M_f32	-3.78200006
/trCurrDaxRef_Amp_M_f32[0]	-146.723007
/trCurrDaxRef_Amp_M_f32[1]	-121.943001
/trCurrQaxCog_Amp_M_f32	-144.667007
/trCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
/trCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
/trCurrQaxPrevIntg_Volt_M_f32	0.371499985
/trCurrQaxRef_Amp_M_f32[0]	-133.947006
1trCurrQaxRef_Amp_M_f32[1]	75.7020035
/trCurrQaxRpl_Amp_M_f32[0]	37.4550018
/trCurrQaxRpl_Amp_M_f32[1]	-2.84500003
/trPosComputationDelay_Rad_M_f32[0]	-2.48799992
/trPosComputationDelay_Rad_M_f32[1]	0.00100000005
/trVoltDax Volt M f32[0]	-7.66699982
/trVoltDax Volt M f32[1]	2.61400008

PICurrCntrl_Per1



Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851999998		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.398999989		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.208499998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	865.320007		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	69.4054031		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0452999994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	69.4054031		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0452999994		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7980.1499		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.0099999978		
k MtrCtrlVirualResQax Ohm f32	0.163000003		
k MtrCurrQaxRefModifDsb Cnt lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	16.9069004		
k MtrVoltDaxIntegLoLim Volt f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	11.2285004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-18.3610001		
k VoltSatQaxPolyCoeff Uls f32	22.7819996		
k_deadtimeVScale_Uls_f32	0.963999987		
t CommOffsetTbIX UIs u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	- Toodic
MtrCntrl Write ModIdx UIs u16p16(val)	63176	63176 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	182.914001	182.914001 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-20.4382992	-20.4382992 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.0793879	2.07939005 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	50220	50220 ± 1.52588E-05	
		30220 ± 1.32300L=03	
	14 0324189	14 0323007 ± 0 0625	
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]	14.9324188 182.914001	14.9323997 ± 0.0625 182.914001 ± 7.81E-03	· ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.144 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.873001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.5
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.738007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	14.3669996
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-5.50199986
MtrPosComputationDelay_Rad_M_f32[1]	4.21099997
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.448000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.254799992
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	22.2399998
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.5784988
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.966000021
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.5784988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.966000021
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0
k_MtrCtrlVirualResQax_Ohm_f32	0.0949999988
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1





		•	
Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.8994007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.4134998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	2.44199991		
k_VoltSatQaxPolyCoeff_Uls_f32	19.2689991		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	671		
t_CommOffsetTblY_Cnt_u16[1]	876		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2125	2125	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-126.528008	-126.528 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.5821819	-17.5821991 ± 4.88E-04	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	24.1644421	24.1644001 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	1587	1587 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-126.528008	-126.528 ± 7.81E-03	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.145 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009

2016-04-22, 16:28:30+0530



Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16600001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.221985
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0280000009 0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.232999995
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.27301
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrOavCog Amp M f32[1]	-186.395996 -144.667007
MtrCurrQaxCog_Amp_M_f32	
MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1]	37.4550018 -2.84500003
MtrCurrQaxPrevIntg Volt M f32	1.06570005
MtrCurrQaxRef Amp M f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-3.16700006
MtrPosComputationDelay_Rad_M_f32[1]	3.09599996
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_currSensFailSclFac_Uls_M_f32	0.777999997 0.0450000018
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.837800026
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	75.4597015
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.830900013
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	75.4597015
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.830900013
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6667.54004
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7823.27002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0 0
k_MtrCtrlVirualResDax_Ohm_f32	0.200000003
k MtrCtrlVirualResQax Ohm f32	0.010999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	26.6909008
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	20.4568005
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	20.2509995
k_VoltSatQaxPolyCoeff_Uls_f32	18.1280003
k_deadtimeVScale_Uls_f32	0.963999987
t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1]	4611 5579
t_CommOffsetTblY_Cnt_u16[0]	912
t_CommOffsetTblY_Cnt_u16[1]	1211
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1468		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1468	1468	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	11103	11103 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace	p Call Trace			
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.146 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.93900001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.36699998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	826.950989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-163.621994
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.26100004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60300004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-401.145996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-278.5
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006

PICurrCntrl_Per1



N	In and Walter		
Name MtrCurrDayPot Amp M (220)	Input Value -146.723007		
MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	-144.667007		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg Volt M f32	13.5303001		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003		
MtrPosComputationDelay_Rad_M_f32[0]	3.42499995		
MtrPosComputationDelay_Rad_M_f32[1]	-0.836000025		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851999998		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.398999989		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.633599997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17.0797005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.727199972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17.0797005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.727199972		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7980.1499		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.182999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.169		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	16.2366009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	22.8831997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	3.96000004		
k_VoltSatQaxPolyCoeff_Uls_f32	7.87699986		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2363		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	<u> </u>
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63176	63176 ± 1	✓
	03170		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	182.914001	182.914001 ± 7.81E-03	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)		182.914001 ± 7.81E-03 -20.4382992 ± 4.88E-04	
	182.914001		~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	182.914001 -20.4382992	-20.4382992 ± 4.88E-04	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	182.914001 -20.4382992 2.0793879	-20.4382992 ± 4.88E-04 2.07939005 ± 4.88E-04	· · · · · · · · · · · · · · · · · · ·
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	182.914001 -20.4382992 2.0793879 41490	-20.4382992 ± 4.88E-04 2.07939005 ± 4.88E-04 41490 ± 1.52588E-05	~



est Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
	-
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.010999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.527999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.268999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-311.075012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-305.570007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
htrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.12899995
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.556999981
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	354.154999
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	556.525024
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
ftrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
ftrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
ftrCtrl_Vecu_Volt_M_f32[0]	24.8479996
ItrCtrl_Vecu_Volt_M_f32[1]	27.2080002
ltrCurrDaxPrevIntg_Volt_M_f32	-7.71299982
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
ltrCurrDaxRef_Amp_M_f32[1]	-186.395996
NtrCurrQaxCog_Amp_M_f32	-144.667007
htrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
ItrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
htrCurrQaxPrevIntg_Volt_M_f32	30.4999008
ItrCurrQaxRef_Amp_M_f32[0]	171.485992
ltrCurrQaxRef_Amp_M_f32[1]	163.787003
ltrCurrQaxRpl_Amp_M_f32[0]	37.4550018
ItrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
ItrPosComputationDelay_Rad_M_f32[0]	5.14300013
htrPosComputationDelay_Rad_M_f32[1]	0.453999996
htrVoltDax_Volt_M_f32[0]	-7.66699982
//dtrVoltDax_Volt_M_f32[1]	2.61400008
/trVoltQax_Volt_M_f32[0]	-7.66699982
/trVoltQax_Volt_M_f32[1]	2.61400008

2016-04-22, 16:28:30+0530



Namo	Input Value		
Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.723999977		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.111000001		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.233400002		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	52.7086983 0.584299982		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	52.7086983 0.584299982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.564299982		
SlowDataAccessBufIndex_Cnt_M_u16			
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3827.27002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32	0.131999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.131999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.3010998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxInterHiLim Volt f32	13.0423002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k VoltSatDaxPolyCoeff Uls f32	7.04799986		
k_VoltSatQaxPolyCoeff_Uls_f32	6.82399988		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTbIX_Uls_u3p13[0]	5022		
t_CommOffsetTbIX_UIs_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	672		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	672	672	rtooui
MtrCntrl Write Modldx Uls u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.05482388	-2.05482006 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.38767862	4.38768005 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49075	49075 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	
MtrCurrQaxFinalRef Amp M f32[1]	220	220 ± 7.81E-03	
ma our qual manter_mnp_m_loz[1]	220	220 ± 1.01L=00	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.148 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.75999999
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	0.0970000029
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	979.52301
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	987.510986
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl MtrImpedQax Ohm M f32[0]	0.0970000029
MtrCtrl MtrImpedQax Ohm M f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10699999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.653999984
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-420.446991
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	525.913025
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl MtrVoltQaxFF Volt M f32[1]	12.6160002
MtrCtrl Vecu Volt M f32[0]	29.0240002
MtrCtrl_Vecu_Volt_M_f32[1]	30.3600006
MtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef Amp M f32[1]	-216.972
MtrCurrQaxCog Amp M f32	5.72399998
MtrCurrQaxFinalRef Amp M f32[0]	-105.246002
MtrCurrQaxFinalRef Amp M f32[1]	41.6290016
MtrCurrQaxPrevIntg Volt M f32	-16.7549
MtrCurrQaxRef Amp M f32[0]	31.5869999
MtrCurrQaxRef Amp M f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay Rad M f32[0]	-2.02900004
MtrPosComputationDelay Rad M f32[1]	-1.17700005
MtrVoltDax Volt M f32[0]	-28.2420006

PICurrCntrl_Per1



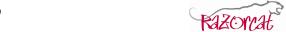
Microsona	Name	Input Value		
Microficial Automatini Service Us M. 192 0.84999977	MtrVoltDax_Volt_M_f32[1]	12.6160002		
Microficial Automatini Service Us M. 192 0.84999977		-28.2420006		
Pictornice Currisons-FallSchae Ule M M22		12.6160002		
Picum-Cint MicroarDassBillarBate, Us. M, 152 0.9375 Picum-Cint MicroarDassBillarBate, Us. M, 152 0.048989894 Picum-Cint MicroarBasBillarBate, Us. M, 152 0.048989894 Picum-Cint MicroarBasBillarBate, Us. 152 118 Picum-Cint MicroarBate, Set Termit Us. 152 0.80598999 Picum-Cint MicroarBate, Fill M, set Termit Us. 152 0.80598999 Picum-Cint MicroarBate, Fill M, set Termit Us. 152 0.80598999 Picum-Cint MicroarBate, Fill M, set Termit Us. 152 0.8059999 Picum-Cint MicroarBate, Fill M, set Termit Us. 152 0.00599999 Picum-Cint MicroarBate, Fill M, set Termit Us. 152 0.00599999 Picum-Cint MicroarBate, Fill M, set Termit Us. 152 0.00599999 Picum-Cint MicroarBate, Cint Us. 152 0.00599999 Picum-Cint MicroarBate, Fill M, set Termit Us. 152 0.00599999 Picum-Cint MicroarBate, Cint Us. 152 0.0059999 Picum	PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977		
Picumoni, Mircurassalini, M. st. Previous Us. 73.		0.816999972		
Picturn Carl Mirvecurilit M_ str Previologu Us. 132 22.179983	PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.9375		
PicturCrist Mivesurif M_ str PrevOuput_Uis_22	PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.0469999984		
PRICUMPORT Min/vecuFit M. str. Temm Uls. 12 0.05599999	PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PicumCruft Mr/voicinaFFFill Ms 17 mm D Us 12	PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
Picturn Lam Microlica FFFI II, M. et Prevoloput Us. 192	PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.7649002		
Picum'cnt Mirviolicas/FFFIL M, str. Pers-Output, Uls, 132 1118	PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.605599999		
Picuricant MirvoilQuaFFFIE M_str.TermN_ UIs_182	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
Picurrort, MrVoliCaxFFFEI, M. st. TermD_Uis_G2 0.605599999	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
Siewbashaceassbufindex_Cnt_M_u16	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	87.7649002		
k_LOAFdbackSignalSclFacSlew_UlspS_132 k_MirCritCurt.copSecOrTranFcEnable_Cnt_lgc l_ k_MirCritTeadbackControlDisable_Crit_lgc l_ k_MirCritTeadbackControlDisable_Crit_lgc_Dri l_ k_MirCritTeadbackControlDisable_Crit_lgc_Dri larget_MirCritTead_MirCritTeadbackControlDisable_Crit_lgc_Dri larget_MirCritTead_MirCritTead_MirCritTead_MirCrit_Read_MirCritTead_Mircrit_Read_MirCritTead_MircritControlDisable_Crit_lgc_Dri larget_MirCritTead_MircritControlDisable_Crit_lgc_Dri larget_MirCritTead_MircritControlDisable_Crit_lgc_Dri larget_MirCritTead_MircritControlDisable_Crit_lgc_Dri larget_MirCrit_Read_MircritControlOids_Crit_Lgc_Dri larget_MirCrit_Read_MircritControlOids_Crit_Lgc_Dri larget_MirCrit_Read_MircritControlOids_Crit_Lgc_Dri larget_MirCrit_Read_MircritControlOids_Crit_Lgc_Dri larget_Mircrit_Read_Mircrit_Crit_Lgc_Dri larget_Mircrit_Read_Mircrit_Crit_Lgc_Dri larget_Mircrit_Read_Mircrit_Crit_Read_Mircrit_Crit_Lgc_Dri larget_Mircrit_Read_Mircrit_Crit_Lgc_Dri larget_Mircrit_Read_Mircrit_Crit_Read_Mircrit_Crit_Read_Mircrit_Crit_Read_Mircrit_Crit_Read_Mircrit_Crit_Read_Mircr	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.605599999		
k_IHCOFCHURCHCOSS-BEOT/TRAFFCRABLE_CRL_Igc	SlowDataAccessBufIndex_Cnt_M_u16	0		
K_MtrCitriCurrLoopSecOrTranFcEnable_Ont_lgc	k_CLOAFdbackSignalSclFacSlew_UlspS_f32	208.033005		
k, MrtCrttiFeedbackControlDisable_Cnt_igc 1 k, MrtCrttiVirualReabax_Ohm_f32 0.01799999912 k, MrtCrttiVirualReabax_Ohm_f32 0.200000003 k, MrtCrutiVirualResbax_Ohm_f32 0.200000003 k, MrtCrutiVirualResModiffSplicn_Cnt_igc 0 k, MrtVoriDaxRefModiffSplicn_Cnt_igc 1 k, MrtVoriDaxRefModiffSplicn_ModiffSplicn_Cnt_igc 1 k, MrtVoriDaxRefModiffSplicn_ModiffSplicn_Cnt_igc 1 k, MrtVoriDaxRefModiffSplicn_ModiffSplicn_Cnt_igc 1 k, MrtVoriDaxRefModiffSplicn_Cnt_igc 1 k, VoriSatiDaxPoryCoeff_Ust_igs_2 2.0.4769993 k, VoriSatiDaxPoryCoeff_Ust_igs_32 4.35599995 k, VoriSatiDaxPoryCoeff_Ust_igs_32 4.35599995 k, D	k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5		
k_MtrCirtVirualResDax_Ohm_f32	k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
K_MtrCtrtVirualResQax_Ohm_f32	k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
K_MtrCurrQaxReModifDsb_CnLjgc	k_MtrCtrlVirualResDax_Ohm_f32	0.0179999992		
K_MtrCurrQaxRefModifRpiEn_Cnt_lgc	k_MtrCtrlVirualResQax_Ohm_f32	0.20000003		
K_MtrVoltDaxIntegHiLim_Volt_[32	k MtrCurrQaxRefModifDsb Cnt lgc	1		
K_MtrVoltDaxIntegHiLim_Volt_f32	k MtrCurrQaxRefModifRplEn Cnt lgc	0		
K_MITVORIDaxIntegLoLim_Volt_132		17.5195007		
k_MtrVoltQaxIntegHiLim_Volt_f32 4.98169994 k_MtrVoltQaxIntegLoLim_Volt_f32 -30.2000008 k_MtrVoltQaxIntegLoLim_Volt_f32 -30.2000008 k_VoltSatDaxPolyCoeff_Uls_f32 -20.4769993 k_VoltSatQaxPolyCoeff_Uls_f32 4.35599995 k_CeadtimeVScale_Uls_f32 0.97799985 t_CommOffsetTbIX_Uls_u3p13[0] 4811 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 889 t_CommOffsetTbIY_Cnt_u16[1] 1543 target_MtrCntrl_Read_MoldxSrlComSvcDft_Cnt_lgc_tvl 0 target_MtrCntrl_Read_MoldCurt_coal/tigInten_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MnCurt_coal/tigInten_Cnt_lgc_tpr 1 target_MtrCntrl_Read_MtrCurrOffXx_Amp_f32_Val 53.040009 target_MtrCntrl_Read_MtrCurrOffXx_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_MtrCurrOffXx_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 MtrCntrl_Write_MtrDax_Voltage_Volt_f32(val) 131.109009 131.109094 ± 7.81E-03	k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxintegLoLlm_Volt_f32	k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
K_VoltSatDaxPolyCoeff_Uls_f32	k_MtrVoltQaxIntegHiLim_Volt_f32	4.98169994		
MirVollVecuFiltEnable_Cnt_lgc		-30.2000008		
k_VoltSatQaxPolyCoeff_Uls_f32	k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_deadtimeVScale_Uls_1322		-20.4769993		
t_CommOffsetTbIX_UIs_u3p13[0]	k_VoltSatQaxPolyCoeff_Uls_f32	4.35599995		
t_CommOffsetTbIX_UIs_u3p13[0]		0.977999985		
t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 889 t_CommOffsetTbIY_Cnt_u16[1] 1543 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59,3040009 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 50,0610008 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50,0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_val 0 Name		4611		
t_CommOffsetTbIY_Cnt_u16[1] 1543 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 59.0610008 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 1798	t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[1] 1543 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val 59.3040009 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_val 59.3040009 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1798 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 1798		889		
target_MtrCntrl_Read_IndrLoaMtgtnEn_Cnt_Igc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1798 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 ✓ MtrCntrl_Write_ModIdx_UIs_u16p16(val) 0 0 ± 1 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 ✓ MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 ✓		1543		
target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1798 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 V MtrCntrl_Write_Modidx_UIs_u16p16(val) 0 0 ± 1 V MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 V MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 V MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 V MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 V MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 V		0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrOffcomOffset_Cnt_u16_ptr 1798 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 ✓ MtrCntrl_Write_Modldx_UIs_u16p16(val) 0 0 ± 1 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 ✓ MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 ✓		1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1798 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 ✓ MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 ✓		1		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 ✓ MtrCntrl_Write_MtrDavIolage_Us_u16p16(val) 0 0 ± 1 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 131.109009 131.108994 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 ✓ MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 ✓	target MtrCntrl Read MtrCurrDax Amp f32 Val	59.3040009		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 ✓ MtrCntrl_Write_MtrDavIolage_Us_u16p16(val) 0 0 ± 1 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 131.109009 131.108994 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 ✓ MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 ✓	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	1798		
Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 • MtrCntrl_Write_Modldx_UIs_u16p16(val) 0 0 ± 1 • MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 • MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 • MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 • MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 • MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 • MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 •		50.0610008		
Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 ✓ MtrCntrl_Write_Modldx_Uls_u16p16(val) 0 0 ± 1 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 ✓ MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 ✓		0		
MtrCntrl_Write_CommOffset_Cnt_u16(val) 1798 1798 MtrCntrl_Write_ModIdx_UIs_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03		Actual Value	Expected Value	Result
MtrCntrl_Write_ModIdx_UIs_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03	MtrCntrl Write CommOffset Cnt u16(val)		· · · · · · · · · · · · · · · · · · ·	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.109009 131.108994 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03	MtrCntrl Write Modldx Uls u16p16(val)		0 ± 1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -28.4440193 -28.4440002 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03	MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	131.109009	131.108994 ± 7.81E-03	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -10.4937534 -10.4938002 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03	MtrCntrl Write MtrDaxVoltage Volt f32(val)			•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 24302 24302 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03				
MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ± 0.0625 ✔ MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 ✔	MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)			•
MtrCurrQaxFinalRef_Amp_M_f32[0] 131.109009 131.108994 ± 7.81E-03 ✓	= = = : : ;			
		131.109009		v
				✓



est Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
/trCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
	0.079999982
/trCtrl_MtrDampTermDax_Ohm_M_f32[0] /trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0089999961
	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	
MtrCtrl_MtrDawlptegralCaip_Ohm_M_f32[1]	0.079999982 1.99100006
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.046 -57.7280006
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	352.100006
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.634000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.12699997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-489.911011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1007.60999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
/trCtrl_Vecu_Volt_M_f32[0]	17.7010002
/trCtrl_Vecu_Volt_M_f32[1]	20.0610008
/ltrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
/trCurrDaxRef_Amp_M_f32[0]	-146.723007
/trCurrDaxRef_Amp_M_f32[1]	-121.943001
/trCurrQaxCog_Amp_M_f32	59.3040009
/trCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
/trCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
/trCurrQaxPrevIntg_Volt_M_f32	30.4363995
/trCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
/trCurrQaxRpl_Amp_M_f32[1]	-66.7229996
/trPosComputationDelay_Rad_M_f32[0]	-5.35500002
MtrPosComputationDelay_Rad_M_f32[1]	1.125
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax Volt M f32[1]	-23.1870003

PICurrCntrl_Per1



Value 30005 370003 399993 000005 99997 000024 79993 43999 30003 79993 43999 30003 56982 20996
370003 399993 300005 39997 300024 779993 330003 79993 43999 30003 56982 20996
999993 900005 99997 900024 79993 43999 30003 79993 43999 30003 56982 20996
000005 000024 79993 43999 30003 79993 43999 30003 56982 20996
99997 900024 79993 43999 30003 79993 43999 30003 56982 20996
79993 43999 30003 79993 43999 30003 56982 20996
79993 43999 30003 79993 43999 30003 56982 20996
43999 30003 79993 43999 30003 56982 20996
43999 30003 79993 43999 30003 56982 20996
30003 79993 43999 30003 56982 20996 9999998 0000004
79993 43999 30003 56982 20996 9999998 0000004
43999 30003 56982 20996 9999998 0000004
43999 30003 56982 20996 9999998 0000004
30003 56982 20996 9999998 0000004
56982 20996 9999998 0000004
20996 2099998 2000004 59009
20996 2099998 2000004 59009
9999998 9000004 59009
59009
59009
59009
59009
99962
32999
99962
00002
99977
999979
10008
41003
al Value Expected Value Resu
1636
220 ± 7.81E-03
220 ± 7.81E-03 -10.1279001 ± 4.88E-04
220 ± 7.81E-03 229996 -66.7229996 ± 7.81E-03
2 4 2 2

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



est Step 2.150 (Repeat Count = 1) ame	Input Value
	o o
astDataAccessBufIndex_Cnt_M_u16 trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_var
trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
trCntrl Read MtrCurrQax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
trCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
trCtrl MtrDampTermDax Ohm M f32[1]	0.112999998
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.79400003
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.268000007
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1.13
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	893.153992
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.472000003
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.03400004
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-816.005981
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-233.619995
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
trCtrl_Vecu_Volt_M_f32[0]	18.9510002
trCtrl_Vecu_Volt_M_f32[1]	21.3110008
trCurrDaxPrevIntg_Volt_M_f32	-9.66300011
trCurrDaxRef_Amp_M_f32[0]	-133.947006
trCurrDaxRef_Amp_M_f32[1]	75.7020035
trCurrQaxCog_Amp_M_f32	83.9489975
trCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
trCurrQaxFinalRef_Amp_M_f32[1]	119.721001
trCurrQaxPrevIntg_Volt_M_f32	20.3910999
trCurrQaxRef_Amp_M_f32[0]	106.072998
trCurrQaxRef Amp M f32[1]	-112.455002
trCurrQaxRpl Amp M f32[0]	67.4899979
trCurrQaxRpl_Amp_M_f32[1]	119.721001
trPosComputationDelay_Rad_M_f32[0]	-6.19099998
trPosComputationDelay_Rad_M_f32[1]	0.512000024
trVoltDax Volt M f32[0]	-3.59500003
	-3.59500003 -28.4209995
trVoltDax_Volt_M_f32[1] trVoltQax_Volt_M_f32[0]	-28.4209995 -3.59500003
trVoltQax_Volt_M_f32[1]	-28.4209995
trvoligax_voli_M_isz[i] CurrCntrl CurrSensFailSclFac Uls M f32	0.911000013
CurrCntrl_InverterFailSclFac_Uls_M_f32	0.513000013
CurrCntrl_InverterFallSclFac_Ols_M_132 CurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_132	0.0749000013
CurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.565999985
CurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
ICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 ICurrCntrl MtrVecuFilt M str.TermN Uls f32	-1118 40.2612
	0.925599992
CurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118 -1118
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	40.2612
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.925599992
lowDataAccessBufIndex_Cnt_M_u16	1
_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002
_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996
_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
_MtrCtrlVirualResDax_Ohm_f32	0.140000001
_MtrCtrlVirualResQax_Ohm_f32	0.0560000017

2016-04-22, 16:28:30+0530



Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.4277		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.11270046		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-25		
k_VoltSatQaxPolyCoeff_Uls_f32	0.398000002		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1099		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1099	1099	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-97.5970001	-97.5970001 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.00629377319	-0.00629376993 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.8549962	4.85500002 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	948	948 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979	67.4899979 ± 7.81E-03	*
MtrCurrQaxFinalRef_Amp_M_f32[1]	-97.5970001	-97.5970001 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.151 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.684000015





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.978999972
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-984.268005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	119.455002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.566999972
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83399999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	762.239014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-528.901978
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008 21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-27.3339996
MtrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxCog Amp M f32	-144.667007
MtrCurrQaxFinalRef Amp M f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	2.85220003
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	3.82500005
MtrPosComputationDelay_Rad_M_f32[1]	2.33800006
MtrVoltDax_Volt_M_f32[0]	-25.3770008
MtrVoltDax_Volt_M_f32[1]	21.3880005
MtrVoltQax_Volt_M_f32[0]	-25.3770008
MtrVoltQax_Volt_M_f32[1]	21.3880005
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00400000019
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.716700017
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	60.2319984 0.522199988
PICurrCotrl_MtrVecuFilt_M_str.TermD_UIs_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	60.2319984
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.522199988
SlowDataAccessBufIndex_Cnt_M_u16	0
k CLOAFdbackSignalSclFacSlew UlspS f32	5911.31982
k ILOAFdbackSignalSclFacSlew UlspS f32	2332.93994
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0
k MtrCtrlFeedbackControlDisable Cnt lqc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0209999997
k_MtrCtrlVirualResQax_Ohm_f32	0.150999993
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	13.2849998
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	11.2693005
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	25
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995
k_deadtimeVScale_UIs_f32	0.963
t_CommOffsetTblX_Uls_u3p13[0]	4611
t_CommOffsetTblX_Uls_u3p13[1]	5579
t_CommOffsetTblY_Cnt_u16[0]	23
t_CommOffsetTblY_Cnt_u16[1]	212
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1 207.047000
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	665

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	665	665	✓
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.825012	131.824997 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.1349812	-16.1350002 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-25.1170063	-25.1170006 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	13084	13084 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.825012	131.824997 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.152 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.904999971
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.704999983
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-822.044006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-449.259003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.922999978
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.349999994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	161.807007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	765.385986
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	20.9669991
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998





		•	
Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	115.814003		
MtrCurrQaxCog_Amp_M_f32	-41.5750008		
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001		
MtrCurrQaxPrevIntg_Volt_M_f32	30.6926994		
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpl_Amp_M_f32[0]	191.369003		
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001		
MtrPosComputationDelay_Rad_M_f32[0]	4.6960001		
MtrPosComputationDelay_Rad_M_f32[1]	2.56500006		
MtrVoltDax_Volt_M_f32[0]	-29.3959999		
MtrVoltDax_Volt_M_f32[1]	-1.94000006		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.9400006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.65079999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	0		
PICurrCotrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	76.1873016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0882999972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	76.1873016 0.0882999972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 SlowDataAccessBufIndex_Cnt_M_u16	0.0882999972		
	7083.27002		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32			
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	947.890015		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlVirualPosDay_Ohm_f32	0.126000002		
k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32	0.115999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.113999997		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.48570013		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	13.9652004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	0		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003		
k deadtimeVScale Uls f32	0.987999976		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t CommOffsetTblY Cnt u16[0]	1237		
t CommOffsetTblY Cnt u16[1]	383		
target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1724		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	739	739	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	28516	28516 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	
MtrCntrl Write MtrDaxVoltage Volt f32(val)	2.61400008	2.61400008 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.9400006	-1.9400006 ± 4.88E-04	
		49797 ± 1.52588E-05	
MtrCntrl Write PhaseAdvanceFinal Rev ((0)16(val)	49797		
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32	49797 5.48570013		
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]	49797 5.48570013 -220	5.48570013 ± 0.0625 -220 ± 7.81E-03	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
	-
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.273999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.469999999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-538.278992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-363.735992
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.36099994
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0199999996
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-870.234009
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	991.184998
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
ftrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
ftrCtrl_Vecu_Volt_M_f32[0]	18.9510002
ftrCtrl_Vecu_Volt_M_f32[1]	21.3110008
ItrCurrDaxPrevIntg_Volt_M_f32	10.2959995
/trCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
1trCurrQaxCog_Amp_M_f32	48.8400002
htrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002
htrCurrQaxFinalRef_Amp_M_f32[1]	127.972
/trCurrQaxPrevIntg_Volt_M_f32	7.01200008
ItrCurrQaxRef_Amp_M_f32[0]	-146.723007
htrCurrQaxRef_Amp_M_f32[1]	-121.943001
ItrCurrQaxRpI Amp M f32[0]	-147.343002
ItrCurrQaxRpl Amp M f32[1]	127.972
ItrPosComputationDelay_Rad_M_f32[0]	4.78000021
ItrPosComputationDelay Rad M f32[1]	-2.88599992
ttrVoltDax_Volt_M_f32[0]	-17.1070004
/trVoltDax Volt M f32[1]	15.9390001
/trVoltQax_Volt_M_f32[0]	-17.1070004
/trVoltQax_Volt_M_f32[1]	15.9390001

2016-04-22, 16:28:30+0530



Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851000011		
PICurrCntrl InverterFailSclFac Uls M f32	0.465999991		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.371499985		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.143000007		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	99.3730011		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1426		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	99.3730011		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.1426		
SlowDataAccessBufIndex Cnt M u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2409.94995		
k ILOAFdbackSignalSclFacSlew UlspS f32	4854.70996		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.093999968		
k_MtrCtrlVirualResQax_Ohm_f32	0.0769999996		
	1		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc			
k_MtrVoltDaxIntegHiLim_Volt_f32	18.2031002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	3.43330002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.0909996		
k_VoltSatQaxPolyCoeff_Uls_f32	20.9540005		
k_deadtimeVScale_UIs_f32	0.954999983		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTbIX_UIs_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	568		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	568	568	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.55785704	4.5578599 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.42357612	1.42358005 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	63084	63084 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002	-147.343002 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-147.343002	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	127.972	
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0359999985	
/trCtrl MtrDampTermDax Ohm M f32[1]	0.075000003	
/trCtrl MtrDampTermQax Ohm M f32[0]	0.0419999994	
/trCtrl MtrDampTermQax Ohm M f32[1]	0.0280000009	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.648	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.782000005	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-530.372009	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-420.145996	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017	
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.47300005	
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.94400006	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	736.344971	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	379.115997	
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-7.66699982	
MtrCtrl MtrVoltDaxFF Volt M f32[1]	2.61400008	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999	
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-1.9400006	
	18.5559998	
/trCtrl_Vecu_Volt_M_f32[0]	20.9160004	
/trCtrl_Vecu_Volt_M_f32[1]	7.36499977	
/trCurrDaxPrevIntg_Volt_M_f32		
/trCurrDaxRef_Amp_M_f32[0]	-65.1900024	
/trCurrDaxRef_Amp_M_f32[1]	-216.972	
MtrCurrQaxCog_Amp_M_f32	-41.5750008	
/trCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	
/trCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	
ftrCurrQaxPrevIntg_Volt_M_f32	11.7653999	
ItrCurrQaxRef_Amp_M_f32[0]	31.5869999	
ItrCurrQaxRef_Amp_M_f32[1]	-186.395996	
MtrCurrQaxRpl_Amp_M_f32[0]	-76.8769989	
/trCurrQaxRpl_Amp_M_f32[1]	-153.238998	
MtrPosComputationDelay_Rad_M_f32[0]	-0.195999995	
/trPosComputationDelay_Rad_M_f32[1]	-0.303000003	
/ltrVoltDax_Volt_M_f32[0]	-28.2420006	

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrVoltDax_Volt_M_f32[1] 12.6160002 MtrVoltQax_Volt_M_f32[0] -28.2420006 MtrVoltQax_Volt_M_f32[1] 12.6160002 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.237000003 0.481000006 PICurrCntrl InverterFailSclFac Uls M f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.982200027 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.0469999984 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -627.179993 $PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32$ 64 5255966 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.514999986 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43 1699982 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -627.179993 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 64 5255966 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.514999986 SlowDataAccessBufIndex_Cnt_M_u16 0 $k_CLOAFdbackSignalSclFacSlew_UlspS_f32$ 6110.83008 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7208.8501 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.0909999982 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.172999993 k MtrCurrQaxRefModifDsb Cnt lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc k_MtrVoltDaxIntegHiLim_Volt_f32 27.6355991 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -10.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 26.4790993 k MtrVoltQaxIntegLoLim Volt f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc k VoltSatDaxPolyCoeff Uls f32 8.55000019 k_VoltSatQaxPolyCoeff_Uls_f32 -12 8280001 k deadtimeVScale Uls f32 0.987999976 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t CommOffsetTblY Cnt u16[0] 889 t_CommOffsetTblY_Cnt_u16[1] 1543 0 $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2927 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Actual Value **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 2927 2927 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 ± 1 150 039001 + 7 81F-03 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 150 039001 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -1.24673235 -1.24672997 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.78008938 -4 78008986 + 4 88F-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 33385 33385 ± 1.52588E-05 0 ± 0.0625 MtrCurrDaxPrevIntg Volt M f32 MtrCurrQaxFinalRef_Amp_M_f32[0] 150.039001 150.039001 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[1] 41.6290016 41.6290016 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
htrCtrl MtrCurrDaxMaxVal Amp M f32[0]	6.18900013
	83.0540009
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978
ItrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.079999982
htrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10399997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.256000012
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	458.355011
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1015.39001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.77700001
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.27199996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	309.817993
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-766.486023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
ftrCtrl_Vecu_Volt_M_f32[0]	17.7010002
/ltrCtrl_Vecu_Volt_M_f32[1]	20.0610008
/trCurrDaxPrevIntg_Volt_M_f32	0.851999998
/ltrCurrDaxRef_Amp_M_f32[0]	-146.723007
/trCurrDaxRef_Amp_M_f32[1]	-121.943001
/trCurrQaxCog_Amp_M_f32	48.8400002
/trCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
/trCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
/trCurrQaxPrevIntg_Volt_M_f32	24.9650002
/trCurrQaxRef_Amp_M_f32[0]	-133.947006
1trCurrQaxRef_Amp_M_f32[1]	75.7020035
/trCurrQaxRpl_Amp_M_f32[0]	191.369003
/trCurrQaxRpl_Amp_M_f32[1]	107.137001
MtrPosComputationDelay_Rad_M_f32[0]	-4.36899996
/trPosComputationDelay_Rad_M_f32[1]	-4.83900023
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax Volt M f32[1]	-23.1870003

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



FIGUITORIUI_FELT			OIL CTUSIO
Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.287900001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	70.1921005		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.3741		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	1118		
	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl MtrVoltQaxFFFilt M str.TermN UIs f32	70.1921005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.3741		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.8564997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.1163998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	-25		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1789		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
			Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	65339	65339 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-164.506989	-164.507004 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.4447479	-17.4447002 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	9.78332424	9.78332043 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	4010	4010 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	-5.81056309	-5.81056023 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-164.506989	-164.507004 ± 7.81E-03	✓
MtrCurrOayEinalDef Amp M f32[1]	66 7220006	66 7220006 ± 7 81E 03	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

-66.7229996

-66.7229996 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 2.156 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	79.6729965
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxPrevIntg_Volt_M_f32	-26.8785992
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32[0]	-212.632996
MtrCurrQaxRpl_Amp_M_f32[1]	-205.085007
MtrPosComputationDelay_Rad_M_f32[0]	1.52600002
MtrPosComputationDelay_Rad_M_f32[1]	-2.68400002
MtrVoltDax_Volt_M_f32[0]	-27.0669994
MtrVoltDax_Volt_M_f32[1]	28.1070004
MtrVoltQax_Volt_M_f32[0]	-27.0669994
MtrVoltQax_Volt_M_f32[1]	28.1070004
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.40000006
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0590000004
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.967899978
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5851002
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.5851002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.887899995
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5194.8999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003
k_MtrCtrlVirualResQax_Ohm_f32	0.0219999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1





Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.98460007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5142002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	25		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	*
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.9229965	-87.9229965 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70514989 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95905995 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996	-212.632996 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-87.9229965	-87.9229965 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.157 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.397000015	





Name	
MRCIrt, MrDaxHreprotionalGain, Ohm, M., 132(1) 161.654999 MRCIrt, MrDaxPropotionalGain, Ohm, M., 132(1) 161.654999 MRCIrt, MrDaxPropotionalGain, Ohm, M., 132(1) 161.6599997 161.654999 MRCIrt, MrImpedDax, Ohm, M., 132(1) 0.115999997 MRCIRT, MrImpedDax, Ohm, M., 132(1) 0.0270000007 MRCIRT, MrImpedDax, Ohm, M., 132(1) 0.0270000007 MRCIRT, MrImpedDax, Ohm, M., 132(1) 0.0270000007 MRCIRT, MrImpedDax, Ohm, M., 132(1) 0.0370000007 MRCIRT, MrGaxIntegralGain, Ohm, M., 132(1) 0.63000016 170.535004 MRCIRT, MrGaxIntegralGain, Ohm, M., 132(1) 1.63000016 1.70.535004 MRCIRT, MrGaxIntegralGain, Ohm, M., 132(1) 2.57549992 1.70.535004 MRCIRT, MrGaxIntegralGain, Ohm, M., 132(1) 2.57549992 1.63.02 MRCIRT, MrVarIDaxFF, Volt, M., 132(1) 8.5599984 MRCIRT, MrVarIDaxFF, Volt, M., 132(1) 8.5599984 MRCIRT, MrVarIDaxFF, Volt, M., 132(1) 1.63000 2.82420006 MRCIRT, MrVarIDaxFF, Volt, M., 132(1) 1.640000 1.6500000 1.650000 1.650000 1.6500000 1.6500000 1.6500000 1.6500000 1.6500000 1.6500000 1.6500000 1.6500000 1.650	ue
International Content Inte	
MITCH_MITDRAPPopolonac Can_Ohm_M_S2(1) 253.688004 MITCH_MITDRAPCOX_Ohm_M_S2(1) 0.115999997 MITCH_MITDRAPCOX_Ohm_M_S2(1) 0.0970000027 MITCH_MITDRAPCOX_Ohm_M_S2(1) 0.0970000027 MITCH_MITDRAPCOX_Ohm_M_S2(1) 0.0970000007 MITCH_MITDRAPCOX_Ohm_M_S2(1) 0.0970000007 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.057 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.053000016 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.770.55004 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.770.55004 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.750.55004 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.750.55004 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.750.55004 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.750.55004 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.750.55004 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.750.55009 0.750.55009 MITCH_MITDRAPPOpolonalGain_Ohm_M_S2(1) 0.750.55009	
MitCit MirtinpedDax, Ohm, M. [32[0] MitCit MirtinpedDax, Ohm, M. [32[1] MitCit MirtinpedQax, Ohm, M. [32[1] MitCit MirtinpedQax, Ohm, M. [32[1] MitCit MirtinpedQax, Ohm, M. [32[1] MitCit MirdaxPropoilonalGain, Ohm, M. [32[0] MitCit MirdaxPropoilonalGain, Ohm, M. [32[1] 16. 302 MitCit MirdaxPropoilonalGain, Ohm, M. [32[1] MitCurridaxPropoilonalGain, Ohm, M. [32[1] MitCurridaxP	
MitCit MirimpedDax, Ohm, M. 132[1] 0.159900002 MitCit MirimpedDax, Ohm, M. 132[1] 0.0270000007 MitCit MirimpedDax, Ohm, M. 132[1] 0.0270000007 MitCit MirimpedDax, Ohm, M. 132[0] 0.057 MitCit MirimpedDax, Ohm, M. 132[0] 1.033000016 MitCit MirimpedDax, Ohm, M. 132[0] 1.70.535004 MitCit MirimpedDax, Ohm, M. 132[0] 1.6320 MitCit MirimpedDax, Ohm, M. 132[1] 8.5509984 MitCit MirimpedDax, Ohm, M. 132[1] 8.5509986 MitCit MirimpedDax, Ohm, M. 132[1] 8.5509988 MitCit MirimpedDax, Ohm, M. 132[1] 12.6160002 MitCit MirimpedDax, Ohm, M. 132[1] 12.2490003 MitCit MirimpedDax, Ohm, M. 132[1] 12.249003 MitCit MirimpedDax, Ohm, M. 132[1] 12.8450003 MitCit MirimpedDax, Ohm, M. 132[1] 12.8450003 MitCit MirimpedDax, Ohm, M. 132[1] <	
MitCit_MirtinpedCax_Ohm_M_f32[1]	
MitCirl_MirinpedQax_Ohm_M_[52[1] 0.027000007 MitCirl_MiricanitegralGain_Ohm_M_[52[0] 0.057 MitCirl_MiricanitegralGain_Ohm_M_[52[0] 0.057 MitCirl_MiricanitegralGain_Ohm_M_[52[1] 0.633000016 MitCirl_MiricanitegralGain_Ohm_M_[52[1] 0.633000016 MitCirl_MiricanitegralGain_Ohm_M_[52[0] -22.57549992 MitCirl_MiricanitegralGain_Ohm_M_[52[0] -25.57549992 MitCirl_MiricanitegralGain_Ohm_M_[52[0] -26.302 MitCirl_MiricanitegralGain_Ohm_M_[52[0] -28.2420006 MitCirl_MiricanitegralGain_Ohm_M_[52[0] -28.2420006 MitCirl_MiricanitegralGain_Ohm_M_[52[0] 18.5559998 MitCirl_MiricanitegralGain_Ohm_M_[52[0] 18.5559998 MitCirl_MiricanitegralGain_Ohm_M_[52[0] 18.5559998 MitCirl_MiricanitegralGain_Ohm_M_[52[0] 29.9650002 49.9650002	
MITCH_MITCaxIntegralGain_Ohm_M_52[0] 0.057	029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_[32[1] 0.633000018 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_[32[1] -170.535004 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[0] -16.302 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[1] 8.55099964 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[1] 126.5009964 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[1] 126.600002 MtrCtrl_MtrVoltDaxFF_Volt_M_[32[1] 126.600002 MtrCtrl_Vocu_Volt_M_[32[1] 20.9160004 MtrCtrl_DaxFrevIntg_Volt_M_[32[0] 20.950002 MtrCurnDaxFrevIntg_Volt_M_[32[0] 20.950002 MtrCurnDaxFrevIntg_Volt_M_[32[0] 37.4550018 MtrCurnDaxFrevIntg_Volt_M_[32[0] 37.4550018 MtrCurnDaxFrevIntg_Volt_M_[32[0] 37.4550018 MtrCurnDaxFrevIntg_Volt_M_[32[0] 24.6130009 MtrCurnDaxFrevIntg_Volt_M_[32[0] 26.8180003 MtrCurnDaxFrevIntg_Volt_M_[32[0] 26.880003 MtrCurnDaxFrevIntg_Volt_M_[32[0] 26.890003 MtrCurnDaxFrevIntg_Volt_M_[32[0] 26.890003 MtrCurnDaxFrevIntg_Volt_M_[32[0] 29.749999 MtrCurnDaxFrevIntg_Volt_M_[32[0] 29.749999 MtrCurnDaxFrevIntg_Volt_M_[32[0] 29.749999	007
MICCIT_MINGaxPropotionalGain_Ohm_M_[32[t]]	
MitCrit_MirQaxPropotionalGain_Ohm_M_[32[1] -25.7549992 MitCrit_MirVoltDaxFF_Volt_M_[32[1] -16.302 MitCrit_MirVoltQaxFF_Volt_M_[32[1] -8.5599984 MitCrit_MirVoltQaxFF_Volt_M_[32[1] -28.2420006 MitCrit_MirVoltQaxFF_Volt_M_[32[1] 12.6160002 MitCrit_Voeu_Volt_M_[32[1] 20.9160004 MitCrit_Voeu_Volt_M_[32[1] 20.9160004 MitCrit_DaxPrevintg_Volt_M_[32[1] -28.569998 MitCrit_DaxRef_Amp_M_[32[0] 20.952002 MitCrit_DaxRef_Amp_M_[32[0] -124.994003 MitCrit_DaxRef_Amp_M_[32[0] 37.4550018 MitCrit_DaxRef_Amp_M_[32[0] 37.4550018 MitCrit_DaxRef_Amp_M_[32[0] 37.4550018 MitCrit_DaxRef_Amp_M_[32[0] 28.450000 MitCrit_DaxRef_Amp_M_[32[0] 21.8451004 MitCrit_DaxRef_Amp_M_[32[0] 6.1890013 MitCrit_DaxRef_Amp_M_[32[0] 6.1890013 MitCrit_DaxRef_Amp_M_[32[0] 6.1890013 MitCrit_DaxRef_Amp_M_[32[0] 6.1890013 MitCrit_DaxRef_Amp_M_[32[0] 6.1890013 MitCrit_DaxRef_Amp_M_[32[0] 6.1890013 MitCrit_DaxRef_Amp_M_[32[0] 6.18900	16
MitCitz MirQaxPropotionalGain_Ohm_M_[32[1] -25.7549992 MitCitz MivVoltbaxFF_Volt_M_[32[1] -16.302 MitCitz MivVoltbaxFF_Volt_M_[32[1] -8.5599984 MitCitz MivVoltbaxFF_Volt_M_[32[0] -28.2420006 MitCitz MivVoltbaxFF_Volt_M_[32[1] 12.6160002 MitCitz MivVoltbaxFF_Volt_M_[32[1] 20.916000 MitCitz Veou_Volt_M_[32[1] 20.9160004 MitCurrDaxRef_Amp_M_[32[1] -8.5659998 MitCurrDaxRef_Amp_M_[32[0] 20.952002 MitCurrDaxRef_Amp_M_[32[1] -124.994003 MitCurrDaxRef_Amp_M_[32[1] -124.994003 MitCurrDaxRef_Amp_M_[32[1] -124.994003 MitCurrDaxRef_Amp_M_[32[1] -124.994003 MitCurrDaxRef_Amp_M_[32[0] 37.455018 MitCurrDaxRef_Amp_M_[32[0] 2.8450003 MitCurrDaxRef_Amp_M_[32[0] 2.284500003 MitCurrDaxRef_Amp_M_[32[0] 3.12845000 MitCurrDaxRef_Amp_M_[32[0] 6.1890013 MitCurrDaxRef_Amp_M_[32[0] 6.1890013 MitCurrDaxRef_Amp_M_[32[0] 6.1890013 MitCurrDaxRef_Amp_M_[32[0] 6.1890013 MitCurrDaxRef_Amp_M_[32[0] 6.1890013	04
MtCtrl_MtVottDaxFF_Volt_M_r32[1] 8.5599984 MtCtrl_MtVoltDaxFF_Volt_M_r32[1] 8.5599984 MtCtrl_MtVoltDaxFF_Volt_M_r32[1] 12.6160002 MtCtrl_MtVoltDaxFF_Volt_M_r32[1] 12.6160002 MtCtrl_Vecu_Volt_M_r32[1] 12.6160002 MtCtrl_Vecu_Volt_M_r32[1] 20.9160004 MtCtrl_DaxPrevIntg_Volt_M_r32[1] 20.9160004 MtCtrlDaxPrevIntg_Volt_M_r32[1] 20.9160004 MtCtrlDaxPrevIntg_Volt_M_r32[1] 20.9052002 MtCtrlDaxPrevIntg_Volt_M_r32[1] 1-124.994003 MtCtrlDaxRef_Amp_M_r32[0] 37.4550018 MtCtrlDaxFrevIntg_Volt_M_r32 5.72399998 MtCtrlDaxFinalRef_Amp_M_r32[0] 37.4550018 MtCtrlDaxFinalRef_Amp_M_r32[0] 37.4550018 MtCtrlDaxFinalRef_Amp_M_r32[0] 24.6130009 MtCtrlDaxFrevIntg_Volt_M_r32 12.8451004 MtCtrlDaxRef_Amp_M_r32[0] 24.6130009 MtCtrlDaxRef_Amp_M_r32[1] 20.9400005 MtCtrlDaxRef_Amp_M_r32[1] 30.9400005 MtCtrlDaxRef_Amp_M_r32[1] 30.9400005 MtCtrlDaxRef_Amp_M_r32[1] 30.9400005 MtCtrlDaxRef_Amp_M_r32[1] 30.9400005 MtCtrlDaxRef_Amp_M_r32[1] 30.9400005 MtCtrlDaxRef_Amp_M_r32[1] 30.9400005 MtVoltDax_Volt_M_r32[1] 30.94000005 MtVoltDax_Volt_M_r32[1] 30.9400005 MtVoltDax_Volt_M_r32[1] 30.9400005 MtVoltDax_Volt_M_r32[1] 30.9400005 MtVoltDax_Volt_M_r32[1] 30.9400005 MtVoltDax_Volt_M_r32[1] 30.9400005 MtVoltDax_Volt_M_r32[1] 30.9400005 MtVoltDax_Volt_M_r32[1] 30.94000005 MtVoltDax_Volt_M_r32[1] 30.9400000000000000000000000000000000000	92
MitChI_MitVoliDaxFF_Volt_M_132[1] 8.55099964 MitChI_MitVoliDaxFF_Volt_M_132[0] -28.2420006 MitChI_MitVoliDaxFF_Volt_M_132[0] 12.6160002 MitChI_MitVoliDaxFF_Volt_M_132[0] 18.555998 MitChI_MitVoliDaxFF_Volt_M_132[1] 20.9160004 MitCurrDaxRer_Amp_M_132[1] -12.699998 MitCurrDaxRer_Amp_M_132[1] -12.4994003 MitCurrQaxFinalRer_Amp_M_132[1] -12.4994003 MitCurrQaxFinalRer_Amp_M_132[0] 37.4550018 MitCurrQaxFinalRer_Amp_M_132[0] 37.4550018 MitCurrQaxFinalRer_Amp_M_132[0] 24.6130009 MitCurrQaxFinalRer_Amp_M_132[0] 24.6130009 MitCurrQaxRep_Amp_M_132[0] 61.8900013 MitCurrQaxRep_Amp_M_132[0] 61.8900013 MitCurrQaxRep_Amp_M_132[0] 61.8900013 MitPosComputationDelay_Rad_M_132[1] 04.8699999 MitPosComputationDelay_Rad_M_132[1] 04.8699999 MitVoltDax_Volt_M_132[1] 21.3880005 MitVoltDax_Volt_M_132[1] 21.3880005 MitVoltQax_Volt_M_132[1] 21.3880005 PICurrCnit_MitVoutDax_StaftuxRatio_Uis_M_132 0.046000019 PICurrCnit_MitVoutD	
MtrChrl_MtrVotiQaxFF_Volt_M_132[0] .28.2420006 MtrChrl_MtrVotiQaxFF_Volt_M_132[1] .12.6160002 .18.555998 MtrChrl_Vecu_Volt_M_132[0] .18.555998 MtrChrl_Vecu_Volt_M_132[1] .20.9160004 .18.555998 MtrChrl_Vecu_Volt_M_132[1] .20.9160004 .18.555998 MtrChrl_DaxPreving_Volt_M_132[0] .20.9052002 .20.9052002 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059998 .18.5059999 .18.5059999 .18.5059999 .18.5059999 .18.5059999 .18.505999999 .18.50599999 .18.50599999 .18.50599999 .18.50599999 .18.50599999 .18.50599999 .18.50599999 .18.5059999999 .18.5059999999 .18.5059999999 .18.5059999999 .18.5059999999 .18.50599999999999999999999999999999999999	1
MtrCtrl_MtrVoltQaxFF_Volt_M_[32[1] 12.6160002 MtrCtrl_Veou_Volt_M_[32[1] 18.5559998 MtrCtrl_Veou_Volt_M_[32[1] 20.9160004 MtrCurrDaxPrevintg_Volt_M_[32] -8.56599998 MtrCurrDaxFed_Amp_M_[32[1] -124.994003 MtrCurrQaxCog_Amp_M_[32] 5.72399998 MtrCurrQaxCog_Amp_M_[32] 37.4550018 MtrCurrQaxFend_Amp_M_[32[1] -2.84500003 MtrCurrQaxPrevintg_Volt_M_[32] 12.8451004 MtrCurrQaxPrevintg_Volt_M_[32] 12.8451004 MtrCurrQaxFel_Amp_M_[32[1] -2.8400003 MtrCurrQaxFel_Amp_M_[32[1] -2.8400003 MtrCurrQaxFel_Amp_M_[32[1] -2.8400003 MtrCurrQaxFel_Amp_M_[32[1] -8.1800003 MtrCurrQaxFel_Amp_M_[32[0] -8.890003 MtrVoltQax_Volt_M_[32[1] -0.486999989 MtrVoltDax_Volt_M_[32[1] -0.486999989 MtrVoltDax_Volt_M_[32[1] -2.53770008 MtrVoltQax_Volt_M_[32[1] -2.380005 MtrVoltQax_Volt_M_[32[1] -2.380005 MtrVoltQax_Volt_M_[32[1] -2.380000 MtrVoltQax_Volt_M_[32[1] -2.380005 MtrVoltQax_Volt	
MITCIT_Vecu_Volt_M_[32]0] 18.5559988 MITCIT_Vecu_Volt_M_[32]1 20.9160004 MITCUTDaxRef_Amp_M_[32]0] 29.9659092 MITCUTDaxRef_Amp_M_[32]0] 209.052002 MITCUTDaxRef_Amp_M_[32]1 -124.994003 MITCUTDaxRef_Amp_M_[32]1 -124.994003 MITCUTQaxFinalRef_Amp_M_[32]1 -2.84500003 MITCUTQaxFinalRef_Amp_M_[32]1 -2.84500003 MITCUTQaxRef_Amp_M_[32]0 12.8451004 MITCUTQaxRef_Amp_M_[32]0 24.6130009 MITCUTQaxRef_Amp_M_[32]0 6.18900013 MITCUTQaxRef_Amp_M_[32]0 6.18900013 MITCUTQaxRef_Amp_M_[32]0 6.18900013 MITCUTQaxRef_Amp_M_[32]0 6.18900013 MITCUTQaxRef_Amp_M_[32]0 6.18900013 MITCUTQaxRef_Amp_M_[32]0 2.974999 MITCOTQaxRef_Amp_M_[32]0 2.974999 MITCOTQaxRef_Amp_M_[32]0 2.974999 MITVOIDax_Volt_M_[32]1 0.486999989 MITVOIDax_Volt_M_[32]1 2.974999 MITVOIDax_Volt_M_[32]1 2.93770008 MITVOIDax_Volt_M_[32]1 2.1380005 MITVOIDax_Volt_M_[32]1 2.1380005	
MitChi_Vecu_Voit_M_[32](1) 20.9160004 MitCurDaxPrevintg_Voit_M_[32] 48.6599998 MitCurDaxRef_Amp_M_[32](1) -124.994003 MitCurDaxRef_Amp_M_[32](1) -124.994003 MitCurDaxRef_Amp_M_[32](1) -124.994003 MitCurQaxFinalRef_Amp_M_[32](1) -2.84500003 MitCurQaxFinalRef_Amp_M_[32](1) -2.84500003 MitCurQaxRef_Amp_M_[32](1) -2.84500003 MitCurQaxRef_Amp_M_[32](1) -2.8451004 MitCurQaxRef_Amp_M_[32](1) -2.9400005 MitCurQaxRef_Amp_M_[32](1) -2.9400005 MitCurQaxRef_Amp_M_[32](1) -3.960000 MitCurQaxRef_Amp_M_[32](1) -3.9460009 MitPosComputationDelay_Rad_M_[52](0) -2.974999 MitPosComputationDelay_Rad_M_[52](1) -2.53770008 MitVoliDax_Volt_M_[32](1) -21.3880005 MitVoliDax_Volt_M_[32](1) -21.3880005 MitVoliDax_Volt_M_[32](1) -21.3880005 MitVoliDax_Volt_M_[32](1) -21.3880005 MitVoliDax_Volt_M_[32](1) -21.3880005 PICurrCntrl_MrourDaxSatFluxRatio_Uis_M_[32] -0.833000004 PICurrCntrl_MrourDaxSatFluxRatio_Uis_M_[32]	
MtrCurrDaxPrevintg_Volt_M_f32 -8.5659998 MtrCurrDaxRef_Amp_M_f32(I) 200.052002 MtrCurrDaxRef_Amp_M_f32(I) -124.994003 MtrCurrQaxCog_Amp_M_f32 5.72399998 MtrCurrQaxFinalRef_Amp_M_f32(I) -2.84500003 MtrCurrQaxFinalRef_Amp_M_f32(I) -2.84500003 MtrCurrQaxRef_Amp_M_f32(I) -2.84500003 MtrCurrQaxRef_Amp_M_f32(I) -2.0440005 MtrCurrQaxRef_Amp_M_f32(I) -2.0440005 MtrCurrQaxRef_Amp_M_f32(I) -8.05600013 MtrOurQaxRef_Amp_M_f32(I) -8.0600013 MtrOurQaxRef_Amp_M_f32(I) -8.0600013 MtrOurQaxRef_Amp_M_f32(I) -8.0600013 MtrOurQaxRef_Amp_M_f32(I) -8.0600013 MtrOurQaxRef_Amp_M_f32(I) -8.06000013 MtrOurDaxRef_Amp_M_f32(I) -9.48000013 MtrOurDaxRef_Amp_M_f32(I) -9.48000001 MtrOurDaxRef_Amp_M_f32(I) -9.06000001 MtrVoltQax_Volt_M_f32(I) -2.53770008 MtrVoltQax_Volt_M_f32(I) -2.53770008 MtrVoltQax_Volt_M_f32(I) -2.1380005 MtrVoltQax_Volt_M_f32(I) -2.1380005 MtrVolt_MxVolt_M	
MtrCurrDaxRef_Amp_M_[32[1] -124.994003 MtrCurrDaxRef_Amp_M_[32[1] -124.994003 MtrCurrQaxFinalRef_Amp_M_[32[0] 37.4550018 MtrCurrQaxFinalRef_Amp_M_[32[1] -2.84500003 MtrCurrQaxPrevintg_Volt_M_[32] 12.8451004 MtrCurrQaxRef_Amp_M_[32[0] 24.6130009 MtrCurrQaxRef_Amp_M_[32[0] 24.6130009 MtrCurrQaxRef_Amp_M_[32[1] -20.9400005 MtrCurrQaxRef_Amp_M_[32[1] 83.054009 MtrDaxComputationDelay_Rad_M_[32[0] 29.749999 MtrPosComputationDelay_Rad_M_[32[0] 25.3770008 MtrVolIDax_Volt_M_[32[1] 04.86999989 MtrVolIDax_Volt_M_[32[1] 21.3880005 MtrVolIDax_Volt_M_[32[1] 21.3880005 MtrVolIDax_Volt_M_[32[1] 21.3880005 PICurrCntt_Cntf_CurrSensFailScifac_Uis_M_[32] 0.0000000019 PICurrCntt_MrCurrDaxSatFluxRatio_Uis_M_[32] 0.833000004 PICurrCntt_MrtvcurrDaxSatFluxRatio_Uis_M_[32] 0.833000004 PICurrCntt_Mrtvcurriit_M_str.TermD_Uis_[32] 1118 PICurrCntt_Mrtvcurriit_M_str.TermD_Uis_[32] 0.461600006 PICurrCnttf_MrtvollQaxFFFiit_M_str.TermD_Uis_[32] 0.717.29998	4
MtrCurrDaxRef_Amp_M_[32[1] -124.994003 MtrCurrQaxFinalRef_Amp_M_[32[0] 5.72399998 MtrCurrQaxFinalRef_Amp_M_[32[1] -2.84500003 MtrCurrQaxFinalRef_Amp_M_[32[1] -2.84500003 MtrCurrQaxRef_Amp_M_[32[0] -2.84500003 MtrCurrQaxRef_Amp_M_[32[1] -20.9400005 MtrCurrQaxRef_Amp_M_[32[1] -20.9400005 MtrCurrQaxRef_Amp_M_[32[1] -6.18900013 MtrCurrQaxRef_Amp_M_[32[1] 83.0540009 MtrPosComputationDelay_Rad_M_[32[0] -2.9749999 MtrPosComputationDelay_Rad_M_[32[1] 0.486999989 MtrVollDax_Voit_M_[32[0] -25.3770008 MtrVollDax_Voit_M_[32[0] -25.3770008 MtrVollQax_Voit_M_[32[0] -26.3770008 MtrVollQax_Voit_M_[32[0] -26.3770008	98
MtrCurrQaxCog_Amp_M_f32 5.72399998 MtrCurrQaxFinalRef_Amp_M_f32[0] 37.4550018 MtrCurrQaxFinalRef_Amp_M_f32[1] -2.8450003 MtrCurrQaxRef_Amp_M_f32[0] 12.2461004 MtrCurrQaxRef_Amp_M_f32[0] 24.6130009 MtrCurrQaxRef_Amp_M_f32[1] -20.9400005 MtrCurrQaxRef_Amp_M_f32[1] 83.0540009 MtrPosComputationDelay_Rad_M_f32[0] 2.9749999 MtrVoltDax_Volt_M_f32[0] -25.377008 MtrVoltDax_Volt_M_f32[1] 0.486999989 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 PICurrCntt_Im_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 PICurrCnttl_Mf32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 PICurrCnttl_Mf22[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 PICurrCnttl_MtrVacuFittl_M_str_Frevirput_Us_f32 0.0000000019 PICurrCntt_MtrCurrDaxSafFituxRatio_Us_M_f32 0.8339000004	2
MtrCurrQaxFinalRef_Amp_M_[32[0] 37.4550018 MtrCurrQaxFreiving_Voll_M_[32] 2.84500003 MtrCurrQaxPreiving_Voll_M_[32] 12.8451004 MtrCurrQaxRef_Amp_M_[32[0] 24.6130009 MtrCurrQaxRef_Amp_M_[32[1] -20.9400005 MtrCurrQaxRpl_Amp_M_[32[1] 83.0540009 MtrDurrCaxRpl_Amp_M_[32[1] 83.0540009 MtrPosComputationDelay_Rad_M_[32[1] 0.486999989 MtrVoltDax_Voll_M_[32[1] 25.3770008 MtrVoltDax_Voll_M_[32[1] 21.3880005 MtrVoltQax_Voll_M_[32[1] 22.53770008 MtrVoltQax_Voll_M_[32[1] 21.3880005 MtrVoltQax_Voll_M_[32[1] 21.3880005 MtrVoltQax_Voll_M_[32[1] 21.3880005 PICurrCntt_CurrSensFailSclFac_Uls_M_[32] 0.70200022 PICurrCntt_M_CurrSensFailSclFac_Uls_M_[32] 0.0040000019 PICurrCntt_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.85379999 PICurrCntt_MtrVoruPaxSatFluxRatio_Uls_M_[32] 0.85379999 PICurrCntt_MtrVoruPaxSatFluxRatio_Uls_M_[32] 1118 PICurrCntt_MtrVoruPaxSatFlux_Mail_TremD_Uls_[32] 717.29988 PICurrCntt_MtrVoruPaxSatFlux_M_[32] 0.461600006	03
MtrCurrQaxFinalRef_Amp_M_[32[0] 37.4550018 MtrCurrQaxFreiving_Voll_M_[32] 2.84500003 MtrCurrQaxPreiving_Voll_M_[32] 12.8451004 MtrCurrQaxRef_Amp_M_[32[0] 24.6130009 MtrCurrQaxRef_Amp_M_[32[1] -20.9400005 MtrCurrQaxRpl_Amp_M_[32[1] 83.0540009 MtrDurrCaxRpl_Amp_M_[32[1] 83.0540009 MtrPosComputationDelay_Rad_M_[32[1] 0.486999989 MtrVoltDax_Voll_M_[32[1] 25.3770008 MtrVoltDax_Voll_M_[32[1] 21.3880005 MtrVoltQax_Voll_M_[32[1] 22.53770008 MtrVoltQax_Voll_M_[32[1] 21.3880005 MtrVoltQax_Voll_M_[32[1] 21.3880005 MtrVoltQax_Voll_M_[32[1] 21.3880005 PICurrCntt_CurrSensFailSclFac_Uls_M_[32] 0.70200022 PICurrCntt_M_CurrSensFailSclFac_Uls_M_[32] 0.0040000019 PICurrCntt_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.85379999 PICurrCntt_MtrVoruPaxSatFluxRatio_Uls_M_[32] 0.85379999 PICurrCntt_MtrVoruPaxSatFluxRatio_Uls_M_[32] 1118 PICurrCntt_MtrVoruPaxSatFlux_Mail_TremD_Uls_[32] 717.29988 PICurrCntt_MtrVoruPaxSatFlux_M_[32] 0.461600006	
MtrCurrQaxFinalRef_Amp_M_f32[1] -2.84500003 MtrCurrQaxRevIntg_Volt_M_f32 12.8451004 MtrCurrQaxRef_Amp_M_f32[0] 24.6130009 MtrCurrQaxRef_Amp_M_f32[1] -20.9400005 MtrCurrQaxRpl_Amp_M_f32[1] 83.0540009 MtrPosComputationDelay_Rad_M_f32[0] 29.749999 MtrPosComputationDelay_Rad_M_f32[1] 0.48699989 MtrVolIDax_Volt_M_f32[0] -25.3770008 MtrVolIDax_Volt_M_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 PICurrCntrl_Mf32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 PICurrCntrl_MtrVoll_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 MtrVolIDax_Volt_M_f32[1] 21.3880005 PICurrCntrl_MtrVoluCaxFEFILM_str.TermD_Uls_f32 37.7729988 PICur	
MtrCurrQaxPrevintg_Volt_M_f32 12.8451004 MtrCurrQaxRef_Amp_M_f32[0] 24.6130009 MtrCurrQaxRef_Amp_M_f32[1] -20.9400005 MtrCurrQaxRef_Amp_M_f32[0] 6.18900013 MtrCurrQaxRp_Amp_M_f32[1] 83.0540009 MtrPosComputationDelay_Rad_M_f32[0] 2.9749999 MtrPosComputationDelay_Rad_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltCax_Volt_M_f32[1] 21.3880005 MtrVoltCax_Volt_M_f32[1] 21.3880005 PICurrCntrl_CurrSensFailSciFac_Uls_M_f32 0.702000022 PICurrCntrl_InverterFailSciFac_Uls_M_f32 0.04000000019 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.85379999 PICurrCntrl_MtrVecuFilt_M_str.PrevUnput_Uls_f32 1118 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 7-717.29988 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 7-717.29988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 7-717.29988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 <t< td=""><td></td></t<>	
MtrCurrQaxRef_Amp_M_f32[0] 24.6130009 MtrCurrQaxRef_Amp_M_f32[1] -20.9400005 MtrCurrQaxRef_Amp_M_f32[0] 6.18900013 MtrCurrQaxRpl_Amp_M_f32[1] 83.0540009 MtrPosComputationDelay_Rad_M_f32[0] 2.9749999 MtrPosComputationDelay_Rad_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltOax_Volt_M_f32[1] 21.3880005 MtrVoltOax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltOax_Volt_M_f32[1] 21.3880005 MtrVoltOax_Volt_M_f32[1] 21.3880005 MtrVoltOax_Volt_M_f32[1] 21.3880005 MtrVoltOax_Volt_M_f32[1] 21.3880005 MtrVoltOax_Volt_M_f32[1] 21.3880005 MtrVoltOax_Volt_M_f32[1] 21.3880005 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.00400000019 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.833000004 PlCurrCntrl_MtrVoltQaxSatFluxRatio_Uls_f32 1118 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.461600006 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.717.299988 PlCurrCntrl_Mtr	
MtrCurrQaxRef_Amp_M_[32[1] -20.9400005 MtrCurrQaxRpl_Amp_M_[32[0] 6.18900013 MtrCurrQaxRpl_Amp_M_[32[1] 83.0540009 MtrPosComputationDelay_Rad_M_[32[0] 2.9749999 MtrPosComputationDelay_Rad_M_[32[1] 0.486999989 MtrVoltDax_Volt_M_[32[0] -25.3770008 MtrVoltDax_Volt_M_[32[1] 21.3880005 MtrVoltQax_Volt_M_[32[1] 21.3880005 PlCurrCntr_M_[32[1] 21.3880005 PlCurrCntr_M_[32[1] 21.3880005 PlCurrCntr_M_[4] 22.377008 MtrVoltQax_Volt_M_[32[1] 21.3880005 PlCurrCntr_M_[4] 0.702000022 PlCurrCntr_M_[4] 0.853799999 PlCurrCntr_M_[4] 0.853799999 PlCurrCntr_M_[4] MtrVecuFilt_M_[4] MtrVecuFilt_M_[4] Mst. Previoput_Uis_[52] PlCurrCntr_M_[4] Mst. Previo	
MtrCurrQaxRpl_Amp_M_f32[0] 6,18900013 MtrCurrQaxRpl_Amp_M_f32[1] 83.0540009 MtrPosComputationDelay_Rad_M_f32[0] 2,9749999 MtrPosComputationDelay_Rad_M_f32[1] 0,48699989 MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltQax_Volt_M_f32[1] 21.3880005 MtrVoltCax_Volt_M_f32[1] 21.3880005 MtrVoltCax_Volt_M_f32[1] 21.3880005 PlCurrCntr_Cntr_CurrSensFailSclFac_Uls_M_f32 0.70200022 PlCurrCntr_IntrCurrCavsSatFluxRatio_Uls_M_f32 0.04000000019 PlCurrCntr_MtrCurrCavsSatFluxRatio_Uls_M_f32 0.833000004 PlCurrCntr_MtrCurrCavsSatFluxRatio_Uls_M_f32 1118 PlCurrCntr_MtrVecuFilt_M_str.PrevOutput_Uls_f32 717.29998 PlCurrCntr_MtrVecuFilt_M_str.TermD_Uls_f32 87.3075027 PlCurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 1118 PlCurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PlCurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PlCurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PlCurrCntr_	
MtrCurrQaxRpl_Amp_M_32[1] 83.0540009 MtrPosComputationDelay_Rad_M_63[0] 2.9749999 MtrPosComputationDelay_Rad_M_63[1] 0.486999899 MtrVoltDax_Volt_M_632[1] 21.3880005 MtrVoltDax_Volt_M_632[1] 21.3880005 MtrVoltQax_Volt_M_632[1] 21.3880005 MtrVoltQax_Volt_M_632[1] 21.3880005 MtrVoltQax_Volt_M_632[1] 21.3880005 MtrVoltQax_Volt_M_632[1] 21.3880005 MtrVoltQax_Volt_M_632[1] 21.3880005 PlCurrCntrl_CurrSensFallSclFac_Uls_M_632 0.702000022 PlCurrCntrl_MtrVerelailSclFac_Uls_M_632 0.00400000019 PlCurrCntrl_MtrVecuFiit_M_str.FrevIls_632 1118 PlCurrCntrl_MtrVecuFiit_M_str.PrevIlput_Uls_632 1118 PlCurrCntrl_MtrVecuFiit_M_str.FrevD_Uls_632 777.29998 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.PrevIlput_Uls_632 0.461600006 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_632 777.29998 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_632 37.3075027 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_632 37.3075027 PlCurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_632 5911.31982 R_ILOAFdbackSigna	
MtrPosComputationDelay_Rad_M_[32[0] 2.9749999 MtrPosComputationDelay_Rad_M_[32[1] 0.486999899 MtrVoltDax_Volt_M_[32[0] -25.3770008 MtrVoltDax_Volt_M_[32[0] -25.3770008 MtrVoltQax_Volt_M_[32[0] -25.3770008 MtrVoltQax_FILE_M_[32[0] -25.3770008 MtrVoltQax_FILE_M_[32[0] -26.3800005 PICurrCntrl_MtrVoltCurSatFFill_M_str_PrevInput_Uls_[32] 0.83000004 PICurrCntrl_MtrVoltQax_FILE_M_str_TermD_Uls_[32] 1118 PICurrCntrl_MtrVoltQax_FILE_M_str_TermD_Uls_[32] 1118 PICurrCntrl_MtrVoltQax_FILE_M_str_TermD_Uls_[32] 1118 PICurrCntrl_MtrVoltQax_FILE_M_str_TermD_Uls_[32] 717.299988 PICurrCntrl_MtrVoltQax_FILE_M_str_TermD_Uls_[32] 717.299988 PICurrCntrl_MtrVoltQax_FILE_M_str_TermD_Uls_[32] 0.461600006 SlowDataAccessBuflindex_Cnt_M_u16 0	
MtrPosComputationDelay_Rad_M_[32[1] 0.48699989 MtrVoltDax_Volt_M_[32[0] -25.3770008 MtrVoltDax_Volt_M_[32[0] -25.3770008 MtrVoltDax_Volt_M_[32[0] -25.3770008 MtrVoltQax_Volt_M_[32[1] 21.3880005 MtrVoltQax_Volt_M_[32[1] 21.3880005 PICurrCntrl_CurrSensFailSclFac_UIs_M_[32] 0.702000022 PICurrCntrl_InverterFailSclFac_UIs_M_[32] 0.853799999 PICurrCntrl_MtrCurrQaxSatFluxRatio_UIs_M_[32] 0.833000004 PICurrCntrl_MtrVecuFiit_M_str.Prevlnput_UIs_[32] 1118 PICurrCntrl_MtrVecuFiit_M_str.Prevlnput_UIs_[32] 717.299988 PICurrCntrl_MtrVecuFiit_M_str.TermD_UIs_[32] 87.3075027 PICurrCntrl_MtrVoltQaxFFFiit_M_str.Prevlouput_UIs_[32] 1118 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_UIS_[32] 1118 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_UIS_[32] 87.3075027 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_UIS_[32] 87.3075027 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_UIS_[32] 9.61600006 SlowDataAccessBuflindex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UIspS_[32] 5911.31982 k_IntrCutrIceabackSonatSclFacSlew_UIspS_[32] 0.1	9
MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] -25.3770008 MtrVoltQax_Volt_M_f32[1] 21.3880005 PICurrCntd_CurrSensFailScIFac_Uls_M_f32 0.702000022 PICurrCntrl_InverterFailScIFac_Uls_M_f32 0.00400000019 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl_MtrVecuFiit_M_str.Prevloutput_Uls_f32 1118 PICurrCntrl_MtrVecuFiit_M_str.Prevloutput_Uls_f32 1118 PICurrCntrl_MtrVecuFiit_M_str.TermN_Uls_f32 7-77.29988 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 0.461600006 PICurrCntrl_MtrVoltQaxFFFiit_M_str.Prevloutput_Uls_f32 1118 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 7-77.29988 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 7-77.29988 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 7-71.299988 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 7-71.299988 PICurrCott_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 0.461600006 SlowDataAccessBufindex_Cnt_M_u16 0 K_CLOAFdbackSignalSclFacSlew_UlspS_f32 5911.31982 k_ILOAFdbackSignalSclFacSlew_UlspS_f32	
MtrVoltDax_Volt_M_[32[1] 21.3880005 MtrVoltQax_Volt_M_[32[0] -25.3770008 MtrVoltQax_Volt_M_[32[1] 21.3880005 PICurrCntr_CurrSensFailSclFac_Uls_M_[32] 0.70200022 PICurrCntr_I_InverterFailSclFac_Uls_M_[32] 0.00400000019 PICurrCntr_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.833000004 PICurrCntr_MtrVecuFilt_M_str.PrevInput_Uls_[32] 1118 PICurrCntr_MtrVecuFilt_M_str.PrevOutput_Uls_[32] -717.299988 PICurrCntr_MtrVecuFilt_M_str.TermD_Uls_[32] 0.461600006 PICurrCntr_MtrVecuFilt_M_str.TermD_Uls_[32] 0.461600006 PICurrCntr_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_[32] -717.299988 PICurrCntr_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_[32] -717.299988 PICurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 37.3075027 PICurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 37.3075027 PICurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 37.3075027 PICurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 0.461600006 SlowDataAccessBufindex_Cnt_M_uf6 0 K_LOAFdbackSignalSclFacSlew_UlspS_[32] 5911.31982 K_MtrCltrVirualResQax_Ohm_[32] 0.144999996 K_MtrCltrVirua	89
MtrVoltDax_Volt_M_[32[1] 21.3880005 MtrVoltQax_Volt_M_[32[0] -25.3770008 MtrVoltQax_Volt_M_[32[1] 21.3880005 PICurrCntrl_CurSensFailSclFac_Uls_M_[32] 0.70200022 PICurrCntrl_InverterFailSclFac_Uls_M_[32] 0.00400000019 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_[32] 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_[32] -717.299988 PICurCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_[32] 87.3075027 PICurCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 0.461600006 PICurCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_[32] 1118 PICurCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_[32] 717.299988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 37.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 37.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 37.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 37.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 0.461600006 SlowDataAccessBufindex_Cnt_M_uf6 0 k_LOLAFdbackSignalSclFacSlew_UlspS_[32] 5911.31982 <tr< td=""><td>08</td></tr<>	08
MtrVoltQax_Volt_M_[32[0] -25.3770008 MtrVoltQax_Volt_M_[32[1] 21.3880005 PICurrCntrl_CurrSensFailSclFac_Uls_M_[32] 0.70200002 PICurrCntrl_MtrveterFailSclFac_Uls_M_[32] 0.00400000019 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.833799999 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_[32] 1118 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_[32] -717.29988 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 87.3075027 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 0.461600006 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_[32] 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 717.29988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 87.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 87.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 0.461600006 SlowDataAccessBufindex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_[32] 5911.31982 k_ILOAFdbackSignalSclFacSlew_UlspS_[32] 5911.31982 k_IntrCrit/urloupSecOrTranFcEnable_Cnt_Igc 0 k_MtrCrit	
MtrVoltQax_Volt_M_[32][1] 21.3880005 PICurrCntrl_CurrSensFailSclFac_Uls_M_[32] 0.702000022 PICurrCntrl_InverterFailSclFac_Uls_M_[32] 0.00400000019 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.853799999 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_[32] 1118 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_[32] -717.299988 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_[32] 0.461600006 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 0.461600006 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_[32] 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_[32] 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_[32] 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_[32] 87.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 87.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 0.461600006 SlowDataAccessBuflndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_[32] 5911.31982 k_ILOAFdbackSignalSclFacSlew_UlspS_[32] 5911.31982 k_MtrCrtICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCrtICurrLoopSecOrTranFcEnable_Cnt_lgc 1	
PiCurrCntrl_CurrSensFailSciFac_Uls_M_f32 0.702000022 PiCurrCntrl_InverterFailSciFac_Uls_M_f32 0.0040000019 PiCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.85379999 PiCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PiCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 1118 PiCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 7-717.29988 PiCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 7-717.29988 PiCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.461600006 PiCurrCntrl_MtrVotiQaxFFFilt_M_str.PrevInput_Uls_f32 1118 PiCurrCntrl_MtrVotiQaxFFFilt_M_str.PrevInput_Uls_f32 0.461600006 PiCurrCntrl_MtrVotiQaxFFFilt_M_str.PrevInput_Uls_f32 7-717.29988 PiCurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 7-717.29988 PiCurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PiCurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 0.461600006 SlowDataAccessBufIndex_Cnt_M_u16 0	
PiCurrCntrl_InverterFailSclFac_Uls_M_f32	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.853799999 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1118 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 7-717.299988 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 87.3075027 PICurrCntrl_MtrVotiCaxFFFilt_M_str.PrevInput_Uls_f32 0.461600006 PICurrCntrl_MtrVotiCaxFFFilt_M_str.PrevOutput_Uls_f32 7-717.299988 PICurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PICurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PICurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PICurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PICurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PICurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 87.3075027 PICurrCntrl_MtrVotiQaxFFFilt_M_str.TermD_Uls_f32 911.31982 K_LOAFdbackSignalSclFacSlew_UlspS_f32 5911.31982 K_MtrCurrCaxRefindsclSew_UlspS_f32 5911.31982 K_MtrCurrCurrLoopSecOrTranFcEnable_Cnt_lgc 0 K_MtrCurrQaxRefindodiffple_Cnt_lgc 1 K_MtrCurrQaxRefindodiffple_Cnt_lgc 1 <td></td>	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	04
PICurrCntr_MtrVecuFilt_M_str.TermN_UIs_f32	
PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32	38
PICurrCntrl MtrVoltQaxFFFilt M_str.PrevInput Uls_f32	7
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -717.299988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 87.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.461600006 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5911.31982 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2332.93994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_ControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.155000001 k_MtrCtrlVirualResQax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegHiLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 0.963 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTblX_Uls_u3p13[0] 4611	06
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -717.299988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 87.3075027 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.461600006 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5911.31982 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2332.93994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_ControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.155000001 k_MtrCtrlVirualResQax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegHiLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 0.963 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTblX_Uls_u3p13[0] 4611	
PICurrCntrl_MtrVoltQaxFFFit_M_str.TermN_UIs_f32 87.3075027 PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_UIs_f32 0.461600006 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 5911.31982 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 2332.93994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResDax_Ohm_f32 0.155000001 k_MtrCtrrVirualResDax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_Igc 1 k_MtrCurrQaxRefModifRplEn_Cnt_Igc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltQaxFiltFFEnable_Cnt_Igc 1 k_MtrVoltQaxFiltFFEnable_Cnt_Igc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQevLittenable_Cnt_Igc 1 k_VoltSatDaxPolyCoeff_UIs_f32 0.963 k_VoltSatQaxPolyCoeff_UIs_f32 0.963 k_CommOffsetTblX_UIs_u3p13[0] 4611 t_CommOffsetTblY_Cnt_u16[0] 163 t_CommOffsetTblY_Cnt_u16[0]	38
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.461600006 SlowDataAccessBuflindex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5911.31982 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2332.93994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResDax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegHiLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 0.963 k_VoltSatQaxPolyCoeff_Uls_f32 0.963 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblY_Cnt_u16[0] 163 t_CommOffsetTblY_Cnt_u16[0] 163	
SlowDataAccessBuflndex_Cnt_M_u16 0	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5911.31982 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2332.93994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResDax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 -9.64999962 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQexIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQexIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQexIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQexIntegLoLim_Volt_f32 0.96300003 k_VoltSatDaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblY_Cnt_u16[0] 163 t_CommOffsetTblY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	00
k_ILOAFdbackSignalScIFacSlew_UlspS_f32 2332.93994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResDax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_Igc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_Igc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegHiLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_Igc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegHiLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 0.96300003 k_VoltSatDaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblY_Cnt_u16[0] 163 t_CommOffsetTblY_Cnt_u16[1] 1236 <td>•</td>	•
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResQax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResQax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	4
k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResQax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxIntegHiLim_Volt_f32 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_MtrCtrlVirualResQax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	96
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	01
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_MtrVoltDaxIntegHiLim_Volt_f32 5.5145998 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_MtrVoltQaxIntegHiLim_Volt_f32 6.05779982 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntr_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	y <u>L</u>
k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	•
k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_VoltSatDaxPolyCoeff_Uls_i32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_i32 0 k_deadtimeVScale_Uls_i32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	ö2
k_VoltSatQaxPolyCoeff_Uls_f32 0 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 4611 t_CommOffsetTbIX_Uls_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
k_deadtimeVScale_UIs_f32 0.963 t_CommOffsetTbIX_UIs_u3p13[0] 4611 t_CommOffsetTbIX_UIs_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	03
t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 163 t_CommOffsetTblY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
t_CommOffsetTbIX_UIs_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
t_CommOffsetTbIX_UIs_u3p13[1] 5579 t_CommOffsetTbIY_Cnt_u16[0] 163 t_CommOffsetTbIY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
t_CommOffsetTblY_Cnt_u16[0] 163 t_CommOffsetTblY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
t_CommOffsetTblY_Cnt_u16[1] 1236 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0	
· · · · · - · - ·	
target_iviti-ontri_kead_iviodidx5ricom5vcDtt Cnt Igc Val 1	
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -207.917999	99
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1357	

2016-04-22, 16:28:30+0530



Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1357	1357	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	12.7000008	12.6999998 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.4071095	-2.40710998 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.17013788	-4.17013979 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3723	3723 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	12.7000008	12.6999998 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.158 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.49000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.741999984
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-517.109009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-593.112976
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.15900004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60500002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	323.631989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-319.569
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998



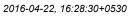


Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	115.814003		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001		
MtrCurrQaxPrevIntg_Volt_M_f32	20.2117004		
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpI_Amp_M_f32[0] MtrCurrQaxRpI_Amp_M_f32[1]	-105.246002 41.6290016		
MtrPosComputationDelay Rad M f32[0]	4.70800018		
MtrPosComputationDelay_Rad_M_f32[1]	-2.68499994		
MtrVoltDax Volt M f32[0]	-29.3959999		
MtrVoltDax_Volt_M_f32[1]	-1.9400006		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.94000006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.189799994		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	97.3968964 0.662699997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	97.3968964		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.662699997		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7083.27002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	947.890015		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.023		
k_MtrCtrlVirualResQax_Ohm_f32	0.0179999992		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_Mtr/CutrQaxRefModifRplEn_Cnt_lgc	1 10 2020005		
k_MtrVoltDaxIntegHiLim_Volt_f32 k_MtrVoltDaxIntegLoLim_Volt_f32	19.8029995 -22.409998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.0914993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.1960001		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.0979996		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	1081		
t_CommOffsetTblY_Cnt_u16[1]	1779		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1 136.341003		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	903		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target MtrCntrl Read SysState Cnt Enum Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1779	1779	Result
MtrCntrl Write ModIdx Uls u16p16(val)	64749	64749 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-171.029999	-171.029999 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.74271894	-3.74271989 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.37356234	-6.37355995 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	10301	10301 ± 1.52588E-05	-
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-171.029999	-171.029999 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	-



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
FastDataAccessBufIndex Cnt M u16	1
######################################	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
htrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
htrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
1trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
1trCtrl Vecu Volt M f32[0]	18.9510002
ItrCtrl Vecu Volt M f32[1]	21.3110008
ItrCurrDaxPrevIntg Volt M f32	-18.5370007
ItrCurrDaxRef Amp M f32[0]	-146.723007
trCurrDaxRef_Amp_M_f32[1]	-121.943001
ItrCurrQaxCog Amp M f32	79.6729965
ItrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
ItrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
ItrCurrQaxPrevIntg Volt M f32	11.4363003
htrCurrQaxRef_Amp_M_f32[0]	-146.173996
ltrCurrQaxRef_Amp_M_f32[1]	-213.335007
ItrCurrQaxRpl Amp M f32[0]	-212.632996
ItrCurrQaxRpl Amp M f32[1]	-205.085007
trPosComputationDelay_Rad_M_f32[0]	1.52600002
htrPosComputationDelay_rad_M_r32[1]	-2.68400002
	-27.0669994
htrVoltDax_Volt_M_f32[0]	28.1070004
/trVoltDax_Volt_M_f32[1]	
/trVoltQax_Volt_M_f32[0] /trVoltQax_Volt_M_f32[1]	-27.0669994 28.1070004





Name	Input Value		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.400000006		
PICurrCntrl InverterFailSclFac Uls M f32	0.059000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.827000022		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	16.5851002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-43.1699982		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	16.5851002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.887899995		
SlowDataAccessBufIndex Cnt M u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	2663.65991		
k ILOAFdbackSignalSclFacSlew UlspS f32	5194.8999		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	1		
k MtrCtrlFeedbackControlDisable Cnt Igc	0		
k MtrCtrlVirualResDax Ohm f32	0.112000003		
k MtrCtrlVirualResQax Ohm f32	0.021999999		
k MtrCurrQaxRefModifDsb Cnt Igc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.4445		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	23.5652008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k VoltSatDaxPolyCoeff Uls f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	14.658		
k deadtimeVScale Uls f32	0.958999991		
t_CommOffsetTbIX_UIs_u3p13[0]	6528		
t CommOffsetTbIX UIs u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	65		
target MtrCntrl Read MtrCurrQax Amp f32 Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	65	65	rtoour
MtrCntrl Write ModIdx Uls u16p16(val)	0	0 ± 1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.9229965	-87.9229965 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70514989 ± 4.88E-04	
MtrCntrl Write MtrQaxVoltage Volt f32(val)	3.95906138	3.95905995 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	
MtrCurrQaxFinalRef Amp M f32[0]	-212.632996	-212.632996 ± 7.81E-03	
MtrCurrQaxFinalRef Amp M f32[1]	-87.9229965	-87.9229965 ± 7.81E-03	
ca., day, man tol., unb.m.loz[1]	O1.0220000	01.0220000 1 1.01L-00	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.142000005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.272000015
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	243.257004
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-253.089996
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.713999987
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.870999992
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	49.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-366.458008
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
/trCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
VtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
VtrCurrQaxPrevIntg_Volt_M_f32	30.3402004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
/trCurrQaxRef_Amp_M_f32[1]	75.7020035
/trCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
/trPosComputationDelay_Rad_M_f32[0]	-2.15700006
VtrPosComputationDelay_Rad_M_f32[1]	4.67700005
/trVoltDax_Volt_M_f32[0]	-3.59500003
/trVoltDax_Volt_M_f32[1]	-28.4209995
/trVoltQax_Volt_M_f32[0]	-3.59500003
MtrVoltQax_Volt_M_f32[1]	-28.4209995
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.499500006
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976 78.8641068
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	78.8641968 0.388500005
PlCurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 PlCurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	570.700012
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uis_f32	-657.099976
	-657.099976 78.8641968
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	78.8641968 0.388500005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	
SlowDataAccessBufIndex_Cnt_M_u16	0
_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996
<pre> c_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc c_MtrCtrlEeadbackControlDisable_Cnt_lgc c_mtrctrlEeadbackControlDisa</pre>	0
<pre>(_MtrCtrlFeedbackControlDisable_Cnt_lgc</pre>	0.193000004
(_MtrCtrlVirualResDax_Ohm_f32	
C_MtrCtrlVirualResQax_Ohm_f32	0.114





W.			
Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.67009997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.1749001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	65339	65339 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	182.914001	182.914001 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-19.8980999	-19.8980999 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.02442813	2.02443004 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33456	33456 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	-4.57000017	-4.57000017 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	182.914001	182.914001 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 2.161 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.51999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.203999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-569.184021

2016-04-22, 16:28:30+0530



Name	
MicCirt Microgan Comm. M. 52(1)	
MicCut MirmpedDac, Ohm, M, 192(1)	
MicCurl MirmpedDax, Ohm, M, 152(1)	
MicCirl, MirimpedCax, Ohm, M, (32(1) 0.0270000023 MicCirl, MirimpedCax, Ohm, M, (32(1) 0.027000007 MicCirl, MirimpedCax, Ohm, M, (32(1) 0.027000007 MicCirl, MirimpedCax, Ohm, M, (32(1) 0.02809999 MicCirl, MirimpedCax, Ohm, M, (32(1) 1.02809999 MicCirl, MirimpedCax, Ohm, M, (32(1) 1.02809999 MicCirl, MirimpedCax, Ohm, M, (32(1) 1.0302 MicCirl, MirimpedCax, Ohm, M, (32(1) 1.0302 MicCirl, MirimpedCax, Ohm, M, (32(1) 1.0302 MicCirl, MirimpedCax, F., (31, M, (32(1) 1.0302 MicCirl, MirimpedCax, M, (32(1) 1.0302 MirimpedCax, M, (32(1)	
MicCurl Mirrigordia Chem, M. [521]	
MicCarl MicCardinegra(Cain_Ohm_M_G2(1) 0.150990995 MicCarl_MicCarl_MicCarl_Chin_M_G2(1) 0.8209985 MicCarl_MicCarl_Chin_M_G2(1) 0.8209985 MicCarl_MicCarl_Chin_M_G2(1) 0.8209985 MicCarl_MicCarl_Chin_M_G2(1) 0.8209985 MicCarl_MicCarl_Chin_M_G2(1) 0.8209985 MicCarl_MicCarl_Chin_M_G2(1) 0.8209985 MicCarl_Chin_M_G2(1) 0.85099984 0.8220006 MicCarl_MicCarl_MicCarl_MicCarl_M_G2(1) 0.85099984 0.8220006 MicCarl_MicCarl_MicCarl_MicCarl_M_G2(1) 0.85099995 0.8220006 0.822	
MicCut MicCountegra(Sain Ohm M. 52(1) 0.82299985 MicCut MicCountegra(Sain Ohm M. 52(1) 322.345001 442.492004 MicCut MicCountegra(Sain Ohm M. 52(1) 322.345001 442.492004 MicCut Mi	
MicCirt MirCasPropotionalGain_Ohm_M_152(1) 332.345001 MirCirt MirCasPropotionalGain_Ohm_M_152(1) 332.345001 MirCirt MirCasPropotionalGain_Ohm_M_152(1) 16.302 MirCirt MirColtDasPr_Voll_M_152(1) 16.302 MirCirt MirColtDasPr_Voll_M_152(1) 12.6160002 MirCirt_MirColt_MirColt_M_152(1) 12.6160002 MirCirt_MirColt_MirColt_M_152(1) 7.4809995 MirCirt_Vecu_Voll_M_152(1) 7.4809995 MirCirt_DasPrevints_Voll_M_152(1) 7.4809995 MirCurtDasPrevints_Voll_M_152(1) 7.4809995 MirCurtDasPrevints_Voll_M_152(1) 7.57020035 MirCurtDasP	
MicCirc MiCourFopoliona[Gair, Ohm, M. [32[1] 332.345001 MiCCirt MiCVollDasFF, Volt, M. [32[0] 16.302 MiCCirt MiVollDasFF, Volt, M. [32[0] 28.5599964 MiCCirt MiVollGasFF, Volt, M. [32[0] 12.6160002 MiCCirt MiVollGasFF, Volt, M. [32[0] 12.6160002 MiCCirt Veou, Volt, M. [32[0] 5.1209981 MiCCirt Veou, Volt, M. [32[0] 7.4099991 MiCCirt Veou, Volt, M. [32] 7.5702005 MiCCirt Veou, Volt, M. [32[1] 7.5702005 MiCCirt Veou, Volt, M. [32] 7.5702003 MiCCirt Veou, Volt, M. [32] 7.5702003 MiCCirt Veou, Volt, M. [32] 7.5702003 MiCCirt Veou, Volt, M. [32] 7.570200 7.5809998 MiCCirt Veou, Volt, M. [32] 7.570200 7.5809998 MiCCirt Veou, Volt, M. [32] 7.570200 7.5809998 MiCCirt Veou, Volt, M. [32] 7.5809998 7.58099998 7.58099999 7.58099999 7.58099999 7.58099999 7.58099999 7.58099999 7.58099999 7.58099999 7.58099999 7.58099999 7.58099999 7.58099999 7.580999999 7.580999999 7.580999999 7.580999999 7.580999999 7.5809999999 7.580999999 7.580999999 7.5809999999 7.5809999999 7.5809999999 7.58099999999999 7.5809999999999 7.580999999999999999999999999999999999999	
MicCirc MirCordDesire Voit M. F32 1 832.345001 MirCirc MirVoltDasFF Voit M. F32 1 8.5099964 MirCirc MirVoltDasFF Voit M. F32 1 8.5099964 MirCirc MirVoltDasFF Voit M. F32 1 8.5099964 MirCirc MirVoltDasFF Voit M. F32 1 12.6160002 MirCirc MirVoltDasFF Voit M. F32 1 12.6160002 MirCirc MirVoltDasFF Voit M. F32 1 12.6160002 MirCirc Vecu. Voit M. F32 1 7.6099991 7.6090	
MicCirt MirVollDas/F Volt M, 152(1) 8.5509964	
MirCurl MirVolDaxFF, Volt, M, F32[1] 8,55089964 MirCurl MirVolDaxFF, Volt, M, F32[1] 12,8160002 MirCurl MirVolDaxFF, Volt, M, F32[1] 12,8160002 MirCurl MirVolDaxFF, Volt, M, F32[1] 7,4099995 MirCurl Vecu, Volt, M, F32[1] 7,4099995 MirCurl Vecu, Volt, M, F32[1] 7,5099996 MirCurl CaxPevinig, Volt, M, F32[1] 7,7020035 MirCurl CaxPevinig, Volt, M, F32[1] 7,57020035 MirCurl CaxPevinig, Volt, M, F32[1] 2,84500003 MirCurl CaxPevinig, Volt, M, F32[1] 2,84500003 MirCurl CaxPevinig, Volt, M, F32[1] 2,84500003 MirCurl CaxPevinig, Volt, M, F32[1] 1,12450002 MirCurl CaxPevinig, Volt, M, M, F32[1] 1,12450002 1,105240002 MirCurl CaxPevinig, Volt, M, F32[0] 1,105240002 MirVolDax, Volt, M, F32[0] 1,105240002 1,105240002 MirVolDax, Volt, M, F32[0] 1,105240002 1,1052400	
MirCirt MirVol(DaxFF, Volt, M, 132(1) 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 12.6180002 13.3181006 12.6180002 12.61800003 13.3181006 12.61800003 13.3181006 13.3181006 12.61800003 13.3181006 13.3181006 12.61800003 13.3181006 13.318100	
Mr.Cirl MirVolfQaxFF_Volt_M_52[1] 12,6160002 MirCirl Vecu_Volt_M_52[0] 5.1209981 MirCirl Vecu_Volt_M_52[0] 5.1209981 MirCirl Vecu_Volt_M_52[0] 5.1209981 MirCirl Vecu_Volt_M_52[0] 7.809995 MirCirl Vecu_Volt_M_52[0] 133,94706 MirCirrDaxRerl_Amp_M_52[0] 133,94706 MirCirrDaxRerl_Amp_M_52[0] 75,702035 MirCirrDaxRerl_Amp_M_52[1] 75,702035 MirCirrDaxRerl_Amp_M_52[1] 75,702035 75,702035 MirCirrDaxRerl_Amp_M_52[0] 75,702035 75,702035 MirCirrDaxRerl_Amp_M_52[0] 75,702035 75,702035 MirCirrDaxRerl_Amp_M_52[0] 75,702033 75,702033 MirCirrDaxRerl_Amp_M_52[0] 75,702033 75,702033 MirCirrDaxRerl_Amp_M_52[0] 75,702033 75,702033 MirCirrDaxRerl_Amp_M_52[0] 75,70203 75,702	
MitCit Vecu Voit M. 52(1)	
MrtCott Veou Volt M. [321] 7.48009995 MrtCurrbaxPrevintg, Volt M. [32] -27.6930008 MrtCurrbaxRef Amp, M. [321] -75.7020035 MrtCurrbaxRef Amp, M. [321] 75.7020035 MrtCurrbaxRef Amp, M. [321] 57.2399998 MrtCurrQaxFinalRef Amp, M. [321] -2.84500003 MrtCurrbaxRef Amp, M. [321] -2.84500003 MrtCurrbaxRef Amp, M. [321] 106.072998 MrtCurrbaxRef Amp, M. [321] -112.455002 MrtPosComputationDelay, Rad, M. [321] -12.546002 MrtPosComputationDelay, Rad, M. [321] 25.3770008 MrtVoltbax, Volt, M. [321] 21.3880005 PlCurrbott, MrtVoltbax, MrtLaxelo, Uls, M. [32 0.513000011 PlCurrbott, MrtVoltbax, MrtLaxelo, U	
MITCurrDaxPreVining Vorl, M, 152	
MITCUITDARREL_Amp_M_132(1) 75.7020035 MITCUITDARREL_Amp_M_132(1) 75.7020035 MITCUITDARREL_Amp_M_132(1) 37.459018 MITCUITDARFINAREL_Amp_M_132(0) 37.459018 MITCUITDARFINAREL_Amp_M_132(1) 2.24590003 MITCUITDARFINAREL_Amp_M_132(1) 162.2 26.476293 MITCUITDARFINAREL_Amp_M_132(1) 112.455002 MITCUITDARFINAREL_Amp_M_132(1) 112.455002 MITCUITDARREL_Amp_M_132(1) 112.455002 MITCUITDARREL_Amp_M_132(1) 112.455002 MITCUITDARREL_Amp_M_132(1) 112.455002 MITCUITDARREL_Amp_M_132(1) 166.072998 MITCUITDARREL_Amp_M_132(1) 16.0299989 MITCUITDARREL_Amp_M_132(1) 2.3999999 MITCUITDARREL_Amp_M_132(1) 2.50999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.50999999 MITCUITDARREL_AMP_M_132(1) 2.50999999 MITCUITDARREL_AMP_M_132(1) 2.50999999 MITCUITDARREL_AMP_M_132(1) 2.50999999 MITCUITDARREL_AMP_M_132(1) 2.50999999 MITCUITDARREL_AMP_M_132(1) 2.50999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.509999999 MITCUITDARREL_AMP_M_132(1) 2.5099999999 MITCUITDARREL_AMP_M_132(1) 2.50999999999 MITCUITDARREL_AMP_M_132(1) 2.5	
MrCurrDaxRef_Amp_M_[32[1] 75.7020035 MrCurrDaxCog_Amp_M_B_32 57.299998 MrCurrDaxFinalRef_Amp_M_132[0] 37.4550018 MrCurrDaxFinalRef_Amp_M_132[0] 26.4762993 MrCurrDaxFinalRef_Amp_M_132[0] 106.072998 MrCurrDaxRef_Amp_M_132[0] 106.072998 MrCurrDaxRef_Amp_M_132[0] -105.246002 MrCurrDaxRef_Amp_M_132[0] -105.246002 MrCurrDaxRef_Amp_M_132[0] -105.246002 MrCurrDaxRef_Amp_M_132[0] -2.53002 MrCurrDaxRef_Amp_M_132[0] -2.53002 MrCurrDaxRef_Amp_M_132[0] -2.5370008 MrVoltDax_Voll_M_132[0] -2.53770008 MrVoltDax_Voll_M_132[0] -2.53770008 MrVoltDax_Voll_M_132[0] -2.53770008 MrVoltDax_Voll_M_132[1] 21.3880005 MrVoltDax_Voll_M_132[1] 21.3880005 MrVoltDax_Voll_M_132[1] 21.3880005 MrVoltDax_Voll_M_152[1] 21.3880005 MrVoltDax_Voll_M_152[1] 21.3880005 MrVoltDax_Voll_M_152[1] 21.3880005 MrVoltDax_Voll_M_152[1] 21.3880005 MrVoltDax_Mrtef_Hax_M_152[1] 21.	
MICCUTDARREL, Amp. M. [32] 1 75.7020035 MITCUTDARCOG, Amp. M. [32] 1 57.239998 MITCUTDARFIREL Amp. M. [32] 1 2.84500003 MITCUTDARFIREL Amp. M. [32] 1 2.84500003 MITCUTDARFIREL Amp. M. [32] 1 106.072998 MITCUTDARREL Amp. M. [32] 1 112.455002 MITCUTDARREL Amp. M. [32] 1 112.455002 MITCUTDARREL Amp. M. [32] 1 41.6280016 MITCUTDARREL Amp. M. [32] 1 41.6280016 MITPOSCOMPUTATION P. [32] 1 2.3699989 MITVOITORAREL AMP. M. [32] 1 21.3800005 MITVOITORAR [1.4] 1.52[0] 25.3770008 MITVOITORA VOIL M. [32] 1 21.3800005 MITVOITORA VOIL M. [32] 1 21.3800005 MITVOITORA VOIL M. [32] 1 21.3800005 PICUTTORIT J. CHUTSENSFAISUFER U. M. [32] 0.911000013 PICUTCORIT J. MITVOUTDARSASTIFUXRETO. U. M. [32] 0.1329 PICUTCORIT J. MITVOUTDARSASTIFUXRETO. U. M. [32] 0.55999985 PICUTCORIT J. MITVOUTDARSASTIFUXRETO. U. M. [32] 0.700012 PICUTCORIT J. MITVOUTDARSETIFUX M. St. TERM. U. J. [32] 0.744499981 PICUTCORIT J. MITVOUTDARS FERIX M. St. TERM. U. J. [32] 0.74449998	
MitCurrQaxFinalRef_Amp_M_[32] 5,72399998 MitCurrQaxFinalRef_Amp_M_[32]() 37,4550018 MitCurrQaxFrevIntg_Volt_M_[32] 28,4500003 MitCurrQaxRef_Amp_M_[32]() 106,072998 MitCurrQaxRef_Amp_M_[32]() 106,072998 MitCurrQaxRef_Amp_M_[32]() 112,455002 MitCurrQaxRef_Amp_M_[32]() 106,072998 MitCurrQaxRef_Amp_M_[32]() 112,455002 MitCurrQaxRef_Amp_M_[32]() 23,6999989 MitPosComputationDelay_Rad_M_[32]() 2,36999989 MitPosComputationDelay_Rad_M_[32]() 2,36999989 MitPosComputationDelay_Rad_M_[32]() 2,3700008 MitPosComputationDelay_Rad_M_[32]() 2,3700008 MitVoltQax_Voll_M_[32]() 25,3770008 MitVoltQax_Voll_M_[32]() 22,3770008 MitVoltQax_Voll_M_[32]() 22,3770008 MitVoltQax_Voll_M_[32]() 21,3880005 PiCurrCntt_I_MrourPasSaffixQate_Us_M_[32] 0,51300001 PiCurrCntt_I_MrourPasSaffixQate_Us_M_[32] 0,51300001 PiCurrCntt_I_MrourPasSaffixQate_Us_M_[32] 0,53999988 PiCurrCntt_I_MrourPasSaffixQate_Us_M_[32] 0,599999988 PiCurrCntt_I_	
MtrCurrQaxFinalRef_Amp_M_f32[0] 37.4550018 MtrCurrQaxFinalRef_Amp_M_f32[1] -2.84500003 MtrCurrQaxFerling_Volt_M_f32 26.4762993 MtrCurrQaxFef_Amp_M_f32[0] 106.072998 MtrCurrQaxFef_Amp_M_f32[1] -112.455002 MtrCurrQaxFef_Amp_M_f32[1] -105.246002 MtrCurrQaxFef_Amp_M_f32[1] -105.246002 MtrCurrQaxFef_Amp_M_f32[1] -105.246002 MtrOutQaxFef_Amp_M_f32[1] -2.650000 MtrPosComputationDelay_Rad_M_f32[0] -2.36999989 MtrVoltDax_Volt_M_f32[1] -2.760000 MtrVoltDax_Volt_M_f32[1] -2.53770008 MtrVoltDax_Volt_M_f32[1] -2.53770008 MtrVoltDax_Volt_M_f32[1] -2.3890005 MtrVoltDax_Max_Max_Max_Max_Max_Max_Max_Max_Max_M	
MtrCurrQaxFinalRef_Amp_M_f32[1] -2.84500003 MtrCurrQaxFevintg_Volt_M_f32 26.4762993 MtrCurrQaxRef_Amp_M_f32[0] 106.072988 MtrCurrQaxRef_Amp_M_f32[1] -112.455002 MtrCurrQaxRef_Amp_M_f32[1] -112.455002 MtrCurrQaxRef_Amp_M_f32[1] -105.246002 MtrCurrQaxRef_Amp_M_f32[1] -105.246002 MtrPosComputationDelay_Rad_M_f32[1] 2.36999989 MtrPosComputationDelay_Rad_M_f32[1] 2.5700008 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[1] 21.3880005 PlCurrCntrl_M_f1_M_f32[1] 21.3880005 PlCurrCntrl_M_f1_M_f1_M_f1_M_f1_M_f1_M_f1_M_f1_M_f	
MtrCurrQaxPrevintg_Volt_M_f32 26.4762993 MtrCurrQaxRef_Amp_M_f32[0] 106.072998 MtrCurrQaxRef_Amp_M_f32[1] -112.455002 MtrCurrQaxRef_Amp_M_f32[1] -105.246002 MtrCurrQaxRef_Amp_M_f32[1] -105.246002 MtrCurrQaxRef_Amp_M_f32[1] -105.246002 MtrPosComputationDelay_Rad_M_f32[0] -2.58999899 MtrPosComputationDelay_Rad_M_f32[1] -2.6700008 MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[1] -21.3880005 MtrVoltDax_Volt_M_f32[1] -21.3880005 MtrVoltDax_Volt_M_f32[1] -21.3880005 MtrVoltDax_Volt_M_f32[1] -21.3880005 MtrVoltDax_Volt_M_f32[1] -21.3880005 PICurrCntr_GurrSansFailsGlFac_Uls_M_f32 -0.911000013 PICurrCntr_M_furredraxSaff-tuxRatio_Uls_M_f32 -0.912000013 PICurrCntr_M_furredraxSaff-tuxRatio_Uls_M_f32 -0.1329 PICurrCntr_M_furredraxSaff-tuxRatio_Uls_M_f32 -0.565999985 PICurrCntr_M_furredraxSaff-tuxRatio_Uls_M_f32 -0.655999985 PICurrCntr_M_furredraxSaff-tuxRatio_Uls_M_f32 -0.744499981 PICurrCntr_M_furredraxSaff-tuxRatio_Uls_M_fa2 -0.74	
MtrCurrQaxRef_Amp_M_f32[0] 106.072998 MtrCurrQaxRef_Amp_M_f32[1] -112.455002 MtrCurrQaxRpL_Amp_M_f32[0] -105.246002 MtrCurrQaxRpL_Amp_M_f32[1] 41.6290016 MtrDosComputationDelay_Rad_M_f32[0] 2.86999999 MtrVoltDax_Volt_M_f32[0] 2.53770008 MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltQax_Volt_M_f32[1] 21.3880005 MtrVoltQax_Volt_M_f32[2] 0.513000011 PlCurrCntl_MtrVourCax_Fill_M_str_Asto_Uls_f32 0.55999995 PlCurrCntl_MtrVouculif_M_fst_Terrev_Uls_f32 3.3250008 PlCurrCntl_MtrVoltQax_FFill_M_str_Terrev_Uls_f32 3.7700012	
MtrCurrQaxRef_Amp_M_[32[1] -112.455002 MtrCurrQaxRpl_Amp_M_[32[1] -106.246002 MtrCurrQaxRpl_Amp_M_[32[1] 416.6290016 MtrPosComputationDelay_Rad_M_[32[1] 2.36999889 MtrPosComputationDelay_Rad_M_[32[1] 2.67000008 MtrVolIDax_Voit_M_[32[1] 21.3880005 MtrVolIDax_Voit_M_[32[1] 21.3880005 MtrVolIQax_Voit_M_[32[1] 21.3880005 MtrVolIQax_Voit_M_[32[1] 21.3880005 PICurrCntt_CurrSensFailSclFac_Uls_M_[32] 0.911000013 PICurrCntt_CurrSensFailSclFac_Uls_M_[32] 0.913000011 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.555999985 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.555999985 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.555999985 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.9100011 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.700012 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.700012 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.74449981 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.74449981 PICurrCntt_MincurrDaxSatFluxRatio_Uls_M_[32] 0.744499981 <td< td=""><td></td></td<>	
MtrCurrQaxRpl_Amp_M_52(1) -105.246002 MtrCurrQaxRpl_Amp_M_52(1) 41.6290016 MtrPosComputationDelay_Rad_M_52(0) 2.36999989 MtrPosComputationDelay_Rad_M_52(1) 2.6700008 MtrVoliDax_Volt_M_52(0) -25.3770008 MtrVoliDax_Volt_M_52(1) 21.3880005 MtrVoliQax_Volt_M_52(1) 21.3880005 MtrVoliQax_Volt_M_52(1) 21.3880005 MtrVollQax_Volt_M_52(1) 21.3880005 MtrVollQax_Volt_M_52(1) 21.3880005 MtrVollQax_Volt_M_52(1) 21.3880005 MtrVollQax_Volt_M_52(2) Us_M52 PlCurrCntt_CurrsensFailsClFac_Uls_M_52 0.911000013 PlCurrCntt_InverterFailSclFac_Uls_M_52 0.513000011 PlCurrCntt_MtrVcurracaxSatFluxRatio_Uls_M_52 0.56599985 PlCurrCntt_MtrVcurracaxSatFluxRatio_Uls_M_52 0.56599985 PlCurrCntt_MtrVcurracaxSatFluxRatio_Uls_M_52 0.56599985 PlCurrCntt_MtrVcurracaxSatFluxRatio_Uls_M_52 0.570,700012 PlCurrCntt_MtrVcurratit_M.str.TermD_Uls_132 0.74449981 PlCurrCntt_MtrVcuracurfit_M.str.TermD_Uls_132 0.74449981 PlCurrCntt_MtrVoltQaxFFFilt_M.str.TermD_Uls_132 0.744499981 <td></td>	
MtrCurrQaxRpl_Amp_M_f32[1] 41.6290016 MtrPosComputationDelay_Rad_M_f32[0] 2.36999989 MtrVoltDax_Volt_M_f32[0] 2.67000008 MtrVoltDax_Volt_M_f32[1] 2.5770008 MtrVoltDax_Volt_M_f32[0] -25.377008 MtrVoltDax_Volt_M_f32[0] -25.377008 MtrVoltQax_Volt_M_f32[1] 21.3880005 MtrVoltQax_Volt_M_f32[0] -25.377008 MtrVoltQar_MINC_MINC_MINC_MINC_MINC_MINC_MINC_MINC	
MtrCurrQaxRpl_Amp_M_32[1] 41.6290016 MtrPosComputationDelay_Rad_M_32[0] 2.3699989 MtrVoltDax_Volt_M_132[0] 2.5770008 MtrVoltDax_Volt_M_132[1] 2.5770008 MtrVoltDax_Volt_M_132[0] -25.3770008 MtrVoltDax_Volt_M_132[1] 21.3880005 MtrVoltQax_Volt_M_132[1] 21.3880005 PICurrCntt_LCurrSensFailSciFac_Uls_M_132 0.911000013 PICurrCntt_MtrCurrDaxSatFluxRatio_Uls_M_132 0.513000011 PICurrCntt_MtrCurrQaxSatFluxRatio_Uls_M_132 0.565999985 PICurrCntt_MtrVecuFilt_M_str.Previput_Uls_132 0.565999985 PICurrCntt_MtrVecuFilt_M_str.Previput_Uls_132 0.764499981 PICurrCntt_MtrVecuFilt_M_str.TermN_Uls_132 43.3250008 PICurrCntt_MtrVecuFilt_M_str.TermN_Uls_132 0.744499981 PICurrCntt_MtrVoltQaxFFFilt_M_str.TermD_Uls_132 0.744499981 PICurrCntt_MtrVoltQaxFFFilt_M_str.TermD_Uls_132 0.744499981 PICurrCntt_MtrVoltQaxFFFilt_M_str.TermD_Uls_132 0.74449981 PICurrCntt_MtrVoltQaxFFFilt_M_str.TermD_Uls_132 0.74449981 PICurrCntr_MtrVoltQaxFFFilt_M_str.TermD_Uls_132 0.74449981 R_UcurrCntr_MtrVoltQaxFFilt_M_str.TermD_Uls_132 0.7	
MtrPosComputationDelay_Rad_M_[32[0] 2,8699989 MtrPosComputationDelay_Rad_M_[32[1] 2,67000008 MtrVoltDax_Volt_M_[32[0] 25,3770008 MtrVoltDax_Volt_M_[32[1] 21,3880005 MtrVoltDax_Volt_M_[32[0] 25,3770008 MtrVoltDax_Volt_M_[32[1] 21,3880005 MtrVoltQax_Volt_M_[32[1] 21,3880005 PICurrCntt_CurrSensFailsGFac_Uls_M_[32] 0,911000013 PICurrCntt_InverterFailSclFac_Uls_M_[32] 0,513000011 PICurrCntt_MtrUcurrSastEffuxRatio_Uls_M_[32] 0,565999985 PICurrCntt_MtrVecuFilt_M_str.Previnput_Uls_[32] 0,565999985 PICurrCntt_MtrVecuFilt_M_str.Previnput_Uls_[32] 570,700012 PICurrCntt_MtrVecuFilt_M_str.Previnput_Uls_[32] 570,700012 PICurrCntt_MtrVecuFilt_M_str.TermD_Uls_[32] 0,744499981 PICurrCntt_MtrVoliQaxFFFilt_M_str.Previnput_Uls_[32] 0,744499981 PICurrCntt_MtrVoliQaxFFFilt_M_str.TermD_Uls_[32] 0,744499981 PICurrCntt_MtrVoliQaxFFFIlt_M_str.TermD_Uls_[32] 0,744499981 PICurrCntr_MtrVoliQaxFFFIlt_M_str.TermD_Uls_[32] 0,744499981 SlowDataAccessBufindex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UlspS_[32] 6616,02	
MtrPosComputationDelay_Rad_M_[32[1] 2.67000008 MtrVolIDax_Volt_M_[52[0] -25.3770008 MtrVolIDax_Volt_M_[52[1] 21.3880005 MtrVolIQax_Volt_M_[52[0] -26.3770008 MtrVolIQax_Volt_M_[52[0] -25.3770008 MtrVolIQax_Volt_M_[52[0] -26.3770008 MtrVolICax_Volt_M_[52[0] -26.3770008 MtrVolICax_Volt_M_[52[0] -21.3880005 PICurrCntrl_InverterFailsGlac_Uls_M_[52] 0.911000013 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[52] 0.1329 PICurrCntrl_MtrVecurFilt_M_Str.PervInput_Uls_[52] 0.565999985 PICurrCntrl_MtrVecurFilt_M_str.PervInput_Uls_[52] 0 PICurrCntrl_MtrVecurFilt_M_str.TermN_Uls_[62] 43.3250008 PICurrCntrl_MtrVecurFilt_M_str.TermN_Uls_[52] 0.744499981 PICurrCntrl_MtrVolQaxFFFilt_M_str.TermD_Uls_[32] 0.74449981 PICurrCntrl_MtrVolQaxFFFilt_M_str.TermD_Uls_[32] 570.700012 PICurrCntrl_MtrVolQaxFFFilt_M_str.TermD_Uls_[32] 43.3250008 PICurrCntrl_MtrVolQaxFFFilt_M_str.TermD_Uls_[32] 577.700012 PICurrCntrl_MtrVolQaxFFFilt_M_str.TermD_Uls_[32] 6616.02002 K_LOAFdbackSignalSciFacSiew_UlspS_[32] 6616.02002	
MttVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltDax_Volt_M_f32[1] 21.3880005 MtrVoltDax_Volt_M_f32[0] -25.3770008 MtrVoltQax_Volt_M_f32[1] 21.3880005 MtrVoltCax_Volt_M_f32[1] 21.3880005 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.911000013 PICurrCntrl_MtrCurrSensEquixRatio_Uls_M_f32 0.513000011 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.565999985 PICurrCntrl_MtrVecuFilt_M_str.Previoput_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.Previoput_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.744499981 PICurrCntrl_MtrVelocAFFFIIt_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVolQaxFFFIIt_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVolQaxFFFIIt_M_str.TermD_Uls_f32 0 PICurrCntrl_MtrVolQaxFFFIIt_M_str.TermD_Uls_f32 0.744499981 SlowDataAccessBufindex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_MtrCitrVirualResDax_Ohm_f32 0.191 k_MtrCitrVirualResDax_Ohm_f32 <td></td>	
MttVoltDax_Volt_M_f32[1] 21.3880005 MttVoltQax_Volt_M_f32[0] -25.3770008 MttVoltQax_Volt_M_f32[1] 21.3880005 PICurrCntt_CurrSensFailSclFac_Uls_M_f32 0.911000013 PICurrCntt_IntrourDaxSafFluxRatio_Uls_M_f32 0.513000011 PICurrCntt_MtrCurrQaxSafFluxRatio_Uls_M_f32 0.56599985 PICurrCntt_MtrVecuFilt_M_str.Prevloput_Uls_f32 0 PICurrCntt_MtrVecuFilt_M_str.Prevloput_Uls_f32 570.70012 PICurrCntt_MtrVecuFilt_M_str.FermN_Uls_f32 43.3250008 PICurrCntt_MtrVecuFilt_M_str.TermD_Uls_f32 0.744499981 PICurrCntt_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 0.744499981 PICurrCntt_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 570.70012 PICurrCntt_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 43.3250008 PICurrCntt_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 43.3250008 PICurrCntt_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 37.44499981 SlowDataAccessBufindex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 k_MtrCtrUruaResDax_Ohm_f32 0.191 k_MtrCtrVirualResDax_Ohm_f32 0.191	
MttVoltQax_Volt_M_f32[0] -25.3770008 MtrVoltQax_Volt_M_f32[1] 21.3880005 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.911000013 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.513000011 PICurrCntrl_MtrCurrCaxSatFluxRatio_Uls_M_f32 0.1329 PICurrCntrl_MtrCurcAxSatFluxRatio_Uls_M_f32 0.56599985 PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.PrevDut_Uls_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 43.3250008 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 SlowDataAccessBuffindex_Ont_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILLOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 k_ILCOTGCurtLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc	
MtrVoltQax_Voit_M_f32[1] 21.3880005 PICurrCntrl_CurrSensFallSclFac_Uls_M_f32 0.911000013 PICurrCntrl_IntrCurrDaxSatFluxRatio_Uls_M_f32 0.513000011 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.1329 PICurrCntrl_MtrVecuFilt_M_str.PrevNiput_Uls_f32 0.565999985 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 43.3250008 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.744499981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 0.744499981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevDut_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.744499981 SlowDataAccessBuffindex_Cnt_M_uf6 1 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILLOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.0529999994 k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 k_MtrVoltDaxIntegHiLim_Voit_f32 25.6000004 k_MtrVoltQaxFiteFFenable_C	
PICurrCntrl_InverterFailSciFac_Uls_M_f32 0.911000013 PICurrCntrl_InverterFailSciFac_Uls_M_f32 0.513000011 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.565999985 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.565999985 PICurrCntrl_MtrVecuFilt_M_str.Prevluput_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.Prevluput_Uls_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.Prevluput_Uls_f32 43.3250008 PICurrCntrl_MtrVecuFilt_M_str.Prevluput_Uls_f32 0.744499981 PICurrCntrl_MtrVecuFilt_M_str.Prevluput_Uls_f32 0.744499981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.Prevluput_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevVoltput_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevVoltput_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.744499981 SlowDataAccessBufIndex_Cnt_M_u16 1 k_CLOAFdbackSignalSciFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSciFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSciFacSlew_UlspS_f32 5777.70996 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResQax_Ohm_f32 0.52999994 k_MtrCurrQaxRefModifrDs_Cnt_lgc 1 k_MtrCurrQaxRefModifrDs_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 25.6000004 k_MtrVoltDaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_VoltSatDaxPolyCoeff_Uls_f32 -1.4650003 k_CaeddimevScale_Uls_f32 -1.4650003 k_CommOffsetTblV_Cnt_uls[0] -1.530 k_CommOffsetTblY_Cnt	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.1329 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.66599985 PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 0.700012 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 43.3250008 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.PrevOutput_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.PrevOutput_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.PrevOutput_Uls_f32 570.700012 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermN_Uls_f32 43.3250008 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFilt_M_str.TermD_Uls_f32 0.777.70996 PICurrCntrl_MtrVotQaxFefilt_M_str.TermD_Uls_f32 0.777.70996 PICurrCntrl_C	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.1329 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.66599985 PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 0.700012 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 43.3250008 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.PrevOutput_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.PrevOutput_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.PrevOutput_Uls_f32 570.700012 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermN_Uls_f32 43.3250008 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFFilt_M_str.TermD_Uls_f32 0.74449981 PICurrCntrl_MtrVotQaxFFilt_M_str.TermD_Uls_f32 0.777.70996 PICurrCntrl_MtrVotQaxFefilt_M_str.TermD_Uls_f32 0.777.70996 PICurrCntrl_C	
PICurrCntrl_MtrVecuFit_M_str.Previnput_Uis_f32 0.56599985 PICurrCntrl_MtrVecuFit_M_str.Previnput_Uis_f32 0 PICurrCntrl_MtrVecuFit_M_str.Previnput_Uis_f32 570.700012 PICurrCntrl_MtrVecuFit_M_str.TermD_Uis_f32 43.3250008 PICurrCntrl_MtrVecuFit_M_str.TermD_Uis_f32 0.74449981 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uis_f32 0 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uis_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uis_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uis_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uis_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uis_f32 0.744499981 SlowDataAccessBuflindex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UispS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UispS_f32 5777.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResOax_Ohm_f32 0.191 k_MtrCtrlVirualResOax_Ohm_f32 0.952999994 k_MtrCtrlVirualResOax_Ohm_f32 0.0529999994 k_MtrVoltDaxIntegHiLim_Volt_f32 21.6300000 k_MtrVoltDaxIntegHiLim_Volt_f32	
PICurrCntrl MtrVecuFitt_M_str.PrevInput_UIs_f32 570.700012 PICurrCntrl MtrVecuFitt_M_str.PrevOutput_UIs_f32 570.700012 PICurrCntrl MtrVecuFitt_M_str.TermN_UIs_f32 43.3250008 PICurrCntrl MtrVecuFitt_M_str.TermD_UIs_f32 0.74449981 PICurrCntrl MtrVoltQaxFFFitt_M_str.PrevInput_UIs_f32 0.74449981 PICurrCntrl MtrVoltQaxFFFitt_M_str.PrevInput_UIs_f32 570.700012 PICurrCntrl MtrVoltQaxFFFitt_M_str.TermN_UIs_f32 570.700012 PICurrCntrl MtrVoltQaxFFFitt_M_str.TermN_UIs_f32 43.3250008 PICurrCntrl MtrVoltQaxFFFitt_M_str.TermN_UIs_f32 43.3250008 PICurrCntrl MtrVoltQaxFFFitt_M_str.TermN_UIs_f32 43.3250008 PICurrCntrl MtrVoltQaxFFFitt_M_str.TermN_UIs_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFitt_M_str.TermN_UIs_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFitt_M_str.TermD_UIs_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFitt_M_str.TermD_UIs_f32 43.3250008 PICurrCntrl_MtrVoltQaxFfFitt_M_str.TermD_UIs_f32 43.3250008 PICurrCntrl_MtrVoltQaxFfitt_M_str.TermD_UIs_f32 43.3250008 PICurrCntrl_MtrVoltQaxFitt_M_str.TermD_UIs_f32 40.05299999999999999999999999999999999999	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 43.3250008 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.744499981 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 0.744499981 SlowDataAccessBuflndex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 k_MtrCdrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResDax_Ohm_f32 0.0529999994 k_MtrCtrVirualResQax_Ohm_f32 1 k_MtrCurrQaxRefModiffDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegHiLim_Volt_f32 25.6000004 k_MtrVoltQaxIntegHiLim_Volt_f32 25.6000004 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegHiLim_Volt_f32 20.3560009 k_MtrVoltQaxIntegLoLim_Volt_f32 20.3560009 k_VollSatQaxPolyCoeff_Uls_f32	
PICurrCntri_MtrVocuFilt_M_str.TermD_Uls_f32 0.744499981 PICurrCntri_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 0 PICurrCntri_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 570.700012 PICurrCntri_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 43.3250008 PICurrCntri_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 0.744499981 SlowDataAccessBufindex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResDax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModiffDsb_Cnt_lgc 1 k_MtrCurrQaxRefModiffpsb_Cnt_lgc 1 k_MtrVoltDaxIntegHilLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegHilLim_Volt_f32 25.6000004 k_MtrVoltQaxFiltFEnable_Cnt_lgc 1 k_MtrVoltQaxFiltefEnable_Cnt_lgc 1 k_MtrVoltQaxFiltefEnable_Cnt_lgc 1 k_MtrVoltQaxFiltefEnable_Cnt_lgc 1 k_MtrVoltQaxFiltefEnable_Cnt_lgc 1 k_MtrVoltQaxFiltefEnable_Cnt_lgc 1	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 0 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.744499981 SlowDataAccessBufindex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResDax_Ohm_f32 0.0529999994 k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegHiLim_Volt_f32 25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegLoLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 20.3560009 k_MtrVoltQaxIntegLoLim_Sf32 -20.3560009 k_VoltSatDaxPolyCoeff_Uls_f32 -1.46500003 <td></td>	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	
PICurrCntrl_MtrVoltQaxFFFit_M_str.TermN_UIs_f32 43.3250008 PICurrCntrl_MtrVoltQaxFFFitt_M_str.TermD_UIs_f32 0.744499981 SlowDataAccessBufIndex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 5777.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResDax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHolim_Volt_f32 21.1630993 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLolim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLolim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLolim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLolim_Volt_f32 20.3560009 k_VoltSatQaxPolyCoeff_UIs_f32 1.46500003 k_deadtimeVScale_UIs_f32 0.971000016 t_CommOffsetTblX_UIs_u3p13[1] 7003 t_Com	
PICurrCntrl_MtrVoltQaxFFFit_M_str.TermD_UIs_f32 0.744499981 SlowDataAccessBufIndex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 5777.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResDax_Ohm_f32 0.0529999994 k_MtrCtrlVirualResQax_Ohm_f32 1 k_MtrCtrlVirualResQax_Ohm_f32 1 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCvlotaxRefModifDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegHiLim_Volt_f32 25.6000004 k_MtrVoltQaxFitFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegHiLim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxIntegLoLim_S12 -20.3560009 k_VoltSatDaxPolyCoeff_UIs_f32 -20.3560009 k_VoltSatDaxPolyCoeff_UIs_f32 -1.46500003 k_OdatimeVScale_UIs_f32 0.971000016 t_CommOffsetTblY_UIs_u3p13[0] 502	
SlowDataAccessBufIndex_Cnt_M_u16	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegHiLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTblX_Uls_u3p13[0] 5022 t_CommOffsetTblY_Cnt_u16[0] 365 t_CommOffsetTblY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegHiLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegHiLim_Volt_f32 -25.6000004 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxIntegDoLim_Volt_f32 -25.6000004 k_MtrVoltPocuFillEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTblX_Uls_u3p13[1] 7003 t_CommOffsetTblY_Cnt_u16[0] 365 t_CommOffsetTblY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr </td <td></td>	
k_ILOAFdbackSignalScIFacSlew_UlspS_f32 5777.70996 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrIVirualResDax_Ohm_f32 0.191 k_MtrCtrIVirualResDax_Ohm_f32 0.0529999994 k_MtrCtrIVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLoLim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLoLim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLoLim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLoLim_Volt_f32 25.6000004 k_MtrVoltQaxIntegLoLim_Volt_f32 20.3560009 k_VoltSatDaxPolyCoeff_Uls_f32 1.46500003 k_VoltSatQaxPolyCoeff_Uls_f32 1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTblX_Uls_u3p13[0] 5022 t_CommOffsetTblY_Cnt_u16[0] 365 t_CommOffsetTblY_Cnt_u16[1] <td< td=""><td></td></td<>	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVeucFiltEnable_Cnt_lgc 1 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_VoltSatDaxPolyCoeff_Uls_f32 1 k_VoltSatDaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTblX_Uls_u3p13[0] 5022 t_CommOffsetTblY_Cnt_u16[0] 365 t_CommOffsetTblY_Cnt_u16[1] 1530 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_Val 1	
k_MtrCtrIFeedbackControlDisable_Cnt_lgc 1 k_MtrCtrIVirualResDax_Ohm_f32 0.191 k_MtrCtrIVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatDaxPolyCoeff_Uls_f32 -1.46500003 k_Journal Common	
k_MtrCtrlVirualResDax_Ohm_f32 0.191 k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVeuFiltEnable_Cnt_lgc 1 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltSatDaxPolyCoeff_Uls_f32 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_Val 1	
k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatDaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_vtl 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_Val 1	
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_vtr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbiX_Uls_u3p13[0] 5022 t_CommOffsetTbiX_Uls_u3p13[1] 7003 t_CommOffsetTbiY_Cnt_u16[0] 365 t_CommOffsetTbiY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_vtr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_MtrVoltDaxIntegHiLim_Volt_f32 21.1630993 k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_vta 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_MtrVoltDaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_MtrVoltQaxFiltFFEnable_Ont_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_MtrVoltQaxIntegHiLim_Volt_f32 26.3924999 k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_MtrVoltQaxIntegLoLim_Volt_f32 -25.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_VoltSatDaxPolyCoeff_Uls_f32 -20.3560009 k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_VoltSatQaxPolyCoeff_Uls_f32 -1.46500003 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTbIX_Uls_u3p13[0] 5022 t_CommOffsetTbIX_Uls_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_deadtimeVScale_UIs_132 0.971000016 t_CommOffsetTbIX_UIs_u3p13[0] 5022 t_CommOffsetTbIX_UIs_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
k_deadtimeVScale_UIs_f32 0.971000016 t_CommOffsetTbIX_UIs_u3p13[0] 5022 t_CommOffsetTbIX_UIs_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1	
t_CommOffsetTbIX_UIs_u3p13[0] 5022 t_CommOffsetTbIX_UIs_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
t_CommOffsetTbIX_UIs_u3p13[1] 7003 t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1	
t_CommOffsetTbIY_Cnt_u16[0] 365 t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1	
t_CommOffsetTbIY_Cnt_u16[1] 1530 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1	
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1	
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1	
0	
target MtrCntrl Read MotCurrl oaMtgtpEn Cnt loc ntr	
a.goorodd_motodireodinigaiEn_Ont_igo_pti	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984	
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 5000	
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -34.6189995	
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	5000	5000	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	58.7199974	58.7200012 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	22.5505848	22.5506001 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	19.9384365	19.9384003 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33552	33552 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✓
MtrCurrQaxFinalRef Amp M f32[1]	58.7199974	58.7200012 ± 7.81E-03	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.162 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.74399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.96099997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	756.674988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	76.4720001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.591000021
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.175999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	730.219971
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1004.94
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001		
MtrCurrQaxPrevIntg_Volt_M_f32	17.6383991		
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009		
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	2.68400002		
MtrPosComputationDelay_Rad_M_f32[1]	5.81400013		
MtrVoltDax_Volt_M_f32[0]	-29.3959999		
MtrVoltDax_Volt_M_f32[1]	-1.9400006		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.9400006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0040000019		
PICurrCottl_MtrCurrOaxSatFluxRatio_Uls_M_f32	0.756799996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	44.6861992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.246199995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0.240199993		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	0		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	44.6861992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.246199995		
SlowDataAccessBufIndex Cnt M u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982		
k ILOAFdbackSignalSclFacSlew UlspS f32	2332.93994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.42719996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	4.96659994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	20.9540005		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	841		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		le le
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	841	841	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	178.335999	178.335999 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.189992487	-0.189991996 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.81125021	4.81125021 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27583	27583 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	178.335999	178.335999 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	•



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓	

Input Value
0
target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
target_MtrCntrl_Read_ModIdxSrlComSvcDft Cnt_lgc_Val
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
target MtrCntrl Read MtrCurrDax Amp f32 Val
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
target MtrCntrl Read MtrCurrQax Amp f32 Val
target_MtrCntrl_Read_SysState_Cnt_Enum_Val
67.4899979
119.721001
0.0109999999
0.039000008
0.0989999995
0.0170000009
1.75899994
1.75899994
-948.984009
-948.984009 -975.934021
0.0419999994
0.0280000009
0.0419999994
0.0280000009
0.261000007
0.754000008
-227.466003
-453.338013 40.000
-16.302
8.55099964
-25.3770008
21.3880005
18.9510002
21.3110008
-27.3339996
31.5869999
-186.395996
-144.667007
37.4550018
-2.84500003
23.1366997
171.485992
163.787003
37.4550018
-2.84500003
-2.94899988
0.0060000005
-7.66699982 2.61400008

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value Name MtrVoltQax_Volt_M_f32[0] -7.66699982 MtrVoltQax_Volt_M_f32[1] 2.61400008 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.702000022 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.00400000019 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.155699998 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -43.1699982 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ 0 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 29.8064003 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0 197799996 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 29.8064003 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0 197799996 SlowDataAccessBufIndex_Cnt_M_u16 5911.31982 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2332.93994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 $k_MtrCtrlVirualResQax_Ohm_f32$ 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 27.6096992 k_MtrVoltDaxIntegHiLim_Volt_f32 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -11.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 20.0156002 k_MtrVoltQaxIntegLoLim_Volt_f32 -11.6000004 k MtrVoltVecuFiltEnable Cnt lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k VoltSatQaxPolyCoeff Uls f32 -12.2449999 k_deadtimeVScale_Uls_f32 0.963 t CommOffsetTblX Uls u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 163 t CommOffsetTblY Cnt u16[1] 1236 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target MtrCntrl Read MtrCurrDax Amp f32 Val -207.917999 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4296 target MtrCntrl Read MtrCurrQax Amp f32 Val 59.7319984 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 4296 4296 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 220 ± 7.81E-03 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 220 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -2.60241628 -2 60242009 + 4 88F-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.05112982 -4.05112982 ± 4.88E-04 7965 7965 + 1 52588F-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32 0 ± 0.0625 0 220 220 ± 7.81E-03 MtrCurrQaxFinalRef Amp M f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1] -2.84500003 -2.84500003 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
WtrCntrl Read ModIdxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
WtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
VtrCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
VtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-200.556
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.085000009
MtrCtrl MtrDampTermDax_Ohm M f32[1]	0.112999998
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.041999994
MtrCtrl MtrDampTermQax_Ohm M f32[1]	0.0280000009
	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.949
	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994 -761.935974
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	0.0850000009
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.30399999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.403015
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
/trCtrl_Vecu_Volt_M_f32[0]	24.8479996
/trCtrl_Vecu_Volt_M_f32[1]	27.2080002
/trCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
/trCurrQaxCog_Amp_M_f32	-42.6814995
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
/trCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	2.99090004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.331
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax Volt M f32[1]	2.61400008





Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013		
PICurrCntrl InverterFailSclFac UIs M f32	0.513000011		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.195500001		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	90.7209015		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.617500007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	90.7209015		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.617500007		
SlowDataAccessBufIndex Cnt M u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	6616.02002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.191		
k MtrCtrlVirualResQax Ohm f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.4471998		
k MtrVoltDaxIntegLoLim Volt_162	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.4596996		
k MtrVoltQaxIntegLoLim Volt f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9090004		
k_deadtimeVScale_UIs_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	674		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
		· ·	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	674	
MtrCntrl_Write_ModIdx_UIs_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-210.986511	0 ± 1	
	2.37544227	-210.985992 ± 7.81E-03 2.37543988 ± 4.88E-04	
MtrCottl_Write_MtrDoxVoltage_Volt_f32(val)			
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.23418236	4.23417997 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	29688	29688 ± 1.52588E-05	· ·
MtrCurrOavFineIRef Arm M f32	0	0 ± 0.0625	
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	
MtrCurrQaxFinalRef_Amp_M_f32[1]	-210.986511	-210.985992 ± 7.81E-03	<u> </u>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Cntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr Cntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val Cntrl_Read_ModlcurrLoaMtgtnEn_Cnt_lgc_ptr Cntrl_Read_MtrCurrDax_Amp_f32_Val Cntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr Cntrl_Read_MtrCurrQax_Amp_f32_Val Cntrl_Read_SysState_Cnt_Enum_Val 97 0009 198 1994 1009 120 110 1009
Cntri_Read_ModIdxSriComSvcDft_Cnt_igc_Val Cntri_Read_MotCurrLoaMtgtnEn_Cnt_igc_ptr Cntri_Read_MtrCurrDax_Amp_f32_Val Cntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr Cntri_Read_MtrCurrQax_Amp_f32_Val Cntri_Read_SysState_Cnt_Enum_Val 97 1009 198 1994 1009 199 12 12
Cntri_Read_ModIdxSriComSvcDft_Cnt_igc_Val Cntri_Read_MotCurrLoaMtgtnEn_Cnt_igc_ptr Cntri_Read_MtrCurrDax_Amp_f32_Val Cntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr Cntri_Read_MtrCurrQax_Amp_f32_Val Cntri_Read_SysState_Cnt_Enum_Val 97 1009 198 1994 1009 199 12 12
Cntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr Cntrl_Read_MtrCurrDax_Amp_f32_Val Cntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr Cntrl_Read_MtrCurrQax_Amp_f32_Val Cntrl_Read_SysState_Cnt_Enum_Val 97 10009 198 1994 1009 199 12 12
Cntri_Read_MtrCurrDax_Amp_f32_Val Cntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr Cntri_Read_MtrCurrQax_Amp_f32_Val Cntri_Read_SysState_Cnt_Enum_Val 97 10009 198 1994 1009 199 12 12
Cntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr Cntri_Read_MtrCurrQax_Amp_f32_Val Cntri_Read_SysState_Cnt_Enum_Val 97 10009 198 1994 10009 199 12 12
Cntrl_Read_MtrCurrQax_Amp_f32_Val Cntrl_Read_SysState_Cnt_Enum_Val 97 10009 198 19994 10009 1999 12 12
Cntri_Read_SysState_Cnt_Enum_Val 97 1009 198 1994 1009 199 12 112
97 0009 98 9994 0009 99 2 12
009 98 994 009 99 2 12
009 98 994 009 99 2 12
98 994 009 99 2 12 01
994 009 99 2 12 01
009 99 2 12 01
99 2 12 01
12 01
12 01
01
009
98
009
98
16
29
07
08
5
03
95
6
3
11
 07
01
16
93
96
00
5
5
9
9 1
9 11 005
9 1
99: 999 70:

PICurrCntrl_Per1

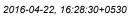


Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.513000011		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.211199999		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	6.4671998		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0860000029		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	6.4671998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.0860000029		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k CLOAFdbackSignalSclFacSlew UlspS f32	6616.02002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.191		
k_MtrCtrlVirualResQax_Ohm_f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.2147999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.0927		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	22.7819996		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4608	4608	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-216.344513	-216.343994 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	6.19107008	6.19107008 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	23.8335838	23.8335991 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	62638	62638 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.344513	-216.343994 ± 7.81E-03	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.166 (Repeat Count = 1) Name	Innut Value
	Input Value
FastDataAccessBufIndex_Cnt_M_u16	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16600001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.221985
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.232999995
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-121.924004
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	483.27301
VtrCtrl MtrVoltDaxFF Volt M f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl MtrVoltQaxFF Volt M f32[0]	18.6380005
VtrCtrl MtrVoltQaxFF Volt M f32[1]	-23.1870003
VtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
	20.0610008
WtrCtrl_Vecu_Volt_M_f32[1]	
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
VtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-31.9654999
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	0.29429999
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00600000005
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax Volt M f32[0]	-7.66699982





Name	Input Value		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.1822		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	57.8652992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.414700001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	57.8652992		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.414700001		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k CLOAFdbackSignalSclFacSlew UlspS f32	6145.56982		
k ILOAFdbackSignalSclFacSlew UlspS f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.193000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.6170006		
k MtrVoltDaxIntegLoLim Volt f32	-22.4099998		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	29.5634995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k VoltSatQaxPolyCoeff Uls f32	19.2689991		
k_deadtimeVScale_UIs_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
		Fune stad Value	Daguit
Name MirCodel Write CommonCiffont Cost (146(1))	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	· ·
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65339	65339 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	· ·
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.52427006	-2.52427006 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.29863739	-4.29863977 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38369	38369 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	·
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	<u> </u>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.167 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.722
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039999991
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0179999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.45899999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.934998
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.041999994
MtrCtrl_MtrlmpedOax_Ohm_M_f32[1]	0.028999992
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994 0.0289999992
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl_MtrQayIntegralGain_Ohm_M_f32[0]	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.261000007 0.754999995
	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.338989 -16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-10.302 8.55200005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3889999
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-0.216499999
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.397003
MtrCurrQaxCog_Amp_M_f32	-26.6075001
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg Volt M f32	24.4969997
MtrCurrQaxRef Amp M f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787994
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl Amp M f32[1]	-2.84599996
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay Rad M f32[1]	0.00700000022
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl CurrSensFailSclFac Uls M f32	0.851999998
PICurrCntrl InverterFailSclFac UIs M f32	0.513000011
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.953199983
PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.778999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	96.5500031
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	69.4054031
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.3134
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	76.1873016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.821500003
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3865.98999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.140000001
k_MtrCtrlVirualResQax_Ohm_f32	0.0769999996
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.3593998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.2693005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-31		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	-12.2449999		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	665		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	665	665	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	160.638489	160.639008 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	16.7303963	16.7304001 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-25.987236	-25.9871998 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61579	61579 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.638489	160.639008 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.168 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95000005
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.937012
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
	0.0850000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] MtrCtrl_MtrQayIntegralGain_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999 0.305000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996 903.403992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDayEE_Volt_M_f32[0]	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995 5.12000081
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	5.26809978
MtrCurrDayRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-21.2495003
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	15.5079002
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.331999987
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00400000019
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.486799985
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.76819998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.5784988
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.889199972
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	99.3730011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.472600013
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2156.63989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0209999997
k_MtrCtrlVirualResQax_Ohm_f32	0.172999993
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	0.465799987
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	13.9652004
k_MtrVoltQaxIntegLoLim_Volt_f32	0
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9090004
k_deadtimeVScale_Uls_f32	0.958999991
t_CommOffsetTblX_Uls_u3p13[0]	459
t_CommOffsetTblX_Uls_u3p13[1]	5775
t_CommOffsetTblY_Cnt_u16[0]	1237
t_CommOffsetTblY_Cnt_u16[1]	383
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1724
0	

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62849	62849 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	29.4615059	29.4615002 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.88322926	2.88322997 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.83131933	-3.83132005 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22576	22576 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-4.57000017	-4.57000017 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	29.4615059	29.4615002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_	

Test Step 2.169 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.723
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0189999994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46000004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.935974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.029999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.029999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.755999982
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.339996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55300045
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3899994
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	21.7219009
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.397995

© Report created by TESSY V3.1.11, report template V2.1

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxCog_Amp_M_f32 -5.17549992 MtrCurrQaxFinalRef_Amp_M_f32[0] 37.4550018 MtrCurrQaxFinalRef_Amp_M_f32[1] -2.84500003 $MtrCurrQaxPrevIntg_Volt_M_f32$ 7.74660015 MtrCurrQaxRef_Amp_M_f32[0] 171.485992 MtrCurrQaxRef_Amp_M_f32[1] 163.789001 MtrCurrQaxRpl_Amp_M_f32[0] 37.4550018 $MtrCurrQaxRpl_Amp_M_f32[1]$ -2.84699988 MtrPosComputationDelay_Rad_M_f32[0] -2.94899988 MtrPosComputationDelay_Rad_M_f32[1] 0.00800000038 MtrVoltDax_Volt_M_f32[0] -7.66699982 MtrVoltDax_Volt_M_f32[1] 2 61400008 MtrVoltQax_Volt_M_f32[0] -7.66699982 2 61400008 MtrVoltQax Volt M f32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.723999977 0.0450000018 PICurrCntrl InverterFailSclFac Uls M f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.807699978 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.735800028 $PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32$ -340.130005 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -717.299988 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 52.7086983 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.75029999 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 1118 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32$ 16.5851002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0452999994 SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6110.83008 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k MtrCtrlFeedbackControlDisable Cnt lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.0909999982 k MtrCtrlVirualResQax Ohm f32 0.155000001 k MtrCurrQaxRefModifDsb Cnt Igo k_MtrCurrQaxRefModifRplEn_Cnt_lgc k MtrVoltDaxIntegHiLim Volt f32 11.4057999 k_MtrVoltDaxIntegLoLim_Volt_f32 -11.6000004 k MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 28.1163998 11.6000004 k MtrVoltQaxIntegLoLim Volt f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k VoltSatDaxPolyCoeff Uls f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 18.1280003 k_deadtimeVScale_Uls_f32 0.958999991 t_CommOffsetTblX_Uls_u3p13[0] 459 t_CommOffsetTblX_Uls_u3p13[1] 5775 t_CommOffsetTblY_Cnt_u16[0] 771 t_CommOffsetTblY_Cnt_u16[1] 1636 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 1789 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -34.6189995 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1636 1636 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 62849 62849 ± 1 131 509491 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 131.509995 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 2.6636548 2.66365004 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 6.66147232 6.66146994 ± 4.88E-04 4051 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 4051 -11.6000004 -11.6000004 ± 0.0625 MtrCurrDaxPrevIntg Volt M f32 MtrCurrQaxFinalRef_Amp_M_f32[0] 131.509491 131.509995 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[1]

-2.84500003

-2.84500003 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
/dtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)		
	target_MtrCntrl_Read_ModIdxSrIComSvcDft_Cnt_lgc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)		
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOrrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
AtrChtl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]		
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997	
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998	
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95099998	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.937988	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.305999994	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.405029	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995	
/trCtrl_Vecu_Volt_M_f32[0]	18.9510002	
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008	
/trCurrDaxPrevIntg_Volt_M_f32	27.2064991	
/trCurrDaxRef_Amp_M_f32[0]	-146.723007	
/trCurrDaxRef_Amp_M_f32[1]	-121.943001	
MtrCurrQaxCog_Amp_M_f32	0.182500005	
/trCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	
/trCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	
MtrCurrQaxPrevIntg_Volt_M_f32	14.6610003	
/trCurrQaxRef_Amp_M_f32[0]	-133.947006	
/trCurrQaxRef_Amp_M_f32[1]	75.7020035	
/trCurrQaxRpl_Amp_M_f32[0]	67.4899979	
/trCurrQaxRpl_Amp_M_f32[1]	119.721001	
/trPosComputationDelay_Rad_M_f32[0]	2.33500004	
MtrPosComputationDelay_Rad_M_f32[1]	-0.333000004	
/trVoltDax_Volt_M_f32[0]	-7.66699982	
/trVoltDax_Volt_M_f32[1]	2.61400008	
MtrVoltQax_Volt_M_f32[0]	-7.66699982	
MtrVoltQax_Volt_M_f32[1]	2.61400008	

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



PICURCINIT Lorder FailsolFac, Uls M, 132 0.389999879 PICURCINI Inverter FailsolFac, Uls M, 132 0.23990004 PICURCINI MicCurbasSaFilurRatio, Uls M, 132 0.729000024 PICURCINI MicCurbill M, 1st PrevOutput, Uls 132 1118 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.840799999 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.840799999 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.840799999 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.8779993 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.8779999 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.7980000021 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.986000021 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.98600001 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.98600001 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.98600001 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.98600001 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.98600001 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.98600001 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.98600001 PICURCINI MicCurbill M, 1st Term D, Uls 132 0.98600001 PICURCINI Miccurbill M, 1st Term D, Uls 132 0.9899000001 PICURCINI Miccurbill M, 1st Term D, Uls 132 0.989900001 PICURCINI Miccurbill M, 1st Term D, Uls 132 0.9899900001 PICURCINI Miccurbill M, 1st Term D, Uls 132 0.98999000001 PICURCINI Miccurbill M, 1st Term D, Uls 132 0.98999000001 PICURCINI Miccurbill M, 1st Term D, Uls 132 0.989990000001 PICURCINI Miccurbill M, 1st Term D, Uls 132 0.9899000000000000000000000000000000000	Name	Input Value		
Picurrotnt_Inverter FallsGrac_Uis_M_132 0.388999899 PiCurrotnt_MrCurrDaxSafFuxRatio_Uis_M_132 0.728000024 PiCurrotnt_MrCurrDaxSafFuxRatio_Uis_M_132 0.728000024 PiCurrotnt_MrVecuFil_M_str.Previnput_Uis_132 1118 PiCurrotnt_MrVecuFil_M_str.Previnput_Uis_132 1118 PiCurrotnt_MrVecuFil_M_str.Previnput_Uis_132 0.64799999 PiCurrotn_MrVecuFil_M_str.Previnput_Uis_132 0.64799999 PiCurrotn_MrVecuFil_M_str.Previnput_Uis_132 0.64799999 PiCurrotn_MrVecuFil_M_str.Previnput_Uis_132 427.179993 PiCurrotn_MrVediDaxFFFil_M_str.Previnput_Uis_132 427.179993 PiCurrotn_MrVediDaxFFFil_M_str.Previnput_Uis_132 37.3795027 PiCurrotn_MrVediDaxFFFIL_M_str.Previnput_Uis_132 37.3795027 PiCurrotn_MrVediDaxFFFIL_M_str.TermD_Uis_132 0.988000021 PiCurrotn_MrVediDaxFFFIL_M_str.TermD_Uis_132 0.988000021 PiCurrotn_MrVediDaxFFFIL_M_str.TermD_Uis_132 0.988000021 PiCurrotn_MrVediDaxFFFIL_M_str.TermD_Uis_132 0.988000021 PiCurrotn_MrVediDaxFFIL_M_str.TermD_Uis_132 0.9880000000000000000000000000000000000	PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	· ·		
PICUTCHIS MITCUTTDASSAFT LURABIO_UIS_M_152 0.725000024 PICUTCHIS MITCUTTDASSAFT LURABIO_UIS_M_152 784,150005 7		0.398999989		
PIGUTCHIL MITVEUFILM Str. Previoput_UIs_132		0.203600004		
PICUrChtI_MIVecuFit_M_strTemD_UIs_f32	PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000024		
PICUrChtI_MIVecuFit_M_strTemD_UIs_f32		-784.130005		
PICUTCHIL_MITVECUFIL_M_str.TermN_UIs_f32		1118		
PICurCntf_MirvecuFit_M_str.TermD_Uis_132 0.640799999 PICurCntf_Mirvior(CaxFFFIIL M_str.Pervolput_Uis_132 627.179993 PICurCntf_Mirvior(CaxFFFIIL M_str.Pervolput_Uis_132 627.179993 PICurCntf_Mirvior(CaxFFFIIL M_str.PermD_Uis_162 0.96000021 PICurCntf_Mirvior(CaxFFFIIL M_str.PermD_Uis_162 0.96000021 SlowDataAccessBufindex_Cnt_M_url 6 1 k_CLO.AFdbackSignalSclFacSlew_UispS_172 6145.56982 k_UICAFdbackSignalSclFacSlew_UispS_172 6489.7002 k_Mircir(Curt_LoopSecOrTranFCEnable_Cnt_Igc 1 k_Mircir(Surl-GospacCortranFCEnable_Cnt_Igc 0.19300004 k_Mircir(Surl-GospacCortranFCEnable_Cnt_Igc 0.19300004 k_Mircir(VirualResDax_Cohm_172 0.19300004 k_Mircir(VirualResDax_Cohm_172 0.19300004 k_Mircir(VirualResDax_Cohm_172 0.19300004 k_Mircir(CaxResPervodiffip)En_Cnt_Igc 1 k_Mircir(CaxResPervodiffip)En_Cnt_Igc 1 k_Mircir(CaxResPervodiffip)En_Cnt_Igc 1 k_Mircir(CaxResPervodiffip)En_Cnt_Igc 1 k_Mircir(CaxResPervodiffip)En_Cnt_Igc 1 k_Mircir(CaxResPervodiffip)En_Cnt_Igc 1 k_Mirci		87.7649002		
PCurrChtr MtrVoltQaxFFFitt M_str.Prevloput_Uls_132 -627.17993 PCurrCht MtrVoltQaxFFFitt M_str.Prevloput_Uls_132 -627.17993 PCurrCht MtrVoltQaxFFFitt M_str.PremV_uls_132 0.96600021 SlowDataAccessBuffndex Cnt_M_ulf		0.640799999		
PCurrCntr MtrVotCaxFFFit M_str. PrevOutput_Uis_132 87.375027 PCurrCntr MtrVotCaxFFFit M_str. Term_Uis_132 87.3075027 PCurrCntr MtrVotCaxFFFit M_str. Term_Uis_132 0.966000021 SlowDataAccessBufindex_Cnt_M_u16 1		-627.179993		
PICurrCntrl_MtrVottQaxFFFit_M_str.TermN_UIs_f32 87.3075027 PICurrCntrl_MtrVottQaxFFFit_M_str.TermD_UIs_f32 0.966000021 SlowDataAccessBufindex_Cnt_M_u16 1 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6145.56982 k_ILCAFdbackSignalSclFacSlew_UIspS_f32 64897002 k_MtrCtrICrtropoSecOrTnaFChable_Cnt_Igc 1 k_MtrCtrVirualResQbax_Ohm_f32 0.193000004 k_MtrCtrVirualResQbax_Ohm_f32 0.0179999992 k_MtrCtrVirualResQbax_Ohm_f32 0.0179999992 k_MtrCutrVirualResQbax_Ohm_f32 0.0179999992 k_MtrCutrVirualResQbax_Ohm_f32 0.0179999992 k_MtrCutrQaxRefModiffxplEn_Cnt_Igc 1 k_MtrVottDaxIntegHill.Im_Volt_f32 30.3612995 k_MtrVottDaxIntegLoLim_Volt_f32 30.2000008 k_MtrVottQaxIntegLoLim_Volt_f32 29.5142002 k_MtrVottQaxIntegLoLim_Volt_f32 14.5 k_MtrVottQaxIntegLoLim_Volt_f32 18.2779999 k_VottSatDaxPolyCoeff_UIs_f32 18.2779999 k_VottSatDaxPolyCoeff_UIs_f32 18.2779999 k_VottSatDaxPolyCoeff_UIs_f32 18.2779999 k_UcommOffsetTbtx_UIs_usp13[0] 6528		-627.179993		
PICurrCntri_MrtvoltQaxFFFitL_Mstr.TermD_Uls_f32 0.966000021 SlowDataAccessBufundex_Cnt_M_u16 1 k_CLOAF dasckSignalSclFacslew_UlspS_f32 6145.56982 k_MtrCtrICurrLoopSecOfTranFcEnable_Cnt_Ugc 6489.7002 k_MtrCtrICurrLoopSecOfTranFcEnable_Cnt_Ugc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.193000004 k_MtrCtrIVirualResDax_Ohm_f32 0.179999992 k_MtrCurrQaxRefModiffSple_Cnt_Ugc 0 k_MtrCurrQaxRefModiffSple_Cnt_Ugc 1 k_MtrVoltDaxIntegHiLm_Volt_f32 30.3612995 k_MtrVoltDaxIntegHiLm_Volt_f32 30.2000008 k_MtrVoltDaxIntegHiLm_Volt_f32 30.2000008 k_MtrVoltQaxIntegHiLm_Volt_f32 41.5 k_MtrVoltQaxIntegHiLm_Volt_f32 14.5 k_MtrVoltQaxIntegHiLm_Volt_f32 17.8769998 k_MtrVoltQaxIntegHiLm_Volt_f32 18.2779999 k_MtrVoltQaxIntegHiLm_Volt_f32 7.8769998 k_VoltSatQaxPolyCoeff_Uls_f32 0.96699979 k_VoltSatQaxPolyCoeff_Uls_f32 0.966999979 t_CommOffsetTblY_Cls_u3p13(f) 6528 t_CommOffsetTblY_Cls_u3p13(f) 66 t_CommOffsetTblY_Cls_u4f(f)		87.3075027		
SlowDataAccessBufIndex_Cnt_M_u16 1		0.966000021		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6145.56982 k_ILLOAFdbackSignalSclFacSlew_UlspS_f32 6489.7002 k_MrtCrUtrucpSecOtTranFCenable_Cnt_lgc 1 k_MrtCrUtrivalResDax_Ohm_f32 0.193000004 k_MtrCtrUtrualResQax_Ohm_f32 0.0179999992 k_MtrCturQaxRefModiffSpl_Cn_Llgc 0 k_MtrCturQaxRefModiffSpl_Cn_Llgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 30.3612995 k_MtrVoltDaxIntegHiLim_Volt_f32 30.302000008 k_MtrVoltDaxIntegHiLim_Volt_f32 30.2000008 k_MtrVoltQaxIntegHiLim_Volt_f32 30.2000008 k_MtrVoltQaxIntegHiLim_Volt_f32 40.200000 k_MtrVoltQaxIntegLoLim_Volt_f32 41.5 k_MtrVoltQaxIntegLoLim_Volt_f32 14.5 k_MtrVoltVeucFiltEnable_Cnt_lgc 1 k_VoltSalDaxPolyCoeff_Uls_f32 7.8769998 k_VoltSalDaxPolyCoeff_Uls_f32 0.99699997 k_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIX_Uls_u3p13[1] 211 target_MtrCntr_Read_MotCurLoaMtghEn_Cnt_lgc_ptr 1 target_MtrCntr_Read_MotCurLoaMtghEn_Cnt_lgc_ptr 1 target_MtrCntr_Read_MtrCurrDax_Amp_f32_Val		1		
k_LICOAFdbackSignalScIFacSlew_UispS_f32 6489.7002 k_MtrCtrICurtLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrIVirualResDax_Ohm_f32 0.193000004 k_MtrCtrIVirualResDax_Ohm_f32 0.01799999992 k_MtrCtrIVirualResDax_Ohm_f32 0.01799999992 k_MtrCurtQaxRefModiffplE_Cnt_lgc 1 k_MtrCurtQaxRefModiffplE_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 30.3612995 k_MtrVoltDaxIntegHiLim_Volt_f32 30.2000008 k_MtrVoltQaxFiltefFEnable_Cnt_lgc 1 k_MtrVoltQaxFiltefFenable_Cnt_lgc 1 k_MtrVoltQaxFiltefFenable_Cnt_lgc 1 k_MtrVoltQaxFiltefFenable_Cnt_lgc 1 k_MtrVoltQaxFiltefFenable_Cnt_lgc 1 k_VoltSalDaxPolyCoeff_Uls_f32 14.5 k_VoltSalDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSalDaxPolyCoeff_Uls_f32 7.8769986 k_deadtimeVScale_Uls_J32 0.99699979 t_CommOffsetTbX_Uls_u3p13[0] 6528 t_CommOffsetTbY_Cnt_u16[0] 76 t_CommOffsetTbY_Cnt_u16[1] 211 target_MtrCntrl_Read_MotCurt_LoaMginEn_Cnt_lgc_ptr 1		6145.56982		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlCurrLoopSecOrtrolDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.193000004 k_MtrCtrlVirualResDax_Ohm_f32 0.0179999992 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrVotIDaxIntegHiLim_Volt_f32 3.03612995 k_MtrVotIDaxIntegHiLim_Volt_f32 30.2000008 k_MtrVotIQaxIntegHiLim_Volt_f32 30.2000008 k_MtrVotIQaxIntegLoLim_Volt_f32 1 k_MtrVotIQaxIntegLoLim_Volt_f32 14.5 k_MtrVotIQaxIntegLoLim_Volt_f32 1.5 k_MtrVotIQaxIntegLoLim_Volt_f32 1.5 k_MtrVotIQaxIntegLoLim_Volt_f32 1.5 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIX_Cls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_v1a 1 t				
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.193000004 k_MtrCtrlVirualResQax_Ohm_f32 0.0179999992 k_MtrCurrQaxRefModifDs_Cnt_lgc 0 k_MtrVoltDaxRefModifRplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 30.3612995 k_MtrVoltDaxIntegLoLim_Volt_f32 -30.2000008 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_MtrVollQaxIntegHiLim_Volt_f32 29.5142002 k_MtrVollQaxIntegHiLim_Volt_f32 14.5 k_MtrVollQaxIntegLoLim_Volt_f32 14.5 k_MtrVollCaxIntegLoLim_Volt_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[f] 8192 t_CommOffsetTblY_Cnt_u16[0] 76 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val 59.7319984 target_MtrCntrl_Read_MtrCurrGnCmOffSet_Cnt_u16_ptr <td>- · · · -</td> <td></td> <td></td> <td></td>	- · · · -			
k_MtrCtrlVirualResDax_Ohm_f32 0.193000004 k_MtrCtrlVirualResDax_Ohm_f32 0.0179999992 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCvrQaxRefModifIpEIn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 30.3612995 k_MtrVoltDaxIntegLoLim_Volt_f32 -30.2000008 k_MtrVoltQaxIntegHiLim_Volt_f32 1 k_MtrVoltQaxIntegHiLim_Volt_f32 29.5142002 k_MtrVoltQaxIntegLoLim_Volt_f32 -14.5 k_MtrVoltOx UpditEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
k_MtrCtr/VirualResQax_Ohm_f32 0.0179999992 k_MtrCurrQaxRefModiffSb_Cnt_lgc 0 k_MtrCurrQaxRefModiffSplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 30.3612995 k_MtrVoltDaxIntegHiLim_Volt_f32 -30.2000008 k_MtrVoltQaxIntegHiLim_Volt_f32 29.5142002 k_MtrVoltQaxIntegHiLim_Volt_f32 29.5142002 k_MtrVoltQaxIntegHiLim_Volt_f32 -14.5 k_MtrVoltVcucrFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrI_Read_IvtrLoaMtghEn_Cnt_lgc_ptr 1 target_MtrCntrI_Read_MoldxSriComSvcDf_Cnt_lgc_Val 1 target_MtrCntrI_Read_MoltCurrClax_Amp_f32_Val 59.7319984 target_MtrCntrI_Read_MtrCurrQax_Amp_f32_Val 59.7319984		0.193000004		
k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHil.im_Volt_f32 30.3612995 k_MtrVoltDaxIntegHil.im_Volt_f32 -30.200008 k_MtrVoltDaxIntegLoLim_Volt_f32 -30.200008 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHil.im_Volt_f32 29.5142002 k_MtrVoltQaxIntegHil.im_Volt_f32 14.5 k_MtrVoltQaxIntegHil.im_Volt_f32 18.277999 k_MtrVoltQaxIntegHil.im_Volt_f32 18.2779999 k_VoltSatCaxPolyCoeff_Uls_f32 17.87699986 k_MtrComOffSetTibI_VIs_g32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTibI_VIs_g32 8192 t_CommOffsetTibI_VIs_g313[1] 8192 t_CommOffsetTibI_VIs_uls_g13[1] 211 targe		0.0179999992		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 30.3612995 k_MtrVoltQaxIntegLoLim_Volt_f32 -30.2000008 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_MtrVoltQaxIntegHiLim_Volt_f32 29.5142002 k_MtrVoltQaxIntegHiLim_Volt_f32 -14.5 k_MtrVoltVeuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.8769986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_MoldxSrlComSvcDft_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MolCurrLoaMtghEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MolCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffcomOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 59.7319984				
k_MtrVoltDaxIntegHiLim_Volt_f32 30.3612995 k_MtrVoltQaxFiltFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHolLim_Volt_f32 29.5142002 k_MtrVoltQaxIntegHiLim_Volt_f32 29.5142002 k_MtrVoltQaxIntegLoLim_Volt_f32 -14.5 k_MtrVoltQaxDrolt/vecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_ModldxSrlComSvcDft_cnt_lgc_val 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_cnt_lgc_val 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984		1		
k_MtrVoltDaxIntegLoLim_Volt_f32 -30.2000008 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 29.5142002 k_MtrVoltVecuFiltEnable_Cnt_lgc -14.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblV_Uls_u3p13[0] 6528 t_CommOffsetTblY_Cnt_u16[0] 76 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_val 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
k_MtrVoltQaxIntegHitLim_Volt_f32 1 k_MtrVoltQaxIntegLoLim_Volt_f32 29.5142002 k_MtrVoltQaxIntegLoLim_Volt_f32 -14.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984		-30.2000008		
k_MtrVoltQaxIntegHiLim_Volt_f32 29.5142002 k_MtrVoltQaxIntegLoLim_Volt_f32 -14.5 k_MtrVoltVecuFitEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIX_Uls_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_Modldx5rlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MctCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
k_MtrVoltQaxIntegLoLim_Volt_f32 -14.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MoldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984		29.5142002		
k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_vtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 6528 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_vtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
k_deadtimeVScale_UIs_f32 0.996999979 t_CommOffsetTbIX_UIs_u3p13[0] 6528 t_CommOffsetTbIX_UIs_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_vtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
t_CommOffsetTblX_Uls_u3p13[0] 6528 t_CommOffsetTblX_Uls_u3p13[1] 8192 t_CommOffsetTblY_Cnt_u16[0] 76 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_vtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984		0.996999979		
t_CommOffsetTbIX_UIs_u3p13[1] 8192 t_CommOffsetTbIY_Cnt_u16[0] 76 t_CommOffsetTbIY_Cnt_u16[1] 211 target_MtrCntrl_Read_vtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984		6528		
t_CommOffsetTblY_Cnt_u16[0] 76 t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_vtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
t_CommOffsetTblY_Cnt_u16[1] 211 target_MtrCntrl_Read_vtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
target_MtrCntrl_Read_\trLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 65 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984				
Name Actual Value Expected Value Result			Expected Value	Result
·	MtrCntrl Write CommOffset Cnt u16(val)	65	· ·	
			11	•
		-	-	
11.2011000 11.012 00				•
7.7.2.42.7.7.4.0.0				-
				•
				_
MtrCurrQaxFinalRef Amp M f32[0] 37.4550018 37.4550018 ± 7.81E-03 ▼	▽ − − −			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

-44.2014999

-44.2014999 ± 7.81E-03

MtrCurrQaxFinalRef_Amp_M_f32[1]



est Step 2.171 (Repeat Count = 1)		
lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	0	
ftrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
htrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
ItrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
ltrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556	
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997	
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009	
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998	
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.34499999	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10000002	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.875	
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009	
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998	
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.70000005	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.73999	
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005	
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003	
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995	
trCtrl_Vecu_Volt_M_f32[0]	18.5559998	
trCtrl_Vecu_Volt_M_f32[1]	20.9160004	
trCurrDaxPrevIntg_Volt_M_f32	-18.5370007	
trCurrDaxRef_Amp_M_f32[0]	-146.723007	
ItrCurrDaxRef_Amp_M_f32[1]	-121.943001	
trCurrQaxCog_Amp_M_f32	5.54050016	
trCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	
ltrCurrQaxFinalRef_Amp_M_f32[1]	-66.7249985	
trCurrQaxPrevIntg_Volt_M_f32	12.7981997	
ItrCurrQaxRef_Amp_M_f32[0]	-133.947006	
trCurrQaxRef_Amp_M_f32[1]	75.7020035	
	67.4899979	
trCurrQaxRpl_Amp_M_f32[0]		
trCurrQaxRpI_Amp_M_f32[1]	119.721001	
trPosComputationDelay_Rad_M_f32[0]	-0.532000005	
trPosComputationDelay_Rad_M_f32[1]	3.21000004	
trVoltDax_Volt_M_f32[0]	18.6380005	
trVoltDax_Volt_M_f32[1]	-23.1889992	
trVoltQax_Volt_M_f32[0]	-7.66699982	
trVoltQax_Volt_M_f32[1]	2.61400008	
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993	
CurrCntrl_InverterFailSclFac_Uls_M_f32	0.111000001	
CurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.398900002	
CurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.71420002	
CurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001	
CurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005	
CurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	58.6543999	
CurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.052099999	
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1118	
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1118	
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	97.3968964	
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.830900013	
owDataAccessBufIndex_Cnt_M_u16	0	
CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991	
ILOAFdbackSignalSclFacSlew UlspS f32	4019.20996	
_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0	
_MtrCtrlFeedbackControlDisable_Cnt_lgc	1	
_MtrCtrlVirualResDax_Ohm_f32	0.112000003	
_MtrCtrlVirualResQax_Ohm_f32	0.0219999999	
o	1	

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	22.8146992		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	0		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	6.82399988		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTbIX_UIs_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1357		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1357	1357	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-206.977509	-206.977005 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.8397541	-17.8397999 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.52724576	-2.52725005 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42135	42135 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-206.977509	-206.977005 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7249985	-66.7249985 ± 7.81E-03	~

Test Step Call Trace				-0
<u> </u>				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	•

Test Step 2.172 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.087006	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0109999999	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.100000001	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16799998	





Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.223999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.127000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0960000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.234999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.274994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.618
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1889992
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.397995
MtrCurrQaxCog_Amp_M_f32	10.8985004
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988
	15.8292999
MtrCurrQaxPrevIntg_Volt_M_f32	
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.789001
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7249985
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00800000038
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61599994
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.816999972
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.489399999
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.703400016
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40.2612
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.176699996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl MtrVoltQaxFFFilt M str.TermN UIs f32	16.5851002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.727199972
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7823.27002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996
k_MtrCtrlVirualResQax_Ohm_f32	0.114
k MtrCurrQaxRefModifDsb Cnt lgc	0
k MtrCurrQaxRefModifRplEn Cnt lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	28.3733006
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	31
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003
k_VoltSatQaxPolyCoeff_Uls_f32	4.35599995
k_deadtimeVScale_Uls_f32	0.963
t_CommOffsetTbIX_UIs_u3p13[0]	459
t_CommOffsetTbIX_UIs_u3p13[1]	5775
	1081
t_CommOffsetTblY_Cnt_u16[0]	
t_CommOffsetTblY_Cnt_u16[1]	1779
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	903
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1779	1779	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63111	63111 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1.44797277	1.44797003 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.59212399	4.59212017 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3269	3269 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988	-2.84699988 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.173 (Repeat Count = 1)	ranger i de la companya de la compa
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.723999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0199999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46099997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.937012
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0309999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0309999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.757000029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.341003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.5539999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3910007
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.399002
MtrCurrQaxCog_Amp_M_f32	16.2565002

PICurrCntrl_Per1



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg_Volt_M_f32	15.6167002		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.789993		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84800005		
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988		
MtrPosComputationDelay_Rad_M_f32[1]	0.0089999961		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.446700007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.692600012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	60.2319984		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.306199998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	570.700012 570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	78.8641968		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.584299982		
SlowDataAccessBufIndex_Cnt_M_u16	7083.27002		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.023		
k MtrCtrlVirualResQax Ohm f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.67889977		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.5		
k MtrVoltQaxIntegLoLim Volt f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k VoltSatDaxPolyCoeff Uls f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	-6.98999977		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTbIX_UIs_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	150.3815	150.382004 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.62001944	7.62001991 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	19.0553932	19.0554008 ± 4.88E-04	~
	4000	4062 ± 1.52588E-05	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	4062		
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	
			~



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Test Step 2.174 (Repeat Count = 1) Name	Input Value
	Input value
FastDataAccessBufIndex_Cnt_M_u16	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95200002
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.939026
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.307000011
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-174.839996
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	903.406006
VtrCtrl MtrVoltDaxFF Volt M f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
VtrCtrl MtrVoltQaxFF Volt M f32[0]	-3.59500003
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-28.4209995
htrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	21.6145
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	18.8419991
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpI_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.333999991
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax Volt M f32[0]	-3.59500003

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[1]	-28.4209995		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.499000013		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.716799974		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.681800008		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	76.1873016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404900014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	43.3250008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.605599999		
SlowDataAccessBufIndex Cnt M u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991		
k ILOAFdbackSignalSclFacSlew UlspS f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k MtrCurrQaxRefModifRpIEn Cnt Igc	1		
k MtrVoltDaxIntegHiLim Volt f32	21.7716999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.1749001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	0.398000002		
k deadtimeVScale UIs f32	0.971000016		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTbIX_UIs_u3p13[1]	5775		
t_CommOffsetTbIY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target MtrCntrl Read SysState Cnt Enum Val	2		
		Europete d Welter	D14
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✓
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	63635	63635 ± 1	- V
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-13.4024963	-13.4025002 ± 7.81E-03	· ·
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.73418069	-3.73417997 ± 4.88E-04	- J
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.10272765	-3.10273004 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38437	38437 ± 1.52588E-05	_
MtrCurrDaxPrevIntg_Volt_M_f32	21.7716999	21.7716999 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	-13.4024963	-13.4025002 ± 7.81E-03	_
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl Write CommOffset Cnt u16	1	~



Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
/trCtrl_MtrDampTermDax_Ohm_M_f32[0] /trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0850000009 0.112999998
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0419999994
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10099995
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.875999
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.79999995
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.740997
//trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
/ltrCtrl_Vecu_Volt_M_f32[0]	29.0240002
/trCtrl_Vecu_Volt_M_f32[1]	30.3600006
MtrCurrDaxPrevIntg_Volt_M_f32	-27.3339996
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	26.9724998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7259979
MtrCurrQaxPrevIntg_Volt_M_f32	21.7777996
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006 75.7020035
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl Amp M f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005
MtrPosComputationDelay_Rad_M_f32[1]	3.22000003
MtrVoltDax Volt M f32[0]	18.6380005
MtrVoltDax_Volt_M_i32[1]	-23.1900005
MtrVoltQax_Volt_M_f32[0]	-25.3770008
MtrVoltQax_Volt_M_f32[1]	21.3880005
PICurrCntrl CurrSensFailSclFac Uls M f32	0.851000011
PICurrCntrl InverterFailSclFac Uls M f32	0.757000029
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.476399988
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.671000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	99.3730011
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.404500008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	44.6861992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.82130003
SlowDataAccessBufIndex_Cnt_M_u16	1
:_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
c_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5
<_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
<_MtrCtrlVirualResDax_Ohm_f32	0.193000004
<_MtrCtrlVirualResQax_Ohm_f32	0.155000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1





Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.9853992		
k_MtrVoltDaxIntegLoLim_Volt_f32	-31		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.3924999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4000		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4000	4000	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.42178202	4.42178011 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	29.774456	29.7744999 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61525	61525 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	-
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	-

Test Step Call Trace	Test Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.176 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.087997	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0120000001	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.109999999	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16900003
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.225006
MtrCtrl MtrImpedDax Ohm M f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.128000006
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.236000001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.276001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6190004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1900005
MtrCtri_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.399002
MtrCurrQaxCog Amp M f32	32.3305016
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84800005
MtrCurrQaxPrevIntg_Volt_M_f32	2.29920006
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.789993
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7259979
	-2.94899988
MtrPosComputationDelay_Rad_M_f32[0]	
MtrPosComputationDelay_Rad_M_f32[1]	0.0089999961
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.6170001
MtrVoltQax_Volt_M_f32[0]	-29.3959999
MtrVoltQax_Volt_M_f32[1]	-1.9400006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.237000003
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.150000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.844699979
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.6602
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	64.5255966
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0997999981
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	29.8064003
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.925599992
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	1
k MtrCtrlFeedbackControlDisable Cnt lqc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.191
k_MtrCtrlVirualResQax_Ohm_f32	0.0529999994
k_MtrCurrQaxRefModifDsb_Cnt_lqc	1
~	
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	15.4042997
k_MtrVoltDaxIntegLoLim_Volt_f32	0
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	4.96659994
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003
k_deadtimeVScale_Uls_f32	0.996999979
t_CommOffsetTblX_Uls_u3p13[0]	4611
t_CommOffsetTblX_Uls_u3p13[1]	5579
t_CommOffsetTblY_Cnt_u16[0]	163
t_CommOffsetTblY_Cnt_u16[1]	1236
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	656
gv.iii_i out_iii out_oii_oii_oii_oii_pii	

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	656	656	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.6963588	-0.696358979 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	17.6341515	17.6341991 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	34365	34365 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84800005	-2.84800005 ± 7.81E-03	~

Test Step Call Trace	est Step Call Trace			✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.177 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.102
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.876999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.89999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.742004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-0.216499999
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001





Name	Input Value		
MtrCurrQaxCog_Amp_M_f32	48.4044991		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7269974		
MtrCurrQaxPrevIntg_Volt_M_f32	14.2393999		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979		
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001		
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005		
MtrPosComputationDelay_Rad_M_f32[1]	3.23000002		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.191		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.749800026		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.627799988		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	87.3075027		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.175400004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	57.8652992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.1426		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	947.890015		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.191		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	27.0930996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.0927		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4608	4608	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.96718693	2.96719003 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	30.4839344	30.4839001 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	60999	60999 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	~
	<u> </u>		



Test Step Call Trace	est Step Call Trace			V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
/trCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
/trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
	-205,089005
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0.079999982
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0130000003
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.119999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.226013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.128999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0979999974
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.237000003
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.277008
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6199999
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.191
/ltrCtrl_Vecu_Volt_M_f32[0]	18.9510002
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008
/trCurrDaxPrevIntg_Volt_M_f32	-9.05200005
/trCurrDaxRef_Amp_M_f32[0]	31.5869999
/trCurrDaxRef_Amp_M_f32[1]	-186.399994
/ltrCurrQaxCog_Amp_M_f32	53.7625008
/trCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
/trCurrQaxFinalRef_Amp_M_f32[1]	-2.84899998
/trCurrQaxPrevIntg_Volt_M_f32	11.8942003
/trCurrQaxRef_Amp_M_f32[0]	171.485992
/trCurrQaxRef_Amp_M_f32[1]	163.791
/trCurrQaxRpl_Amp_M_f32[0]	-213.026993
//trCurrQaxRpl_Amp_M_f32[1]	-66.7269974
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.0099999978
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax Volt M f32[1]	2.61800003

PICurrCntrl_Per1



Name	Input Value		
	Input Value		
MtrVoltQax_Volt_M_f32[0]	-27.0669994		
MtrVoltQax_Volt_M_f32[1]	28.1070004		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.465999991		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.498699993		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.616999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	97.3968964 0.763000011		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	44.6861992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.514999986		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4854.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.7719002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5634995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.1960001		
k_VoltSatQaxPolyCoeff_Uls_f32	20.9540005		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62849	62849 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.3498068	-10.3498001 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.6227875	-17.6228008 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38411	38411 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	21.7719002	21.7719002 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	•
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84899998	-2.84899998 ± 7.81E-03	•

Test Step Call Trace	Test Step Call Trace			V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•



Test Step 2.179 (Repeat Count = 1)	Innut Value	
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16 MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	0	
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(var)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_var	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.725998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0439999998	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0219999999	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994	
MtrCtrl_MtrDayProportionalCoin_Ohm_M_f32[1]	1.46300006 -948.984009	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.939026	
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0419999994	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.032999998	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.032999998	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.759000003	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.342987	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55599976	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3929996	
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002	
MtrCtrl_Vecu_Volt_M_f32[1]	21.3120003	
MtrCurrDaxPrevIntg_Volt_M_f32	-5.69950008	
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999	
MtrCurrDaxRef_Amp_M_f32[1]	-186.401001	
MtrCurrQaxCog_Amp_M_f32	62.6925011	
MtrCurrQaxFinalRef_Amp_M_f32[0] MtrCurrQaxFinalRef_Amp_M_f32[1]	37.4550018 -2.84599996	
MtrCurrQaxPrevIntg_Volt_M_f32	17.4958	
MtrCurrQaxRef Amp M f32[0]	171.485992	
MtrCurrQaxRef_Amp_M_f32[1]	163.792007	
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018	
MtrCurrQaxRpl Amp M f32[1]	-2.8499999	
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988	
MtrPosComputationDelay_Rad_M_f32[1]	0.0109999999	
MtrVoltDax_Volt_M_f32[0]	-7.66699982	
MtrVoltDax_Volt_M_f32[1]	2.61500001	
MtrVoltQax_Volt_M_f32[0]	-17.1070004	
MtrVoltQax_Volt_M_f32[1]	15.9399996	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.962000012	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.439666986	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.74940002	
PICurrCotrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.59899977	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	23.1455994	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl MtrVecuFilt M str.TermN Uls f32	954.236023 98.3255997	
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.532559991	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-424.399994	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	45.25	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18.3894005	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.623560011	
SlowDataAccessBufIndex_Cnt_M_u16	1	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7085.81982	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2937.1001	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1	
k_MtrCtrlVirualResDax_Ohm_f32	0.160332993	
k_MtrCtrlVirualResQax_Ohm_f32	0.154667005	
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0	





Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	0		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.4384995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.3633003		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	0.320666999		
k_VoltSatQaxPolyCoeff_Uls_f32	10.2349997		
k_deadtimeVScale_Uls_f32	0.968666971		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5587		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1237		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-125.123001		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1505		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.7156982		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1505	1505	-
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	111.643494	111.643997 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-30.021286	-30.0212994 ± 4.88E-04	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.666092694	0.666091979 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18624	18624 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	•
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	111.643494	111.643997 ± 7.81E-03	✓

Fest Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.180 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4459991	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10300004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.877998
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.114
	0.0850000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1] MtrCtrl_MtrQayIntegralGain_Ohm_M_f32[0]	0.114
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016 1.10000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029 454.742044
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.743011 25.2770000
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3889999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4220009
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48199987
MtrCurrDaxPrevIntg_Volt_M_f32	-5.39400005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.944
MtrCurrQaxCog_Amp_M_f32	70.7294998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7279968
MtrCurrQaxPrevIntg_Volt_M_f32	3.1875
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7030029
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.722
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005
MtrPosComputationDelay_Rad_M_f32[1]	3.24000001
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1919994
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1879997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0769999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.416166991
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.968200028
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.582799971
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	22.5478001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	256.125
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	45.6320992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.235679999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-1002.79999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	110.253998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	11.2356005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.51244998
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7438.18018
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2475.6001
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	1
k MtrCtrlFeedbackControlDisable Cnt lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.160332993
k_MtrCtrlVirualResQax_Ohm_f32	0.185167
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k MtrCurrQaxRefModifRplEn Cnt lgc	1
k MtrVoltDaxIntegHiLim Volt f32	31
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.6999981
k_MtrVoltQaxFiltFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	30.2145004
k MtrVoltQaxIntegLoLim Volt f32	-30.7682991
k_MtrVoltVecuFiltEnable_Cnt_lgc	-30.7062991
k_VoltSatDaxPolyCoeff_Uls_f32	-3.72033
k_VoltSatQaxPolyCoeff_Uls_f32	11.6547003
	0.968666971
k_deadtimeVScale_UIs_f32	
t_CommOffsetTbIX_UIs_u3p13[0]	5022
t_CommOffsetTbIX_UIs_u3p13[1]	7003
t_CommOffsetTblY_Cnt_u16[0]	365
t_CommOffsetTblY_Cnt_u16[1]	1531
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-129.938995
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	419





Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	129.727005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1531	1531	~
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63482	63482 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-62.5164948	-62.5164986 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.35798788	4.35798645	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-5.79095459	-5.79095316	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	59833	59833	~
MtrCurrDaxPrevIntg_Volt_M_f32	31	31 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-62.5164948	-62.5164986 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7279968	-66.7279968 ± 7.81E-03	~

Test Step Call Trace	est Step Call Trace			✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.181 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.089996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0140000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.171
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.22699
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.238000005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.278015
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6210003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1919994
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3120003
MtrCurrDaxPrevIntg_Volt_M_f32	-5.08850002
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.401001
MtrCurrQaxCog_Amp_M_f32	78.7665024

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value MtrCurrQaxFinalRef_Amp_M_f32[0] 37.4550018 MtrCurrQaxFinalRef_Amp_M_f32[1] -2.8499999 MtrCurrQaxPrevIntg_Volt_M_f32 12.9105997 MtrCurrQaxRef_Amp_M_f32[0] 171.485992 MtrCurrQaxRef_Amp_M_f32[1] 163.792007 MtrCurrQaxRpl_Amp_M_f32[0] -213.026993 MtrCurrQaxRpl_Amp_M_f32[1] -66.7279968 MtrPosComputationDelay_Rad_M_f32[0] -2.94899988 MtrPosComputationDelay_Rad_M_f32[1] 0.0109999999 MtrVoltDax_Volt_M_f32[0] -7 66699982 MtrVoltDax_Volt_M_f32[1] 2.61899996 MtrVoltQax_Volt_M_f32[0] -27 0669994 MtrVoltQax_Volt_M_f32[1] 28.1079998 PICurrCntrl CurrSensFailSclFac Uls M f32 0.0120000001 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.392666996 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.642499983 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.566600025 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 65.3214035 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ 144.326004 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 44.2155991 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.13256 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 1250.13 125.012001 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 10.2364998 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.142560005 SlowDataAccessBufIndex_Cnt_M_u16 k CLOAFdbackSignalSclFacSlew UlspS f32 7790.52979 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2014.09998 k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc 0 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ k MtrCtrlVirualResDax Ohm f32 0.160332993 k_MtrCtrlVirualResQax_Ohm_f32 0.156669945 k MtrCurrQaxRefModifDsb Cnt lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 11.7658005 k_MtrVoltDaxIntegLoLim_Volt_f32 -4.80000019 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 30.2355995 k_MtrVoltQaxIntegLoLim_Volt_f32 -20.5599995 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 -7.76133013 k VoltSatQaxPolyCoeff_Uls_f32 21.3213997 k_deadtimeVScale_Uls_f32 0.968666971 t_CommOffsetTblX_Uls_u3p13[0] 459 $t_CommOffsetTblX_Uls_u3p13[1]$ 5775 t_CommOffsetTblY_Cnt_u16[0] 771 t CommOffsetTblY_Cnt_u16[1] 1637 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target MtrCntrl Read_MtrCurrDax_Amp_f32_Val -134.755005 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 0 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 175.738007 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 0 0 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 159.447479 159.447006 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 29.9311619 29.9312 ± 4.88E-04 2.4175899 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 2.41802621 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 50320 50320 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 ± 0.0625 37.4550018 MtrCurrQaxFinalRef_Amp_M_f32[0] 37.4550018 ± 7.81E-03 MtrCurrQaxFinalRef_Amp_M_f32[1] 159.447479 159.447006 ± 7.81E-03

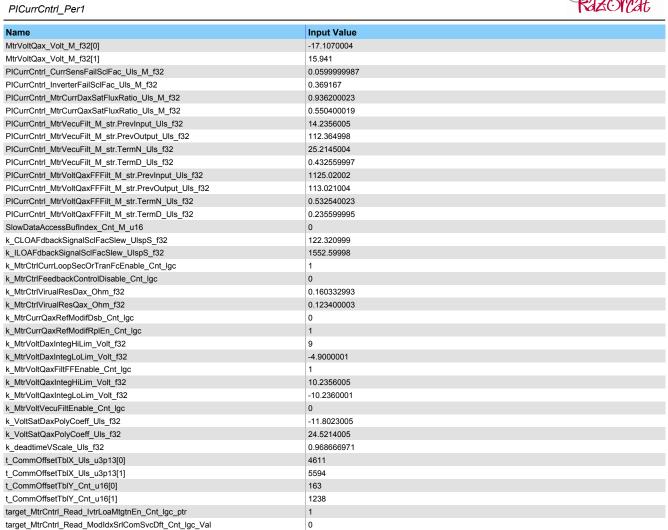


Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
astDataAccessBufIndex_Cnt_M_u16	1
ItrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
htrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc Val
htrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target_MtrCntrl Read_MotCurrLoaMtgtnEn Cnt lgc ptr
trCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
htrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl Read_MtrCurrOffComOffset Cnt u16 ptr
ItrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	
	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.726997
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0450000018
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.023
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46399999
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.940002
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0340000018
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0340000018
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.75999999
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.343994
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55700016
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3939991
trCtrl_Vecu_Volt_M_f32[0]	18.9510002
trCtrl_Vecu_Volt_M_f32[1]	21.3129997
trCurrDaxPrevIntg_Volt_M_f32	-4.78299999
trCurrDaxRef_Amp_M_f32[0]	31.5869999
trCurrDaxRef_Amp_M_f32[1]	-186.401993
trCurrQaxCog_Amp_M_f32	86.8034973
trCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
trCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988
ItrCurrQaxPrevIntg_Volt_M_f32	3.9934001
trCurrQaxRef Amp M f32[0]	171.485992
trCurrQaxRef_Amp_M_f32[1]	163.792999
trCurrQaxRpl Amp M f32[0]	37.4550018
trCurrQaxRpl Amp M f32[1]	-2.85100007
trPosComputationDelay_Rad_M_f32[0]	-2.94899988
ItrPosComputationDelay Rad M f32[1]	0.0120000001
htrVoltDax_Volt_M_f32[0]	-7.66699982
trVoltDax_Volt_M_i32[1]	2.61599994

2016-04-22, 16:28:30+0530





target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1238	1238	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	63482	63482 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	39.5345001	39.5345001 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.5138645	-17.5138569	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	10.9310913	10.9310942	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55097	55097	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	39.5345001	39.5345001 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988	-2.84699988 ± 7.81E-03	~

-139.570999

201.748993

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val

 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val



Test Step 2.183 (Repeat Count = 1)	v
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target _MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-110.000999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	100.021004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0225000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0425000004 0.0421000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0282000005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.214499995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.321449995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	102.325996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	102.214996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0320999995
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0313999988
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0124000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0214000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.321399987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.321399987
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	102.021004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	102.320999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-1.02139997 2.32139993
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.0214005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	11.0214005
MtrCtrl_Vecu_Volt_M_f32[0]	11.0214005
MtrCtrl_Vecu_Volt_M_f32[1]	20.1452007
MtrCurrDaxPrevIntg_Volt_M_f32	-3.21420002
MtrCurrDaxRef_Amp_M_f32[0]	10.2356005
MtrCurrDaxRef_Amp_M_f32[1]	25.3255997
MtrCurrQaxCog_Amp_M_f32	22.3255997
MtrCurrQaxFinalRef_Amp_M_f32[0]	33.2355995
MtrCurrQaxFinalRef_Amp_M_f32[1]	31.2145004
MtrCurrQaxPrevIntg_Volt_M_f32	2.36540008
MtrCurrQaxRef_Amp_M_f32[0]	12.3255997 25.2145004
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32[0]	35.0214005
MtrCurrQaxRpl Amp M f32[1]	110.021004
MtrPosComputationDelay_Rad_M_f32[0]	-2.0144999
MtrPosComputationDelay_Rad_M_f32[1]	5.02139997
MtrVoltDax_Volt_M_f32[0]	2.65440011
MtrVoltDax_Volt_M_f32[1]	3.21449995
MtrVoltQax_Volt_M_f32[0]	-10.3249998
MtrVoltQax_Volt_M_f32[1]	11.3255997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	9.9999975e-005
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0122999996
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.214499995
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.654100001
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	10.2356005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	110.236
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	20.2145004 0.214499995
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	110.325996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	112.320999
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	10.3213997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0122999996
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10.0214005
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	100.213997
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	
k_MtrCtrlVirualResDax_Ohm_f32	0.00120000006
·	

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

		•	
Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	10.2356005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.36540008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.3255997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.32559967		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.2356005		
k_VoltSatQaxPolyCoeff_Uls_f32	20.3213997		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	10		
t_CommOffsetTblX_Uls_u3p13[1]	1757		
t_CommOffsetTblY_Cnt_u16[0]	160		
t_CommOffsetTblY_Cnt_u16[1]	260		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	23.0214005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	10		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	100.214996		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	10	10	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-120.021004	-120.021004 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.10058939	-1.10058916	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-22.7908764	-22.7908802	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12259	12259 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-2.36540008	-2.36540008 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	33.2355995	33.2355995 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-120.021004	-120.021004 ± 7.81E-03	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.184 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	110.200996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	99.0123978	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120999999	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112300001	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0214000009	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0125000002	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.02139997	





Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23559999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	10.2356005 21.2145004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0214000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0122999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00124999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	9.9999975e-005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.214499995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.421499997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	90.2141037
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	254.320999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-2.02139997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.21449995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-10.0214005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	5.02139997
MtrCtrl_Vecu_Volt_M_f32[0]	6.32140017
MtrCtrl_Vecu_Volt_M_f32[1]	21.2014008
MtrCurrDaxPrevIntg_Volt_M_f32	-2.36540008
MtrCurrDaxRef_Amp_M_f32[0]	32.1245003
MtrCurrDaxRef_Amp_M_f32[1]	21.0214005
MtrCurrQaxCog_Amp_M_f32	11.2545996
MtrCurrQaxFinalRef_Amp_M_f32[0]	14.2563
MtrCurrQaxFinalRef_Amp_M_f32[1]	12.3654003
MtrCurrQaxPrevIntg_Volt_M_f32	10.3249998
MtrCurrQaxRef_Amp_M_f32[0]	10.2356005
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32[0]	32.2145004 22.0123997
MtrCurrQaxRpl_Amp_M_i32[1]	33.2014008
MtrPosComputationDelay_Rad_M_f32[0]	-1.0244
MtrPosComputationDelay_Rad_M_f32[1]	3.02139997
MtrVoltDax_Volt_M_f32[0]	1.25460005
MtrVoltDax_Volt_M_f32[1]	0.214499995
MtrVoltQax_Volt_M_f32[0]	23.3255997
MtrVoltQax_Volt_M_f32[1]	23.0125008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	9.9999975e-005
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.231399998
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.145300001
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.74119997
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	11.3255997
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	111.325996
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	11.3255997
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.145600006
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	145.320999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	123.250999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12.3255997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.123600014
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	122.320999
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	200.214005
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32	0 0.123599999
k_MtrCtrlVirualResQax_Ohm_f32	0.12309999
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0.112300001
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	11.3255997
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.02559996
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	12.3255997
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.25689983
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-5.02139997
k_VoltSatQaxPolyCoeff_Uls_f32	10.2356005
k_deadtimeVScale_Uls_f32	0.971000016
t_CommOffsetTblX_Uls_u3p13[0]	8192
t_CommOffsetTblX_Uls_u3p13[1]	2633
t_CommOffsetTblY_Cnt_u16[0]	110
t_CommOffsetTblY_Cnt_u16[1]	365
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	22.3213997
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	12



PICurrCntrl_Per1

Name	Input Value		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	90.2145004		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	110	110	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	18429	18429	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-1.05249786	-1.05250001 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	3.21449995	3.21449995	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	5.02139997	5.02139997	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37454	37454	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-1.05249786	-1.05250001 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	12.3654003	12.3654003 ± 7.81E-03	✓

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Case 3: Path Test

Description

Vector Description:

TS 3.1(k_MtrCurrQaxRefModifRplEn_Cnt_lgc ==
TRUE)=False&&(MtrCurrQaxRefModif_Amp_T_f32>=220)=True&&(MtrCurrQaxFinalRef_Amp_T_f32>=220)=True&&(MtrCurrDaxRefModif_Amp_k_MtrVoltQaxIntegLoLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegHiLim_Volt_f32)=True&&(MtrCurrQaxIntg_Volt_T_f32>=k_MtrVoltQaxIntegHiLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32>=k_MtrVoltQaxIntegHiLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32)=K_MtrVoltQaxIntegHiLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32)=K_MtrVoltQaxIntegLoLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32)=True&&(MtrCurrLoopSecOrTranFcEnable_Cnt_lgc ==
TRUE)=False&&(k_MtrCurrQaxFiltFFEnable_Cnt_lgc==TRUE)=False&&(VoltSatnRatio_Uls_T_f32>
D_ONE_ULS_f32)=True&&(ModidxSrlComSvcDft_Cnt_T_lgc==TRUE)=False&&(k_MtrCurrQaxRefModifDsb_Cnt_lgc == FALSE)=True
TS 3.2(k_MtrCurrQaxRefModifRplEn_Cnt_lgc ==
TRUE)=True&&(MtrCurrQaxRefModif_Amp_T_f32>=220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<=-220)=True&&(MtrCurrQaxFinalRef_Amp_==TRUE)=True&&(k_MtrCurrQaxRefModifDsb_Cnt_lgc == FALSE)=False
FALSE)=False
FALSE)=False

TS 3.3&&(MtrCurrQaxRefModif_Amp_T_f32<=-220)=False&&(MtrCurrQaxFinalRef_Amp_T_f32<=-220)=False&&(MtrCurrQaxIntg_Volt_T_f32>= k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&&&(MtrCurrQaxIntg_Volt_T_f32>= k_MtrVoltQaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T _ 132>= k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&(MtrCurrQaxIntg_Volt_T _ 132>= k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&(k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc == FALSE)=True&&(VoltSatnRatio_Uls_T_f32>= k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&(VoltSatnRatio_Uls_T_f32>= k_MtrVoltQaxIntegLoLim_Volt_f32= k D_ONE_ULS_F32)=False

TS 3.4

TS 3.4

(MtrCurrDaxRefModif_Amp_T_f32<=0)=False&&&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_lgc==FALSE)=True&&(MtrVoltQaxFiltFFEnable_Cnt_lgc==TRUE)=True&&(IvtrLoaMtgtnEn_Cnt_T_lgc==FALSE)=True&&(MtrCurrDaxIntghen_Cnt_T_lgc==FALSE)=True&&(MtrCurrDaxIntghen_Cnt_T_lgc==FALSE)=True&&(IvtrLoaMtgtnEn_Cnt_T_lgc==FALSE)=True&&(MtrVoltQaxIntghen_Cnt_T_lgc==FALSE)=False
TS 3.7(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegHiLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegLoLim_Volt_f32)=False
TS 3.8(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegHiLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegLoLim_Volt_f32)=False

k_MtrVoltQaxIntegLoLim_Volt_f32)=False TS 3.9

(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&(MtrVolt_DaxIntegLoLim_Volt_f32)=False&(MtrVolt_DaxIntegLoLim_Volt_f32)=False&(MtrVolt_f32) k_MtrVoltDaxIntegLoLim_Volt_f32)=False

Test Step 3.1 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	





Name	Input Value
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0049999989
	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5
MtrCtrl_Vecu_Volt_M_f32[1]	5
MtrCurrDaxPrevIntg_Volt_M_f32	-31
MtrCurrDaxRef_Amp_M_f32[0]	-220
MtrCurrDaxRef_Amp_M_f32[1]	-220
MtrCurrQaxCog_Amp_M_f32	-220
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220
MtrCurrQaxPrevIntg_Volt_M_f32	-31
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220
MtrCurrQaxRpl_Amp_M_f32[0]	-220
MtrCurrQaxRpl_Amp_M_f32[1]	-220
MtrPosComputationDelay Rad M f32[0]	-3.89064002
MtrPosComputationDelay_Rad_M_f32[1]	-6.28318977
MtrVoltDax_Volt_M_f32[0]	-31
MtrVoltDax_Volt_M_f32[1]	-31
MtrVoltQax Volt M f32[0]	-31
MtrVoltQax Volt M f32[1]	-31
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	
	0
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	-0.996816993
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	1.96346009e-005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	-0.996816993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.96346009e-005
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0
k MtrCtrlVirualResQax Ohm f32	0
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	0
k_MtrVoltDaxIntegLoLim_Volt_f32	-31
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	0
k_MtrVoltQaxIntegLoLim_Volt_f32	-31
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-25
k_VoltSatQaxPolyCoeff_Uls_f32	-25

PICurrCntrl_Per1



Name	Input Value		
k_deadtimeVScale_Uls_f32	0.94999988		
t_CommOffsetTblX_Uls_u3p13[0]	0		
t_CommOffsetTblX_Uls_u3p13[1]	0		
t_CommOffsetTblY_Cnt_u16[0]	0		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62259	62259 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.33995581	-3.3399601 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.37745404	-3.37744999 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	321	321 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	~
MtrCurrQaxFinalRef Amp M f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 3.2 (Repeat Count = 1)	🗸
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024

2016-04-22, 16:28:30+0530

PICurrCntrl_Per1



		•	
Name	Input Value		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	31		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	31		
MtrCtrl_Vecu_Volt_M_f32[0]	31		
MtrCtrl_Vecu_Volt_M_f32[1]	31		
MtrCurrDaxPrevIntg_Volt_M_f32	31		
MtrCurrDaxRef_Amp_M_f32[0]	220		
MtrCurrDaxRef_Amp_M_f32[1]	220		
MtrCurrQaxCog_Amp_M_f32	220		
MtrCurrQaxFinalRef_Amp_M_f32[0]	220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	220 31		
MtrCurrQaxPrevIntg_Volt_M_f32	220		
MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1]	220		
MtrCurrQaxRpl_Amp_M_f32[0]	220		
MtrCurrQaxRpl_Amp_M_f32[1]	220		
MtrPosComputationDelay_Rad_M_f32[0]	6.28318977		
MtrPosComputationDelay_Rad_M_f32[1]	6.28318977		
MtrVoltDax_Volt_M_f32[0]	31		
MtrVoltDax Volt M f32[1]	31		
MtrVoltQax_Volt_M_f32[0]	31		
MtrVoltQax_Volt_M_f32[1]	31		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl InverterFailSclFac Uls M f32	1		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1350		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1350		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50928.6016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996827006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1350		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1350		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50928.6016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996827006		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.20000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.200000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	31		
k_MtrVoltDaxIntegLoLim_Volt_f32	0		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	31		
k_MtrVoltQaxIntegLoLim_Volt_f32	0		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1 25		
k_VoltSatDaxPolyCoeff_UIs_f32	25 25		
k_VoltSatQaxPolyCoeff_Uls_f32	1		
k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0]	8192		
t_CommOffsetTblX_Uis_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	2000		
t_CommOffsetTblY_Cnt_u16[1]	2000		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	5000		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	5000	5000	Nesu
ma ona_vviite_oominonset_ont_u io(vai)	0	0 ± 1	
MtrCntrl Write ModIdy I IIs u16n16(val)		-220 ± 7.81E-03	
MtrCntrl_Write_ModIdx_UIs_u16p16(val) MtrCntrl_Write_MtrCurrQayFinalRef_Amp_f32(val)			
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-220 21.9203072	21.9202995 ± 4.88E-04	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-220 21.9203072 21.9203072	21.9202995 ± 4.88E-04 21.9202995 ± 4.88E-04	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-220 21.9203072	21.9202995 ± 4.88E-04	





Name	Actual Value	Expected Value	Result
MtrCurrQaxFinalRef Amp M f32[1]	-220	-220 ± 7.81E-03	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
	·
FastDataAccessBufIndex_Cnt_M_u16	1
trCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.115000002
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0579999983
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0320000015
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0869999975
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0649999976
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.227
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	537.232971
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	67.9840012
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.109999999
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0270000007
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0920000002
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.703000009
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.75199997
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	462.437012
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-685.195984
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.6930008
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.219999999
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.45499992
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.1830006
trCtrl_Vecu_Volt_M_f32[0]	22.3540001
trCtrl_Vecu_Volt_M_f32[1]	24.7140007
trCurrDaxPrevIntg_Volt_M_f32	-23.0620003
trCurrDaxRef_Amp_M_f32[0]	37.4550018
trCurrDaxRef_Amp_M_f32[1]	-2.84500003
trCurrQaxCog_Amp_M_f32	-55.5390015
trCurrQaxFinalRef_Amp_M_f32[0]	160.044006
trCurrQaxFinalRef_Amp_M_f32[1]	165.242004
trCurrQaxPrevIntg_Volt_M_f32	8.08899975
trCurrQaxRef_Amp_M_f32[0]	220
trCurrQaxRef_Amp_M_f32[1]	220
trCurrQaxRpl_Amp_M_f32[0]	160.044006
ltrCurrQaxRpl_Amp_M_f32[1]	165.242004
htrPosComputationDelay_Rad_M_f32[0]	-5.08799982
ItrPosComputationDelay_Rad_M_f32[1]	-4.26300001

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value	
MtrVoltDax_Volt_M_f32[0]	3.45499992	
MtrVoltDax_Volt_M_f32[1]	-19.1830006	
MtrVoltQax_Volt_M_f32[0]	3.45499992	
MtrVoltQax_Volt_M_f32[1]	-19.1830006	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.638000011	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.880900025	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.978999972	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-657.099976	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47050.1992	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0229000002	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-657.099976	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47050.1992	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0229000002	
SlowDataAccessBufIndex_Cnt_M_u16	1	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7088.3501	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0	
k_MtrCtrlVirualResDax_Ohm_f32	0.194999993	
k_MtrCtrlVirualResQax_Ohm_f32	0.142000005	
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0	
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0	
k_MtrVoltDaxIntegHiLim_Volt_f32	17.9123993	
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.69999988	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0	
k_MtrVoltQaxIntegHiLim_Volt_f32	19.4449997	
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.69999988	
k_MtrVoltVecuFiltEnable_Cnt_lgc	0	
k_VoltSatDaxPolyCoeff_Uls_f32	-19.4559994	
k_VoltSatQaxPolyCoeff_Uls_f32	-18.6200008	
k_deadtimeVScale_Uls_f32	0.95599997	
t_CommOffsetTblX_Uls_u3p13[0]	4170	
t_CommOffsetTblX_Uls_u3p13[1]	6749	
t_CommOffsetTblY_Cnt_u16[0]	177	
t_CommOffsetTblY_Cnt_u16[1]	340	
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1	
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0	
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975	
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	335	
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-145.169006	
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2	
Name	Actual Value Expected Value	Resul
MtrCntrl Write CommOffset Cnt u16(val)	315 315	
MtrCntrl Write ModIdx Uls u16p16(val)	50872 50872 ± 1	
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	110.296997 110.296997 ± 7.81E-	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.219999999	
MtrCntrl Write MtrQaxVoltage Volt f32(val)	-19.1830006 -19.1830006 ± 4.88E	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	53720 53720±1.52588E-05	
MtrCurrDaxPrevIntg Volt M f32	-0.699999988 -0.699999988 ± 0.06	
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006 160.044006 ± 7.81E-	-
MtrCurrQaxFinalRef_Amp_M_f32[1]	110.296997 110.296997 ± 7.81E-	

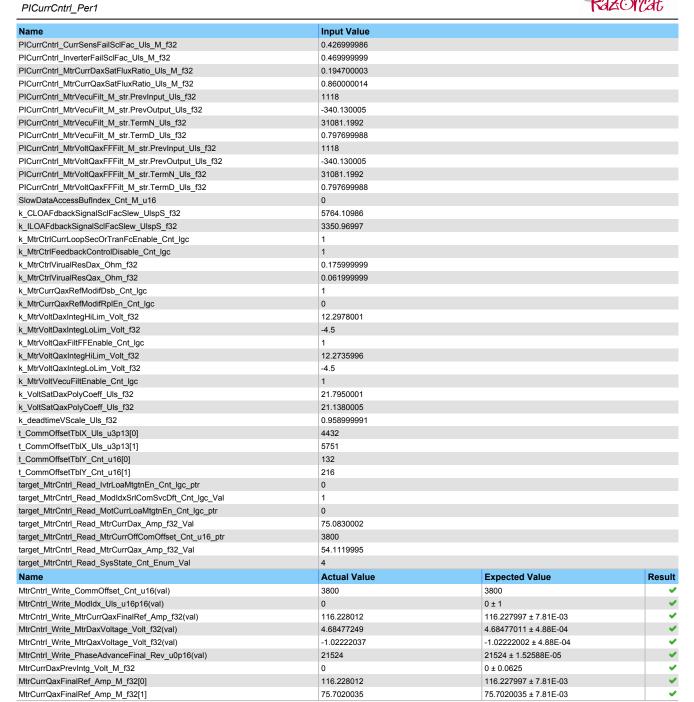


Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
/dtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_NodIdxSrlComSvcDft_Cnt_lgc_Val	
	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)		
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0179999992	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0460000001	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.167999998	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62100005	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	720.525024	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-487.845001	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0960000008	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.103	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.075000003	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0649999976	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.60500002	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.33500004	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-418.748993	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	590.754028	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.3319998	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.40700006	
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-22.3460007	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994	
/trCtrl Vecu Volt M f32[0]	16.8080006	
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993	
MtrCurrDaxPrevIntg Volt M f32	14.7060003	
MtrCurrDaxRef_Amp_M_f32[0]	220	
MtrCurrDaxRef_Amp_M_f32[1]	220	
MtrCurrQaxCog_Amp_M_f32	177.763	
MtrCurrQaxFinalRef Amp M f32[0]	-133.947006	
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035	
MtrCurrQaxPrevIntg Volt M f32	12.4979	
MtrCurrQaxRef_Amp_M_f32[0]	160.044006	
MtrCurrQaxRef Amp M f32[1]	165.242004	
//trCurrQaxRpl_Amp_M_f32[0]	-133,947006	
//trCurrQaxRpl Amp M f32[1]	75.7020035	
лисипдахкрг_Апр_м_isz[i] //trPosComputationDelay_Rad_M_f32[0]	0.277999997	
	-5.2579999	
MtrPosComputationDelay_Rad_M_f32[1]		
MtrVoltDax_Volt_M_f32[0]	-22.3460007	
VtrVoltDax_Volt_M_f32[1]	-17.8169994	
MtrVoltQax_Volt_M_f32[0]	-22.3460007	
MtrVoltQax_Volt_M_f32[1]	-17.8169994	

2016-04-22, 16:28:30+0530





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓



Test Step 3.5 (Repeat Count = 1) Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0370000005
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.824000001
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
/ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0700000003
/ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003
/ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0160000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.286000013
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.41499996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-730.362
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-412.898987
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	14.4589996
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.13000011
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	22.5750008
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	22.8969994
MtrCtrl_Vecu_Volt_M_f32[0]	18.7189999
MtrCtrl_Vecu_Volt_M_f32[1]	21.0790005
MtrCurrDaxPrevIntg_Volt_M_f32	15.9169998
MtrCurrDaxRef_Amp_M_f32[0]	-69.0940018
MtrCurrDaxRef_Amp_M_f32[1]	161.973007
MtrCurrQaxCog_Amp_M_f32	-152.050995
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxPrevIntg_Volt_M_f32	20.0867996
MtrCurrQaxRef_Amp_M_f32[0]	-200.556
MtrCurrQaxRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxRpl_Amp_M_f32[0]	106.072998
/trCurrQaxRpl_Amp_M_f32[1]	-112.455002
MtrPosComputationDelay_Rad_M_f32[0]	-0.430000007
MtrPosComputationDelay_Rad_M_f32[1]	-5.92700005
/trVoltDax_Volt_M_f32[0]	22.5750008
//dtrVoltDax_Volt_M_f32[1]	22.8969994
MtrVoltQax_Volt_M_f32[0]	22.5750008
MtrVoltQax_Volt_M_f32[1]	22.8969994
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.418000013
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.300000012
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.902100027
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.675000012
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8419.69043
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.634800017
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	8419.69043
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.634800017
SlowDataAccessBufIndex_Cnt_M_u16	0
CLOAFdbackSignalSclFacSlew_UlspS_f32	6857.12012
<pre>c_ILOAFdbackSignalSclFacSlew_UlspS_f32</pre>	2799.87988
<_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
<_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0289999992
<_MtrCtrlVirualResQax_Ohm_f32	0.188999996
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0

2016-04-22, 16:28:30+0530





Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.2152004		
k_MtrVoltDaxIntegLoLim_Volt_f32	3.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.2434006		
k_MtrVoltQaxIntegLoLim_Volt_f32	3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.026		
k_VoltSatQaxPolyCoeff_Uls_f32	-23.2660007		
k_deadtimeVScale_Uls_f32	0.999000013		
t_CommOffsetTblX_Uls_u3p13[0]	4342		
t_CommOffsetTblX_Uls_u3p13[1]	7724		
t_CommOffsetTblY_Cnt_u16[0]	1124		
t_CommOffsetTblY_Cnt_u16[1]	1178		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3317		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	3.89299989		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3317	3317	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-154.578003	-154.578003 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.454691678	0.454690993 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.97426176	-4.97425985 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27332	27332 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	~
MtrCurrQaxFinalRef_Amp_M_f32[0]	-154.578003	-154.578003 ± 7.81E-03	~
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002	-112.455002 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 3.6 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.057

2016-04-22, 16:28:30+0530

PICurrCntrl_Per1



Montantingstrate		
MICH. J. Michael Ambate (Proc. 1928) 192000004 1024	Name	Input Value
Mindle M		•
MOCH MICH Michael Programmation Come M. 1921 1924 1924 1924 1924 1924 1924 1924 1924 1924 1924 1924 1925		
Mortor Minispector (DM, M, M20)		
MoCH Johnsendox Ohm M (20)		
MoCH_MimpedDat_OnlM_2020		
MoCH, MittyperStork, Chm, M, E2019 MOCH MittyperStork, M, E2019 MOCH MIT		
Mocifie Manimediase, Chin M (2011) Mocifie Manimediase, Chin M (2011) Mocifie Manimediase, Chin M (2011) Mocifie Manimediase (Chin M (2011) Mocifie Manimedia (Chin M (2011) Mocifie Mani	MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0270000007
Microf January (1997)	MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.120999999
Michael Mandaminger Sear, Dem M. 1950	MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0909999982
Michical Machine (Michical Machine) (Michine)	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87699997
MIRCEL MANIBASE YOR, M. 2020 7.310002 MIRCEL MANIBASE YOR, M. 2020 7.310002 MIRCEL MANIBASE YOR, M. 2020 1.1319999 MIRCEL MANIBASE YOR, M. 2020 1.1319999 MIRCEL MANIBASE YOR, M. 2020 1.14139999 MIRCEL MANIBASE YOR, M. 2020 1.14139999 MIRCEL MANIBASE YOR, M. 2020 1.14139999 MIRCEL MANIBASE YOR, M. 2020 1.14149999 MIRCEL MANIBASE ARE, M. 2020 1.1414999 MIR	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.648999989
MIRCEL MANIBASE YOR, M. 2020 7.310002 MIRCEL MANIBASE YOR, M. 2020 7.310002 MIRCEL MANIBASE YOR, M. 2020 1.1319999 MIRCEL MANIBASE YOR, M. 2020 1.1319999 MIRCEL MANIBASE YOR, M. 2020 1.14139999 MIRCEL MANIBASE YOR, M. 2020 1.14139999 MIRCEL MANIBASE YOR, M. 2020 1.14139999 MIRCEL MANIBASE YOR, M. 2020 1.14149999 MIRCEL MANIBASE ARE, M. 2020 1.1414999 MIR	MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-603.161987
MICH. MIVOLDARY VAIL, M. (2019) 3.0529997 MICH. MIVOLDARY VAIL, M. (2019) 1.13.31998 MICH. MIVOLDARY VAIL, M. (2019) 1.13.31998 MICH. MIVOLDARY VAIL, M. (2019) 1.10.31998 MICH. VAIL VAIL VAIL VAIL VAIL VAIL VAIL VAIL		-712.994019
Michic Mevillanser, Vol. 19, 20(1) 3.0525997		
MacCH_MAVACLASEF_Vol_M_12(9) MaCCH_Volat_Vol_M_12(9) MaCCH_Volat_Vol_		
MicCit Vest Vest M. 1920 14.099988 MicCit Vest Vest M. 1920 14.099988 MicCit Vest Vest M. 1920 14.099988 MicCit Vest		
McCircl Vest, Vol. M. (52(1) 16,700005		
Michin Vesu, Vot. M. 52(1) 15,7700005 12,2810001 12,2813004 12,2813004 12,2813004 12,2813004 12,2813004 12,2813004 12,2813004 12,2813004 12,2813004 12,2813004 12,2813004 12,2813004 12,2813006		
Michaelweit Michae		
MCOURDARED_MIN_MISCIT 132.813004 MCOURDARED_MIN_MISCIT 3.1439965 MCOURDARED_MIN_MISCIT 3.1439965 MCOURDARED_MIN_MISCIT 3.06.73007 MCOURDARED_MIN_MISCIT 3.06.73007 MCOURDARED_MIN_MISCIT 3.0777 MCOURDARED_MIN_MISCIT 19.7207 MCOURDARED_MIN_MISCIT 19.72101 MCOURDARED_MIN_MISCIT 19.72101 MCOURDARED_MIN_MISCIT 19.72101 MCOURDARED_MIN_MISCIT 26.73007 MCOURDARED_MIN_MISCIT 26.730006 MCOURDARED_MIN_MISCIT 26.730006 MCOURDARED_MIN_MISCIT 26.730006 MCOURDARED_MIN_MISCIT 3.8270006 MCOURDARED_MIN_MISCIT 3.827006 MCOURDARE		
McCurrakerd, Amy, M. 1921 McCurrakerd, Amy, M. 1920 McCurrakernalerd, Amy, M. 1920 McCurrakernalerd, Amy, M. 1920) McCurrakerd, Amy, M. 1920) McCurrakerd, Amy, M. 1920) McCurrakerd, Amy, M. 1920 McCurrakerd, Amy, M. 1920) McCurrakerd, Amy, M. 1920 McCurrakerd, McC		
Michael Ace, Amp. M. 120	MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
Michaelinaelinelinelinelinelinelinelinelinelinelin	MtrCurrDaxRef_Amp_M_f32[1]	-9.14299965
MACURCASFINATE, Amp. M. 1921 201 792007 MACURCASFINATE, Amp. M. 1921 19 74101 MACURCASFIL, Amp. M. 1921 19 74201 MACURCASFIL, Amp. M. 1921 208 82099 MACURCASFIL, Amp. M. 1921 208 82099 MACURCASFIL, Amp. M. 1921 208 72007 MACURCASFIL, Amp. M. 1921 208 72007 MACURCASFIL, Amp. M. 1921 208 72007 MAPPOLOCOMULATION BUY, M. 1921 5 7200013 MAPPOLOCOMULATION BUY, M. 1921 5 7200013 MAYORIDA, Vol. M. 1921 5 74000013 MAYORIDA, Vol. M. 1921 5 74000014 MAYORIDA, Vol. M. 1921 5 7400006 PLOURCH, M. MORIDASSIFILATION, UN. M. 1921 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	MtrCurrQaxCog_Amp_M_f32	-51.1100006
McCurdasPreving, Vol. M. 192 13.3757 McCurdasPret, Amp, M. 2920 57.4899879 McCurdasPret, Amp, M. 1921 119.71001 119.7210 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.7210 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.7210 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.7210 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.7210 119.72101 119.7	MtrCurrQaxFinalRef_Amp_M_f32[0]	205.820999
McCurdasPreving, Vol. M. 192 13.3757 McCurdasPret, Amp, M. 2920 57.4899879 McCurdasPret, Amp, M. 1921 119.71001 119.7210 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.7210 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.7210 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.7210 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.72101 119.7210 119.72101 119.7	MtrCurrQaxFinalRef_Amp_M_f32[1]	-206.792007
Michardskef, Amp. M. 52(7) Michardskef, Amp. M.		
Michard Safe J. Amp. M. 2471 119 7 1001		
MichardsRej Amp M. 182(0) 208.82099		
MCDurDackPul Amp M, 12(11) 206.792007 MRPPosComputationDelay_Rad M, 12(11) -5.73500013 MIVPosComputationDelay_Rad M, 12(11) -5.73500013 MIVPollax_Volf_M, 12(21) -1.339998 MIVVOIDIAX_Volf_M, 12(21) -5.40700006 MIVVOIDIAX_Volf_M, 12(21) -5.40700006 MIVVOIDIAX_Volf_M, 12(21) -5.40700006 PICUT-CINT_CURS-ensiralSeTac_Us_M, 13(2) 0.42399995 PICUT-CINT_MIVED-CORRESTED_LINE_13(2) 0.399000011 PICUT-CINT_MIVED-CURS-assET-Line_Ratio_Us_M, 15(2) 0.712199986 PICUT-CINT_MIVED-LINE_13(2) 0.712199986 PICUT-CINT_MIVED-LINE_13(2) 1.914190002 PICUT-CINT_MIVED-LINE_13(1) 12(2) PICUT-CINT_MIVED-LINE_13(1) 12(2) PICUT-CINT_MIVED-LINE_13(1) 12(2) PICUT-CINT_MIVED-LINE_14(1) 12(2) PICUT-CINT_MIVED-LINE_14(1) 12(2) PICUT-CINT_MIVED-LINE_14(1) 12(2) PICUT-CINT_MIVED-LINE_15(1) 12(2) PICUT-CINT_MIVED-LINE_15(1) 12(2) PICUT-CINT_MIVED-LINE_15(1) 12(2) PICUT-CINT_MIVED-LINE_15(1) 12(2)		
MPPsGComputationDelay_Rad_M_S2[1] 3-287000013 MirVroilDax_Volt_M_S2[0] 1-13319998 MirVroilDax_Volt_M_S2[1] 5-73800013 MirVroilDax_Volt_M_S2[1] 5-73800013 MirVroilDax_Volt_M_S2[1] 5-73800013 MirVroilDax_Volt_M_S2[1] 5-73800013 MirVroilDax_Volt_M_S2[1] 5-840700008 MirVroilCax_Volt_M_S2[1] 5-840700008 5-840700008 MirVroilCax_Volt_M_S2[1] 5-840700008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-84070008 5-8407008 5-		
MPPosComputationCleay_Rad_M_G2[1]		
MINVOIDEX_VoIL_M_G2[1]		
Mirvolitax_Volt_M_52[1] 5-4070006 Mirvolitax_Volt_M_52[1] 5-4070006 PiGurichti, CurismeFailsGiFac_Uis_M_52 0.42399995 PiGurichti, CurismeFailsGiFac_Uis_M_52 0.42399995 PiGurichti, MircurbaxSafFiuRatio_Uis_M_52 0.71219996 PiGurichti, MircurbaxSafFiuRatio_Uis_M_52 0.85100023 PiGurichti, MircurbaxSafFiuRatio_Uis_M_52 0.85100023 PiGurichti, MircurbaxSafFiuRatio_Uis_M_52 0.85100023 PiGurichti, MircurbaxSafFiuRatio_Uis_M_52 0.85100023 PiGurichti, MircurbaxSafFiuRatio_Uis_M_52 184.19002 PiGurichti, MirveuFift_M_st. Prevolupt_Uis_52 184.19002 PiGurichti, MirveuFift_M_st. Prevolupt_Uis_52 189.19004 PiGurichti, MirveuFift_M_st. Prevolupt_Uis_52 199.19004 PiGurichti, MirveuFift_M_st. Prevolupt_Uis_52 199.19004 PiGurichti, MirveuFift_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFFFIft_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFFFIft_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFFFIft_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFFFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFFFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFFFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFFFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, MirvelGaxFIT_M_st. Prevolupt_Uis_52 194.19002 PiGurichti, M		
MrVolicax_Volt_M_52(1)		
MEVOLICAL Vol. M. 132[1]	MtrVoltDax_Volt_M_f32[1]	-5.40700006
PICUTCONT Inverter Fail SciFac Uls M, 132 0.42399995 PICUTCONT Inverter Fail SciFac Uls M, 132 0.71219986 PICUTCONT Inverter Fail SciFac Uls M, 132 0.65100023 PICUTCONT MICTUTCASSAFI LURARIO Uls M, 132 0.65100023 PICUTCONT MICTUTCASSAFI LURARIO Uls M, 132 0.65100023 PICUTCONT MICTUTCASSAFI LURARIO Uls M, 132 194,190002 PICUTCONT MICTUCAL M, 151,722 194,190002 PICUTCONT MICTUCAL M, 151,722 12079,9004 PICUTCONT M, 151,724	MtrVoltQax_Volt_M_f32[0]	-11.3319998
PlCurrCntrl_InverterFallsEfac_Uis_M_132 0.39500011 PlCurrCntrl_MtrCurrDaxSaffuxRatio_Uis_M_132 0.65100023 PlCurrCntrl_MtrVecuFill_M_5tPrevippu_Uis_132 3.8799992 PlCurrCntrl_MtrVecuFill_M_5tPrevippu_Uis_132 12079.9004 PlCurrCntrl_MtrVecuFill_M_5tPrevippu_Uis_132 12079.9004 PlCurrCntrl_MtrVecuFill_M_5trTermD_Uis_132 12079.9004 PlCurrCntrl_MtrVecuFill_M_5trTermD_Uis_132 12079.9004 PlCurrCntrl_MtrVecuFill_M_5trTermD_Uis_132 3.8799992 PlCurrCntrl_MtrVetuFill_M_5trTermD_Uis_132 3.8799992 PlCurrCntrl_MtrVoltDaxFFFIll_M_5trTermD_Uis_132 3.8799992 PlCurrCntrl_MtrVoltDaxFFFIll_M_5trTermD_Uis_132 194.190002 PlCurrCntrl_MtrVoltDaxFFFIll_M_5trTermD_Uis_132 194.190002 PlCurrCntrl_MtrVoltDaxFFFIll_M_5trTermD_Uis_132 12079.9004 PlCurrCntrl_MtrVoltDaxFFFIll_M_5trTermD_Uis_132 12079.9004 PlCurrCntrl_MtrVoltDaxFFFIll_M_5trTermD_Uis_132 12079.9004 PlCurrCntrl_MtrVoltDaxFFFIll_M_5trTermD_Uis_132 0.29820011 SlowDataAccessBufndex_Cnt_M_Ui5 0	MtrVoltQax_Volt_M_f32[1]	-5.40700006
PICUrrCnit MirCurrDaxSalFluxRatio, Uls M, 132 0.851000023 PICUrrCnit MirCurrDaxSalFluxRatio, Uls M, 132 0.851000023 PICUrrCnit MirVecuFill, M, str PrevOutput, Uls, 132 194, 190002 PICUrrCnit MirVecuFill, M, str PrevOutput, Uls, 132 12079 9004 PICUrrCnit MirVecuFill, M, str TermD, Uls, 132 12079 9004 PICUrrCnit MirVecuFill, M, str TermD, Uls, 132 0.29820011 PICUrrCnit MirVecuFill, M, str PrevOutput, Uls, 132 0.29820011 PICUrrCnit MirVelOSAFFFII, M, str PrevOutput, Uls, 132 12079 9004 PICUrrCnit MirVolOSAFFFII, M, str PrevOutput, Uls, 132 12079 9004 PICUrrCnit MirVolOSAFFFII, M, str PrevOutput, Uls, 132 12079 9004 PICUrrCnit MirVolOSAFFFII, M, str TermD, Uls, 132 12079 9004 PICURCNIT MirVolOSAFFFII, M, str TermD, Uls, 132 12079 9004 PICURCNIT MirVolOSAFFFII, M, str TermD, Uls, 132 12079 9004 PICURCNIT MirVolOSAFFFII, M, str TermD, Uls, 132 12079 9004 PICURCNIT MIRVOLOSAFFFII, M, str TermD, Uls, 132 12079 9004 PICURCNIT MIRVOLOSAFFFII, M, str TermD, Uls, 132 12079 9004 R, LI, OAFbackSignalSciFacSlew, UlspS, 132 7603,8001 R, LI, OAFbackSignalSciFacSlew, UlspS, 132 7603,8001 R, LI, OAFbackSignalSciFacSlew, UlspS, 132 7603,8001 R, MirVolTCNTLOSSEOOTTanFCEnable, Crt. Igo 0 0 R, MirVolTCNTLOSSEOOTTanFCEnable, Crt. Igo 0 0 0 0 0 R, MirVolTCNARREMOdifDsb, Crt. Igo 0 0 0 0 0 0 R, MirVolTCNARREMOdifDsb, Crt. Igo 0 0 0 0 0 0 R, MirVolTCNARREMOdifDsb, Crt. Igo 0 0 0 0 0 0 0 0 0	PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.423999995
PICUrrCnit MirCurrQaxSalFluxRatio_Uis_M_32 0.85100023 PICUrrCnit MirVecuFit M. str Previoptu Uis_f32 38,7999992 PICUrrCnit MirVecuFit M. str Previoptu Uis_f32 12079.9004 PICUrrCnit MirVecuFit M. str Term Uis_f32 0.298200011 PICUrrCnit MirVecuFit M. str Term Uis_f32 0.298200011 PICUrrCnit MirVolQaxFFFit M. str Previoptu Uis_f32 194.190002 PICUrrCnit MirVolQaxFFFit M. str Previoptu Uis_f32 194.190002 PICUrrCnit MirVolQaxFFFit M. str Previoptu Uis_f32 194.190002 PICUrrCnit MirVolQaxFFFit M. str Previoptu Uis_f32 12079.9004 PICUrrCnit MirVolQaxFFFit M. str Term Uis_f32 12079.9004 PICUrrCnit MirVolQaxFFIt M. str Term Uis_f32 12079.9004 PICURCNIT MirVolQaxFFIT M. str Term Uis_f32 12079.9004 PICURCNIT MirVolQaxFIT M. str Term Uis_f32 1209820011 SiwDataAccessBufindex, Cnt M. uis_f6 0.98820011 SiwDataAccessBufindex, Cnt M. uis_f6 0.9882001 SiwDataAccessBufindex, Cnt M. uis_f6 0.98	PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.395000011
PICUrrChif_Mir/ecuFit_M_str_Pervinput_Uis_f32	PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.712199986
PICUrrChif_Mir/ecuFit_M_str_Pervinput_Uis_f32	PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.651000023
PICurrCntf_MtrVecuFit_Ms.rPrevOutput_Uis_f32		
PICurrChtf MtrVecuFit M. str. TermN_UIs 532 0.28200011 PICurrChtf MtrVecuFit M. str. TermD_UIS 532 0.28200011 PICurrChtf MtrVollQaxFFFit M. str. PrevPoutput_UIS 532 194.190002 PICurrChtf MtrVollQaxFFFit M. str. PrevPoutput_UIS 532 12079.9004 PICurrChtf MtrVollQaxFFFit M. str. TermD_UIS 532 12079.9004 PICurrChtf MtrVollQaxFFFit M. str. TermD_UIS 532 12079.9004 PICurrChtf MtrVollQaxFFFit M. str. TermD_UIS 532 0.288200011 SlowDataAccessBufIndex_Cnt_M_u16 0		
PICurrCnrl_MtrVecuFilt_M_str.TermD_Uis_f32 0.298200011 PICurrCnttl_MtrVoltCaxFFFilt_M_str.Previolput_Uis_f32 -194.190002 PICurrCnttl_MtrVoltCaxFFFilt_M_str.TermN_Uis_f32 12079.9004 PICurrCnttl_MtrVoltCaxFFFilt_M_str.TermD_Uis_f32 0.298200011 SlowDataAccessBufindex_Cnt_M_u16 0 k_C.OAFdbackSignalScFacSlew_UispS_f32 3678.44995 k_ILLOAFdbackSignalScFacSlew_UispS_f32 7603.6001 k_MtrCtrlCurt_copSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFedbackControlDisable_Cnt_lgc 0 k_MtrCtrlYrivalResDax_Ohm_f32 0.168999998 k_MtrCurrCaxRefModifDs_Cnt_lgc 1 k_MtrCurrCaxRefModifDs_Cnt_lgc 0 k_MtrCurrCaxRefModifSplEn_Cnt_lgc 0 k_MtrCurrCaxRefModifSplEn_Cnt_lgc 1 k_MtrVoltDaxIntegHiLm_Volt_f32 4.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegLiLim_Volt_f32 4.5 k_MtrVoltQaxIntegLiLim_Volt_f32 4.5 k_VoltSatQaxPolyCoeff_Uis_f32 2.01860008 k_VoltSatQaxPolyCoeff_Uis_f32 2.01860008 k_Ceadtime		
PICurrCntr MtrVoltQaxFFFilt M, str. Previnput_Uis_132 -194.190002 PICurrCntt MtrVoltQaxFFFilt M, str. Previnput_Uis_132 -194.190002 PICurrCntt MtrVoltQaxFFFilt M, str. Previnput_Uis_132 12079.9004 PICurrCntr MtrVoltQaxFFFilt M, str. TermD_Uis_132 0.298200011 SlowDataAccessBufindex_Cnt_M_u16 0.		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uis_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uis_f32 12079.9004 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uis_f32 0.289200011 SlowDataAccessBuflndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UispS_f32 3678.44995 k_ILCAFdbackSignalSclFacSlew_UispS_f32 7603.6001 k_MtrCtrtrLoopSecOrTranFCEnable_Cnt_lgc 0 k_MtrCtrVirualResDax_Ohm_f32 0.0439999998 k_MtrCtrVirualResDax_Ohm_f32 0.0439999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCutrQaxRefModifRpiEn_Cnt_lgc 0 k_MtrVoltDaxIntegli-Lim_Volt_f32 3.1203003 k_MtrVoltDaxIntegli-Lim_Volt_f32 4.5 k_MtrVoltDaxIntegli-Lim_Volt_f32 4.5 k_MtrVoltDaxIntegli-Lim_Volt_f32 4.5 k_MtrVoltDaxIntegli-Lim_Volt_f32 4.5 k_MtrVoltDaxIntegli-Lim_Volt_f32 4.5 k_MtrVoltDaxIntegli-Lim_Volt_f32 4.5 k_VoltSatDaxPolyCoeff_Uis_f32 2.01860008 k_VoltSatDaxPolyCoeff_Uis_f32 2.01860008 k_VoltSatDaxPolyCoeff_Uis_f32 2.01860008 k_Com		
PICurrCntri_MtrVoltQaxFFFitL_M_str.TermD_UIs_f32 12079.9004 PICurrCntri_MtrVoltQaxFFFitL_M_str.TermD_UIs_f32 0.298200011 SlowDataAccessBuflndex_Cnt_M_u16 0 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 3678.44995 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7603.6001 k_MtrCtriCurrLoopSecOrTranFcEnable_Cnt_Igc 0 k_MtrCtriVirualResDax_Cohm_f32 0.0439999998 k_MtrCtriVirualResDax_Cohm_f32 0.166999996 k_MtrCurrQaxRefModiffSb_Cnt_Igc 1 k_MtrCurrQaxRefModiffSb_Cnt_Igc 0 k_MtrVoltDaxintegHollim_Volt_f32 30.1203003 k_MtrVoltDaxintegHollim_Volt_f32 4.5 k_MtrVoltQaxintegLolim_Volt_f32 8.9559978 k_MtrVoltQaxintegLolim_Volt_f32 4.5 k_MtrVoltQaxintegLolim_Volt_f32 4.5 k_MtrVoltQaxintegLolim_Volt_f32 4.5 k_MtrVoltQaxintegLolim_Volt_f32 4.5 k_VoltSatQaxPolyCoeff_UIs_f32 4.5 k_VoltSatQaxPolyCoeff_UIs_f32 20.1860008 k_deadtimeVScale_UIs_f32 4.099999 k_UctomOffsetTbix_UIs_u3p13[1] 5882 L_CommOffsetTbix_UIs_u3p13[1]		
PICurCntri_MtrVoliQaxFFFilt_M_str.TemD_Uls_132 0.298200011 SlowDataAccessBufindex_Cnt_M_url6 0 C.CLOAF dbackSignalSciFacSlew_UlspS_132 3678.44995 k_ILOAF dbackSignalSciFacSlew_UlspS_182 7603.6001 k_MtrCtriCurt.copSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtriFeedbackControliDisable_Cnt_lgc 0.439999998 k_MtrCtriVirualResDax_Ohm_132 0.166999996 k_MtrCurQaxRefModiffSplEn_Cnt_lgc 1 k_MtrCurQaxRefModiffSplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_132 4.5 k_MtrVoltDaxIntegHiLim_Volt_132 4.5 k_MtrVoltQaxFittFFEnable_Cnt_lgc 1 k_MtrVoltQaxFittFFEnable_Cnt_lgc 4.5 k_MtrVoltQaxFittFfEnable_Cnt_lgc 4.5 k_MtrVoltQaxFittSpeHiLim_Volt_132 4.5 k_MtrVoltQaxFittSpeHiLim_Volt_132 4.5 k_MtrVoltQaxFittSpeHiLim_Volt_132 4.5 k_MtrVoltQaxFittSpeHilim_Volt_132 4.5 k_VoltSalbaxPolyCoeff_Uls_132 20.2080000 k_VoltSalbaxPolyCoeff_Uls_132 20.2080000 k_CommOffsetTbix_Uls_u3p13[1] 5882 t_CommOffsetTbix_Uls_u3p13[1] 882 </td <td></td> <td></td>		
SlowDataAccessBufIndex_Cnt_M_u16 0 k_CLOAFdbackSignalSicFacSiew_UlspS_f32 3678.44995 k_ILOAFdbackSignalSicFacSiew_UlspS_f32 7603.6001 k_MtrCtrivtrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrivfreedbackControlDisable_Cnt_lgc 0 k_MtrCtrivfreedbackControlDisable_Cnt_lgc 0 0 k_MtrCtrivfrualResDax_Ohm_f32 0.043999998 k_MtrCurVirualResDax_Ohm_f32 0.166999996 k_MtrCurVirualResDax_Ohm_f32 0.166999996 k_MtrCurQaxRefModiffplEn_Cnt_lgc 1 k_MtrCurQaxRefModiffplEn_Cnt_lgc 0 0 k_MtrVolIDaxIntegHit.im_Volt_f32 0.1203003 k_MtrVolIDaxIntegLoLim_Volt_f32 0.16909998 0.16909998 k_MtrVolIDaxIntegLoLim_Volt_f32 0.169099996 0.16900003 0.16900003 k_MtrVolIDaxIntegLoLim_Volt_f32 0.16900003 0.		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3678.44995 k_ILLOAFdbackSignalSclFacSlew_UlspS_f32 7603.6001 k_MtrCtrtCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrtVirualResDax_Ohm_f32 0.0439999998 k_MtrCtrtVirualResDax_Ohm_f32 0.166999996 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltDaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_VoltSalQaxPolyCoeff_Uls_f32 24.520999 k_VoltSalQaxPolyCoeff_Uls_f32 20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIX_Cls_u16[0] 1813 t_commOffsetTbIX_Cls_u16[1] 183 targe		0.298200011
k_ILOAFdbackSignalScIFacSiew_UlspS_f32 7603.6001 k_MtrCtrICurt_copSecOfTranFcEnable_Cnt_lgc 0 k_MtrCtrIFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.043999998 k_MtrCtrIVirualResQax_Ohm_f32 0.16699996 k_MtrCurQaxRefModifDsb_Cnt_lgc 1 k_MtrCurGaxRefModifDsb_Cnt_lgc 1 k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltDaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 8.9559978 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTblX_Uls_u3p13[f] 5882 t_CommOffsetTblX_Uls_u3p13[f] 5882 t_CommOffsetTblY_Cnt_u16[f] 1813 t_CommOffsetTblY_Cnt_u16[f] 182 target_MtrCntrl_Re	SlowDataAccessBufIndex_Cnt_M_u16	0
k_MtrCtriCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtriCireedbackControlDisable_Cnt_lgc 0 k_MtrCtriVirualResDax_Ohm_f32 0.043999998 k_MtrCtrVirualResQax_Ohm_f32 0.166999996 k_MtrCurQaxRefModiffDsb_Cnt_lgc 1 k_MtrCurQaxRefModiffDsb_Cnt_lgc 0 k_MtrVolIDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVolIDaxIntegHiLim_Volt_f32 4.5 k_MtrVolIDaxFiltFFEnable_Cnt_lgc 1 k_MtrVolIDaxFiltFFEnable_Cnt_lgc 1 k_MtrVolIQaxFiltEnable_Cnt_lgc 1 k_MtrVolIQaxFiltEnable_Cnt_lgc 1 k_VolISatDaxPolyCoeff_Uls_f32 4.5 k_VolISatDaxPolyCoeff_Uls_f32 24.5209999 k_VolISatQaxPolyCoeff_Uls_f32 20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTblX_Uls_u3p13[0] 1516 t_CommOffsetTblX_Uls_u3p13[1] 5882 t_CommOffsetTblY_Cnt_u16[0] 1813 t_reget_MtrCnttr_Read_IvtrLoaMtghtEn_Cnt_lgc_ptr 0 target_MtrCnttr_Read_MoldCwrLoaMtghtEn_Cnt_lgc_ptr 1 target_MtrCnttr_Read_MoldCurrLoaMtghtEn_Cnt_lgc_ptr 1	k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3678.44995
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.0439999998 k_MtrCtrlVirualResDax_Ohm_f32 0.166999996 k_MtrCurrQaxRefModiffDsb_Cnt_lgc 1 k_MtrCurrQaxRefModiffSplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltDaxIntegHiLim_Volt_f32 4.5 k_MtrVoltDaxIntegHiLim_Volt_f32 8.9559978 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 15f6 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_IvrtLoaMtgtnEn_Cnt_lgc_Dtr 0 target_MtrCntrl	k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7603.6001
k_MtrCtrlVirualResDax_Ohm_f32 0.043999998 k_MtrCtrlVirualResQax_Ohm_f32 0.16699996 k_MtrCurrQaxRefModifDsb_Cht_lgc 1 k_MtrCurrQaxRefModifBplEn_Cht_lgc 0 k_MtrVoltDaxIntegHiLm_Volt_f32 30.1203003 k_MtrVoltDaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 8.9555978 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_VoltSatDaxPolyCoeff_Uls_f32 24.520999 k_VoltSatDaxPolyCoeff_Uls_f32 20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cont_u16[1] 183 target_MtrCntrl_Read_IvtLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr 1	k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrVirualResQax_Ohm_f32 0.16699996 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltDaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoCff_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.520999 k_VoltSatQaxPolyCoeff_Uls_f32 20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_NotdxSrtComSvcDft_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModdxSrtComSvcDft_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModdxSrtComSvcDft_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModdxSrtComSvcDft_Cnt_lgc_ptr 1	k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrVirualResQax_Ohm_f32 0.16699996 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltDaxIntegHiLim_Volt_f32 4.5 k_MtrVoltQaxFitIFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxIntegLoCff_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatDaxPolyCoeff_Uls_f32 20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_lvtLoaMtgnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MoldXsrlComSvcDft_Cnt_lgc_v4l 1 target_MtrCntrl_Read_MoldCurrLoaMtgtnEn_Cnt_lgc_ptr 1	k_MtrCtrlVirualResDax_Ohm_f32	0.043999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_MtrVoltQaxIntegLoLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5 k_MtrVoltQaxIntegLoLim_Volt_f32 1 k_MtrVoltQaxIntegLoLim_Volt_f32 24.5 k_MtrVoltQaxIntegLoLim_Volt_f32 24.5 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_ModldxSriComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModldxSriComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 0		
k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltDaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTblX_Uls_u3p13[0] 1516 t_CommOffsetTblY_Cnt_u16[0] 1813 t_CommOffsetTblY_Cnt_u16[1] 183 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr 1		
k_MtrVoltDaxIntegHiLim_Volt_f32 30.1203003 k_MtrVoltDaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTblX_Uls_u3p13[0] 1516 t_CommOffsetTblX_Uls_u3p13[1] 5882 t_CommOffsetTblY_Cnt_u16[0] 1813 t_commOffsetTblY_Cnt_u16[1] 183 target_MtrCntrl_Read_lotrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxsrlComSvcDft_Cnt_lgc_val 1 target_MtrCntrl_Read_ModlcurrLoaMtgtnEn_Cnt_lgc_ptr 1		
k_MtrVoltDaxIntegLoLim_Volt_f32 4.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_commOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlcurrLoaMtgtnEn_Cnt_lgc_ptr 1		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
k_MtrVoltVecuFittEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
k_VoltSatDaxPolyCoeff_Uls_f32 24.5209999 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
k_deadtimeVScale_Uls_f32 0.99000001 t_CommOffsetTbIX_Uls_u3p13[0] 1516 t_CommOffsetTbIX_Uls_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
t_CommOffsetTbIX_UIs_u3p13[0] 1516 t_CommOffsetTbIX_UIs_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1	k_VoltSatQaxPolyCoeff_Uls_f32	-20.1860008
t_CommOffsetTbIX_UIs_u3p13[1] 5882 t_CommOffsetTbIY_Cnt_u16[0] 1813 t_CommOffsetTbIY_Cnt_u16[1] 183 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1	k_deadtimeVScale_Uls_f32	0.99000001
t_CommOffsetTblY_Cnt_u16[0] 1813 t_CommOffsetTblY_Cnt_u16[1] 183 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1	t_CommOffsetTblX_Uls_u3p13[0]	1516
t_CommOffsetTblY_Cnt_u16[0] 1813 t_CommOffsetTblY_Cnt_u16[1] 183 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_ModlCurrLoaMtgtnEn_Cnt_lgc_ptr 1	t_CommOffsetTblX_Uls_u3p13[1]	5882
t_CommOffsetTblY_Cnt_u16[1] 183 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
taliyet_iviti-Oriti_read_iviti-OritiDax_Allip_loz_vati 59.//319964		
	target_ivitrCritti_keau_ivitrCurrDax_Amp_t32_val	ეფ. r ე 1990 4

PICurrCntrl_Per1

MtrCurrQaxFinalRef_Amp_M_f32[1]

2016-04-22, 16:28:30+0530



-206.792007 ± 7.81E-03

Name	Input Value		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3803		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	45.3779984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3803	3803	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.2210007	-87.2210007 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.68405461	-2.68405008 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.15912867	-4.1591301 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	915	915 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-87.2210007	-87.2210007 ± 7.81E-03	~

-206.792007

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 3.7 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.087006
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16799998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.223999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.127000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0960000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.234999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.274994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.618
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1889992
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998

PICurrCntrl Per1

2016-04-22, 16:28:30+0530



Input Value Name MtrCurrDaxRef_Amp_M_f32[0] 31.5869999 MtrCurrDaxRef_Amp_M_f32[1] -186.397995 MtrCurrQaxCog_Amp_M_f32 10.8985004 MtrCurrQaxFinalRef_Amp_M_f32[0] 37.4550018 MtrCurrQaxFinalRef_Amp_M_f32[1] -2.84699988 MtrCurrQaxPrevIntg_Volt_M_f32 15.8292999 MtrCurrQaxRef_Amp_M_f32[0] 171.485992 MtrCurrQaxRef_Amp_M_f32[1] 163,789001 MtrCurrQaxRpl_Amp_M_f32[0] -213.026993 $MtrCurrQaxRpl_Amp_M_f32[1]$ -66 7249985 MtrPosComputationDelay_Rad_M_f32[0] -2.94899988 $MtrPosComputationDelay_Rad_M_f32[1]$ 0.00800000038 MtrVoltDax_Volt_M_f32[0] -7.66699982 2 61599994 MtrVoltDax Volt M f32[1] MtrVoltQax_Volt_M_f32[0] -28.2420006 12.6160002 MtrVoltQax_Volt_M_f32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.911000013 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.816999972 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.489399999 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.703400016 $PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32$ -627.179993 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 40.2612 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.176699996 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 1118 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 16.5851002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.727199972 SlowDataAccessBufIndex Cnt M u16 0 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5911 31982 k ILOAFdbackSignalSclFacSlew UlspS f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k MtrCtrlFeedbackControlDisable Cnt lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0 144999996 k_MtrCtrlVirualResQax_Ohm_f32 0.114 k MtrCurrQaxRefModifDsb Cnt lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k MtrVoltDaxIntegHiLim_Volt_f32 28.3733006 k_MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 k MtrVoltQaxFiltFFEnable Cnt lgc k_MtrVoltQaxIntegHiLim_Volt_f32 31 k MtrVoltQaxIntegLoLim Volt f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 $k_VoltSatQaxPolyCoeff_Uls_f32$ 4 35599995 k_deadtimeVScale_Uls_f32 0.963 t_CommOffsetTbIX_Uls_u3p13[0] 459 t_CommOffsetTblX_Uls_u3p13[1] 5775 t_CommOffsetTblY_Cnt_u16[0] 1081 t_CommOffsetTblY_Cnt_u16[1] 1779 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 903 $target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val$ -34.6189995 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value Expected Value** Result Name $MtrCntrl_Write_CommOffset_Cnt_u16(val)$ 1779 1779 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63111 63111 ± 1 220 ± 7.81E-03 MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val) 220 1.44797277 1.44797003 ± 4.88E-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 4.59212017 ± 4.88E-04 4.59212399 MtrCntrl Write MtrQaxVoltage Volt f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 3269 3269 ± 1.52588E-05 -22.4099998 -22.4099998 ± 0.0625 MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0] 220 220 ± 7.81E-03 MtrCurrQaxFinalRef Amp M f32[1] -2.84699988 -2.84699988 ± 7.81E-03



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 3.8 (Repeat Count = 1) Name	Input Value
	o
FastDataAccessBufIndex_Cnt_M_u16	·
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0430000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.43400002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.70599997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	362.112
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	65.1259995
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.064000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0209999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.063000001
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.356999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.65200001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-894.130005
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-888.995972
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-29.9890003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	29.243
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-29.7110004
/trCtrl MtrVoltQaxFF Volt M f32[1]	3.61899996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
WtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	19.7509995
MtrCurrDaxRef Amp M f32[0]	67.4899979
MtrCurrDaxRef Amp M f32[1]	119.721001
	-181.929001
MtrCurrQaxCog_Amp_M_f32	
WtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxPrevIntg_Volt_M_f32	7.82140017
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220
MtrCurrQaxRpl_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008
MtrPosComputationDelay_Rad_M_f32[0]	-0.541999996
MtrPosComputationDelay_Rad_M_f32[1]	6.08400011
MtrVoltDax_Volt_M_f32[0]	-29.7110004
MtrVoltDax_Volt_M_f32[1]	3.61899996
MtrVoltQax_Volt_M_f32[0]	-29.7110004

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



r icuironal_r eri			TOPECTOOL
Name	Input Value		
MtrVoltQax Volt M f32[1]	3.61899996		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.416999996		
PICurrCntrl InverterFailSclFac Uls M f32	0.787		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.190799996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.708000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	48410.1016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0835999995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	48410.1016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0835999995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10.1009998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4233.2002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.087999995		
k_MtrCtrlVirualResQax_Ohm_f32	0.0099999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.9371996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.1975002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.59399998		
k_VoltSatQaxPolyCoeff_Uls_f32	8.35700035		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTbIX_Uls_u3p13[0]	4914		
t_CommOffsetTbIX_Uls_u3p13[1]	7782		
t_CommOffsetTblY_Cnt_u16[0]	1099		
t_CommOffsetTblY_Cnt_u16[1]	1672		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-72.4260025		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4932		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	77.189003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1672	1672	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62324	62324 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	44.2269897	44.2270012 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	9.26780319	9.26780033 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-9.9237175	-9.92372036 ± 4.88E-04	✓

Fest Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

19279

12.9371996

44.2269897

46.8180008

19279 ± 1.52588E-05

12.9371996 ± 0.0625

44.2270012 ± 7.81E-03

46.8180008 ± 7.81E-03

MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)

MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrQaxFinalRef_Amp_M_f32[0]

MtrCurrQaxFinalRef_Amp_M_f32[1]



Test Step 3.9 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-133.947006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	75.7020035
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0320000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.057999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.34800005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.11099994 -942.771973
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-942.771973 -380.85199
	0.093999968
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl MtrImpedDax Ohm M f32[1]	0.087999995
MtrCtrl_MtrImpedDax_Onm_M_f32[1] MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.096000008
MtrCtrl_MtrImpedQax_Onm_M_f32[u] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0960000008
MtrCtn_MtrImpedQax_Onm_M_t32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.35000002
	0.749000013
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.3530006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.3040009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.4720001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.099999
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997
MtrCurrDaxPrevIntg_Volt_M_f32	15.0869999
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004
MtrCurrDaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxCog_Amp_M_f32	114.531998
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998
MtrCurrQaxFinalRef Amp M f32[1]	-112.455002
MtrCurrQaxPrevIntg_Volt_M_f32	5.42920017
MtrCurrQaxRef_Amp_M_f32[0]	191.369003
MtrCurrQaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxRpl_Amp_M_f32[0]	106.072998
MtrCurrQaxRpl_Amp_M_f32[1]	-112.455002
MtrPosComputationDelay Rad M f32[0]	2.61800003
MtrPosComputationDelay_Rad_M_f32[1]	-1.04299998
MtrVoltDax_Volt_M_f32[0]	26.4720001
MtrVoltDax_Volt_M_f32[1]	6.099999
MtrVoltQax_Volt_M_f32[0]	26.4720001
MtrVoltQax_Volt_M_f32[1]	6.099999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.600000024
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.89999976
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.179199994
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.257999986
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	404.899994
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.578299999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	404.899994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.578299999
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2048.80005
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3548.88989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0790000036
k_MtrCtrlVirualResQax_Ohm_f32	0.177000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k MtrCurrQaxRefModifRplEn Cnt lgc	0

PICurrCntrl_Per1

MtrCurrQaxFinalRef_Amp_M_f32[1]

2016-04-22, 16:28:30+0530



Input Value k_MtrVoltDaxIntegHiLim_Volt_f32 2.39529991 k_MtrVoltDaxIntegLoLim_Volt_f32 -13.1999998 k MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 13.2297001 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 21.0030003 k_VoltSatQaxPolyCoeff_Uls_f32 -9.26399994 k_deadtimeVScale_Uls_f32 0.950999975 t_CommOffsetTblX_Uls_u3p13[0] 1810 t_CommOffsetTblX_Uls_u3p13[1] 2335 t_CommOffsetTbIY_Cnt_u16[0] 157 t_CommOffsetTblY_Cnt_u16[1] 712 $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 $target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val$ 107.702003 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 4540 $target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val$ -34.6189995 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 4540 4540 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 105.060005 105.059998 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 2.34449911 2.34450006 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 29.3876286 29.3875999 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 55487 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0.910027981 0.910028994 ± 0.0625 MtrCurrQaxFinalRef_Amp_M_f32[0] 106.072998 106.072998 ± 7.81E-03

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

105.060005

105.059998 ± 7.81E-03

2016-04-22, 16:27:52+0530





 Project
 Ap_PICurrCntrl

 Module
 PICurrCntrl

 Test Object
 LoaMtgtnSclFac

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Plcurrentcontrol
Configuration File	D:\Synergy_Work_Area\Plcurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract)\utp\contract -I\$(SOURCEROOT)\utp\contract)\utp\contract -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Descript Name	Text
Module 'PICurrCntrl'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:15 Module Design Document:PlCurrentContrl.doc Module Design Document:PlCurrentContrl.doc Module Design Document Version:11 Data Dictionary Version:14 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):NA Total RAM Used (Bytes):NA Total RAM Used (Bytes):NA Special Test Requirements: Test Date:4/22/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnSclFac "". for variables "k_CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32, PlCurrCntrl_CurrSentSelisClFac_Uls_M_f32 and PlCurrCntrl_InverterSiolSclFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range."

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	



Test Case 1: Metric Test

Description

Vector Description:

TS 1.1Shortest Path==>(MotCurrLoaMtgtnEn_Cnt_T_lgc == TRUE)=False&&(1>=([D_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=True&&(1>=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=True
TS 1.2Longest Path==>(MotCurrLoaMtgtnEn_Cnt_T_lgc == TRUE)=True&&(0>=([D_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(0<=([D_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(0<=([D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=false&&(0<=([D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=false

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.00125000009	0.00124999997 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00125000009	0.00124999997 ± 4.88E-04	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓



Test Case 2: Boundary Test

Description

Vector Description: TS 2.1AII_Min

TS 2.1All_Min
TS 2.2All_Max
TS 2.2All_Max
TS 2.3MotCurrLoaMtgtnEn_Cnt_T_lgc==>Min
TS 2.4MotCurrLoaMtgtnEn_Cnt_T_lgc==>Max
TS 2.5lvtrLoaMtgtnEn_Cnt_T_lgc==>Min
TS 2.6lvtrLoaMtgtnEn_Cnt_T_lgc==>Min
TS 2.6lvtrLoaMtgtnEn_Cnt_T_lgc==>Max
TS 2.7PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32==>Min
TS 2.8PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32==>Mix
TS 2.9PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32==>Mid
TS 2.10k_CLOAFdbackSignalSclFacSlew_UlspS_f32==>Min
TS 2.11k_CLOAFdbackSignalSclFacSlew_UlspS_f32==>Max
TS 2.12k_CLOAFdbackSignalSclFacSlew_UlspS_f32==>Mid
TS 2.13k_CLOAFdbackSignalSclFacSlew_UlspS_f32==>Mid
TS 2.14k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>Mid
TS 2.15k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>Mix
TS 2.16k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>Max
TS 2.16k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>default
TS 2.17k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>default
TS 2.18PlCurrCntrl_InverterFailSclFac_Uls_M_f32==>Min TS 2.18PlCurrCntrl InverterFailSclFac Uls M_f32==>Min TS 2.19PlCurrCntrl InverterFailSclFac_Uls_M_f32==>Max TS 2.20PlCurrCntrl_InverterFailSclFac_Uls_M_f32==>Mid

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.00125000009	0.00125 ± 4.88E-04	~
PICurrCntrl InverterFailSclFac Uls M f32	0.00125000009	0.00125 ± 4.88E-04	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.2 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.3 (Repeat Count = 1)			~
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.2356		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.2356		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7702.76		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6599.26		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	✓





Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.3256		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.325		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7466.02		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5753.88		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.1425		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.1245		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6719.13		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1788.25		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.982391238	0.982392 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.348031253	0.348032 ± 4.88E-04	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.6 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.3256		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.3256		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3280.9		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2855.33		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.735712528	0.735713 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	_	

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
lvtrLoaMtgtnEn_Cnt_T_lgc	0
MotCurrLoaMtgtnEn_Cnt_T_lgc	0
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.2256
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7916.52

2016-04-22, 16:27:52+0530



LoaMtgtnSclFac

Name	Input Value		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2851.42		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.989565074	0.989565 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.582027555	0.582027 ± 4.88E-04	~

Test Step Call Trace					✓	
	Actual Function	Count	Expected Function	Count	Result	
	none	0	*** No Call Expected ***	0	~	

Test Step 2.8 (Repeat Count = 1)	Test Step 2.8 (Repeat Count = 1)			
Name	Input Value			
lvtrLoaMtgtnEn_Cnt_T_lgc	0			
MotCurrLoaMtgtnEn_Cnt_T_lgc	1			
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1			
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.3256			
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2075.21			
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1642.61			
Name	Actual Value	Expected Value	Result	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.740598738	0.740599 ± 4.88E-04	✓	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.530926228	0.530926 ± 4.88E-04	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.5		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.3244		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1404.3		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4636.46		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.324462473	0.324462265 ± 4.88E-04	•
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.903957546	0.903957 ± 4.88E-04	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

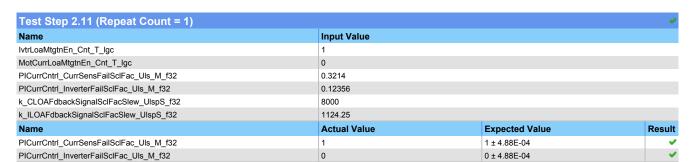
Test Step 2.10 (Repeat Count = 1)			·
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.5326		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.1256		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5044.23		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.531349957	0.53135 ± 4.88E-04	✓
PICurrCntrl InverterFailSclFac Uls M f32	0.756128728	0.756129 ± 4.88E-04	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

LoaMtgtnSclFac







Test Step Call Trace						✓
Actua	l Function	Count	Expected Function	Count	Resu	ılt
none		0	*** No Call Expected ***	0		~

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.2356		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.3256		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	100		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4636.46		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.223099992	0.2231 ± 4.88E-04	~
PICurrCntrl InverterFailSclFac Uls M f32	0.905157566	0.905157 ± 4.88E-04	✓

Test Step Call Trace				✓
Actual Function	Coun	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.8332		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.1425		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4217		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	497.261		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.204657629	0.204657659 ± 4.88E-04	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.2461		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.7888		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5544.15		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.79005003	0.79005 ± 4.88E-04	✓





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.87426		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	892.101		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.888487339	0.888487 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	~

Test Step Call Trace					V
Actual Function	Count	Expected Function	Count	Resi	ılt
none	0	*** No Call Expected ***	0		•

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.2145		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.6125		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	892.101		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	100		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.326012611	0.326012671 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.600000024	0.6 ± 4.88E-04	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.3214		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.5362		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	886.92		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1071.5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.432264984	0.432265 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.670137525	0.670138 ± 4.88E-04	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 2.18 (Repeat Count = 1)	
Name	Input Value
lvtrLoaMtgtnEn_Cnt_T_lgc	0
MotCurrLoaMtgtnEn_Cnt_T_lgc	1
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.3698
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2332

2016-04-22, 16:27:52+0530





Name	Input Value		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3405.61		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0782999992	0.0782994 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.42570129	0.425701 ± 4.88E-04	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.19 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.8032		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3197.44		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1505.18		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.403519988	0.40352 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.20 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0325		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.654		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4477.15		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3969.39		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.592143774	0.592143 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Case 3: Path test

Description

Vector Description:

Vector Description:

TS 3.1(MotCurrLoaMtgtnEn_Cnt_T_igc == TRUE)=False&&(1>=((ID_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=True&&(1>=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=True

TS 3.2(MotCurrLoaMtgtnEn_Cnt_T_igc == TRUE)=True&&(0>=((D_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(0<=((D_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(0>=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=false&&(0<=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=false&&(0<=(D_MTRCTRLISRRATE_MS_F32 * k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=false&&(1>=(D_MTRCTRLISRRATE_MS_F32 * k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(1>=(D_MTRCTRLISRRATE_MS_F32 * k_LOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=False&&(1<=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=False&&(1<=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=False
TS 3.4(1<=((D_MTRCTRLISRRATE_MS_F32 * k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=true
TS 3.5(0>=((D_MTRCTRLISRRATE_MS_F32 * k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=True
TS 3.6(0>=((D_MTRCTRLISRRATE_MS_F32 * k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=True
TS 3.6(0>=((D_MTRCTRLISRRATE_MS_F32 * k_LOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=True
TS 3.6(0>=((D_MTRCTRLISRRATE_MS_F32 * k_LOAFdbackSignalSclFacSlew_Uls

Test Step 3.1 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.00125000009	0.00125 ± 4.88E-04	~
PICurrCntrl InverterFailSclFac UIs M f32	0.00125000009	0.00125 ± 4.88E-04	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	·
PICurrCntrl InverterFailSclFac Uls M f32	0	0 ± 4.88E-04	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Name	Input Value		
	· · · · · · · · · · · · · · · · · · ·		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.2356		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.2356		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7702.76		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6599.26		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	•
PICurrCntrl InverterFailSclFac Uls M f32	1	1 ± 4.88E-04	

LoaMtgtnSclFac



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 3.4 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	2		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	0		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6599.26		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	2	2 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	✓

Test Step Call Trace					V
Actual Function	Count	Expected Function	Count	Resi	ılt
none	0	*** No Call Expected ***	0		•

Test Step 3.5 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	-1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	0		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	-1	-1 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.6 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	0		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	✓

Test Step Call Trace					✓
	Actual Function	Count	Expected Function	Count	Result
	none	0	*** No Call Expected ***	0	~

Test Step 3.7 (Repeat Count = 1)		✓
Name	Input Value	
lvtrLoaMtgtnEn_Cnt_T_lgc	0	
MotCurrLoaMtgtnEn_Cnt_T_lgc	0	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.2356	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	2	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7702.76	

2016-04-22, 16:27:52+0530



LoaMtgtnSclFac

Name	Input Value		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	0		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	2	2 ± 4.88E-04	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 3.8 (Repeat Count = 1)			
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	-1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	0		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	-1	-1 ± 4.88E-04	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.9 (Repeat Count = 1)			✓
Name	Input Value		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	2		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0 ± 4.88E-04	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1 ± 4.88E-04	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

2016-04-22, 16:27:43+0530



IntegralStateVarNonOperState

Project Ap_PICurrCntrl
Module PICurrCntrl

Test Object IntegralStateVarNonOperState

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Plcurrentcontrol
Configuration File	D:\Synergy_Work_Area\PIcurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$ (SOURCEROOT)\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Name	Text
Module 'PICurrCntri'	Name of Tester:Priyanka Shitole Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:15 Module Design Document:PlCurrentContrl.doc Module Design Document:PlCurrentContrl.doc Module Design Document:PlCurrentContrl.doc Module Design Document Version:11 Data Dictionary Version:14 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):NA Total FLASH Used (Bytes):NA Total CALS Used (Bytes):NA Special Test Requirements: Test Date:4/22/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnSclFac "". for variables ""k, CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32. PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32 and PlCurrCntrl_InverterFailSclFac_Uls_M_f32" to achieve 100% path coverage. NOTE 3: In function PlCurrCntrl_Perf PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f3: variables are going out of range."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9



Test Case 1: Metric Test

IntegralStateVarNonOperState

Description

Vector Description:

TS 1.1Longest Path==>(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=True TS 1.2Shortest Path==>(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=False

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	•
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391		
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723		
SysState_Cnt_T_Enum	2		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	~
MtrCurrQaxPrevIntg_Volt_M_f32	-6.72300005	-6.72300005	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Case 2: Boundary test

Description

Vector Description:

TS 2.1All Min TS 2.2All Max

TS 2.3SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_DISABLE
TS 2.4SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OFF
TS 2.5SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OPERATE
TS 2.6SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_WARMINIT
TS 2.7SysState_Cnt_T_Enum=>RTE_TRANSITION_StaMd_Mode

Test Step 2.1 (Repeat Count = 1)			
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
MtrCurrQaxPrevIntg Volt M f32	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.2 (Repeat Count = 1)	✓
Name	Input Value
MtrCurrDaxPrevIntg_Volt_M_f32	31
MtrCurrQaxPrevIntg_Volt_M_f32	31

2016-04-22, 16:27:43+0530



IntegralStateVarNonOperState

Name	Input Value		
SysState_Cnt_T_Enum	4		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	7.8214		
MtrCurrQaxPrevIntg_Volt_M_f32	19.751		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	8.089		
MtrCurrQaxPrevIntg_Volt_M_f32	-23.062		
SysState_Cnt_T_Enum	1		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391		
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723		
SysState_Cnt_T_Enum	2		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	✓
MtrCurrQaxPrevIntg Volt M f32	-6.72300005	-6.72300005	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

Test Step 2.6 (Repeat Count = 1)			
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	22.3449993		
MtrCurrQaxPrevIntg_Volt_M_f32	25.7600002		
SysState_Cnt_T_Enum	3		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

2016-04-22, 16:27:43+0530



IntegralStateVarNonOperState

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	12.4499998		
MtrCurrQaxPrevIntg_Volt_M_f32	11.3400002		
SysState_Cnt_T_Enum	4		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Case 3: Path Test

Description

Vector Description:

TS 3.1(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=True TS 3.2(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=False

Test Step 3.1 (Repeat Count = 1)			
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391		
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723		
SysState_Cnt_T_Enum	2		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	✓
MtrCurrQaxPrevIntg Volt M f32	-6.72300005	-6.72300005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~