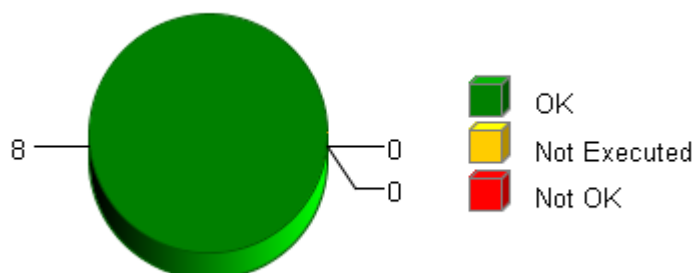


Summary

Total Test Objects: 8
Successful: 8
Failed: 0
Not Executed: 0
Date: 2016-04-22
Time: 16:31:23+0530

Overall Test Object Results (including Coverage)



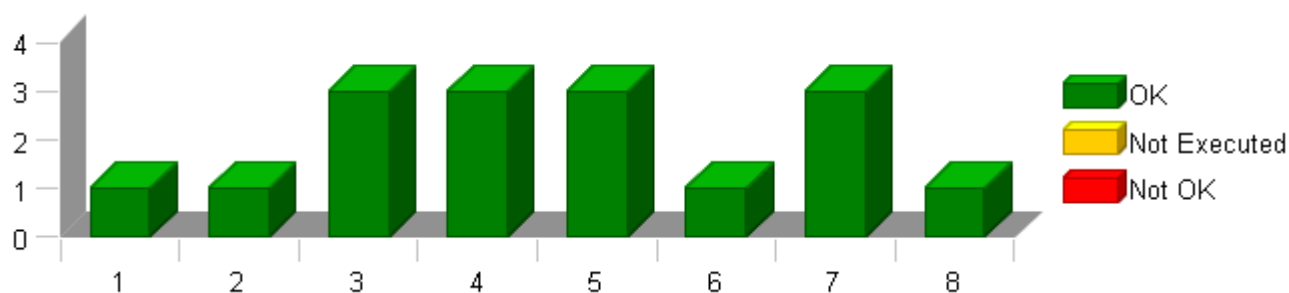
Selected Project Items

Test Object "CBD_UnitTest/PICurrCntrl/CalLowPassFiltBilinearOut"
Test Object "CBD_UnitTest/PICurrCntrl/CalLowPassFiltBilinearTerm"
Test Object "CBD_UnitTest/PICurrCntrl/CalLowPassFiltVecuOut"
Test Object "CBD_UnitTest/PICurrCntrl/IntegralStateVarNonOperState"
Test Object "CBD_UnitTest/PICurrCntrl/LoaMtgtnScfFac"
Test Object "CBD_UnitTest/PICurrCntrl/PICurrCntrl_Init"
Test Object "CBD_UnitTest/PICurrCntrl/PICurrCntrl_Per1"
Test Object "CBD_UnitTest/PICurrCntrl/PICurrCntrl_Per2"

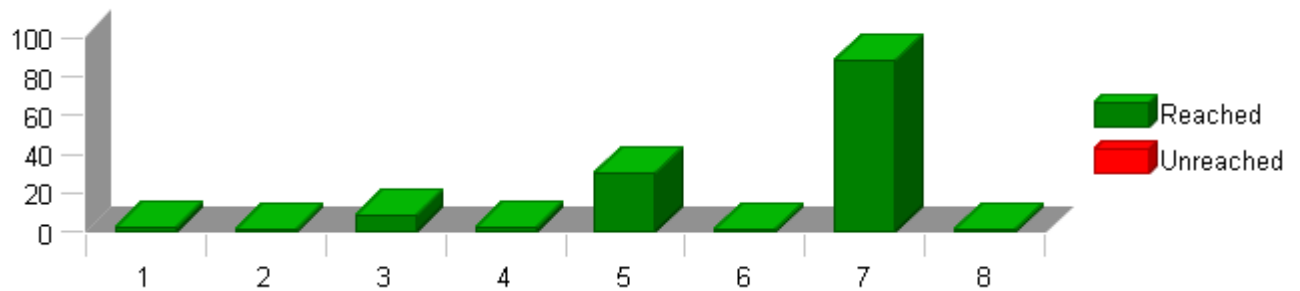
Used Test Environments

GNU GCC GNU GVD (Default)

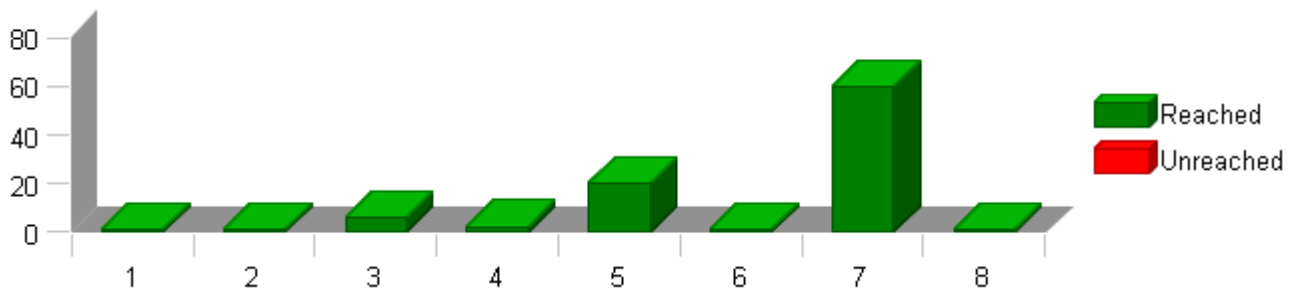
Test Case Results for Each Test Object (without Coverage)



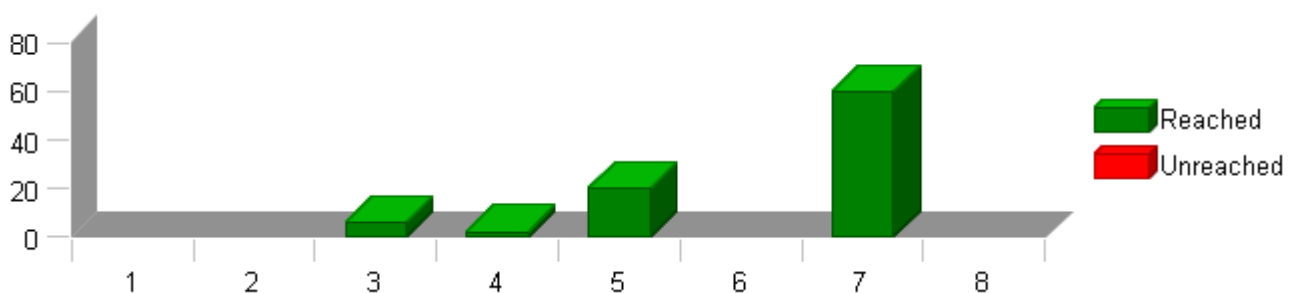
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object

The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object

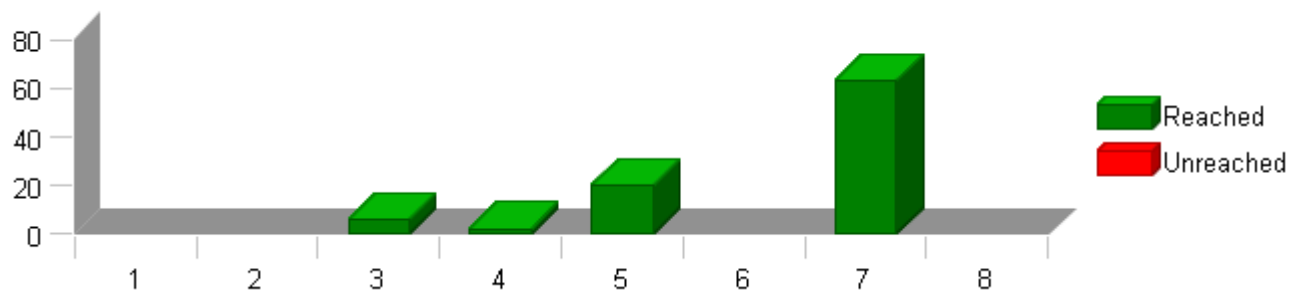
The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

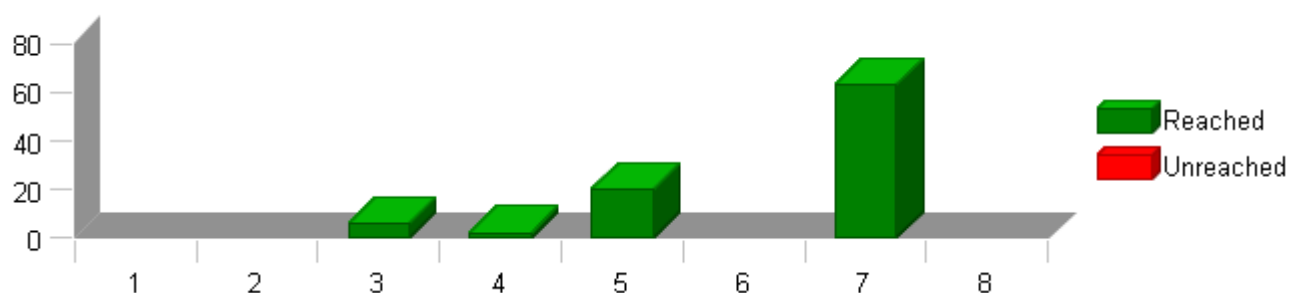
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2016-04-22, 16:31:23+0530

Project Ap_PICurrCntrl



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	Ap_PICurrCntrl	100 %	100 %	100 %	100 %	100 %	16 of 16 passed	✓
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	16 of 16 passed	✓
	PICurrCntrl	100 %	100 %	100 %	100 %	100 %	16 of 16 passed	✓
1	CalLowPassFiltBilinearOut	100 %	100 %	-	-	-	1 of 1 passed	✓
2	CalLowPassFiltBilinearTerm	100 %	100 %	-	-	-	1 of 1 passed	✓
3	CalLowPassFiltVecuOut	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
4	IntegralStateVarNonOperState	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
5	LoaMtgnSciFac	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
6	PICurrCntrl_Init	100 %	100 %	-	-	-	1 of 1 passed	✓
7	PICurrCntrl_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
8	PICurrCntrl_Per2	100 %	100 %	-	-	-	1 of 1 passed	✓

TEST DETAILS REPORT

2016-04-22, 16:27:19+0530

CalLowPassFiltBilinearTerm



Project	Ap_PICurrCntrl
Module	PICurrCntrl
Test Object	CalLowPassFiltBilinearTerm

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Picurrentcontrol
Configuration File	D:\Synergy_Work_Area\Picurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS=-D_sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D_sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'PICurrCntrl'	*****Unit Test Information***** Name of Tester: Priyanka Shitole Code File(s) Under Test: Ap_PICurrCntrl.c Code File(s) Version: 15 Module Design Document: PICurrentCntrl.doc Module Design Document Version: 11 Data Dictionary Version: 14 Unit Test Plan Version: 3 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): NA Total RAM Used (Bytes): NA Total CALS Used (Bytes): NA Special Test Requirements: Test Date: 4/22/2016 Comments: "Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2 : Out of range value is given in function ""LoaMtgtnScfFac "" for variables ""k_CLOAFdbackSignalScfFacSlew_UlspS_f32,k_ILoAFdbackSignalScfFacSlew_UlspS_f32, PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 and PICurrCntrl_InverterFailScfFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PICurrCntrl_Per1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

2016-04-22, 16:27:19+0530

CalLowPassFiltBilinearTerm



Test Case 1: BoundaryTest

Description Vector Description:

TS1.1All Min
TS1.2All Max
TS1.3Freq_Hz_T_f32=Min
TS1.4Freq_Hz_T_f32=Max
TS1.5Freq_Hz_T_f32=Pos
TS1.6TimeCons_Sec_T_f32=Min
TS1.7TimeCons_Sec_T_f32=Max
TS1.8TimeCons_Sec_T_f32=Pos

Test Step 1.1 (Repeat Count = 1)

Name	Input Value			
Freq_Hz_T_f32	0.100000001			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str			
TimeCons_Sec_T_f32	6.2500003e-005			
Name	Actual Value	Expected Value	Result	
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	50928.5781	50928.582	✓	
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1.963457e-005	1.96345682e-005	✓	

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value			
Freq_Hz_T_f32	2.14748365e+009			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str			
TimeCons_Sec_T_f32	0.100000001			
Name	Actual Value	Expected Value	Result	
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-1	-1	✓	
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1	1	✓	

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value			
Freq_Hz_T_f32	0.100000001			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str			
TimeCons_Sec_T_f32	0.00240000011			
Name	Actual Value	Expected Value	Result	
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1325.29102	1325.29114	✓	
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.000753414293	0.000753414177	✓	

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.4 (Repeat Count = 1)

Name	Input Value			
Freq_Hz_T_f32	2.14748365e+009			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str			
TimeCons_Sec_T_f32	0.0214000009			
Name	Actual Value	Expected Value	Result	
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-1	-1	✓	
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1	1	✓	

TEST DETAILS REPORT

2016-04-22, 16:27:19+0530

CalLowPassFiltBilinearTerm



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.5 (Repeat Count = 1)

Name	Input Value		
Freq_Hz_T_f32	25348.7129		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
TimeCons_Sec_T_f32	0.0365200005		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-0.999656141	-0.999656141	✔
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.99965626	0.99965626	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.6 (Repeat Count = 1)

Name	Input Value		
Freq_Hz_T_f32	250.412292		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
TimeCons_Sec_T_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	19.3382893	19.3382912	✓
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.0468641147	0.046864111	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.7 (Repeat Count = 1)

Name	Input Value		
Freq_Hz_T_f32	1050.32556		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
TimeCons_Sec_T_f32	0.100000001		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-0.996969402	-0.996969402	✓
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.996978581	0.996978581	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.8 (Repeat Count = 1)

Name	Input Value		
Freq_Hz_T_f32	286.223602		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
TimeCons_Sec_T_f32	0.00300000003		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-0.629299283	-0.629299283	✓
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.729553878	0.729553878	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530

PICurrCntrl_Per2



Project	Ap_PICurrCntrl
Module	PICurrCntrl
Test Object	PICurrCntrl_Per2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Plicurrentcontrol
Configuration File	D:\Synergy_Work_Area\Plicurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS=-D_sqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D_sqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'PICurrCntrl'	*****Unit Test Information***** Name of Tester: Priyanka Shitole Code File(s) Under Test: Ap_PICurrCntrl.c Code File(s) Version: 15 Module Design Document: PlicurrentCntrl.doc Module Design Document Version: 11 Data Dictionary Version: 14 Unit Test Plan Version: 3 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): NA Total RAM Used (Bytes): NA Total CALS Used (Bytes): NA Special Test Requirements: Test Date: 4/22/2016 Comments: "Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2 : Out of range value is given in function ""LoadMtgtnScfFac "" for variables ""k_CLOAFdbackSignalScfFacSlew_UlspS_f32,k_ILOAFdbackSignalScfFacSlew_UlspS_f32, PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 and PICurrCntrl_InverterFailScfFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PICurrCntrl_Per1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530

PICurrCntrl_Per2



Test Case 1: BoundaryTest

Description Vector Description:

TS1.1All Min
TS1.2All Max
TS1.3EstKe_VpRadpS_f32 = MIN
TS1.4EstKe_VpRadpS_f32 = Max
TS1.5EstKe_VpRadpS_f32 = Pos
TS1.6EstR_Ohm_f32 = Min
TS1.7EstR_Ohm_f32 = Max
TS1.8EstR_Ohm_f32 = Pos
TS1.9MRFMtrVel_MtrRadpS_f32 = Min
TS1.10MRFMtrVel_MtrRadpS_f32 = Max
TS1.11MRFMtrVel_MtrRadpS_f32 = Neg
TS1.12MRFMtrVel_MtrRadpS_f32 = Zero
TS1.13MRFMtrVel_MtrRadpS_f32 = Pos
TS1.14CorrMtrPosElec_Rev_f32 = Min
TS1.15CorrMtrPosElec_Rev_f32 = Max
TS1.16CorrMtrPosElec_Rev_f32 = Pos
TS1.17MtrCurrDaxRef_Amp_f32 = Min
TS1.18MtrCurrDaxRef_Amp_f32 = Max
TS1.19MtrCurrDaxRef_Amp_f32 = Pos
TS1.20MtrCurrDaxRef_Amp_f32 = Zero
TS1.21MtrCurrDaxRef_Amp_f32 = Neg
TS1.22MtrVoltDax_Volt_M_f32[2] = Min
TS1.23MtrVoltDax_Volt_M_f32[2] = Max
TS1.24MtrVoltDax_Volt_M_f32[2] = Neg
TS1.25MtrVoltDax_Volt_M_f32[2] = Zero
TS1.26MtrVoltDax_Volt_M_f32[2] = Pos
TS1.27MtrVoltQax_Volt_M_f32[2] = Min
TS1.28MtrVoltQax_Volt_M_f32[2] = Max
TS1.29MtrVoltQax_Volt_M_f32[2] = Neg
TS1.30MtrVoltQax_Volt_M_f32[2] = Zero
TS1.31MtrVoltQax_Volt_M_f32[2] = Pos
TS1.32MtrCurrQaxFinalRef_Amp_M_f32[2] = Min
TS1.33MtrCurrQaxFinalRef_Amp_M_f32[2] = Max
TS1.34MtrCurrQaxFinalRef_Amp_M_f32[2] = Neg
TS1.35MtrCurrQaxFinalRef_Amp_M_f32[2] = Zero
TS1.36MtrCurrQaxFinalRef_Amp_M_f32[2] = Pos
TS1.37SlowDataAccessBufIndex_Cnt_M_u16 = Min
TS1.38SlowDataAccessBufIndex_Cnt_M_u16 = Max

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220		
MtrVoltDax_Volt_M_f32[0]	-31		
MtrVoltDax_Volt_M_f32[1]	-31		
MtrVoltQax_Volt_M_f32[0]	-31		
MtrVoltQax_Volt_M_f32[1]	-31		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0250000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.00499999989		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1118		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-220		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-30.0473728	-30.0473728 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	2.09737158	2.09737206 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	220		
MtrVoltDax_Volt_M_f32[0]	31		
MtrVoltDax_Volt_M_f32[1]	31		
MtrVoltQax_Volt_M_f32[0]	31		
MtrVoltQax_Volt_M_f32[1]	31		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.999984741		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.075000003		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.125650004		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	1118		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	220		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	7.06780577	7.06782198 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	76.7888641	76.7888641 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-154.242996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-57.1660004		
MtrVoltDax_Volt_M_f32[0]	-29.9080009		
MtrVoltDax_Volt_M_f32[1]	-13.9139996		
MtrVoltQax_Volt_M_f32[0]	1.25399995		
MtrVoltQax_Volt_M_f32[1]	-20.5		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.476000011		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0250000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0790000036		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	688.880005		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	213.205994		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	43.1762505	43.1762466 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-12.0380945	-12.0380983 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.4 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-4.47100019		
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.723999		
MtrVoltDax_Volt_M_f32[0]	5.57999992		
MtrVoltDax_Volt_M_f32[1]	14.0050001		
MtrVoltQax_Volt_M_f32[0]	26.6100006		
MtrVoltQax_Volt_M_f32[1]	-26.9500008		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.915000021		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.075000003		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.123000003		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	409.079987		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	210.386002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	32.0637131	32.0637398 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	70.7293549	70.7293396 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.5 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	97.2080002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	209.817993		
MtrVoltDax_Volt_M_f32[0]	-17.2469997		
MtrVoltDax_Volt_M_f32[1]	-29.434		
MtrVoltQax_Volt_M_f32[0]	16.6630001		
MtrVoltQax_Volt_M_f32[1]	-7.27199984		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.694000006		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0260000005		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0209999997		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	163.839996		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-49.0620003		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	24.2441082	24.2441139 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	21.5036201	21.5036144 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.6 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-192.279007		
MtrCurrQaxFinalRef_Amp_M_f32[1]	104.983002		
MtrVoltDax_Volt_M_f32[0]	24.5849991		
MtrVoltDax_Volt_M_f32[1]	-1.83800006		
MtrVoltQax_Volt_M_f32[0]	-13.7519999		
MtrVoltQax_Volt_M_f32[1]	-20.2409992		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.370999992		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0610000007		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.00499999989		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-314.470001		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-11.6210003		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-12.4378471	-12.4378471 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	22.1681595	22.1681633 ± 0.00048828	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.7 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-2.46000004		
MtrCurrQaxFinalRef_Amp_M_f32[1]	153.630005		
MtrVoltDax_Volt_M_f32[0]	5		
MtrVoltDax_Volt_M_f32[1]	1.52900004		
MtrVoltQax_Volt_M_f32[0]	-11.7320004		
MtrVoltQax_Volt_M_f32[1]	-9.96500015		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.779999971		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0689999983		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.125650004		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	279.26001		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-113.587997		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	47.7400093	47.7400093 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-5.03275585	-5.03276062 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.8 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-57.2830009		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-109.836998		
MtrVoltDax_Volt_M_f32[0]	-7.07700014		
MtrVoltDax_Volt_M_f32[1]	-12.2460003		
MtrVoltQax_Volt_M_f32[0]	13.4899998		
MtrVoltQax_Volt_M_f32[1]	-24.4689999		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.180000007		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0540000014		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.104999997		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	692.190002		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	26.1779995		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-20.9283752	-20.9283733 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-0.60405916	-0.604057014 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.9 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	111.466003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	3.68799996		
MtrVoltDax_Volt_M_f32[0]	-9.87199974		
MtrVoltDax_Volt_M_f32[1]	13.0209999		
MtrVoltQax_Volt_M_f32[0]	2.79699993		
MtrVoltQax_Volt_M_f32[1]	2.49399996		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.479000002		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0419999994		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0299999993		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1118		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	120.931		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-3.30000544	-3.29999614 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	50.2249184	50.2249184 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.10 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-131.636002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	143.960999		
MtrVoltDax_Volt_M_f32[0]	19.8519993		
MtrVoltDax_Volt_M_f32[1]	-9.42500019		
MtrVoltQax_Volt_M_f32[0]	-21.5189991		
MtrVoltQax_Volt_M_f32[1]	30.2460003		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.933000028		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0450000018		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0219999999		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	1118		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	66.2429962		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	45.2947922	45.2948074 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	55.6719589	55.6719437 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.11 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	128.199005		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-49.973999		
MtrVoltDax_Volt_M_f32[0]	-4.33300018		
MtrVoltDax_Volt_M_f32[1]	-12.9139996		
MtrVoltQax_Volt_M_f32[0]	27.3500004		
MtrVoltQax_Volt_M_f32[1]	10.0089998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.967000008		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0469999984		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0199999996		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1010.56		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	188.177002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-27.8432713	-27.8432884 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-53.7911644	-53.7911606 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.12 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-13.3149996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-190.268997		
MtrVoltDax_Volt_M_f32[0]	13.0489998		
MtrVoltDax_Volt_M_f32[1]	8.26099968		
MtrVoltQax_Volt_M_f32[0]	-6.94999981		
MtrVoltQax_Volt_M_f32[1]	-11.8059998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.648999989		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0340000018		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0989999995		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	0		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	114.566002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-2.70378375	-2.70378375 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	3.92948556	3.92948604 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.13 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-127.372002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-119.935997		
MtrVoltDax_Volt_M_f32[0]	30.4549999		
MtrVoltDax_Volt_M_f32[1]	-8.5340004		
MtrVoltQax_Volt_M_f32[0]	28.2479992		
MtrVoltQax_Volt_M_f32[1]	-15.4549999		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.907999992		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0529999994		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0450000018		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	12.5		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	135.257004		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-5.30953932	-5.30953503 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	17.1272545	17.1272564 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.14 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	189.746994		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-111.083		
MtrVoltDax_Volt_M_f32[0]	-8.92099953		
MtrVoltDax_Volt_M_f32[1]	-24.1280003		
MtrVoltQax_Volt_M_f32[0]	10.2139997		
MtrVoltQax_Volt_M_f32[1]	-16.2560005		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0590000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0799999982		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	226.929993		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-139.737		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	0.760263801	0.760263324 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	16.3209267	16.3209286 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.15 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-150.468002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	153.128006		
MtrVoltDax_Volt_M_f32[0]	30.1329994		
MtrVoltDax_Volt_M_f32[1]	-14.7460003		
MtrVoltQax_Volt_M_f32[0]	19.8519993		
MtrVoltQax_Volt_M_f32[1]	-9.42500019		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.999984741		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0399999991		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0869999975		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-768.669983		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-77.0139999		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-8.9443779	-8.94437885 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-9.78363514	-9.78363419 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.16 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	179.229996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	20.4080009		
MtrVoltDax_Volt_M_f32[0]	-17.1889992		
MtrVoltDax_Volt_M_f32[1]	-21.1000004		
MtrVoltQax_Volt_M_f32[0]	-4.33300018		
MtrVoltQax_Volt_M_f32[1]	-12.9139996		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.856000006		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.074000001		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.074000001		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	750.690002		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	183.016006		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	38.250412	38.2504349 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	66.8344421	66.8344345 ± 0.00048828	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.17 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	145.681		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-198.222		
MtrVoltDax_Volt_M_f32[0]	19.6550007		
MtrVoltDax_Volt_M_f32[1]	0.603999972		
MtrVoltQax_Volt_M_f32[0]	13.0489998		
MtrVoltQax_Volt_M_f32[1]	8.26099968		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.261000007		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0549999997		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.115000002		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	397.470001		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-220		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-26.1354809	-26.135479 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	39.8556824	39.8556824 ± 0.00048828	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.18 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-138.488007		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-188.169006		
MtrVoltDax_Volt_M_f32[0]	-25.8899994		
MtrVoltDax_Volt_M_f32[1]	-9.69200039		
MtrVoltQax_Volt_M_f32[0]	30.4549999		
MtrVoltQax_Volt_M_f32[1]	-8.5340004		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.479999989		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0700000003		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.120999999		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-159.699997		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	220		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	55.5946236	55.5946236 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	49.569046	49.5690422 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.19 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-22.4979992		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-38.9280014		
MtrVoltDax_Volt_M_f32[0]	-21.9990005		
MtrVoltDax_Volt_M_f32[1]	-11.2440004		
MtrVoltQax_Volt_M_f32[0]	-8.92099953		
MtrVoltQax_Volt_M_f32[1]	-24.1280003		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.460999995		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0329999998		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0850000009		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	715.830017		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	126.559998		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	9.05713081	9.05712509 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-48.5318794	-48.5318794 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.20 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-22.8479996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-171.873993		
MtrVoltDax_Volt_M_f32[0]	18.2189999		
MtrVoltDax_Volt_M_f32[1]	10.0150003		
MtrVoltQax_Volt_M_f32[0]	30.1329994		
MtrVoltQax_Volt_M_f32[1]	-14.7460003		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.975000024		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0599999987		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0179999992		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	611.549988		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	0		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	18.9651871	18.9651871 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	3.27737713	3.27737594 ± 0.00048828	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.21 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.838997		
MtrCurrQaxFinalRef_Amp_M_f32[1]	99.3130035		
MtrVoltDax_Volt_M_f32[0]	8.61600018		
MtrVoltDax_Volt_M_f32[1]	18.5569992		
MtrVoltQax_Volt_M_f32[0]	-17.1889992		
MtrVoltQax_Volt_M_f32[1]	-21.1000004		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.0270000007		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0289999992		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.123999998		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-694.679993		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-210.649994		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	38.6252594	38.6252632 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	18.4046631	18.4046631 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.22 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	10.8450003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	78.3980026		
MtrVoltDax_Volt_M_f32[0]	-31		
MtrVoltDax_Volt_M_f32[1]	-31		
MtrVoltQax_Volt_M_f32[0]	19.6550007		
MtrVoltQax_Volt_M_f32[1]	0.603999972		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.927999973		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0309999995		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.125		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	276.529999		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	101.086998		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-42.0549355	-42.0549355 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	9.4221344	9.42212677 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.23 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-159.959		
MtrCurrQaxFinalRef_Amp_M_f32[1]	10.2200003		
MtrVoltDax_Volt_M_f32[0]	31		
MtrVoltDax_Volt_M_f32[1]	31		
MtrVoltQax_Volt_M_f32[0]	-25.8899994		
MtrVoltQax_Volt_M_f32[1]	-9.69200039		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.215000004		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0680000037		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0350000001		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	76.1100006		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	44.9939995		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-8.34680748	-8.34680462 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	32.2332726	32.2332726 ± 0.00048828	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.24 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	200.919998		
MtrCurrQaxFinalRef_Amp_M_f32[1]	174.417007		
MtrVoltDax_Volt_M_f32[0]	-28.3600006		
MtrVoltDax_Volt_M_f32[1]	-25.6900005		
MtrVoltQax_Volt_M_f32[0]	-4.33300018		
MtrVoltQax_Volt_M_f32[1]	-12.9139996		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.379999995		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0250000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0450000018		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	365.820007		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	61.6679993		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	7.8387332	7.83873224 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-36.5921974	-36.5921974 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.25 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-122.484001		
MtrCurrQaxFinalRef_Amp_M_f32[1]	211.005005		
MtrVoltDax_Volt_M_f32[0]	0		
MtrVoltDax_Volt_M_f32[1]	0		
MtrVoltQax_Volt_M_f32[0]	13.0489998		
MtrVoltQax_Volt_M_f32[1]	8.26099968		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.658999979		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0489999987		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0469999984		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	113.730003		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	98.3949966		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-8.31243134	-8.31243038 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	10.1112747	10.1112766 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.26 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	88.7070007		
MtrCurrQaxFinalRef_Amp_M_f32[1]	108.487999		
MtrVoltDax_Volt_M_f32[0]	25.9799995		
MtrVoltDax_Volt_M_f32[1]	10.2399998		
MtrVoltQax_Volt_M_f32[0]	30.4549999		
MtrVoltQax_Volt_M_f32[1]	-8.5340004		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.375		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0689999983		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0869999975		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	734.219971		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	171.177002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-45.7582397	-45.7582436 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-49.5160789	-49.5160866 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.27 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	200.264008		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.67499995		
MtrVoltDax_Volt_M_f32[0]	-9.53600025		
MtrVoltDax_Volt_M_f32[1]	20.9009991		
MtrVoltQax_Volt_M_f32[0]	-31		
MtrVoltQax_Volt_M_f32[1]	-31		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.222000003		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0599999987		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0799999982		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1103.18994		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	6.81699991		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	38.4061546	38.4061623 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	13.9216318	13.9216299 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.28 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-174.345993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	123.915001		
MtrVoltDax_Volt_M_f32[0]	20.3360004		
MtrVoltDax_Volt_M_f32[1]	-5.22200012		
MtrVoltQax_Volt_M_f32[0]	31		
MtrVoltQax_Volt_M_f32[1]	31		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.00700000022		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0280000009		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0979999974		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	283.220001		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	13.2919998		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-5.7920208	-5.7920208 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-12.8201752	-12.8201752 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.29 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	190.841003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-181.727997		
MtrVoltDax_Volt_M_f32[0]	13.4899998		
MtrVoltDax_Volt_M_f32[1]	-24.4689999		
MtrVoltQax_Volt_M_f32[0]	-21.3600006		
MtrVoltQax_Volt_M_f32[1]	-29.6900005		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.995000005		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0320000015		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0790000036		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	907.409973		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	200.867004		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	1.74072468	1.74073541 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	63.4303246	63.4303207 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.30 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	135.572006		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-60.6110001		
MtrVoltDax_Volt_M_f32[0]	2.79699993		
MtrVoltDax_Volt_M_f32[1]	2.49399996		
MtrVoltQax_Volt_M_f32[0]	0		
MtrVoltQax_Volt_M_f32[1]	0		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.537		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.057		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.125		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	576.669983		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-16.8379993		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	1.86064625	1.86064291 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-26.595644	-26.5956459 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.31 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-30.0400009		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.0149994		
MtrVoltDax_Volt_M_f32[0]	-21.5189991		
MtrVoltDax_Volt_M_f32[1]	30.2460003		
MtrVoltQax_Volt_M_f32[0]	9.97999954		
MtrVoltQax_Volt_M_f32[1]	23.2399998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.0410000011		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0280000009		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0130000003		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-41.7599983		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	188.953995		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-19.9393387	-19.9393406 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-17.1327972	-17.1327972 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.32 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220		
MtrVoltDax_Volt_M_f32[0]	27.3500004		
MtrVoltDax_Volt_M_f32[1]	10.0089998		
MtrVoltQax_Volt_M_f32[0]	5.57999992		
MtrVoltQax_Volt_M_f32[1]	14.0050001		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.195999995		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0680000037		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0120000001		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	887.52002		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	13.9949999		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-38.2654343	-38.2654266 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	23.965292	23.9652901 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.33 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	220		
MtrVoltDax_Volt_M_f32[0]	-6.94999981		
MtrVoltDax_Volt_M_f32[1]	-11.8059998		
MtrVoltQax_Volt_M_f32[0]	-17.2469997		
MtrVoltQax_Volt_M_f32[1]	-29.434		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.236000001		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0599999987		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0280000009		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	945.109985		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	65.5070038		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-79.7318726	-79.7318726 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-1.53988278	-1.53987384 ± 0.00048828	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.34 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-115.349998		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-56.8400002		
MtrVoltDax_Volt_M_f32[0]	28.2479992		
MtrVoltDax_Volt_M_f32[1]	-15.4549999		
MtrVoltQax_Volt_M_f32[0]	24.5849991		
MtrVoltQax_Volt_M_f32[1]	-1.83800006		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.407000005		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0480000004		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0579999983		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1058.32996		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-170.300003		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	34.340477	34.340477 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	39.4124794	39.4124794 ± 0.00048828	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.35 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	0		
MtrCurrQaxFinalRef_Amp_M_f32[1]	0		
MtrVoltDax_Volt_M_f32[0]	10.2139997		
MtrVoltDax_Volt_M_f32[1]	-16.2560005		
MtrVoltQax_Volt_M_f32[0]	27.3500004		
MtrVoltQax_Volt_M_f32[1]	10.0089998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.779999971		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0610000007		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0790000036		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	781.340027		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	37.9490013		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✓
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	21.3793755	21.3793736 ± 0.00048828	✓
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-3.67670774	-3.67671037 ± 0.00048828	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530



PICurrCntrl_Per2

Test Step 1.36 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	95.4800034		
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.419998		
MtrVoltDax_Volt_M_f32[0]	23.3439999		
MtrVoltDax_Volt_M_f32[1]	11.6280003		
MtrVoltQax_Volt_M_f32[0]	-6.94999981		
MtrVoltQax_Volt_M_f32[1]	-11.8059998		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.462000012		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.050999999		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.120999999		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	202.050003		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	-136.925003		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-33.42799	-33.4279938 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-27.4988766	-27.4988689 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

Test Step 1.37 (Repeat Count = 1)

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-22.8479996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-171.873993		
MtrVoltDax_Volt_M_f32[0]	20.3360004		
MtrVoltDax_Volt_M_f32[1]	-5.22200012		
MtrVoltQax_Volt_M_f32[0]	5.57999992		
MtrVoltQax_Volt_M_f32[1]	14.0050001		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	0		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.927999973		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0280000009		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0120000001		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	734.219971		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	98.3949966		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	1	1	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	23.8140182	23.8140163 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	4.81566048	4.81566477 ± 0.00048828	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:39+0530

PICurrCntrl_Per2



Test Step 1.38 (Repeat Count = 1)



Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.838997		
MtrCurrQaxFinalRef_Amp_M_f32[1]	99.3130035		
MtrVoltDax_Volt_M_f32[0]	13.4899998		
MtrVoltDax_Volt_M_f32[1]	-24.4689999		
MtrVoltQax_Volt_M_f32[0]	-17.2469997		
MtrVoltQax_Volt_M_f32[1]	-29.434		
Rte_Inst_Ap_PICurrCntrl	target_Rte_Inst_Ap_PICurrCntrl		
SlowDataAccessBufIndex_Cnt_M_u16	1		
target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32.value	0.215000004		
target_PICurrCntrl_Per2_EstKe_VpRadpS_f32.value	0.0320000015		
target_PICurrCntrl_Per2_EstR_Ohm_f32.value	0.0280000009		
target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32.value	-1103.18994		
target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32.value	171.177002		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32	target_PICurrCntrl_Per2_CorrMtrPosElec_Rev_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32	target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstKe_VpRadpS_f32	target_PICurrCntrl_Per2_EstKe_VpRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_EstR_Ohm_f32	target_PICurrCntrl_Per2_EstR_Ohm_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32	target_PICurrCntrl_Per2_MRFMtrVel_MtrRadpS_f32		
target_Rte_Inst_Ap_PICurrCntrl.PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32	target_PICurrCntrl_Per2_MtrCurrDaxRef_Amp_f32		
Name	Actual Value	Expected Value	Result
SlowDataAccessBufIndex_Cnt_M_u16	0	0	✔
target_PICurrCntrl_Per2_DervLambdaAlphaDiag_Volt_f32.value	-2.86667562	-2.86667776 ± 0.00048828	✔
target_PICurrCntrl_Per2_DervLambdaBetaDiag_Volt_f32.value	-28.6853104	-28.6853104 ± 0.00048828	✔

Test Step Call Trace



Actual Function	Count	Expected Function	Count	Result
Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP0_CheckpointReached	1	✓
Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	Rte_Call_PICurrCntrl_Per2_CP1_CheckpointReached	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:00+0530

PICurrCntrl_Init



Project	Ap_PICurrCntrl
Module	PICurrCntrl
Test Object	PICurrCntrl_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Plicurrentcontrol
Configuration File	D:\Synergy_Work_Area\Plicurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS=-D_sqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D_sqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'PICurrCntrl'	*****Unit Test Information***** Name of Tester: Priyanka Shitole Code File(s) Under Test: Ap_PICurrCntrl.c Code File(s) Version: 15 Module Design Document: PlicurrentCntrl.doc Module Design Document Version: 11 Data Dictionary Version: 14 Unit Test Plan Version: 3 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): NA Total RAM Used (Bytes): NA Total CALS Used (Bytes): NA Special Test Requirements: Test Date: 4/22/2016 Comments: "Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2 : Out of range value is given in function ""LoaMtgtnScfFac "" for variables ""k_CLOAFdbackSignalScfFacSlew_UlspS_f32,k_ILoAFdbackSignalScfFacSlew_UlspS_f32, PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 and PICurrCntrl_InverterFailScfFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PICurrCntrl_Per1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

2016-04-22, 16:28:00+0530

PICurrCntrl_Init



Test Case 1: BoundaryTest

Description Vector Description:

TS1.1All Min
TS1.2All Max
TS1.3k_MtrVoltVecuFiltKn_Hz_f32==>Min
TS1.4k_MtrVoltVecuFiltKn_Hz_f32==>Max
TS1.5k_MtrVoltVecuFiltKn_Hz_f32==>Pos
TS1.6k_MtrVoltVecuFiltKn_Hz_f32==>Def
TS1.7k_PiSamplingTs_Sec_f32==>Min
TS1.8k_PiSamplingTs_Sec_f32==>Max
TS1.9k_PiSamplingTs_Sec_f32==>Pos/Def
TS1.10k_MtrVoltQaxFiltFFKn_Hz_f32==>Min
TS1.11k_MtrVoltQaxFiltFFKn_Hz_f32==>Max
TS1.12k_MtrVoltQaxFiltFFKn_Hz_f32==>Pos
TS1.13k_MtrVoltQaxFiltFFKn_Hz_f32==>Def

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	0.100000001		
k_MtrVoltVecuFiltKn_Hz_f32	0.100000001		
k_PiSamplingTs_Sec_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50928.5781	50928.582	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.963457e-005	1.96345682e-005	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50928.5781	50928.582	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.963457e-005	1.96345682e-005	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CallLowPassFiltBilinearTerm	2	CallLowPassFiltBilinearTerm	2	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	1000		
k_MtrVoltVecuFiltKn_Hz_f32	1000		
k_PiSamplingTs_Sec_f32	0.100000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.996816874	-0.996816874	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996827006	0.996827006	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996816874	-0.996816874	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996827006	0.996827006	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CallLowPassFiltBilinearTerm	2	CallLowPassFiltBilinearTerm	2	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	709.776001		
k_MtrVoltVecuFiltKn_Hz_f32	0.100000001		
k_PiSamplingTs_Sec_f32	0.100000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	30.830986	30.8309879	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0304590296	0.0304590277	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.995515347	-0.995515347	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.995535374	0.995535374	✓

TEST DETAILS REPORT

2016-04-22, 16:28:00+0530

PICurrCntrl_Init



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.4 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	45.4935989		
k_MtrVoltVecuFiltKn_Hz_f32	1000		
k_PiSamplingTs_Sec_f32	0.100000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✔
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.996816874	-0.996816874	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996827006	0.996827006	✔
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.930031955	-0.930031955	✔
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.934607327	0.934607327	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.5 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	414.50531		
k_MtrVoltVecuFiltKn_Hz_f32	59.2360001		
k_PiSamplingTs_Sec_f32	0.100000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.946264148	-0.946264088	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.949004412	0.949004412	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.992320716	-0.992320716	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.992379248	0.992379248	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.6 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	863.495728		
k_MtrVoltVecuFiltKn_Hz_f32	200		
k_PiSamplingTs_Sec_f32	0.100000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✔
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.984084487	-0.984084487	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.984333813	0.984333813	✔
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996313691	-0.996313691	✔
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996327221	0.996327221	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.7 (Repeat Count = 1)

Name	Input Value
k_MtrVoltQaxFiltFFKn_Hz_f32	791.291382
k_MtrVoltVecuFiltKn_Hz_f32	289.923096

TEST DETAILS REPORT

2016-04-22, 16:28:00+0530



PICurrCntrl_Init

Name	Input Value		
k_PiSamplingTs_Sec_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5665817	16.5665836	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0538602099	0.0538602062	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	5.4362607	5.43626118	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.134476185	0.134476185	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.8 (Repeat Count = 1)				
Name	Input Value			
k_MtrVoltQaxFiltFFKn_Hz_f32	801.490417			
k_MtrVoltVecuFiltKn_Hz_f32	302.319794			
k_PiSamplingTs_Sec_f32	0.100000001			
Name	Actual Value	Expected Value	Result	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.989471078	-0.989471078	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.98958081	0.98958081	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996028543	-0.996028543	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996044219	0.996044219	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.9 (Repeat Count = 1)				
Name	Input Value			
k_MtrVoltQaxFiltFFKn_Hz_f32	325.598114			
k_MtrVoltVecuFiltKn_Hz_f32	775.537415			
k_PiSamplingTs_Sec_f32	0.000125000006			
Name	Actual Value	Expected Value	Result	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.28350234	2.28350258	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.233453825	0.23345381	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.82092667	6.82092714	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.113366775	0.113366768	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.10 (Repeat Count = 1)				
Name	Input Value			
k_MtrVoltQaxFiltFFKn_Hz_f32	0.100000001			
k_MtrVoltVecuFiltKn_Hz_f32	14.1302996			
k_PiSamplingTs_Sec_f32	0.0988999978			
Name	Actual Value	Expected Value	Result	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.77222687	-0.77222687	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.814482749	0.814482749	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	31.1850243	31.1850243	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0301340744	0.0301340744	✓	

TEST DETAILS REPORT

2016-04-22, 16:28:00+0530

PICurrCntrl_Init



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.11 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	1000		
k_MtrVoltVecuFiltKn_Hz_f32	761.508179		
k_PiSamplingTs_Sec_f32	0.0340999998		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.987741947	-0.987741947	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.987890422	0.987890422	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.990665376	-0.990665376	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.990751743	0.990751743	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.12 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	213.235001		
k_MtrVoltVecuFiltKn_Hz_f32	815.32312		
k_PiSamplingTs_Sec_f32	0.0340999998		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.988551021	-0.988551021	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.988680661	0.988680661	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.956223905	-0.956223905	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.958059847	0.958059847	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

Test Step 1.13 (Repeat Count = 1)

Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	500		
k_MtrVoltVecuFiltKn_Hz_f32	815.32312		
k_PiSamplingTs_Sec_f32	0.0302000009		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✔
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.987072527	-0.987072527	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.987237513	0.987237513	✔
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.978919864	-0.978919864	✔
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.979355097	0.979355097	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	✓

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530

CalLowPassFiltBilinearOut



Project	Ap_PICurrCntl
Module	PICurrCntl
Test Object	CalLowPassFiltBilinearOut

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Picurrentcontrol
Configuration File	D:\Synergy_Work_Area\Picurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntl.c
Compiler Options	-D_DATA_ACCESS=-D_sqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D_sqrtf=sqrtf -Dconst= -I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'PICurrCntl'	*****Unit Test Information***** Name of Tester: Priyanka Shitole Code File(s) Under Test: Ap_PICurrCntl.c Code File(s) Version: 15 Module Design Document: PICurrentCntl.doc Module Design Document Version: 11 Data Dictionary Version: 14 Unit Test Plan Version: 3 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): NA Total RAM Used (Bytes): NA Total CALS Used (Bytes): NA Special Test Requirements: Test Date: 4/22/2016 Comments: "Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2 : Out of range value is given in function ""LoaMtgtnScfFac "" for variables ""k_CLOAFdbackSignalScfFacSlew_UlspS_f32,k_ILOAFdbackSignalScfFacSlew_UlspS_f32, PICurrCntl_CurrSensFailScfFac_Uls_M_f32 and PICurrCntl_InverterFailScfFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PICurrCntl_Per1 PICurrCntl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530

CalLowPassFiltBilinearOut



Test Case 1: BoundaryTest

Description Vector Description:

TS1.1All Min
TS1.2All Max
TS1.3Input_Uls_T_f32=Min
TS1.4Input_Uls_T_f32=Max
TS1.5Input_Uls_T_f32=Zero
TS1.6Input_Uls_T_f32=Pos
TS1.7Input_Uls_T_f32=Neg
TS1.8LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Min
TS1.9LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Max
TS1.10LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Zero
TS1.11LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Pos
TS1.12LowPassFiltBilinear_T_Str.PrevInput_Uls_f32=Neg
TS1.13LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Min
TS1.14LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Max
TS1.15LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Zero
TS1.16LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Pos
TS1.17LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32=Neg
TS1.18LowPassFiltBilinear_T_Str.TermN_Uls_f32=Min
TS1.19LowPassFiltBilinear_T_Str.TermN_Uls_f32=Max
TS1.20LowPassFiltBilinear_T_Str.TermN_Uls_f32=Zero
TS1.21LowPassFiltBilinear_T_Str.TermN_Uls_f32=Pos
TS1.22LowPassFiltBilinear_T_Str.TermN_Uls_f32=Neg
TS1.23LowPassFiltBilinear_T_Str.TermD_Uls_f32=Min
TS1.24LowPassFiltBilinear_T_Str.TermD_Uls_f32=Max
TS1.25LowPassFiltBilinear_T_Str.TermD_Uls_f32=Zero
TS1.26LowPassFiltBilinear_T_Str.TermD_Uls_f32=Pos
TS1.27LowPassFiltBilinear_T_Str.TermD_Uls_f32=Neg

Test Step 1.1 (Repeat Count = 1)

Name	Input Value	Expected Value	Result
Input_Uls_T_f32	-2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-2.14748365e+009		
CalLowPassFiltBilinearOut()	-9.90352031e+027	-9.90352031e+027	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009	-2.14748365e+009	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-9.90352031e+027	-9.90352031e+027	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value	Expected Value	Result
Input_Uls_T_f32	2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	2.14748365e+009		
CalLowPassFiltBilinearOut()	9.90352031e+027	9.90352031e+027	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009	2.14748365e+009	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value
Input_Uls_T_f32	-2.14748365e+009
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	153.836899

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530



CalLowPassFiltBilinearOut

Name	Input Value		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	8286.36523		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1000.22357		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	5.5236001		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-1.18160589e+010	-1.18160589e+010	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009	-2.14748365e+009	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1.18160589e+010	-1.18160589e+010	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.4 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	42.2523003		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	123.3657		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1050.87891		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	99.9235992		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.14597255e+011	2.14597255e+011	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009	2.14748365e+009	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14597255e+011	2.14597255e+011	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.5 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	0		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-895.362122		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-89.7412033		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	85.9630966		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-5.36210012		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	46166.5781	46166.5742	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	0	0	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	46166.5781	46166.5742	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.6 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	2546.15991		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	258.325989		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	423.574005		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1101.47412		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1283.396		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	602375104	602375104	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2546.15991	2546.15991	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	602375104	602375104	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530

CalLowPassFiltBilinearOut



Test Step 1.7 (Repeat Count = 1) ✓

Name	Input Value		
Input_Uls_T_f32	-2546.15991		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	58.3260002		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	423.574005		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1101.47412		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1283.396		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	595582976	595582976	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2546.15991	-2546.15991	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	595582976	595582976	✔

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.8 (Repeat Count = 1) ✓

Name	Input Value		
Input_Uls_T_f32	252.320999		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	724.321228		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1152.31238		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	6845.52148		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-1.46949297e+013	-1.46949297e+013	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	252.320999	252.320999	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1.46949297e+013	-1.46949297e+013	✔

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.9 (Repeat Count = 1) ✓

Name	Input Value		
Input_Uls_T_f32	356.623108		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1024.547		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	5000.3208		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	12407.3613		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.67081744e+013	2.67081744e+013	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	356.623108	356.623108	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.67081744e+013	2.67081744e+013	✔

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530

CalLowPassFiltBilinearOut



Test Step 1.10 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	52.3652		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	0		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-86.3150024		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	546.398682		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-785.632019		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	37011156	37011152	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	52.3652	52.3652	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	37011156	37011152	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.11 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	11423.2314		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	3582.41992		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1325.12305		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1253.22302		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	17969.7402		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	3.01115412e+010	3.01115412e+010	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	11423.2314	11423.2314	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	3.01115412e+010	3.01115412e+010	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.12 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	11423.2314		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-3582.41992		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1325.12305		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1253.22302		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	17969.7402		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.99827896e+010	2.99827896e+010	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	11423.2314	11423.2314	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.99827896e+010	2.99827896e+010	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.13 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	286.321014		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	12.5235996		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1303.82361		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	23531.3203		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-6.58862801e+016	-6.58862844e+016	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	286.321014	286.321014	✔

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530



CalLowPassFiltBilinearOut

Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-6.58862801e+016	-6.58862844e+016	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.14 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	1123.36523		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	102.823601		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1354.42297		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	29093.4473		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	8.46212324e+016	8.4621241e+016	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	1123.36523	1123.36523	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	8.46212324e+016	8.4621241e+016	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.15 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	563.231018		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-5657.51416		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	0		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-73.3619995		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-89.6539993		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	456722.844	456722.875	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	563.231018	563.231018	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	456722.844	456722.875	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.16 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	968.314514		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-93.1122971		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	525.830017		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1405.12415		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	34655.3242		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.56356413e+010	2.56356413e+010	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	968.314514	968.314514	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.56356413e+010	2.56356413e+010	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530

CalLowPassFiltBilinearOut



Test Step 1.17 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	968.314514		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	193.112305		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1525.82996		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1405.12415		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	34655.3242		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-7.42600868e+010	-7.4260095e+010	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	968.314514	968.314514	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-7.42600868e+010	-7.4260095e+010	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.18 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	12.3512001		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	283.423615		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.4230001		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	40217.2891		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-2.09264749e+014	-2.09264732e+014	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	12.3512001	12.3512001	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-2.09264749e+014	-2.09264732e+014	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.19 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	-88.9124527		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	373.723114		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	56.7743988		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	45779.3203		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	5.58151079e+015	5.58151079e+015	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-88.9124527	-88.9124527	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	5.58151079e+015	5.58151079e+015	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.20 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	-12.3620005		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	56.1230011		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	0		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-741.236511		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	67691.2031	67691.2031	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-12.3620005	-12.3620005	✓

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530



CalLowPassFiltBilinearOut

Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	67691.2031	67691.2031	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.21 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	753.745605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	464.321014		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	111.321404		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	990.090027		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	51341.1641		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	5.72126822e+009	5.72126771e+009	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	753.745605	753.745605	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	5.72126822e+009	5.72126771e+009	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.22 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	753.745605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	464.321014		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	111.321404		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-89.6500015		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	51341.1641		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-449846016	-449845984	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	753.745605	753.745605	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-449846016	-449845984	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.23 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	186.523605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	554.330017		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	165.357407		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	86.6320038		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-3.23541963e+013	-3.23541942e+013	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	186.523605	186.523605	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-3.23541963e+013	-3.23541942e+013	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530



CalLowPassFiltBilinearOut

Test Step 1.24 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	32.5564003		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	644.63208		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	219.654694		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	3.32200003		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	3.02125469e+012	3.02125495e+012	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	32.5564003	32.5564003	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	3.02125469e+012	3.02125495e+012	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.25 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	-89.6320038		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-89.471199		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-78.9599991		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	0	0	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-89.6320038	-89.6320038	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.26 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	236.350006		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	99999.3203		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1000.32098		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	78.4514008		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	10250.0703		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	1.83181312e+009	1.83181312e+009	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	236.350006	236.350006	✔
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1.83181312e+009	1.83181312e+009	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.27 (Repeat Count = 1)

Name	Input Value		
Input_Uls_T_f32	-78.9599991		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	125.629997		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	86.7409973		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	78.4514008		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-20.0699997		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-137512.063	-137512.078	✔
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991	-78.9599991	✔

TEST DETAILS REPORT

2016-04-22, 16:26:59+0530



CalLowPassFiltBilinearOut

Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-137512.063	-137512.078	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0		✓

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530

CalLowPassFiltVecuOut



Project	Ap_PICurrCntrl
Module	PICurrCntrl
Test Object	CalLowPassFiltVecuOut

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Picurrentcontrol
Configuration File	D:\Synergy_Work_Area\Picurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS=-D__sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D__sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'PICurrCntrl'	*****Unit Test Information***** Name of Tester: Priyanka Shitole Code File(s) Under Test: Ap_PICurrCntrl.c Code File(s) Version: 15 Module Design Document: PICurrentControl.doc Module Design Document Version: 11 Data Dictionary Version: 14 Unit Test Plan Version: 3 Optimization Level: 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): NA Total RAM Used (Bytes): NA Total CALS Used (Bytes): NA Special Test Requirements: Test Date: 4/22/2016 Comments: "Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: Out of range value is given in function ""LoaMtgtnScfFac"" for variables ""k_CLOAFdbckSignalScfFacSlew_UlspS_f32, k_LOAFdbckSignalScfFacSlew_UlspS_f32, PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 and PICurrCntrl_InverterFailScfFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PICurrCntrl_Per1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range." *****

Attributes

Name	Value
CTE File	\$(PROJECTROOT)\tessy\persist\tessy\project\0000055e\00002645\database\tdb\00002659\CalLowPassFiltVecuOut.cte
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Float Precision	9
-----------------	---

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530

CalLowPassFiltVecuOut



Test Case 1: Metric Test

Description Vector Description:

TS 1.1 Shortest Path==>(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE)=True&&(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32)=True
TS 1.2 Longest Path==>(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32)=False&&&(*Vecu_Volt_T_f32<=D_VECUMIN_VOLTS_F32)=True

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009	2.14748365e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	360171232		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	5.23559999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	5.23559999	5.23559999	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-6.48613864e+026	-6.48614011e+026	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472	-954881024	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009	1.88593997e+009	✔
target_Vecu_Volt_T_f32	5	5	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530

CalLowPassFiltVecuOut



Test Case 2: Boundary Test

Description Vector Description:

TS 2.1All Min
TS 2.2All Max
TS 2.3k_MtrVoltVecuFiltEnable_Cnt_lgc => min
TS 2.4k_MtrVoltVecuFiltEnable_Cnt_lgc => max
TS 2.5Vecu_Volt1_T_f32 =>min
TS 2.6Vecu_Volt1_T_f32 =>max
TS 2.7Vecu_Volt1_T_f32 =>mid
TS 2.8PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => min
TS 2.9PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => max
TS 2.10PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => zero
TS 2.11PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => pos
TS 2.12PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => neg
TS 2.13PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 =>min
TS 2.14PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 =>max
TS 2.15PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 =>zero
TS 2.16PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 =>pos
TS 2.17PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 =>neg
TS 2.18PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>min
TS 2.19PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>max
TS 2.20PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>zero
TS 2.21PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>pos
TS 2.22PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>neg
TS 2.23PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>min
TS 2.24PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32=>max
TS 2.25PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>zero
TS 2.26PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>pos
TS 2.27PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>neg

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
target_Vecu_Volt_T_f32	5	5	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009	2.14748365e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530

CalLowPassFiltVecuOut



Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	6.12529993		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009	1.25422003e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009	-1.78840998e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009	2.03557005e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992	-453076992	✔
target_Vecu_Volt_T_f32	6.12529993	6.12529993	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.59821005e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.50935002e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-334438016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.17052006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	7.23559999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	7.23559999	7.23559999	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	5.90859838e+026	5.90859986e+026	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-334438016	-334438016	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.17052006e+009	1.17052006e+009	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.5 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.30338995e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-526129984		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.46070003e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.44466995e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.30338995e+009	1.30338995e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-526129984	-526129984	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.46070003e+009	1.46070003e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.44466995e+009	1.44466995e+009	✔
target_Vecu_Volt_T_f32	5	5	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.6 (Repeat Count = 1)

Name	Input Value
PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.93612864e+009

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.5751543e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	255735808		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.20936077e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	4.87158773e+026	4.87158994e+026 ± 9E+20	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	255735808	255736000	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.20936077e+009	1.20936e+009	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.7 (Repeat Count = 1)				✔
Name		Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		1.44801997e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		-2.04112998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		-608366976		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		-1.91424998e+009		
Vecu_Volt_T_f32		target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc		0		
target_Vecu_Volt_T_f32		23.0214005		
Name		Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		1.44801997e+009	1.44801997e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		-2.04112998e+009	-2.04112998e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		-608366976	-608366976	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		-1.91424998e+009	-1.91424998e+009	✔
target_Vecu_Volt_T_f32		23.0214005	23.0214005	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.8 (Repeat Count = 1)				✓
Name		Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		360171232		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		-954881472		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		1.88593933e+009		
Vecu_Volt_T_f32		target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc		1		
target_Vecu_Volt_T_f32		5.23559999		
Name		Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		5.23559999	5.23559999	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		-6.48613864e+026	-6.48614011e+026	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		-954881472	-954881024	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		1.88593933e+009	1.88593997e+009	✓
target_Vecu_Volt_T_f32		5	5	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.9 (Repeat Count = 1)		
Name	Input Value	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748006e+009	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.26342003e+009	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	552748032	
PICurrCntrl_MtrVecuFilt_M str.TermD_Uls_f32	-256136992	

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
target_Vecu_Volt_T_f32	12.3255997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748006e+009	2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.26342003e+009	-1.26342003e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	552748032	552748032	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-256136992	-256136992	✔
target_Vecu_Volt_T_f32	12.3255997	12.3255997	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.10 (Repeat Count = 1) ✓

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	6524180		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20154800		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	231546		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
target_Vecu_Volt_T_f32	7.25360012		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0	0	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	6524180	6524180	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20154800	20154800	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	231546	231546	✔
target_Vecu_Volt_T_f32	7.25360012	7.25360012	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.11 (Repeat Count = 1) ✓

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	32145700		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	12365500		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23651500		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12546.2998		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
target_Vecu_Volt_T_f32	8.21450043		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	8.21450043	8.21450043	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	3.66932429e+018	3.66931989e+018 ± 9000000000000	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23651500	23651500	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12546.2998	12546.2998	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530

CalLowPassFiltVecuOut



Test Step 2.12 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-96547904		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63254200		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	11254700		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	96321		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	9.32559967		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-96547904	-96547904	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63254200	63254200	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	11254700	11254700	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	96321	96321	✔
target_Vecu_Volt_T_f32	9.32559967	9.32559967	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.13 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.75868147e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.92536422e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	795439744		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	5.21449995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	5.21449995	5.21449995	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	3.28889538e+027	3.28890011e+027 ± 9E+21	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.92536422e+009	-1.92536422e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	795439744	795440000	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.14 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	91816200		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.02622003e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-237474000		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	13.3255997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	13.3255997	13.3255997	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.03331282e+027	1.03331001e+027 ± 9E+21	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.02622003e+009	-2.02622003e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-237474000	-237474000	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.15 (Repeat Count = 1)

Name	Input Value
PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-465380000

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.63317005e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.11672e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.30057997e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	8.12450027		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-465380000	-465380000	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.63317005e+009	1.63317005e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.11672e+009	-1.11672e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.30057997e+009	-1.30057997e+009	✔
target_Vecu_Volt_T_f32	8.12450027	8.12450027	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.16 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2543700		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63527100		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	152639		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2543700	2543700	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63527100	63527100	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008	21456.3008	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	152639	152639	✔
target_Vecu_Volt_T_f32	5.32560015	5.32560015	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.17 (Repeat Count = 1)

Name		Input Value	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		96412600	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		-65981500	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		25634	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		3654.02002	
Vecu_Volt_T_f32		target_Vecu_Volt_T_f32	
k_MtrVoltVecuFiltEnable_Cnt_lgc		1	
target_Vecu_Volt_T_f32		6.21460009	
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.21460009	6.21460009	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-6.17994684e+015	-6.17995006e+015	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25634	25634	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	3654.02002	3654.02002	✓
target_Vecu_Volt_T_f32	5	5	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.18 (Repeat Count = 1)

Name	Input Value
PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.084e+009
PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.28743002e+009
PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009
PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-302148992

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.32560015	6.32560015	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	8.35360436e+026	8.35359994e+026	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-302148992	-302148992	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.19 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.41749005e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-721043968		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.06957005e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
target_Vecu_Volt_T_f32	22.1424999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.41749005e+009	-1.41749005e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-721043968	-721043968	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748006e+009	2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.06957005e+009	2.06957005e+009	✔
target_Vecu_Volt_T_f32	22.1424999	22.1424999	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.20 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-45511800		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	758132992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.75474995e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.26762995e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
target_Vecu_Volt_T_f32	6.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.32560015	6.32560015	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.68637094e+027	-1.68637006e+027	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.75474995e+009	1.75474995e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.26762995e+009	-1.26762995e+009	✔
target_Vecu_Volt_T_f32	5	5	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530

CalLowPassFiltVecuOut



Test Step 2.21 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	65478900		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1235700		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124563		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	7.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	65478900	65478900	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1235700	1235700	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0	0	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124563	124563	✔
target_Vecu_Volt_T_f32	7.32560015	7.32560015	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.22 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	23564200		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	65987400		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-12456.2998		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	132645		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	6.21449995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	23564200	23564200	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	65987400	65987400	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-12456.2998	-12456.2998	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	132645	132645	✔
target_Vecu_Volt_T_f32	6.21449995	6.21449995	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.23 (Repeat Count = 1)

Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-980435008
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	991660032
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-416540992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
target_Vecu_Volt_T_f32	11.2563

Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-980435008	-980435008	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	991660032	991660032	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-416540992	-416540992	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	-2.14748006e+009	✓
target_Vecu_Volt_T_f32	11.2563	11.2563	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.24 (Repeat Count = 1)

Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.12730099e+009

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.10160973e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.93757453e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	30.2355995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	30.2355995	30.2355995	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-8.7446079e+027	-8.74461026e+027	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.93757453e+009	1.93757453e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	✔
target_Vecu_Volt_T_f32	5	5	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.25 (Repeat Count = 1)				✔
Name		Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		-2.06284006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		1.40768998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		1.76896e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		12455600		
Vecu_Volt_T_f32		target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc		0		
target_Vecu_Volt_T_f32		5.21449995		
Name		Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		-2.06284006e+009	-2.06284006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		1.40768998e+009	1.40768998e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		1.76896e+009	1.76896e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		12455600	12455600	✔
target_Vecu_Volt_T_f32		5.21449995	5.21449995	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.26 (Repeat Count = 1)				✔
Name		Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		112563		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		1223650		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		124556		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		0		
Vecu_Volt_T_f32		target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc		1		
target_Vecu_Volt_T_f32		6.32567978		
Name		Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32		6.32567978	6.32567978	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		0	0	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32		124556	124556	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32		0	0	✔
target_Vecu_Volt_T_f32		5	5	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

Test Step 2.27 (Repeat Count = 1)		✔
Name	Input Value	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	325648	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	302145	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124540000	

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530



CalLowPassFiltVecuOut

Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	11.2546997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	325648	325648	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	302145	302145	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008	21456.3008	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124540000	124540000	✔
target_Vecu_Volt_T_f32	11.2546997	11.2546997	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Case 3: Path Test

Description	Vector Description: TS 3.1(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE)=False&&(*Vecu_Volt_T_f32>= D_VECUMIN_VOLTS_F32)=False&&(*Vecu_Volt_T_f32<= D_VECUMIN_VOLTS_F32)=True TS 3.2(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE)=True&&(*Vecu_Volt_T_f32>= D_VECUMIN_VOLTS_F32)=True TS 3.3(*Vecu_Volt_T_f32<= D_VECUMIN_VOLTS_F32)=False
-------------	--

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	-2.14748006e+009	✔
target_Vecu_Volt_T_f32	5	5	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	✔
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	✔
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009	2.14748365e+009	✔
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	✔
target_Vecu_Volt_T_f32	31	31	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓

TEST DETAILS REPORT

2016-04-22, 16:27:33+0530

CalLowPassFiltVecuOut



Test Step 3.3 (Repeat Count = 1)

Name	Input Value	Expected Value	Result
PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009	1.25422003e+009	✓
PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009	-1.78840998e+009	✓
PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009	2.03557005e+009	✓
PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992	-453076992	✓
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32	6.12529993	✓
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	6.12529993		
Name	Actual Value	Expected Value	Result
PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009	1.25422003e+009	✓
PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009	-1.78840998e+009	✓
PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009	2.03557005e+009	✓
PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992	-453076992	✓
target_Vecu_Volt_T_f32	6.12529993	6.12529993	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530

PICurrCntrl_Per1



Project	Ap_PICurrCntrl
Module	PICurrCntrl
Test Object	PICurrCntrl_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Plicurrentcontrol
Configuration File	D:\Synergy_Work_Area\Plicurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS=-D__sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D__sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'PICurrCntrl'	*****Unit Test Information***** Name of Tester: Priyanka Shitole Code File(s) Under Test: Ap_PICurrCntrl.c Code File(s) Version: 15 Module Design Document: PlicurrentControl.doc Module Design Document Version: 11 Data Dictionary Version: 14 Unit Test Plan Version: 3 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): NA Total RAM Used (Bytes): NA Total CALS Used (Bytes): NA Special Test Requirements: Test Date: 4/22/2016 Comments: "Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2 : Out of range value is given in function ""LoaMtgtnScfFac"" for variables ""k_CLOAFdbackSignalScfFacSlew_UlspS_f32,k_ILOAFdbackSignalScfFacSlew_UlspS_f32, PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 and PICurrCntrl_InverterFailScfFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PICurrCntrl_Per1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530

PICurrCntl_Per1



Test Case 1: Metric Test

Description Vector Description:

TS 1.1Longest Path==>(k_MtrCurrQaxRefModifRplEn_Cnt_Igc ==
TRUE)=False&&(MtrCurrQaxRefModif_Amp_T_f32>220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<-220)=False&&(MtrCurrQaxFinalRef_Amp_
== TRUE)=False&&(k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc == FALSE)=False&&(k_MtrVoltQaxFiltFFEnable_Cnt_Igc==TRUE)
=True&&(IvtrLoaMtgtnEn_Cnt_T_Igc==FALSE) =True&&(MotCurrLoaMtgtnEn_Cnt_T_Igc==FALSE)
=True&&(k_MtrCtrlFeedbackControlDisable_Cnt_Igc == FALSE)=True&&(VoltSatrRatio_Uls_T_f32 >
D_ONE_ULS_F32)=True&&(PhaseAdvanceFinal_Rad_T_f32 <
D_ZERO_ULS_F32)=True&&(ModIdxSrlComSvcDft_Cnt_T_Igc==TRUE)=True&&(k_MtrCurrQaxRefModifDsb_Cnt_Igc == FALSE)=False
TS 1.2Shortest
Path==>(MtrCurrQaxRefModif_Amp_T_f32>=220)=True&&(MtrCurrQaxFinalRef_Amp_T_f32>=220)=True&&(MtrCurrQaxIntg_Volt_T_f32>=k_Mtr
==
TRUE)=True&&(k_MtrVoltQaxFiltFFEnable_Cnt_Igc==TRUE)=False&&(ModIdxSrlComSvcDft_Cnt_T_Igc==TRUE)=False&&(k_MtrCurrQaxRefMc
== FALSE)=True

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0370000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80200005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.740999997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-489.436005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	938.341003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0199999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10899997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.479999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	916.997009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1002.97998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.1960001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-2.83699989
MtrCtrl_Vecu_Volt_M_f32[0]	5.33099985
MtrCtrl_Vecu_Volt_M_f32[1]	7.69099998
MtrCurrDaxPrevIntg_Volt_M_f32	6.17600012
MtrCurrDaxRef_Amp_M_f32[0]	-146.173996
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxCog_Amp_M_f32	152.016006
MtrCurrQaxFinalRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxPrevIntg_Volt_M_f32	1.08770001
MtrCurrQaxRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxRpl_Amp_M_f32[0]	-133.947006
MtrCurrQaxRpl_Amp_M_f32[1]	75.7020035
MtrPosComputationDelay_Rad_M_f32[0]	-6.23799992
MtrPosComputationDelay_Rad_M_f32[1]	2.11599994
MtrVoltDax_Volt_M_f32[0]	15.1960001
MtrVoltDax_Volt_M_f32[1]	-2.83699989
MtrVoltQax_Volt_M_f32[0]	15.1960001
MtrVoltQax_Volt_M_f32[1]	-2.83699989
PICurrCntl_CurrSensFailSclFac_Uls_M_f32	0.432999998
PICurrCntl_InverterFailSclFac_Uls_M_f32	0.0109999999
PICurrCntl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.335599989
PICurrCntl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.851999998
PICurrCntl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntl_MtrVecuFilt_M_str.TermN_Uls_f32	12079.9004

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.620700002		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	12079.9004		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.620700002		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2475.81006		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2645.06006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.179000005		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0120000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	7.70650005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.0999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.614899993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.26999998		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9449997		
k_deadtimeVScale_Uls_f32	0.962000012		
t_CommOffsetTblX_Uls_u3p13[0]	4809		
t_CommOffsetTblX_Uls_u3p13[1]	5553		
t_CommOffsetTblY_Cnt_u16[0]	663		
t_CommOffsetTblY_Cnt_u16[1]	905		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	114.946999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1956		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1956	1956	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.381894678	0.381895989 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.79481602	-4.79481983 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32410	32410 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035	75.7020035 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	37.4550018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-2.84500003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.41499996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.848999977
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-824.46698
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	454.670013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.196999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.954
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-729.622009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	640.599976
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-1.92999995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.432000011
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	26.6639996
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004
MtrCurrDaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxCog_Amp_M_f32	-198.285995
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556
MtrCurrQaxFinalRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxPrevIntg_Volt_M_f32	31
MtrCurrQaxRef_Amp_M_f32[0]	140.289001
MtrCurrQaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxRpl_Amp_M_f32[0]	-200.556
MtrCurrQaxRpl_Amp_M_f32[1]	-98.4449997
MtrPosComputationDelay_Rad_M_f32[0]	-5.37699986
MtrPosComputationDelay_Rad_M_f32[1]	-2.19300008
MtrVoltDax_Volt_M_f32[0]	-1.92999995
MtrVoltDax_Volt_M_f32[1]	0.432000011
MtrVoltQax_Volt_M_f32[0]	-1.92999995
MtrVoltQax_Volt_M_f32[1]	0.432000011
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.810000002
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0489999987
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.932299972
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-453.029999
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	6.96400023
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.331699997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-453.029999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.96400023
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.331699997
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	1416.70996
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5107.18018
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.057
k_MtrCtrlVirualResQax_Ohm_f32	0.179000005
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	8.02270031
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	24.8586998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-11.7589998		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.63000011		
k_deadtimeVScale_Uls_f32	0.984000027		
t_CommOffsetTblX_Uls_u3p13[0]	2154		
t_CommOffsetTblX_Uls_u3p13[1]	6783		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	253		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1110	1110	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	64487	64487 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	7.35975122	7.35974979 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	0.151220575	0.151221007 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	58832	58832 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556	-200.556 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530

PICurrCntrl_Per1



Test Case 2: Boundary Test



TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Description	Vector Description:
TS 2.1All_Min	
TS 2.2All_Max	
TS 2.3MtrCurrQaxRef_Amp_M_f32[2] = Min	
TS 2.4MtrCurrQaxRef_Amp_M_f32[2] = Max	
TS 2.5MtrCurrQaxRef_Amp_M_f32[2] = zero	
TS 2.6MtrCurrQaxRef_Amp_M_f32[2] = Neg	
TS 2.7MtrCurrQaxRef_Amp_M_f32[2] = Pos	
TS 2.8MtrCurrDaxRef_Amp_M_f32[2] = Min	
TS 2.9MtrCurrDaxRef_Amp_M_f32[2] = Max	
TS 2.10MtrCurrDaxRef_Amp_M_f32[2] = zero	
TS 2.11MtrCurrDaxRef_Amp_M_f32[2] = Neg	
TS 2.12MtrCurrDaxRef_Amp_M_f32[2] = Pos	
TS 2.13MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2] = Min	
TS 2.14MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2] = Max	
TS 2.15MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2] = Pos	
TS 2.16MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Min	
TS 2.17MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Max	
TS 2.18MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Zero	
TS 2.19MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Neg	
TS 2.20MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Pos	
TS 2.21MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[2] = Min	
TS 2.22MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Neg	
TS 2.23MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[2] = Pos	
TS 2.24MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Min	
TS 2.25MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Max	
TS 2.26MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = zero	
TS 2.27MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Neg	
TS 2.28MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Pos	
TS 2.29MtrPosComputationDelay_Rad_M_f32[2] = Min	
TS 2.30MtrPosComputationDelay_Rad_M_f32[2] = Max	
TS 2.31MtrPosComputationDelay_Rad_M_f32[2] = Zero	
TS 2.32MtrPosComputationDelay_Rad_M_f32[2] = Neg	
TS 2.33MtrPosComputationDelay_Rad_M_f32[2] = Pos	
TS 2.34MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Min	
TS 2.35MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Max	
TS 2.36MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Zero	
TS 2.37MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Neg	
TS 2.38MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Pos	
TS 2.39MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Min	
TS 2.40MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Max	
TS 2.41MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Zero	
TS 2.42MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg	
TS 2.43MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Pos	
TS 2.44MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min	
TS 2.45MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Max	
TS 2.46MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Pos	
TS 2.47MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Min	
TS 2.48MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Max	
TS 2.49MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Pos	
TS 2.50MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Min	
TS 2.51MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Max	
TS 2.52MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Pos	
TS 2.53MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Min	
TS 2.54MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Max	
TS 2.55MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Pos	
TS 2.56MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Min	
TS 2.57MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Max	
TS 2.58MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Zero	
TS 2.59MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Neg	
TS 2.60MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Pos	
TS 2.61MtrCurrQaxRpl_Amp_M_f32[2] = Min	
TS 2.62MtrCurrQaxRpl_Amp_M_f32[2] = Max	
TS 2.63MtrCurrQaxRpl_Amp_M_f32[2] = zero	
TS 2.64MtrCurrQaxRpl_Amp_M_f32[2] = Neg	
TS 2.65MtrCurrQaxRpl_Amp_M_f32[2] = Pos	
TS 2.66MtrCurrQaxCog_Amp_M_f32 = Min	
TS 2.67MtrCurrQaxCog_Amp_M_f32 = Max	
TS 2.68MtrCurrQaxCog_Amp_M_f32 = zero	
TS 2.69MtrCurrQaxCog_Amp_M_f32 = Pos	
TS 2.70MtrCurrQaxCog_Amp_M_f32 = Neg	
TS 2.71PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = Min	
TS 2.72PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = Max	
TS 2.73PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = mid	
TS 2.74PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 = Min	
TS 2.75PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 = Max	
TS 2.76PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 = mid	
TS 2.77MtrCurrQaxPrevIntg_Volt_M_f32 = Min	
TS 2.78MtrCurrQaxPrevIntg_Volt_M_f32 = Max	
TS 2.79MtrCurrQaxPrevIntg_Volt_M_f32 = zero	
TS 2.80MtrCurrQaxPrevIntg_Volt_M_f32 = Pos	
TS 2.81MtrCurrQaxPrevIntg_Volt_M_f32 = Neg	
TS 2.82MtrCurrDaxPrevIntg_Volt_M_f32 = Min	
TS 2.83MtrCurrDaxPrevIntg_Volt_M_f32 = Max	
TS 2.84MtrCurrDaxPrevIntg_Volt_M_f32 = zero	
TS 2.85MtrCurrDaxPrevIntg_Volt_M_f32 = Pos	
TS 2.86MtrCurrDaxPrevIntg_Volt_M_f32 = Neg	
TS 2.87k_MtrCtrlFeedbackControlDisable_Cnt_lgc = Min/Def	
TS 2.88k_MtrCtrlFeedbackControlDisable_Cnt_lgc = Max	
TS 2.89k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc = 0/Def	
TS 2.90k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc = 1	
TS 2.91k_CLOAFdbckSignalScfFacSlew_UlspS_f32 = Min	
TS 2.92k_CLOAFdbckSignalScfFacSlew_UlspS_f32 = Max	
TS 2.93k_CLOAFdbckSignalScfFacSlew_UlspS_f32 = Pos	
TS 2.94k_CLOAFdbckSignalScfFacSlew_UlspS_f32 = Def	

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

TS 2.95PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 = Min
TS 2.96PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 = Max
TS 2.97PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 = mid
TS 2.98k_ILOAFdbackSignalScfFacSlew_UlspS_f32 = Min
TS 2.99k_ILOAFdbackSignalScfFacSlew_UlspS_f32 = Max
TS 2.100k_ILOAFdbackSignalScfFacSlew_UlspS_f32 = Pos
TS 2.101k_ILOAFdbackSignalScfFacSlew_UlspS_f32 = Def
TS 2.102PICurrCntrl_InverterFailScfFac_Uls_M_f32 = Min
TS 2.103PICurrCntrl_InverterFailScfFac_Uls_M_f32 = Max
TS 2.104PICurrCntrl_InverterFailScfFac_Uls_M_f32 = mid
TS 2.105k_deadtimeVScale_Uls_f32 = Min
TS 2.106k_deadtimeVScale_Uls_f32 = Max/Def
TS 2.107k_deadtimeVScale_Uls_f32 = Pos
TS 2.108k_MtrCurrQaxRefModifDsb_Cnt_Igc = Min/Def
TS 2.109k_MtrCurrQaxRefModifDsb_Cnt_Igc = Max
TS 2.110MtrCurrQax_Amp_f32 = Min
TS 2.111MtrCurrQax_Amp_f32 = Max
TS 2.112MtrCurrQax_Amp_f32 = Zero
TS 2.113MtrCurrQax_Amp_f32 = Pos
TS 2.114MtrCurrQax_Amp_f32 = Neg
TS 2.115MtrCurrDax_Amp_f32 = Min
TS 2.116MtrCurrDax_Amp_f32 = Max
TS 2.117MtrCurrDax_Amp_f32 = Zero
TS 2.118MtrCurrDax_Amp_f32 = Pos
TS 2.119MtrCurrDax_Amp_f32 = Neg
TS 2.120MtrCtrl_Vecu_Volt_M_f32= Min
TS 2.121MtrCtrl_Vecu_Volt_M_f32=Max
TS 2.122MtrCtrl_Vecu_Volt_M_f32= Pos
TS 2.123ModIdxSrlComSvcDft_Cnt_Igc = Min
TS 2.124ModIdxSrlComSvcDft_Cnt_Igc = Max
TS 2.125SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_DISABLE
TS 2.126SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_OFF
TS 2.127SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_OPERATE
TS 2.128SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_WARMINIT
TS 2.129SysState_Cnt_T_Enum=>RTE_TRANSITION_StaMd_Mode
TS 2.130FastDataAccessBufIndex_Cnt_M_u16 = Min
TS 2.131FastDataAccessBufIndex_Cnt_M_u16 = Max
TS 2.132MotCurrLoaMtgtnEn_Cnt_Igc = Min
TS 2.133MotCurrLoaMtgtnEn_Cnt_Igc = Max
TS 2.134IvtrLoaMtgtnEn_Cnt_Igc = Min
TS 2.135IvtrLoaMtgtnEn_Cnt_Igc = Max
TS 2.136SlowDataAccessBufIndex_Cnt_M_u16 = Min
TS 2.137SlowDataAccessBufIndex_Cnt_M_u16 = Max
TS 2.138t_CommOffsetTblX_Uls_u3p13[2] = Min
TS 2.139t_CommOffsetTblX_Uls_u3p13[2] = Max
TS 2.140t_CommOffsetTblX_Uls_u3p13[2] = Pos
TS 2.141t_CommOffsetTblY_Cnt_u16[2] = Min
TS 2.142t_CommOffsetTblY_Cnt_u16[2] = Max
TS 2.143t_CommOffsetTblY_Cnt_u16[2] = Pos
TS 2.144k_MtrCtrlVirualResDax_Ohm_f32 = Min/Def
TS 2.145k_MtrCtrlVirualResDax_Ohm_f32 = Max
TS 2.146k_MtrCtrlVirualResDax_Ohm_f32 = Pos
TS 2.147k_MtrCtrlVirualResQax_Ohm_f32 = Min/Def
TS 2.148k_MtrCtrlVirualResQax_Ohm_f32 = Max
TS 2.149k_MtrCtrlVirualResQax_Ohm_f32 = Pos
TS 2.150k_VoltSatDaxPolyCoeff_Uls_f32 = Min
TS 2.151k_VoltSatDaxPolyCoeff_Uls_f32 = Max
TS 2.152k_VoltSatDaxPolyCoeff_Uls_f32 = Zero/Def
TS 2.153k_VoltSatDaxPolyCoeff_Uls_f32 = Neg
TS 2.154k_VoltSatDaxPolyCoeff_Uls_f32 = Pos
TS 2.155k_VoltSatQaxPolyCoeff_Uls_f32 = Min
TS 2.156k_VoltSatQaxPolyCoeff_Uls_f32 = Max
TS 2.157k_VoltSatQaxPolyCoeff_Uls_f32 = Zero/Def
TS 2.158k_VoltSatQaxPolyCoeff_Uls_f32 = Neg
TS 2.159k_VoltSatQaxPolyCoeff_Uls_f32 = Pos
TS 2.160MtrCurrOffComOffset_Cnt_u16 = Min
TS 2.161MtrCurrOffComOffset_Cnt_u16 = Max
TS 2.162MtrCurrOffComOffset_Cnt_u16 = Pos
TS 2.163k_MtrVoltQaxFiltFFEnable_Cnt_Igc= Min
TS 2.164k_MtrVoltQaxFiltFFEnable_Cnt_Igc= Max
TS 2.165k_MtrVoltVecuFiltFFEnable_Cnt_Igc= Min
TS 2.166k_MtrVoltVecuFiltFFEnable_Cnt_Igc= Max
TS 2.167k_MtrVoltQaxIntegLoLim_Volt_f32 = >min
TS 2.168k_MtrVoltQaxIntegLoLim_Volt_f32 = >max
TS 2.169k_MtrVoltQaxIntegLoLim_Volt_f32 = >neg
TS 2.170k_MtrVoltQaxIntegLoLim_Volt_f32 = >default
TS 2.171k_MtrVoltQaxIntegHiLim_Volt_f32=>min
TS 2.172k_MtrVoltQaxIntegHiLim_Volt_f32=>max
TS 2.173k_MtrVoltQaxIntegHiLim_Volt_f32=>default
TS 2.174k_MtrVoltQaxIntegHiLim_Volt_f32=>mid
TS 2.175k_MtrVoltDaxIntegLoLim_Volt_f32 =>min
TS 2.176k_MtrVoltDaxIntegLoLim_Volt_f32 =>max
TS 2.177k_MtrVoltDaxIntegLoLim_Volt_f32 =>neg
TS 2.178k_MtrVoltDaxIntegLoLim_Volt_f32 =>default
TS 2.179k_MtrVoltDaxIntegHiLim_Volt_f32 =>min
TS 2.180k_MtrVoltDaxIntegHiLim_Volt_f32 =>max
TS 2.181k_MtrVoltDaxIntegHiLim_Volt_f32 =>mid
TS 2.182k_MtrVoltDaxIntegHiLim_Volt_f32 =>default
TS 2.183k_MtrCurrQaxRefModifRplEn_Cnt_Igc =>min
TS 2.184k_MtrCurrQaxRefModifRplEn_Cnt_Igc =>max/Default

Test Step 2.1 (Repeat Count = 1)



Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.004999999989
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.004999999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.004999999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.004999999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.004999999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.004999999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.004999999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.004999999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5
MtrCtrl_Vecu_Volt_M_f32[1]	5
MtrCurrDaxPrevIntg_Volt_M_f32	-31
MtrCurrDaxRef_Amp_M_f32[0]	-220
MtrCurrDaxRef_Amp_M_f32[1]	-220
MtrCurrQaxCog_Amp_M_f32	-220
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220
MtrCurrQaxPrevIntg_Volt_M_f32	-31
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220
MtrCurrQaxRpl_Amp_M_f32[0]	-220
MtrCurrQaxRpl_Amp_M_f32[1]	-220
MtrPosComputationDelay_Rad_M_f32[0]	-6.28318977
MtrPosComputationDelay_Rad_M_f32[1]	-6.28318977
MtrVoltDax_Volt_M_f32[0]	-31
MtrVoltDax_Volt_M_f32[1]	-31
MtrVoltQax_Volt_M_f32[0]	-31
MtrVoltQax_Volt_M_f32[1]	-31
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.996816993
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.96346009e-005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996816993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.96346009e-005
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	10
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	10
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirualResDax_Ohm_f32	0
k_MtrCtrlVirualResQax_Ohm_f32	0
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	0
k_MtrVoltDaxIntegLoLim_Volt_f32	-31
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-31		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-25		
k_VoltSatQaxPolyCoeff_Uls_f32	-25		
k_deadtimeVScale_Uls_f32	0.949999988		
t_CommOffsetTblX_Uls_u3p13[0]	0		
t_CommOffsetTblX_Uls_u3p13[1]	0		
t_CommOffsetTblY_Cnt_u16[0]	0		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSriComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62259	62259 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.33995581	-3.3399601 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.37745404	-3.37744999 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40903	40903	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.2 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	2		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	2		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	31		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	31		
MtrCtrl_Vecu_Volt_M_f32[0]	31		
MtrCtrl_Vecu_Volt_M_f32[1]	31		
MtrCurrDaxPrevIntg_Volt_M_f32	31		
MtrCurrDaxRef_Amp_M_f32[0]	220		
MtrCurrDaxRef_Amp_M_f32[1]	220		
MtrCurrQaxCog_Amp_M_f32	220		
MtrCurrQaxFinalRef_Amp_M_f32[0]	220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	220		
MtrCurrQaxPrevIntg_Volt_M_f32	31		
MtrCurrQaxRef_Amp_M_f32[0]	220		
MtrCurrQaxRef_Amp_M_f32[1]	220		
MtrCurrQaxRpl_Amp_M_f32[0]	220		
MtrCurrQaxRpl_Amp_M_f32[1]	220		
MtrPosComputationDelay_Rad_M_f32[0]	6.28318977		
MtrPosComputationDelay_Rad_M_f32[1]	6.28318977		
MtrVoltDax_Volt_M_f32[0]	31		
MtrVoltDax_Volt_M_f32[1]	31		
MtrVoltQax_Volt_M_f32[0]	31		
MtrVoltQax_Volt_M_f32[1]	31		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	1		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1350		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1350		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50928.6016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996827006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1350		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1350		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50928.6016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996827006		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	8000		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	8000		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.200000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.200000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	31		
k_MtrVoltDaxIntegLoLim_Volt_f32	0		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	31		
k_MtrVoltQaxIntegLoLim_Volt_f32	0		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	25		
k_VoltSatQaxPolyCoeff_Uls_f32	25		
k_deadtimeVScale_Uls_f32	1		
t_CommOffsetTblX_Uls_u3p13[0]	8192		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	2000		
t_CommOffsetTblY_Cnt_u16[1]	2000		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	5000		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	5000	5000	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	21.9203072	21.9202995 ± 4.88E-04	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	21.9203072	21.9202995 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8192	8192 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnSclFac	1	LoaMtgnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0430000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.43400002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.70599997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	362.112
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	65.1259995
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0640000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0209999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0630000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.356999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.652000001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-894.130005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-888.995972
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.9890003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	29.243
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	19.7509995
MtrCurrDaxRef_Amp_M_f32[0]	67.4899979
MtrCurrDaxRef_Amp_M_f32[1]	119.721001
MtrCurrQaxCog_Amp_M_f32	-181.929001
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxPrevIntg_Volt_M_f32	7.82140017
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRpl_Amp_M_f32[0]	-82.2979965		
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008		
MtrPosComputationDelay_Rad_M_f32[0]	-0.541999996		
MtrPosComputationDelay_Rad_M_f32[1]	6.08400011		
MtrVoltDax_Volt_M_f32[0]	-29.7110004		
MtrVoltDax_Volt_M_f32[1]	3.61899996		
MtrVoltQax_Volt_M_f32[0]	-29.7110004		
MtrVoltQax_Volt_M_f32[1]	3.61899996		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.416999996		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.787		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.190799996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.708000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	48410.1016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0835999995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	48410.1016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0835999995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	10.1009998		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	4233.2002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0879999995		
k_MtrCtrlVirtualResQax_Ohm_f32	0.00999999978		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.9371996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.1975002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.59399998		
k_VoltSatQaxPolyCoeff_Uls_f32	8.35700035		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTblX_Uls_u3p13[0]	4914		
t_CommOffsetTblX_Uls_u3p13[1]	7782		
t_CommOffsetTblY_Cnt_u16[0]	1099		
t_CommOffsetTblY_Cnt_u16[1]	1672		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-72.4260025		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4932		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	77.189003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1672	1672	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62324	62324 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	44.2269897	44.2270012 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	9.26780319	9.26780033 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-9.9237175	-9.92372036 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	19279	19279 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	12.9371996	12.9371996 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	44.2269897	44.2270012 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008	46.8180008 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.4 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0320000015
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.227
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	537.232971
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	67.9840012
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.703000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.75199997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	462.437012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-685.195984
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.6930008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.219999999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.45499992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.1830006
MtrCtrl_Vecu_Volt_M_f32[0]	22.3540001
MtrCtrl_Vecu_Volt_M_f32[1]	24.7140007
MtrCurrDaxPrevIntg_Volt_M_f32	-23.0620003
MtrCurrDaxRef_Amp_M_f32[0]	37.4550018
MtrCurrDaxRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxCog_Amp_M_f32	-55.5390015
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004
MtrCurrQaxPrevIntg_Volt_M_f32	8.08899975
MtrCurrQaxRef_Amp_M_f32[0]	220
MtrCurrQaxRef_Amp_M_f32[1]	220
MtrCurrQaxRpl_Amp_M_f32[0]	160.044006
MtrCurrQaxRpl_Amp_M_f32[1]	165.242004
MtrPosComputationDelay_Rad_M_f32[0]	-5.08799982
MtrPosComputationDelay_Rad_M_f32[1]	-4.26300001
MtrVoltDax_Volt_M_f32[0]	3.45499992
MtrVoltDax_Volt_M_f32[1]	-19.1830006
MtrVoltQax_Volt_M_f32[0]	3.45499992
MtrVoltQax_Volt_M_f32[1]	-19.1830006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.638000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.880900025		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.978999972		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47050.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0229000002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47050.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0229000002		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	10		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	7088.3501		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.194999993		
k_MtrCtrlVirtualResQax_Ohm_f32	0.142000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.9123993		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.699999988		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.4449997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.699999988		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-19.4559994		
k_VoltSatQaxPolyCoeff_Uls_f32	-18.6200008		
k_deadtimeVScale_Uls_f32	0.95599997		
t_CommOffsetTblX_Uls_u3p13[0]	4170		
t_CommOffsetTblX_Uls_u3p13[1]	6749		
t_CommOffsetTblY_Cnt_u16[0]	177		
t_CommOffsetTblY_Cnt_u16[1]	340		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	335		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-145.169006		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	315	315	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	50872	50872 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	110.296997	110.296997 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.219999999	0.219999999 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-19.1830006	-19.1830006 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	53720	53720 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-0.699999988	-0.699999988 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006	160.044006 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	110.296997	110.296997 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-69.0940018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	161.973007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0489999987
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.108000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.824000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.423999995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-284.230011
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	346.425995
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00700000022
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.123999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0790000036
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.00899999961
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.625
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	351.605011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	882.085999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-24.6650009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	18.8299999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.4720001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.0999999
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	-6.72300005
MtrCurrDaxRef_Amp_M_f32[0]	94.3150024
MtrCurrDaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxCog_Amp_M_f32	146.660995
MtrCurrQaxFinalRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972
MtrCurrQaxPrevIntg_Volt_M_f32	24.8390999
MtrCurrQaxRef_Amp_M_f32[0]	0
MtrCurrQaxRef_Amp_M_f32[1]	0
MtrCurrQaxRpl_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRpl_Amp_M_f32[1]	-216.972
MtrPosComputationDelay_Rad_M_f32[0]	0.35800001
MtrPosComputationDelay_Rad_M_f32[1]	-1.33899999
MtrVoltDax_Volt_M_f32[0]	26.4720001
MtrVoltDax_Volt_M_f32[1]	6.0999999
MtrVoltQax_Volt_M_f32[0]	26.4720001
MtrVoltQax_Volt_M_f32[1]	6.0999999
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0170000009
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.653999984
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.588400006
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.407000005
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47672
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.660899997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47672
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.660899997
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2305.86011
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5143.29004
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0260000005
k_MtrCtrlVirtualResQax_Ohm_f32	0.199000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	4.14720011

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.800000012		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	2.33150005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.800000012		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	18.1009998		
k_VoltSatQaxPolyCoeff_Uls_f32	12.7729998		
k_deadtimeVScale_Uls_f32	0.984000027		
t_CommOffsetTblX_Uls_u3p13[0]	4013		
t_CommOffsetTblX_Uls_u3p13[1]	4882		
t_CommOffsetTblY_Cnt_u16[0]	790		
t_CommOffsetTblY_Cnt_u16[1]	931		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4626		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-100.035004		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	931	931	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	64487	64487 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-81.470993	-81.4710007 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-13.922617	-13.9225998 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	1.88431573	1.88431001 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	54289	54289 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-81.470993	-81.4710007 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972	-216.972 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.912
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.71200001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	355.987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-300.080994
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0939999968

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value	Expected Value	Result
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.075000003		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0329999998		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0140000004		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0860000029		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0359999985		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	363.006989		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	428.059998		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	25.0079994		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2439995		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	14.4589996		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.13000011		
MtrCtrl_Vecu_Volt_M_f32[0]	20.2549992		
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998		
MtrCurrDaxPrevIntg_Volt_M_f32	-17.5849991		
MtrCurrDaxRef_Amp_M_f32[0]	212.455994		
MtrCurrDaxRef_Amp_M_f32[1]	89.8619995		
MtrCurrQaxCog_Amp_M_f32	-172.485001		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.723007		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxPrevIntg_Volt_M_f32	16.4962006		
MtrCurrQaxRef_Amp_M_f32[0]	-115.696999		
MtrCurrQaxRef_Amp_M_f32[1]	-141.417007		
MtrCurrQaxRpl_Amp_M_f32[0]	-146.723007		
MtrCurrQaxRpl_Amp_M_f32[1]	-121.943001		
MtrPosComputationDelay_Rad_M_f32[0]	-4.90199995		
MtrPosComputationDelay_Rad_M_f32[1]	-2.21700001		
MtrVoltDax_Volt_M_f32[0]	14.4589996		
MtrVoltDax_Volt_M_f32[1]	-5.13000011		
MtrVoltQax_Volt_M_f32[0]	14.4589996		
MtrVoltQax_Volt_M_f32[1]	-5.13000011		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0419999994		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.861999989		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.897000015		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.652999997		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	29506.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.499300003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	29506.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.499300003		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	7811.3999		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5154.22021		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.00999999978		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0780000016		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	7.24790001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.4308004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-1.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.1009998		
k_VoltSatQaxPolyCoeff_Uls_f32	15.8879995		
k_deadtimeVScale_Uls_f32	0.978999972		
t_CommOffsetTblX_Uls_u3p13[0]	6717		
t_CommOffsetTblX_Uls_u3p13[1]	7750		
t_CommOffsetTblY_Cnt_u16[0]	59		
t_CommOffsetTblY_Cnt_u16[1]	1827		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	80.8180008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	970		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-184.522003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	970	970	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	153.011002	153.011002 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	10.6815271	10.6815004 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-2.84744525	-2.84745002 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61513	61513 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-1.5	-1.5 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.723007	-146.723007 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	153.011002	153.011002 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.173996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-213.335007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0630000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.40199995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.232999995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	115.644997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	546.737976
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0869999975
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.89699996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.37399995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-766.185974
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-58.2080002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-20.0429993
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	7.43900013
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	13.085
MtrCtrl_Vecu_Volt_M_f32[1]	15.4449997
MtrCurrDaxPrevIntg_Volt_M_f32	-17.3029995
MtrCurrDaxRef_Amp_M_f32[0]	-108.124001
MtrCurrDaxRef_Amp_M_f32[1]	178.639008
MtrCurrQaxCog_Amp_M_f32	39.7939987
MtrCurrQaxFinalRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxFinalRef_Amp_M_f32[1]	-27.9839993

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxPrevIntg_Volt_M_f32	19.8957996		
MtrCurrQaxRef_Amp_M_f32[0]	140.470001		
MtrCurrQaxRef_Amp_M_f32[1]	93.5790024		
MtrCurrQaxRpl_Amp_M_f32[0]	-208.287994		
MtrCurrQaxRpl_Amp_M_f32[1]	-27.9839993		
MtrPosComputationDelay_Rad_M_f32[0]	-0.896000028		
MtrPosComputationDelay_Rad_M_f32[1]	1.35300004		
MtrVoltDax_Volt_M_f32[0]	-7.3130002		
MtrVoltDax_Volt_M_f32[1]	3.05299997		
MtrVoltQax_Volt_M_f32[0]	-7.3130002		
MtrVoltQax_Volt_M_f32[1]	3.05299997		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.961000025		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.958999991		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.954400003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.123000003		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	39240.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.217500001		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	39240.1992		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.217500001		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2988.07007		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1052.21997		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.165000007		
k_MtrCtrlVirualResQax_Ohm_f32	0.192000002		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	10.6739998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	22.7896004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-0.714999974		
k_VoltSatQaxPolyCoeff_Uls_f32	10.5810003		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	3841		
t_CommOffsetTblX_Uls_u3p13[1]	4727		
t_CommOffsetTblY_Cnt_u16[0]	222		
t_CommOffsetTblY_Cnt_u16[1]	974		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1850		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-197.354996		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1850	1850	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	81.7690048	81.7689972 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	30.7472401	30.7472 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.13844824	-3.13844991 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	31557	31557 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-208.287994	-208.287994 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	81.7690048	81.7689972 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0240000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.349000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.930000007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	923.77301
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	220.951996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0430000015
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00800000038
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0189999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.12600005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-994.463989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-659.200989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	22.5750008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	22.8969994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992
MtrCtrl_Vecu_Volt_M_f32[0]	25.4869995
MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001
MtrCurrDaxPrevIntg_Volt_M_f32	-6.4460001
MtrCurrDaxRef_Amp_M_f32[0]	-220
MtrCurrDaxRef_Amp_M_f32[1]	-220
MtrCurrQaxCog_Amp_M_f32	161.921005
MtrCurrQaxFinalRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxFinalRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxPrevIntg_Volt_M_f32	18.0524998
MtrCurrQaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxRpl_Amp_M_f32[0]	31.5869999
MtrCurrQaxRpl_Amp_M_f32[1]	-186.395996
MtrPosComputationDelay_Rad_M_f32[0]	3.94899988
MtrPosComputationDelay_Rad_M_f32[1]	-2.61299992
MtrVoltDax_Volt_M_f32[0]	-8.79500008
MtrVoltDax_Volt_M_f32[1]	27.5049992

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-8.79500008		
MtrVoltQax_Volt_M_f32[1]	27.50499992		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.681999981		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.151999995		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.910700023		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.742999971		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17955.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.958999991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17955.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.958999991		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5278.47998		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	6189.22021		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.00700000022		
k_MtrCtrlVirualResQax_Ohm_f32	0.140000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.5515995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	27.4305		
k_MtrVoltQaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-17.1700001		
k_VoltSatQaxPolyCoeff_Uls_f32	11.927		
k_deadtimeVScale_Uls_f32	0.998000026		
t_CommOffsetTblX_Uls_u3p13[0]	1065		
t_CommOffsetTblX_Uls_u3p13[1]	1483		
t_CommOffsetTblY_Cnt_u16[0]	45		
t_CommOffsetTblY_Cnt_u16[1]	1687		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-40.9220009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4262		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	152.016006		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4262	4262	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	71.2929916	71.2929993 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	19.7938614	19.7938995 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	23.777359	23.7773991 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	45522	45522 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	31.5869999	31.5869999 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	71.2929916	71.2929993 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0179999992
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.167999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62100005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	720.525024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-487.845001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0649999976
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.60500002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.33500004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-418.748993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	590.754028
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.3319998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.40700006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_Vecu_Volt_M_f32[0]	16.8080006
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993
MtrCurrDaxPrevIntg_Volt_M_f32	14.7060003
MtrCurrDaxRef_Amp_M_f32[0]	220
MtrCurrDaxRef_Amp_M_f32[1]	220
MtrCurrQaxCog_Amp_M_f32	177.763
MtrCurrQaxFinalRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxPrevIntg_Volt_M_f32	12.4979
MtrCurrQaxRef_Amp_M_f32[0]	160.044006
MtrCurrQaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxRpl_Amp_M_f32[0]	-133.947006
MtrCurrQaxRpl_Amp_M_f32[1]	75.7020035
MtrPosComputationDelay_Rad_M_f32[0]	0.277999997
MtrPosComputationDelay_Rad_M_f32[1]	-5.2579999
MtrVoltDax_Volt_M_f32[0]	-22.3460007
MtrVoltDax_Volt_M_f32[1]	-17.8169994

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-22.3460007		
MtrVoltQax_Volt_M_f32[1]	-17.8169994		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.426999986		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.469999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.194700003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.860000014		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	31081.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.797699988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	31081.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.797699988		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5764.10986		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3350.96997		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.175999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.061999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.2978001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.2735996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.7950001		
k_VoltSatQaxPolyCoeff_Uls_f32	21.1380005		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	4432		
t_CommOffsetTblX_Uls_u3p13[1]	5751		
t_CommOffsetTblY_Cnt_u16[0]	132		
t_CommOffsetTblY_Cnt_u16[1]	216		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	75.0830002		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3800		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	54.1119995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3800	3800	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	116.228012	116.227997 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.68477249	4.68477011 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.02222037	-1.02222002 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	21524	21524 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	116.228012	116.227997 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035	75.7020035 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.10 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20299995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.354000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	868.213013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.690002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.104000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.319999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.254999995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-236.619003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-663.224976
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-15.8149996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-9.85200024
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.448
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.9260006
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997
MtrCurrDaxPrevIntg_Volt_M_f32	5.13399982
MtrCurrDaxRef_Amp_M_f32[0]	0
MtrCurrDaxRef_Amp_M_f32[1]	0
MtrCurrQaxCog_Amp_M_f32	160.160004
MtrCurrQaxFinalRef_Amp_M_f32[0]	209.052002
MtrCurrQaxFinalRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxPrevIntg_Volt_M_f32	12.7323999
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxRpl_Amp_M_f32[0]	209.052002
MtrCurrQaxRpl_Amp_M_f32[1]	-124.994003
MtrPosComputationDelay_Rad_M_f32[0]	3.03600001
MtrPosComputationDelay_Rad_M_f32[1]	-5.3920002
MtrVoltDax_Volt_M_f32[0]	-23.448
MtrVoltDax_Volt_M_f32[1]	-24.9260006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-23.448		
MtrVoltQax_Volt_M_f32[1]	-24.9260006		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.109999999		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.214000002		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.85650003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.504000008		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	38607.8008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.253199995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	38607.8008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.253199995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5287.27002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3540.21997		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0329999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.0209999997		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.5352001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.4337997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	0.853999972		
k_VoltSatQaxPolyCoeff_Uls_f32	-2.3499999		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTblX_Uls_u3p13[0]	4529		
t_CommOffsetTblX_Uls_u3p13[1]	6659		
t_CommOffsetTblY_Cnt_u16[0]	120		
t_CommOffsetTblY_Cnt_u16[1]	597		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	74.0660019		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2932		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-17.6900005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	597	597	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63897	63897 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.96537185	-3.96536994 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.70778918	3.7077899 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	23125	23125 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-124.994003	-124.994003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

PICurrCntrl_Per1

2016-04-22, 16:28:30+0530



Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	24.6130009
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-20.9400005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.88600004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.31599998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	417.908997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-364.772003
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.125
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.00999999978
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0729999989
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0450000018
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.368000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.432000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-959.400024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-873.330017
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-28.7189999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-12.0450001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.9890003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	29.243
MtrCtrl_Vecu_Volt_M_f32[0]	17.9899998
MtrCtrl_Vecu_Volt_M_f32[1]	20.3500004
MtrCurrDaxPrevIntg_Volt_M_f32	9.50599957
MtrCurrDaxRef_Amp_M_f32[0]	-115.696999
MtrCurrDaxRef_Amp_M_f32[1]	-141.417007
MtrCurrQaxCog_Amp_M_f32	175.421997
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556
MtrCurrQaxFinalRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxPrevIntg_Volt_M_f32	27.8554001
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	-200.556
MtrCurrQaxRpl_Amp_M_f32[1]	-98.4449997
MtrPosComputationDelay_Rad_M_f32[0]	0.0939999968
MtrPosComputationDelay_Rad_M_f32[1]	-3.33200002
MtrVoltDax_Volt_M_f32[0]	-29.9890003
MtrVoltDax_Volt_M_f32[1]	29.243
MtrVoltQax_Volt_M_f32[0]	-29.9890003
MtrVoltQax_Volt_M_f32[1]	29.243
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.591000021
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.361999989
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.692700028
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.643000007
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21678.8008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0943000019
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	21678.8008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0943000019
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5215.41016
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1901.98999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.138999999
k_MtrCtrlVirtualResQax_Ohm_f32	0.192000002
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	10.9145002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.5716991		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.7819996		
k_VoltSatQaxPolyCoeff_Uls_f32	0.375		
k_deadtimeVScale_Uls_f32	0.986000001		
t_CommOffsetTblX_Uls_u3p13[0]	1565		
t_CommOffsetTblX_Uls_u3p13[1]	4914		
t_CommOffsetTblY_Cnt_u16[0]	118		
t_CommOffsetTblY_Cnt_u16[1]	769		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-149.003006		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4611		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-214.828995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	769	769	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64618	64618 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-198.919998	-198.919998 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-7.64182472	-7.64182997 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	18.5529175	18.5529003 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	26707	26707 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556	-200.556 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-198.919998	-198.919998 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0480000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.122000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.1239999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.509000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.29100001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-447.415009

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-391.990997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.213
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.442000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	451.035004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	847.624023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-19.4680004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-24.3309994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	30.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.219999999
MtrCtrl_Vecu_Volt_M_f32[0]	20.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	23.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	24.7740002
MtrCurrDaxRef_Amp_M_f32[0]	140.470001
MtrCurrDaxRef_Amp_M_f32[1]	93.5790024
MtrCurrQaxCog_Amp_M_f32	-8.45100021
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	6.23339987
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-3.77699995
MtrPosComputationDelay_Rad_M_f32[1]	-5.58500004
MtrVoltDax_Volt_M_f32[0]	30.6930008
MtrVoltDax_Volt_M_f32[1]	0.219999999
MtrVoltQax_Volt_M_f32[0]	30.6930008
MtrVoltQax_Volt_M_f32[1]	0.219999999
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.127000004
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.860000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.588100016
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.179000005
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	18254.6992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.766499996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18254.6992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.766499996
SlowDataAccessBufIndex_Cnt_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7975.79004
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3201.42993
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0209999997
k_MtrCtrlVirualResQax_Ohm_f32	0.101999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	7.97240019
k_MtrVoltDaxIntegLoLim_Volt_f32	-5.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	1.26639998
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	3.04200006
k_VoltSatQaxPolyCoeff_Uls_f32	14.8559999
k_deadtimeVScale_Uls_f32	0.984000027
t_CommOffsetTblX_Uls_u3p13[0]	1262
t_CommOffsetTblX_Uls_u3p13[1]	5333
t_CommOffsetTblY_Cnt_u16[0]	311
t_CommOffsetTblY_Cnt_u16[1]	1141
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-124.758003
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3668
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-96.3310013
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	3668	3668	✓
MtrCntl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-4.91999912	-4.92000008 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	0.00282990304	0.00282971002 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	9762	9762 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	7.97240019	7.97240019 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	140.289001
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	178.235992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.120999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	248.748993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	78.5080032
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0199999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.155000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0590000004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	853.911011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-267.251007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-24.6650009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	18.8299999
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	-14.5480003
MtrCurrDaxRef_Amp_M_f32[0]	-213.026993
MtrCurrDaxRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxCog_Amp_M_f32	-35.144001
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.173996

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007		
MtrCurrQaxPrevIntg_Volt_M_f32	1.25670004		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32[0]	-146.173996		
MtrCurrQaxRpl_Amp_M_f32[1]	-213.335007		
MtrPosComputationDelay_Rad_M_f32[0]	-2.12100005		
MtrPosComputationDelay_Rad_M_f32[1]	-2.55599999		
MtrVoltDax_Volt_M_f32[0]	-24.6650009		
MtrVoltDax_Volt_M_f32[1]	18.8299999		
MtrVoltQax_Volt_M_f32[0]	-24.6650009		
MtrVoltQax_Volt_M_f32[1]	18.8299999		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.239999995		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.75999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.671500027		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.572000027		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23863		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1391		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	23863		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.1391		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6335.39014		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7999.74023		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.172000006		
k_MtrCtrlVirtualResQax_Ohm_f32	0.128999993		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.8404007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.35560036		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.08800006		
k_VoltSatQaxPolyCoeff_Uls_f32	-2.53399992		
k_deadtimeVScale_Uls_f32	0.995000005		
t_CommOffsetTblX_Uls_u3p13[0]	1229		
t_CommOffsetTblX_Uls_u3p13[1]	1416		
t_CommOffsetTblY_Cnt_u16[0]	1102		
t_CommOffsetTblY_Cnt_u16[1]	1272		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4293		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-168.113007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1272	1272	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65208	65208 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	212.904999	212.904999 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.565230429	-0.565230012 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	14.1953611	14.1954002 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42998	42998 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	212.904999	212.904999 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007	-213.335007 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	91.8850021
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	182.261002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.064000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.101999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0430000015
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-813.039001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	76.7679977
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0839999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0189999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.91199994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.33000004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	572.697998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-525.994019
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	3.45499992
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-19.1830006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	25.0079994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	19.2439995
MtrCtrl_Vecu_Volt_M_f32[0]	21.2989998
MtrCtrl_Vecu_Volt_M_f32[1]	23.6590004
MtrCurrDaxPrevIntg_Volt_M_f32	6.20800018
MtrCurrDaxRef_Amp_M_f32[0]	-212.632996
MtrCurrDaxRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxCog_Amp_M_f32	79.6880035
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993
MtrCurrQaxPrevIntg_Volt_M_f32	28.2577
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRpl_Amp_M_f32[1]	133.692993
MtrPosComputationDelay_Rad_M_f32[0]	2.0480001
MtrPosComputationDelay_Rad_M_f32[1]	5.41300011
MtrVoltDax_Volt_M_f32[0]	25.0079994
MtrVoltDax_Volt_M_f32[1]	19.2439995
MtrVoltQax_Volt_M_f32[0]	25.0079994
MtrVoltQax_Volt_M_f32[1]	19.2439995

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0549999997		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0209999997		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.627399981		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.523000002		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1838.12		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.523899972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	1838.12		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.523899972		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5138.27002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1882.53003		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0289999992		
k_MtrCtrlVirtualResQax_Ohm_f32	0.181999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	11.2284002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-7.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.49779987		
k_MtrVoltQaxIntegLoLim_Volt_f32	-7.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-15.625		
k_VoltSatQaxPolyCoeff_Uls_f32	3.14400005		
k_deadtimeVScale_Uls_f32	0.977999985		
t_CommOffsetTblX_Uls_u3p13[0]	4858		
t_CommOffsetTblX_Uls_u3p13[1]	7209		
t_CommOffsetTblY_Cnt_u16[0]	1186		
t_CommOffsetTblY_Cnt_u16[1]	1407		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3506		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-5.66300011		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1407	1407	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64094	64094 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-137.678986	-137.679001 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.3353996	-16.3353996 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	16.3873444	16.3873005 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	48284	48284 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013	-91.4420013 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-137.678986	-137.679001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.15 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0480000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.122000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.509000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.29100001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-447.415009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-391.990997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.213
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.442000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	451.035004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	847.624023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-19.4680004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-24.3309994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	30.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.219999999
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	24.7740002
MtrCurrDaxRef_Amp_M_f32[0]	140.470001
MtrCurrDaxRef_Amp_M_f32[1]	93.5790024
MtrCurrQaxCog_Amp_M_f32	-8.45100021
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	5.45940018
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-3.77699995
MtrPosComputationDelay_Rad_M_f32[1]	-5.58500004
MtrVoltDax_Volt_M_f32[0]	30.6930008
MtrVoltDax_Volt_M_f32[1]	0.219999999
MtrVoltQax_Volt_M_f32[0]	30.6930008
MtrVoltQax_Volt_M_f32[1]	0.219999999
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.127000004
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.860000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.594299972
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.179000005
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	30983.1992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.636799991
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	30983.1992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.636799991
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7975.79004
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3201.42993
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0209999997
k_MtrCtrlVirtualResQax_Ohm_f32	0.101999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	7.56930017

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.6809006		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	3.04200006		
k_VoltSatQaxPolyCoeff_Uls_f32	14.8559999		
k_deadtimeVScale_Uls_f32	0.984000027		
t_CommOffsetTblX_Uls_u3p13[0]	1262		
t_CommOffsetTblX_Uls_u3p13[1]	5333		
t_CommOffsetTblY_Cnt_u16[0]	311		
t_CommOffsetTblY_Cnt_u16[1]	1141		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-124.758003		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3668		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-96.3310013		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	3668	3668	✔
MtrCntri_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-4.91999912	-4.92000008 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	0.00311908824	0.00311889988 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	9763	9763 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	7.56930017	7.56930017 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.16 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0370000005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.824000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0160000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.286000013
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.41499996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-730.362
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-412.898987
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	14.4589996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.13000011
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	22.5750008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	22.8969994
MtrCtrl_Vecu_Volt_M_f32[0]	18.7189999
MtrCtrl_Vecu_Volt_M_f32[1]	21.0790005
MtrCurrDaxPrevIntg_Volt_M_f32	15.9169998
MtrCurrDaxRef_Amp_M_f32[0]	-69.0940018
MtrCurrDaxRef_Amp_M_f32[1]	161.973007
MtrCurrQaxCog_Amp_M_f32	-152.050995
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxPrevIntg_Volt_M_f32	20.0867996
MtrCurrQaxRef_Amp_M_f32[0]	-200.556
MtrCurrQaxRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxRpl_Amp_M_f32[0]	106.072998
MtrCurrQaxRpl_Amp_M_f32[1]	-112.455002
MtrPosComputationDelay_Rad_M_f32[0]	-0.430000007
MtrPosComputationDelay_Rad_M_f32[1]	-5.92700005
MtrVoltDax_Volt_M_f32[0]	22.5750008
MtrVoltDax_Volt_M_f32[1]	22.8969994
MtrVoltQax_Volt_M_f32[0]	22.5750008
MtrVoltQax_Volt_M_f32[1]	22.8969994
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.418000013
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.300000012
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.902100027
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.675000012
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8419.69043
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.634800017
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	8419.69043
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.634800017
SlowDataAccessBufIndex_Cnt_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6857.12012
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2799.87988
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0289999992
k_MtrCtrlVirtualResQax_Ohm_f32	0.188999996
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	18.2152004
k_MtrVoltDaxIntegLoLim_Volt_f32	-3.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	18.2434006
k_MtrVoltQaxIntegLoLim_Volt_f32	3.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	12.026
k_VoltSatQaxPolyCoeff_Uls_f32	-23.2660007
k_deadtimeVScale_Uls_f32	0.999000013
t_CommOffsetTblX_Uls_u3p13[0]	4342
t_CommOffsetTblX_Uls_u3p13[1]	7724
t_CommOffsetTblY_Cnt_u16[0]	1124
t_CommOffsetTblY_Cnt_u16[1]	1178
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3317
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	3.89299989
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	3317	3317	✓
MtrCntl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-154.578003	-154.578003 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	0.454669058	0.454690993 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-4.97426367	-4.97425985 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27332	27332 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-154.578003	-154.578003 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002	-112.455002 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.057
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.15900004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.762000024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.120999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0909999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87699997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.648999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-603.161987
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-712.994019
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-11.3319998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.40700006
MtrCtrl_Vecu_Volt_M_f32[0]	16.4099998
MtrCtrl_Vecu_Volt_M_f32[1]	18.7700005
MtrCurrDaxPrevIntg_Volt_M_f32	-24.1620007
MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
MtrCurrDaxRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxCog_Amp_M_f32	-51.1100006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	205.820999		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-206.792007		
MtrCurrQaxPrevIntg_Volt_M_f32	13.3757		
MtrCurrQaxRef_Amp_M_f32[0]	67.4899979		
MtrCurrQaxRef_Amp_M_f32[1]	119.721001		
MtrCurrQaxRpl_Amp_M_f32[0]	205.820999		
MtrCurrQaxRpl_Amp_M_f32[1]	-206.792007		
MtrPosComputationDelay_Rad_M_f32[0]	-3.62700009		
MtrPosComputationDelay_Rad_M_f32[1]	-5.73500013		
MtrVoltDax_Volt_M_f32[0]	-11.3319998		
MtrVoltDax_Volt_M_f32[1]	-5.40700006		
MtrVoltQax_Volt_M_f32[0]	-11.3319998		
MtrVoltQax_Volt_M_f32[1]	-5.40700006		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.423999995		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.395000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.712199986		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.651000023		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12079.9004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.298200011		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12079.9004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.298200011		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3678.44995		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7603.6001		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0439999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.166999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.1203003		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.95559978		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.5209999		
k_VoltSatQaxPolyCoeff_Uls_f32	-20.1860008		
k_deadtimeVScale_Uls_f32	0.99000001		
t_CommOffsetTblX_Uls_u3p13[0]	1516		
t_CommOffsetTblX_Uls_u3p13[1]	5882		
t_CommOffsetTblY_Cnt_u16[0]	1813		
t_CommOffsetTblY_Cnt_u16[1]	183		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3803		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	45.3779984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3803	3803	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.2210007	-87.2210007 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.68405461	-2.68405008 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.15912867	-4.1591301 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	915	915 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-87.2210007	-87.2210007 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-206.792007	-206.792007 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.18 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	160.044006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	165.242004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0719999969
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.25100005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.801999986
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	0
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0489999987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.414000005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.66700006
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	158.016998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-944.586975
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.79500008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.5049992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-15.8149996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-9.85200024
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	30.7700005
MtrCurrDaxRef_Amp_M_f32[0]	-146.173996
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxCog_Amp_M_f32	76.5339966
MtrCurrQaxFinalRef_Amp_M_f32[0]	-69.0940018
MtrCurrQaxFinalRef_Amp_M_f32[1]	161.973007
MtrCurrQaxPrevIntg_Volt_M_f32	10.9584999
MtrCurrQaxRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxRpl_Amp_M_f32[0]	-69.0940018
MtrCurrQaxRpl_Amp_M_f32[1]	161.973007
MtrPosComputationDelay_Rad_M_f32[0]	4.77099991
MtrPosComputationDelay_Rad_M_f32[1]	-3.22900009
MtrVoltDax_Volt_M_f32[0]	-15.8149996
MtrVoltDax_Volt_M_f32[1]	-9.85200024

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-15.8149996		
MtrVoltQax_Volt_M_f32[1]	-9.85200024		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.347000003		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.470999986		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.676199973		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.307999998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	20.7000008		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47476.6016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.333499998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	20.7000008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47476.6016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.333499998		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2757.25		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7944.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.104999997		
k_MtrCtrlVirualResQax_Ohm_f32	0.0839999989		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.46549988		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.87349999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	20.5340004		
k_VoltSatQaxPolyCoeff_Uls_f32	-22.2229996		
k_deadtimeVScale_Uls_f32	0.952000022		
t_CommOffsetTblX_Uls_u3p13[0]	1188		
t_CommOffsetTblX_Uls_u3p13[1]	7029		
t_CommOffsetTblY_Cnt_u16[0]	422		
t_CommOffsetTblY_Cnt_u16[1]	1383		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4003		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	103.652		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4003	4003	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	27.5049992	27.5049992 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-9.85200024	-9.85200024 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	51828	51828 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-69.0940018	-69.0940018 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.460000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-980.567993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-630.098022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.119999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87699997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.648999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	764.937988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-605.708008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.7189999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-12.0450001
MtrCtrl_Vecu_Volt_M_f32[0]	22.3540001
MtrCtrl_Vecu_Volt_M_f32[1]	24.7140007
MtrCurrDaxPrevIntg_Volt_M_f32	11.6429996
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	-161.751999
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxPrevIntg_Volt_M_f32	4.84670019
MtrCurrQaxRef_Amp_M_f32[0]	94.3150024
MtrCurrQaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxRpl_Amp_M_f32[0]	-132.813004
MtrCurrQaxRpl_Amp_M_f32[1]	-9.14299965
MtrPosComputationDelay_Rad_M_f32[0]	-0.989000022
MtrPosComputationDelay_Rad_M_f32[1]	-5.421
MtrVoltDax_Volt_M_f32[0]	-28.7189999
MtrVoltDax_Volt_M_f32[1]	-12.0450001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-28.7189999		
MtrVoltQax_Volt_M_f32[1]	-12.0450001		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.768000007		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.610000014		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.385100007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.82099998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	3431.37012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.870999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	3431.37012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.870999992		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3636.04004		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	902.40802		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.023		
k_MtrCtrlVirualResQax_Ohm_f32	0.158000007		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.1237001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-7.19999981		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	22.1184998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6590004		
k_VoltSatQaxPolyCoeff_Uls_f32	0.441000015		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	786		
t_CommOffsetTblX_Uls_u3p13[1]	5267		
t_CommOffsetTblY_Cnt_u16[0]	267		
t_CommOffsetTblY_Cnt_u16[1]	723		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1231		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	99.348999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1231	1231	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.00361538	-4.00362015 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.97006512	2.97006989 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	45494	45494 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	13.1237001	13.1237001 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965	-9.14299965 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-208.287994
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-27.9839993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.086999975
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00700000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62199998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	376.326996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	721.965027
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.010999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0520000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00800000038
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.558000028
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.483999997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	614.892029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1012.16998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	15.5469999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-22.8390007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	27.9990005
MtrCurrDaxRef_Amp_M_f32[0]	106.072998
MtrCurrDaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxCog_Amp_M_f32	131.306
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993
MtrCurrQaxPrevIntg_Volt_M_f32	22.6445999
MtrCurrQaxRef_Amp_M_f32[0]	-108.124001
MtrCurrQaxRef_Amp_M_f32[1]	178.639008
MtrCurrQaxRpl_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRpl_Amp_M_f32[1]	133.692993
MtrPosComputationDelay_Rad_M_f32[0]	-1.926
MtrPosComputationDelay_Rad_M_f32[1]	5.88500023
MtrVoltDax_Volt_M_f32[0]	-29.7110004
MtrVoltDax_Volt_M_f32[1]	3.61899996

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-29.7110004		
MtrVoltQax_Volt_M_f32[1]	3.61899996		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.247999996		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.602999985		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.106299996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.317000002		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13842.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.916499972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	13842.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.916499972		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	4450.8501		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	2508.87012		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.189999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.118000001		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	26.2252998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.19999981		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.4771004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.3699999		
k_VoltSatQaxPolyCoeff_Uls_f32	11.9610004		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTblX_Uls_u3p13[0]	1729		
t_CommOffsetTblX_Uls_u3p13[1]	3269		
t_CommOffsetTblY_Cnt_u16[0]	502		
t_CommOffsetTblY_Cnt_u16[1]	707		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	727		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	41.1769981		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	707	707	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64552	64552 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-147.988007	-147.988007 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	6.52048111	6.52048016 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-12.4609261	-12.4609003 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	7651	7651 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.988007	-147.988007 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993	133.692993 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

PICurrCntl_Per1

2016-04-22, 16:28:30+0530



Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.460000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-229.300995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-277.625
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0350000001
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.119999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	764.937988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-605.708008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.7189999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-12.0450001
MtrCtrl_Vecu_Volt_M_f32[0]	20.2549992
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998
MtrCurrDaxPrevIntg_Volt_M_f32	11.6429996
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	-161.751999
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxPrevIntg_Volt_M_f32	6.56619978
MtrCurrQaxRef_Amp_M_f32[0]	94.3150024
MtrCurrQaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxRpl_Amp_M_f32[0]	-132.813004
MtrCurrQaxRpl_Amp_M_f32[1]	-9.14299965
MtrPosComputationDelay_Rad_M_f32[0]	4.80499983
MtrPosComputationDelay_Rad_M_f32[1]	-6.10300016
MtrVoltDax_Volt_M_f32[0]	-28.7189999
MtrVoltDax_Volt_M_f32[1]	-12.0450001
MtrVoltQax_Volt_M_f32[0]	-28.7189999
MtrVoltQax_Volt_M_f32[1]	-12.0450001
PICurrCntl_CurrSensFailScfFac_Uls_M_f32	0.768000007
PICurrCntl_InverterFailScfFac_Uls_M_f32	0.610000014
PICurrCntl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.495900005
PICurrCntl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.82099998
PICurrCntl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntl_MtrVecuFilt_M_str.TermN_Uls_f32	46503.6992
PICurrCntl_MtrVecuFilt_M_str.TermD_Uls_f32	0.730000019
PICurrCntl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0
PICurrCntl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46503.6992
PICurrCntl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.730000019
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3636.04004
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	902.40802
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.023
k_MtrCtrlVirtualResQax_Ohm_f32	0.158000007
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	29.5695		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.19999981		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.8902998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6590004		
k_VoltSatQaxPolyCoeff_Uls_f32	0.441000015		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	786		
t_CommOffsetTblX_Uls_u3p13[1]	5267		
t_CommOffsetTblY_Cnt_u16[0]	267		
t_CommOffsetTblY_Cnt_u16[1]	723		
target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val	-126.640999		
target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1231		
target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val	99.348999		
target_MtrCntl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	1231	1231	✔
MtrCntl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-1.5012114	-1.50120997 ± 4.88E-04	✔
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	4.75358677	4.75359011 ± 4.88E-04	✔
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	46927	46927 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	29.5695	29.5695 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965	-9.14299965 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.460000008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-980.567993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-630.098022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.017000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.119999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	764.937988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-605.708008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.7189999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-12.0450001
MtrCtrl_Vecu_Volt_M_f32[0]	13.085
MtrCtrl_Vecu_Volt_M_f32[1]	15.4449997
MtrCurrDaxPrevIntg_Volt_M_f32	11.6429996
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	-161.751999
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxPrevIntg_Volt_M_f32	8.98110008
MtrCurrQaxRef_Amp_M_f32[0]	94.3150024
MtrCurrQaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxRpl_Amp_M_f32[0]	-132.813004
MtrCurrQaxRpl_Amp_M_f32[1]	-9.14299965
MtrPosComputationDelay_Rad_M_f32[0]	-0.989000022
MtrPosComputationDelay_Rad_M_f32[1]	-5.421
MtrVoltDax_Volt_M_f32[0]	-28.7189999
MtrVoltDax_Volt_M_f32[1]	-12.0450001
MtrVoltQax_Volt_M_f32[0]	-28.7189999
MtrVoltQax_Volt_M_f32[1]	-12.0450001
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.768000007
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.610000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.72359997
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.82099998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	42029.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.316399992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	42029.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.316399992
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3636.04004
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	902.40802
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.023
k_MtrCtrlVirualResQax_Ohm_f32	0.158000007
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	2.74320006
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.1999998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	12.4398003
k_MtrVoltQaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	24.6590004
k_VoltSatQaxPolyCoeff_Uls_f32	0.441000015
k_deadtimeVScale_Uls_f32	0.996999979
t_CommOffsetTbIX_Uls_u3p13[0]	786
t_CommOffsetTbIX_Uls_u3p13[1]	5267
t_CommOffsetTbIY_Cnt_u16[0]	267
t_CommOffsetTbIY_Cnt_u16[1]	723
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-126.640999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1231

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnl_Read_MtrCurrQax_Amp_f32_Val	99.348999		
target_MtrCntnl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntnl_Write_CommOffset_Cnt_u16(val)	1231	1231	✓
MtrCntnl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntnl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntnl_Write_MtrDaxVoltage_Volt_f32(val)	-4.00388384	-4.00388002 ± 4.88E-04	✓
MtrCntnl_Write_MtrQaxVoltage_Volt_f32(val)	2.96970439	2.9697001 ± 4.88E-04	✓
MtrCntnl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	45493	45493 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	2.74320006	2.74320006 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965	-9.14299965 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-208.287994
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-27.9839993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0869999975
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00700000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62199998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	376.326996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	721.965027
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0520000011
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.00800000038
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.558000028
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.483999997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	614.892029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1012.16998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	15.5469999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-22.8390007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_Vecu_Volt_M_f32[0]	25.4869995
MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001
MtrCurrDaxPrevIntg_Volt_M_f32	27.9990005
MtrCurrDaxRef_Amp_M_f32[0]	106.072998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	-112.455002		
MtrCurrQaxCog_Amp_M_f32	131.306		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013		
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993		
MtrCurrQaxPrevIntg_Volt_M_f32	30.6711006		
MtrCurrQaxRef_Amp_M_f32[0]	-108.124001		
MtrCurrQaxRef_Amp_M_f32[1]	178.639008		
MtrCurrQaxRpl_Amp_M_f32[0]	-91.4420013		
MtrCurrQaxRpl_Amp_M_f32[1]	133.692993		
MtrPosComputationDelay_Rad_M_f32[0]	-1.926		
MtrPosComputationDelay_Rad_M_f32[1]	5.88500023		
MtrVoltDax_Volt_M_f32[0]	-29.7110004		
MtrVoltDax_Volt_M_f32[1]	3.61899996		
MtrVoltQax_Volt_M_f32[0]	-29.7110004		
MtrVoltQax_Volt_M_f32[1]	3.61899996		
PICurrCntrl_CurrSensFailSciFac_Uls_M_f32	0.247999996		
PICurrCntrl_InverterFailSciFac_Uls_M_f32	0.602999985		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0351000018		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.317000002		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17234.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.71329999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17234.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.71329999		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSciFacSlew_UlspS_f32	4450.8501		
k_ILOAFdbackSignalSciFacSlew_UlspS_f32	2508.87012		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.189999998		
k_MtrCtrlVirtualResQax_Ohm_f32	0.118000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	29.1569004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.1117992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.3699999		
k_VoltSatQaxPolyCoeff_Uls_f32	11.9610004		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTblX_Uls_u3p13[0]	1729		
t_CommOffsetTblX_Uls_u3p13[1]	3269		
t_CommOffsetTblY_Cnt_u16[0]	502		
t_CommOffsetTblY_Cnt_u16[1]	707		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	727		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	41.1769981		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	707	707	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64552	64552 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-147.988007	-147.988007 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	11.63941	11.6393995 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-22.2434235	-22.2434006 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	7651	7651 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.988007	-147.988007 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993	133.692993 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0309999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.722
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82299995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	888.947998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-292.006989
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.050999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.061999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.93700004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	9.61999989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	26.7999992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.45499992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.1830006
MtrCtrl_Vecu_Volt_M_f32[0]	16.8080006
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993
MtrCurrDaxPrevIntg_Volt_M_f32	4.04500008
MtrCurrDaxRef_Amp_M_f32[0]	24.6130009
MtrCurrDaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxCog_Amp_M_f32	19.6149998
MtrCurrQaxFinalRef_Amp_M_f32[0]	171.485992
MtrCurrQaxFinalRef_Amp_M_f32[1]	163.787003
MtrCurrQaxPrevIntg_Volt_M_f32	16.8353996
MtrCurrQaxRef_Amp_M_f32[0]	-76.8769989
MtrCurrQaxRef_Amp_M_f32[1]	-153.238998
MtrCurrQaxRpl_Amp_M_f32[0]	171.485992
MtrCurrQaxRpl_Amp_M_f32[1]	163.787003
MtrPosComputationDelay_Rad_M_f32[0]	3.05200005
MtrPosComputationDelay_Rad_M_f32[1]	-4.14699984
MtrVoltDax_Volt_M_f32[0]	3.45499992
MtrVoltDax_Volt_M_f32[1]	-19.1830006
MtrVoltQax_Volt_M_f32[0]	3.45499992
MtrVoltQax_Volt_M_f32[1]	-19.1830006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.463999987		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.409000009		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.794099987		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.640999973		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20241.6992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.221200004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	20241.6992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.221200004		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	897.588013		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1232.52002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.182999998		
k_MtrCtrlVirtualResQax_Ohm_f32	0.193000004		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.7555008		
k_MtrVoltDaxIntegLoLim_Volt_f32	-12.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	22.5324993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.6259995		
k_VoltSatQaxPolyCoeff_Uls_f32	1.32299995		
k_deadtimeVScale_Uls_f32	0.981000006		
t_CommOffsetTblX_Uls_u3p13[0]	2556		
t_CommOffsetTblX_Uls_u3p13[1]	4316		
t_CommOffsetTblY_Cnt_u16[0]	15		
t_CommOffsetTblY_Cnt_u16[1]	40		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	48.8400002		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3024		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-30.7789993		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	40	40	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64290	64290 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.71320462	-1.71314001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	16.3994045	16.3994007 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	30748	30748 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	163.787003	163.787003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.25 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-133.947006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	75.7020035
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0320000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.34800005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.11099994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-942.771973
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-380.85199
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.35000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.749000013
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.3530006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.3040009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.4720001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.0999999
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997
MtrCurrDaxPrevIntg_Volt_M_f32	15.0869999
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004
MtrCurrDaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxCog_Amp_M_f32	114.531998
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxPrevIntg_Volt_M_f32	5.42920017
MtrCurrQaxRef_Amp_M_f32[0]	191.369003
MtrCurrQaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxRpl_Amp_M_f32[0]	106.072998
MtrCurrQaxRpl_Amp_M_f32[1]	-112.455002
MtrPosComputationDelay_Rad_M_f32[0]	2.61800003
MtrPosComputationDelay_Rad_M_f32[1]	-1.04299998
MtrVoltDax_Volt_M_f32[0]	26.4720001
MtrVoltDax_Volt_M_f32[1]	6.0999999
MtrVoltQax_Volt_M_f32[0]	26.4720001
MtrVoltQax_Volt_M_f32[1]	6.0999999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.600000024
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.899999976
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.179199994
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.257999986
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	404.899994
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.578299999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	404.899994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.578299999
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	2048.80005
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	3548.88989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0790000036
k_MtrCtrlVirtualResQax_Ohm_f32	0.177000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	2.39529991

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-13.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.2297001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.0030003		
k_VoltSatQaxPolyCoeff_Uls_f32	-9.26399994		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTblX_Uls_u3p13[0]	1810		
t_CommOffsetTblX_Uls_u3p13[1]	2335		
t_CommOffsetTblY_Cnt_u16[0]	157		
t_CommOffsetTblY_Cnt_u16[1]	712		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	107.702003		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4540		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	4540	4540	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	105.060005	105.059998 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	2.34449911	2.34450006 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	29.3876286	29.3875999 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55487	55487 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0.910027981	0.910028994 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998	106.072998 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	105.060005	105.059998 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.26 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.108000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0979999974
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.266000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.08399999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-150.298996

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-165.235001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.91199994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.56400001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	0
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-6.06799984
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-7.83199978
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	14.4589996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.13000011
MtrCtrl_Vecu_Volt_M_f32[0]	17.9899998
MtrCtrl_Vecu_Volt_M_f32[1]	20.3500004
MtrCurrDaxPrevIntg_Volt_M_f32	-9.29100037
MtrCurrDaxRef_Amp_M_f32[0]	140.289001
MtrCurrDaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxCog_Amp_M_f32	34.7879982
MtrCurrQaxFinalRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxFinalRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxPrevIntg_Volt_M_f32	26.5946007
MtrCurrQaxRef_Amp_M_f32[0]	-147.343002
MtrCurrQaxRef_Amp_M_f32[1]	127.972
MtrCurrQaxRpl_Amp_M_f32[0]	24.6130009
MtrCurrQaxRpl_Amp_M_f32[1]	-20.9400005
MtrPosComputationDelay_Rad_M_f32[0]	2.3440001
MtrPosComputationDelay_Rad_M_f32[1]	-4.34200001
MtrVoltDax_Volt_M_f32[0]	14.4589996
MtrVoltDax_Volt_M_f32[1]	-5.13000011
MtrVoltQax_Volt_M_f32[0]	14.4589996
MtrVoltQax_Volt_M_f32[1]	-5.13000011
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.509000003
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.446999997
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.8125
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.331999987
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	865.320007
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0255999994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	865.320007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0255999994
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3449.11011
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7870.1001
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.164000005
k_MtrCtrlVirtualResQax_Ohm_f32	0.0610000007
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	17.9570999
k_MtrVoltDaxIntegLoLim_Volt_f32	-14.1999998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	16.1431007
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-13.4589996
k_VoltSatQaxPolyCoeff_Uls_f32	12.7139997
k_deadtimeVScale_Uls_f32	0.981999993
t_CommOffsetTblX_Uls_u3p13[0]	1360
t_CommOffsetTblX_Uls_u3p13[1]	5743
t_CommOffsetTblY_Cnt_u16[0]	1436
t_CommOffsetTblY_Cnt_u16[1]	1891
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	5.72399998
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	808
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	177.046997
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	1891	1891	✓
MtrCntl_Write_ModlDx_Uls_u16p16(val)	64356	64356 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-206.744003	-206.744003 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-17.6647816	-17.6648006 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	0.222200543	0.222200006 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8196	8196 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-206.744003	-206.744003 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-20.9400005	-20.9400005 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlDx_Uls_u16p16	1	MtrCntl_Write_ModlDx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.27 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.153
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69299996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-43.257
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.920013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00999999978
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.363999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65900004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-185.072998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-920.171997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.2709999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	20.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	23.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	21.4680004
MtrCurrDaxRef_Amp_M_f32[0]	91.8850021
MtrCurrDaxRef_Amp_M_f32[1]	182.261002
MtrCurrQaxCog_Amp_M_f32	91.9309998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.065002
MtrCurrQaxPrevIntg_Volt_M_f32	18.1345997

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[0]	6.18900013		
MtrCurrQaxRef_Amp_M_f32[1]	83.0540009		
MtrCurrQaxRpl_Amp_M_f32[0]	-166.035004		
MtrCurrQaxRpl_Amp_M_f32[1]	183.065002		
MtrPosComputationDelay_Rad_M_f32[0]	-1.79400003		
MtrPosComputationDelay_Rad_M_f32[1]	-1.37800002		
MtrVoltDax_Volt_M_f32[0]	-7.3130002		
MtrVoltDax_Volt_M_f32[1]	3.05299997		
MtrVoltQax_Volt_M_f32[0]	-7.3130002		
MtrVoltQax_Volt_M_f32[1]	3.05299997		
PICurrCntrl_CurrSensFailSciFac_Uls_M_f32	0.256999999		
PICurrCntrl_InverterFailSciFac_Uls_M_f32	0.933000028		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0346999988		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.460000008		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25640.4004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.400000006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	25640.4004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.400000006		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalSciFacSlew_UlspS_f32	876.684998		
k_ILOAFdbckSignalSciFacSlew_UlspS_f32	2879.57007		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.00200000009		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0710000023		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.07459998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-15.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	5.46850014		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	10.7320004		
k_VoltSatQaxPolyCoeff_Uls_f32	3.59299994		
k_deadtimeVScale_Uls_f32	0.959999979		
t_CommOffsetTblX_Uls_u3p13[0]	4701		
t_CommOffsetTblX_Uls_u3p13[1]	5063		
t_CommOffsetTblY_Cnt_u16[0]	155		
t_CommOffsetTblY_Cnt_u16[1]	873		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	650		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	650	650	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	80.2930069	80.2929993 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.738990366	0.738990009 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.74277258	-4.7427702 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12444	12444 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	80.2930069	80.2929993 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.065002	183.065002 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.28 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0630000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75100005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.246999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	292.062988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	884.252991
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0630000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.740000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.890999973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	260.899994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	994.27301
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.32400036
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-26.5540009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	24.8929996
MtrCurrDaxRef_Amp_M_f32[0]	-218.035004
MtrCurrDaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxCog_Amp_M_f32	21.4759998
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.289001
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.235992
MtrCurrQaxPrevIntg_Volt_M_f32	28.968399
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxRpl_Amp_M_f32[0]	140.289001
MtrCurrQaxRpl_Amp_M_f32[1]	178.235992
MtrPosComputationDelay_Rad_M_f32[0]	-0.916000009
MtrPosComputationDelay_Rad_M_f32[1]	-6.00099993
MtrVoltDax_Volt_M_f32[0]	-8.79500008
MtrVoltDax_Volt_M_f32[1]	27.5049992

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-8.79500008		
MtrVoltQax_Volt_M_f32[1]	27.50499992		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.950999975		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.626999974		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.899699986		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.944000006		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.588		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.588		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5931.81982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3685.94995		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.0540000014		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.05350006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-16.5		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.8121996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	2.45499992		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9769993		
k_deadtimeVScale_Uls_f32	0.961000025		
t_CommOffsetTblX_Uls_u3p13[0]	2294		
t_CommOffsetTblX_Uls_u3p13[1]	6390		
t_CommOffsetTblY_Cnt_u16[0]	11		
t_CommOffsetTblY_Cnt_u16[1]	125		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	20.6149998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	631		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	125	125	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	62980	62980 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-5.06428909	-5.06429005 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-12.7523785	-12.7524004 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27157	27157 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.235992	178.235992 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Test Step 2.29 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.153
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69299996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-43.257
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.920013
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.125
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.00999999978
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.363999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65900004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	185.072998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-920.171997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.2709999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	22.3540001
MtrCtrl_Vecu_Volt_M_f32[1]	24.7140007
MtrCurrDaxPrevIntg_Volt_M_f32	21.4680004
MtrCurrDaxRef_Amp_M_f32[0]	91.8850021
MtrCurrDaxRef_Amp_M_f32[1]	182.261002
MtrCurrQaxCog_Amp_M_f32	91.9309998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.065002
MtrCurrQaxPrevIntg_Volt_M_f32	9.05210018
MtrCurrQaxRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxRpl_Amp_M_f32[0]	-166.035004
MtrCurrQaxRpl_Amp_M_f32[1]	183.065002
MtrPosComputationDelay_Rad_M_f32[0]	-6.28318977
MtrPosComputationDelay_Rad_M_f32[1]	-6.28318977
MtrVoltDax_Volt_M_f32[0]	-7.3130002
MtrVoltDax_Volt_M_f32[1]	3.05299997
MtrVoltQax_Volt_M_f32[0]	-7.3130002
MtrVoltQax_Volt_M_f32[1]	3.05299997
PICurrCntl_CurrSensFailSclFac_Uls_M_f32	0.256999999
PICurrCntl_InverterFailSclFac_Uls_M_f32	0.933000028
PICurrCntl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.786800027
PICurrCntl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.460000008
PICurrCntl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntl_MtrVecuFilt_M_str.TermN_Uls_f32	36325.3984
PICurrCntl_MtrVecuFilt_M_str.TermD_Uls_f32	0.365999997
PICurrCntl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	36325.3984
PICurrCntl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.365999997
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	876.684998
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	2879.57007
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.00200000009
k_MtrCtrlVirtualResQax_Ohm_f32	0.0710000023
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	29.9500999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-17.5		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	27.7511997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	10.7320004		
k_VoltSatQaxPolyCoeff_Uls_f32	3.59299994		
k_deadtimeVScale_Uls_f32	0.959999979		
t_CommOffsetTblX_Uls_u3p13[0]	4701		
t_CommOffsetTblX_Uls_u3p13[1]	5063		
t_CommOffsetTblY_Cnt_u16[0]	155		
t_CommOffsetTblY_Cnt_u16[1]	873		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	650		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	650	650	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	80.2930069	80.2929993 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	0.397277474	0.397278011 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	4.78353119	4.78353024 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	864	864	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	80.2930069	80.2929993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.065002	183.065002 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0630000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75100005

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.246999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	292.062988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	884.252991
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.740000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.890999973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-260.899994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-994.27301
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.32400036
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-26.5540009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	24.8929996
MtrCurrDaxRef_Amp_M_f32[0]	-218.035004
MtrCurrDaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxCog_Amp_M_f32	21.4759998
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.289001
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.235992
MtrCurrQaxPrevIntg_Volt_M_f32	16.3929005
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxRpl_Amp_M_f32[0]	140.289001
MtrCurrQaxRpl_Amp_M_f32[1]	178.235992
MtrPosComputationDelay_Rad_M_f32[0]	6.28318977
MtrPosComputationDelay_Rad_M_f32[1]	6.28318977
MtrVoltDax_Volt_M_f32[0]	-8.79500008
MtrVoltDax_Volt_M_f32[1]	27.5049992
MtrVoltQax_Volt_M_f32[0]	-8.79500008
MtrVoltQax_Volt_M_f32[1]	27.5049992
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.950999975
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.626999974
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0658000037
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.944000006
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	10763.7002
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.228200004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	10763.7002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.228200004
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5931.81982
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3685.94995
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.112000003
k_MtrCtrlVirtualResQax_Ohm_f32	0.0540000014
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	14.7652998
k_MtrVoltDaxIntegLoLim_Volt_f32	-18.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	14.1267004
k_MtrVoltQaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	2.45499992
k_VoltSatQaxPolyCoeff_Uls_f32	16.9769993
k_deadtimeVScale_Uls_f32	0.961000025
t_CommOffsetTblX_Uls_u3p13[0]	2294
t_CommOffsetTblX_Uls_u3p13[1]	6390
t_CommOffsetTblY_Cnt_u16[0]	11
t_CommOffsetTblY_Cnt_u16[1]	125
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	20.6149998
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	631

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntnrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntnrl_Write_CommOffset_Cnt_u16(val)	125	125	✓
MtrCntnrl_Write_Modldx_Uls_u16p16(val)	62980	62980 ± 1	✓
MtrCntnrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntnrl_Write_MtrDaxVoltage_Volt_f32(val)	-5.08662462	-5.08662987 ± 4.88E-04	✓
MtrCntnrl_Write_MtrQaxVoltage_Volt_f32(val)	12.7434855	12.7434998 ± 4.88E-04	✓
MtrCntnrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61575	61575 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.235992	178.235992 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnSclFac	1	LoaMtgnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.31 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.012000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.071000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.027000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.898999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23500001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-113.670998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-827.208008
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.027000007
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.096000008
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.53100002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.80499995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-277.385986
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-517.232971
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	3.87299991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2730007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_Vecu_Volt_M_f32[0]	20.2549992
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998
MtrCurrDaxPrevIntg_Volt_M_f32	-29.7089996
MtrCurrDaxRef_Amp_M_f32[0]	-216.921997
MtrCurrDaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxCog_Amp_M_f32	-124.709999

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	91.8850021		
MtrCurrQaxFinalRef_Amp_M_f32[1]	182.261002		
MtrCurrQaxPrevIntg_Volt_M_f32	3.45029998		
MtrCurrQaxRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxRpl_Amp_M_f32[0]	91.8850021		
MtrCurrQaxRpl_Amp_M_f32[1]	182.261002		
MtrPosComputationDelay_Rad_M_f32[0]	0		
MtrPosComputationDelay_Rad_M_f32[1]	0		
MtrVoltDax_Volt_M_f32[0]	-22.3460007		
MtrVoltDax_Volt_M_f32[1]	-17.8169994		
MtrVoltQax_Volt_M_f32[0]	-22.3460007		
MtrVoltQax_Volt_M_f32[1]	-17.8169994		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.735000014		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.151999995		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.199900001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.30399999		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13385.9004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.58950001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	13385.9004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.58950001		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	1279.38		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	4881.5498		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.075000003		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0209999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.2935009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.1322002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.7220001		
k_VoltSatQaxPolyCoeff_Uls_f32	-13.0010004		
k_deadtimeVScale_Uls_f32	0.992999971		
t_CommOffsetTblX_Uls_u3p13[0]	1827		
t_CommOffsetTblX_Uls_u3p13[1]	5226		
t_CommOffsetTblY_Cnt_u16[0]	1326		
t_CommOffsetTblY_Cnt_u16[1]	1829		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	79.6729965		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	416		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1829	1829	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	65077	65077 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-180.201996	-180.201996 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.96159673	-1.96159995 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-20.0173283	-20.0172997 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33787	33787 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-180.201996	-180.201996 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	182.261002	182.261002 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.32 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-69.0940018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	161.973007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0179999992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0240000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.43599999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.893999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	497.348999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-685.572998
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.123999998
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0790000036
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0839999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.68299997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-594.544983
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	215.455994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.1380005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.920000017
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.448
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.9260006
MtrCtrl_Vecu_Volt_M_f32[0]	13.085
MtrCtrl_Vecu_Volt_M_f32[1]	15.4449997
MtrCurrDaxPrevIntg_Volt_M_f32	-9.7670002
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxCog_Amp_M_f32	-185.608994
MtrCurrQaxFinalRef_Amp_M_f32[0]	-218.035004
MtrCurrQaxFinalRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxPrevIntg_Volt_M_f32	20.1585007
MtrCurrQaxRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxRpl_Amp_M_f32[0]	-218.035004
MtrCurrQaxRpl_Amp_M_f32[1]	11.6370001
MtrPosComputationDelay_Rad_M_f32[0]	-3.05999994
MtrPosComputationDelay_Rad_M_f32[1]	-4.93499994
MtrVoltDax_Volt_M_f32[0]	-23.448
MtrVoltDax_Volt_M_f32[1]	-24.9260006
MtrVoltQax_Volt_M_f32[0]	-23.448
MtrVoltQax_Volt_M_f32[1]	-24.9260006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.989000022		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.867999971		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.249599993		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.578000009		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	39404.3984		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.423099995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	39404.3984		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.423099995		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	7139.7998		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5357.10986		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0689999983		
k_MtrCtrlVirtualResQax_Ohm_f32	0.063000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	11.7138004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.54269981		
k_MtrVoltQaxIntegLoLim_Volt_f32	3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-7.60300016		
k_VoltSatQaxPolyCoeff_Uls_f32	-11.1459999		
k_deadtimeVScale_Uls_f32	0.949999988		
t_CommOffsetTblX_Uls_u3p13[0]	2440		
t_CommOffsetTblX_Uls_u3p13[1]	3744		
t_CommOffsetTblY_Cnt_u16[0]	695		
t_CommOffsetTblY_Cnt_u16[1]	1480		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	0.486999989		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1059		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1059	1059	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-31.1130066	-31.1130009 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-27.2355175	-27.2355003 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	11.2039785	11.2040005 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	1749	1749 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	11.7138004	11.7138004 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-218.035004	-218.035004 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-31.1130066	-31.1130009 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.33 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.66400003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.54200006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	133.104004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	671.512024
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0329999998
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0140000004
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.1230000003
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.1180000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.1070000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.1040000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	831.671021
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	382.882996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.5469999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-22.8390007
MtrCtrl_Vecu_Volt_M_f32[0]	25.4869995
MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001
MtrCurrDaxPrevIntg_Volt_M_f32	10.5640001
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	163.561005
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxFinalRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxPrevIntg_Volt_M_f32	15.6246996
MtrCurrQaxRef_Amp_M_f32[0]	205.820999
MtrCurrQaxRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxRpl_Amp_M_f32[0]	-216.921997
MtrCurrQaxRpl_Amp_M_f32[1]	-184.923996
MtrPosComputationDelay_Rad_M_f32[0]	4.51300001
MtrPosComputationDelay_Rad_M_f32[1]	0.477999985
MtrVoltDax_Volt_M_f32[0]	15.5469999
MtrVoltDax_Volt_M_f32[1]	-22.8390007
MtrVoltQax_Volt_M_f32[0]	15.5469999
MtrVoltQax_Volt_M_f32[1]	-22.8390007
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.418000013
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.851999998
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0666999966
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0399999991
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	41423.8984
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.516300023
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	41423.8984
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.516300023
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	1592.43005
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	5074.45996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.137999997
k_MtrCtrlVirtualResQax_Ohm_f32	0.172000006
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpiEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.20600033		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.79999995		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	21.5585003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-13.408		
k_VoltSatQaxPolyCoeff_Uls_f32	22.0909996		
k_deadtimeVScale_Uls_f32	0.973999977		
t_CommOffsetTblX_Uls_u3p13[0]	1614		
t_CommOffsetTblX_Uls_u3p13[1]	6513		
t_CommOffsetTblY_Cnt_u16[0]	170		
t_CommOffsetTblY_Cnt_u16[1]	1069		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	361		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-72.4260025		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1069	1069	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63832	63832 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-185.429016	-185.429001 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	26.3190727	26.3190994 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	6.55455494	6.55456018 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18824	18824 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997	-216.921997 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-185.429016	-185.429001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.34 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0930000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.148
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-987.179016

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-952.34198
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00999999978
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.40900004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.56299996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-247.072998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-40.618
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	16.8080006
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993
MtrCurrDaxPrevIntg_Volt_M_f32	-21.3630009
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	91.9309998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxFinalRef_Amp_M_f32[1]	183.065002
MtrCurrQaxPrevIntg_Volt_M_f32	20.7061996
MtrCurrQaxRef_Amp_M_f32[0]	-69.0940018
MtrCurrQaxRef_Amp_M_f32[1]	161.973007
MtrCurrQaxRpl_Amp_M_f32[0]	-166.035004
MtrCurrQaxRpl_Amp_M_f32[1]	183.065002
MtrPosComputationDelay_Rad_M_f32[0]	-1.15100002
MtrPosComputationDelay_Rad_M_f32[1]	0.337000012
MtrVoltDax_Volt_M_f32[0]	-7.3130002
MtrVoltDax_Volt_M_f32[1]	3.05299997
MtrVoltQax_Volt_M_f32[0]	-7.3130002
MtrVoltQax_Volt_M_f32[1]	3.05299997
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0839999989
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.887000024
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.235300004
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.460000008
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	4218.1001
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.665600002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	4218.1001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.665600002
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5847.47021
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	1025.58997
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0179999992
k_MtrCtrlVirualResQax_Ohm_f32	0.0780000016
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	12.6604004
k_MtrVoltDaxIntegLoLim_Volt_f32	-3.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	22.7973995
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-4.08099985
k_VoltSatQaxPolyCoeff_Uls_f32	7.89599991
k_deadtimeVScale_Uls_f32	0.961000025
t_CommOffsetTblX_Uls_u3p13[0]	1147
t_CommOffsetTblX_Uls_u3p13[1]	4096
t_CommOffsetTblY_Cnt_u16[0]	189
t_CommOffsetTblY_Cnt_u16[1]	988
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3069
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	988	988	✓
MtrCntl_Write_ModlDx_Uls_u16p16(val)	62980	62980 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-113.022995	-113.023003 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	18.2308731	18.2308998 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	2.63592601	2.63593006 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18401	18401 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-3.5	-3.5 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-166.035004	-166.035004 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-113.022995	-113.023003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlDx_Uls_u16p16	1	MtrCntl_Write_ModlDx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.35 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0630000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.588999987
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.73000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-655.848999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-834.401001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0630000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.720000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65400004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	868.789001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-349.798004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997
MtrCurrDaxPrevIntg_Volt_M_f32	3.15400004
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	21.4759998
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.289001
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.235992
MtrCurrQaxPrevIntg_Volt_M_f32	28.3425999

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[0]	-132.813004		
MtrCurrQaxRef_Amp_M_f32[1]	-9.14299965		
MtrCurrQaxRpl_Amp_M_f32[0]	140.289001		
MtrCurrQaxRpl_Amp_M_f32[1]	178.235992		
MtrPosComputationDelay_Rad_M_f32[0]	-4.09700012		
MtrPosComputationDelay_Rad_M_f32[1]	-1.01900005		
MtrVoltDax_Volt_M_f32[0]	-8.79500008		
MtrVoltDax_Volt_M_f32[1]	27.5049992		
MtrVoltQax_Volt_M_f32[0]	-8.79500008		
MtrVoltQax_Volt_M_f32[1]	27.5049992		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.499000013		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.825999975		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.904100001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.944000006		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21877.4004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.363599986		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	21877.4004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.363599986		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	1867.13		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	506.598999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.180999994		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0680000037		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.13619995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.474		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	7.38500023		
k_VoltSatQaxPolyCoeff_Uls_f32	-23.0559998		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	3464		
t_CommOffsetTblX_Uls_u3p13[1]	6250		
t_CommOffsetTblY_Cnt_u16[0]	1218		
t_CommOffsetTblY_Cnt_u16[1]	1360		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3103		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1360	1360	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63700	63700 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-208.854996	-208.854996 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-7.57722521	-7.57722998 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.40297616	1.40297997 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40433	40433 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.289001	140.289001 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-208.854996	-208.854996 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.36 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.47299999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-143.399002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-302.690002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.386999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.847
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-611.046997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	804.908997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_Vecu_Volt_M_f32[0]	17.9899998
MtrCtrl_Vecu_Volt_M_f32[1]	20.3500004
MtrCurrDaxPrevIntg_Volt_M_f32	17.5130005
MtrCurrDaxRef_Amp_M_f32[0]	-208.287994
MtrCurrDaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxCog_Amp_M_f32	-124.709999
MtrCurrQaxFinalRef_Amp_M_f32[0]	91.8850021
MtrCurrQaxFinalRef_Amp_M_f32[1]	182.261002
MtrCurrQaxPrevIntg_Volt_M_f32	22.6252003
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32[0]	91.8850021
MtrCurrQaxRpl_Amp_M_f32[1]	182.261002
MtrPosComputationDelay_Rad_M_f32[0]	0.372999996
MtrPosComputationDelay_Rad_M_f32[1]	1.69299996
MtrVoltDax_Volt_M_f32[0]	-22.3460007
MtrVoltDax_Volt_M_f32[1]	-17.8169994
MtrVoltQax_Volt_M_f32[0]	-22.3460007
MtrVoltQax_Volt_M_f32[1]	-17.8169994

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.959999979		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0370000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.3393000007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.303999999		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	33920.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.583899975		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	33920.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.583899975		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	211.854996		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1900.18005		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.156000003		
k_MtrCtrlVirtualResQax_Ohm_f32	0.142000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.5790997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-3.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	21.0468998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.6560001		
k_VoltSatQaxPolyCoeff_Uls_f32	6.90999985		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6160		
t_CommOffsetTblX_Uls_u3p13[1]	6291		
t_CommOffsetTblY_Cnt_u16[0]	1130		
t_CommOffsetTblY_Cnt_u16[1]	1422		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-184.522003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	758		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	80.8180008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1422	1422	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	62849	62849 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-113.348999	-113.348999 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-9.24689293	-9.24689007 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	14.5650482	14.5649996 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	63526	63526 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-113.348999	-113.348999 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	182.261002	182.261002 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.37 (Repeat Count = 1)



Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-69.0940018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	161.973007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0179999992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0240000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.53400004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.579999983
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-341.976013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-806.22998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.123999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0790000036
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0839999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.657000005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.386000007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	268.286011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-542.14502
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-15.1960001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-2.83699989
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.448
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.9260006
MtrCtrl_Vecu_Volt_M_f32[0]	26.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	29.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	-17.0869999
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-185.608994
MtrCurrQaxFinalRef_Amp_M_f32[0]	-218.035004
MtrCurrQaxFinalRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxPrevIntg_Volt_M_f32	15.4982004
MtrCurrQaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxRpl_Amp_M_f32[0]	-218.035004
MtrCurrQaxRpl_Amp_M_f32[1]	11.6370001
MtrPosComputationDelay_Rad_M_f32[0]	-3.875
MtrPosComputationDelay_Rad_M_f32[1]	2.73300004
MtrVoltDax_Volt_M_f32[0]	-23.448
MtrVoltDax_Volt_M_f32[1]	-24.9260006
MtrVoltQax_Volt_M_f32[0]	-23.448
MtrVoltQax_Volt_M_f32[1]	-24.9260006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.63499999
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.819999993
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.874000013
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.578000009
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20769.3008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.513000011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	20769.3008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.513000011
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	670.247986
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	2501.06006
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.188999996
k_MtrCtrlVirtualResQax_Ohm_f32	0.138999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	8.58329964

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.80000019		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.8449993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	10.2069998		
k_VoltSatQaxPolyCoeff_Uls_f32	0.906000018		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTblX_Uls_u3p13[0]	1016		
t_CommOffsetTblX_Uls_u3p13[1]	2286		
t_CommOffsetTblY_Cnt_u16[0]	110		
t_CommOffsetTblY_Cnt_u16[1]	537		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-197.354996		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1734		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1734	1734	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.38547489	-0.38547501 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	30.2225418	30.2224998 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24985	24985 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	11.6370001	11.6370001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.38 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.92999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.70000005

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	524.476013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-993.447021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0329999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0140000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.118000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.713999987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-823.801025
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-751.585022
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	4.92999983
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	14.6809998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.5469999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-22.8390007
MtrCtrl_Vecu_Volt_M_f32[0]	16.882
MtrCtrl_Vecu_Volt_M_f32[1]	19.2420006
MtrCurrDaxPrevIntg_Volt_M_f32	-23.8190002
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxCog_Amp_M_f32	163.561005
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxFinalRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxPrevIntg_Volt_M_f32	8.19719982
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	-216.921997
MtrCurrQaxRpl_Amp_M_f32[1]	-184.923996
MtrPosComputationDelay_Rad_M_f32[0]	0.691999972
MtrPosComputationDelay_Rad_M_f32[1]	1.05700004
MtrVoltDax_Volt_M_f32[0]	15.5469999
MtrVoltDax_Volt_M_f32[1]	-22.8390007
MtrVoltQax_Volt_M_f32[0]	15.5469999
MtrVoltQax_Volt_M_f32[1]	-22.8390007
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.141000003
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0670000017
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.445899993
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0399999991
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	38607.8008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.00150000001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	38607.8008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.00150000001
SlowDataAccessBufIndex_Cnt_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3129.08008
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	51.4420013
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.104999997
k_MtrCtrlVirualResQax_Ohm_f32	0.0329999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	1.54809999
k_MtrVoltDaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	14.2641001
k_MtrVoltQaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	10.2329998
k_VoltSatQaxPolyCoeff_Uls_f32	-11.7980003
k_deadtimeVScale_Uls_f32	0.962000012
t_CommOffsetTblX_Uls_u3p13[0]	1408
t_CommOffsetTblX_Uls_u3p13[1]	3505
t_CommOffsetTblY_Cnt_u16[0]	100
t_CommOffsetTblY_Cnt_u16[1]	429
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	152.016006
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4045
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-40.9220009

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	429	429	✓
MtrCntl_Write_ModlIdx_Uls_u16p16(val)	63045	63045 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	185.149994	185.149994 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	10.0092726	10.0093002 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-15.5712671	-15.5712996 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37834	37834 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-7.5	-7.5 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997	-216.921997 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	185.149994	185.149994 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlIdx_Uls_u16p16	1	MtrCntl_Write_ModlIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.39 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc(ptr)	target_MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc_ptr
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(ptr)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_ptr
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(ptr)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_ptr
MtrCntl_Read_SysState_Cnt_Enum(ptr)	target_MtrCntl_Read_SysState_Cnt_Enum_ptr
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.173996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-213.335007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80900002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.64300001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-495.540985
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-831.38501
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0869999975
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.71700001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.625
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	799.594971
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	445.729004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.9510002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.6159992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5.70200014
MtrCtrl_Vecu_Volt_M_f32[1]	8.06200027
MtrCurrDaxPrevIntg_Volt_M_f32	5.15299988
MtrCurrDaxRef_Amp_M_f32[0]	-105.246002
MtrCurrDaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxCog_Amp_M_f32	-209.716003
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008		
MtrCurrQaxPrevIntg_Volt_M_f32	15.6709003		
MtrCurrQaxRef_Amp_M_f32[0]	106.072998		
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002		
MtrCurrQaxRpl_Amp_M_f32[0]	-82.2979965		
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008		
MtrPosComputationDelay_Rad_M_f32[0]	3.39899993		
MtrPosComputationDelay_Rad_M_f32[1]	2.32800007		
MtrVoltDax_Volt_M_f32[0]	-31		
MtrVoltDax_Volt_M_f32[1]	-31		
MtrVoltQax_Volt_M_f32[0]	-31		
MtrVoltQax_Volt_M_f32[1]	-31		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.699000001		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.828000009		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.369500011		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.476000011		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21678.8008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.862100005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	21678.8008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.862100005		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3473.06006		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	466.734985		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVrualResDax_Ohm_f32	0.0130000003		
k_MtrCtrlVrualResQax_Ohm_f32	0.189999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.2995005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.0296993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	22.2140007		
k_VoltSatQaxPolyCoeff_Uls_f32	-4.26499987		
k_deadtimeVScale_Uls_f32	0.958000004		
t_CommOffsetTblX_Uls_u3p13[0]	1556		
t_CommOffsetTblX_Uls_u3p13[1]	5071		
t_CommOffsetTblY_Cnt_u16[0]	718		
t_CommOffsetTblY_Cnt_u16[1]	721		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	54.1119995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1747		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	75.0830002		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	721	721	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	62783	62783 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1.23833871	1.23834002 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	5.32030106	5.3203001 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37838	37838 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-2.5999999	-2.5999999 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008	46.8180008 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.40 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.122000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10899997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.992999971
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-192.371002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	695.664001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00800000038
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0189999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.611000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.84599996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	875.080017
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-275.667999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	29.0550003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.6779995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	31
MtrCtrl_Vecu_Volt_M_f32[0]	18.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	20.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-18.4589996
MtrCurrDaxRef_Amp_M_f32[0]	-213.026993
MtrCurrDaxRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxCog_Amp_M_f32	77.189003
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004
MtrCurrQaxPrevIntg_Volt_M_f32	15.4617996
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpl_Amp_M_f32[0]	160.044006
MtrCurrQaxRpl_Amp_M_f32[1]	165.242004
MtrPosComputationDelay_Rad_M_f32[0]	-3.51399994
MtrPosComputationDelay_Rad_M_f32[1]	-2.17799997
MtrVoltDax_Volt_M_f32[0]	31
MtrVoltDax_Volt_M_f32[1]	31
MtrVoltQax_Volt_M_f32[0]	31
MtrVoltQax_Volt_M_f32[1]	31

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.495000005		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.509999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.244599998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.736000001		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	18254.6992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.245199993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18254.6992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.245199993		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	1865.18005		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7841.00977		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.171000004		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0909999982		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	4.52769995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.1113997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-15.96		
k_VoltSatQaxPolyCoeff_Uls_f32	16.2980003		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	401		
t_CommOffsetTblX_Uls_u3p13[1]	1457		
t_CommOffsetTblY_Cnt_u16[0]	1020		
t_CommOffsetTblY_Cnt_u16[1]	1562		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-17.6900005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	951		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	74.0660019		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	951	951	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.9265556	-14.9266005 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	26.1750889	26.1751003 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37413	37413 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	4.52769995	4.52769995 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006	160.044006 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
InplVarXY_u16_u16Xu16Y_Cnt	1	InplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.41 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0480000004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.11199999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.02100003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	210.968002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-929.856018
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0649999976
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.40199995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0790000036
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	203.302002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	608.874023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.2709999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0
MtrCtrl_Vecu_Volt_M_f32[0]	22.7800007
MtrCtrl_Vecu_Volt_M_f32[1]	25.1399994
MtrCurrDaxPrevIntg_Volt_M_f32	-4.07800007
MtrCurrDaxRef_Amp_M_f32[0]	-212.632996
MtrCurrDaxRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxCog_Amp_M_f32	-145.169006
MtrCurrQaxFinalRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972
MtrCurrQaxPrevIntg_Volt_M_f32	30.3006992
MtrCurrQaxRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxRpl_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRpl_Amp_M_f32[1]	-216.972
MtrPosComputationDelay_Rad_M_f32[0]	4.01200008
MtrPosComputationDelay_Rad_M_f32[1]	-3.62100005
MtrVoltDax_Volt_M_f32[0]	0
MtrVoltDax_Volt_M_f32[1]	0
MtrVoltQax_Volt_M_f32[0]	0
MtrVoltQax_Volt_M_f32[1]	0
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.264999986
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.444000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.477999985
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.499000013
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	20.7000008
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23863
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404900014
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	20.7000008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	23863
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.404900014
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	4784.52979
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	1499.40002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0649999976
k_MtrCtrlVirtualResQax_Ohm_f32	0.0179999992
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.72659993		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.3562002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-21.0760002		
k_VoltSatQaxPolyCoeff_Uls_f32	22.4570007		
k_deadtimeVScale_Uls_f32	0.961000025		
t_CommOffsetTblX_Uls_u3p13[0]	868		
t_CommOffsetTblX_Uls_u3p13[1]	1049		
t_CommOffsetTblY_Cnt_u16[0]	1020		
t_CommOffsetTblY_Cnt_u16[1]	1034		
target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val	-214.828995		
target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1164		
target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val	-149.003006		
target_MtrCntl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	1034	1034	✔
MtrCntl_Write_ModIdx_Uls_u16p16(val)	62980	62980 ± 1	✔
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	44.3240051	44.3240013 ± 7.81E-03	✔
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	13.7315779	13.7315998 ± 4.88E-04	✔
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	17.04949	17.0494995 ± 4.88E-04	✔
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	48919	48919 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	44.3240051	44.3240013 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972	-216.972 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.42 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.101999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0430000015
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.1209999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.802999973
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.56299996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-946.299988

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-752.830994
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.944999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.273
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-629.994019
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	916.687988
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.32400036
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-26.5540009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-15.9300003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.7889996
MtrCtrl_Vecu_Volt_M_f32[0]	21.2910004
MtrCtrl_Vecu_Volt_M_f32[1]	23.6509991
MtrCurrDaxPrevIntg_Volt_M_f32	0.981999993
MtrCurrDaxRef_Amp_M_f32[0]	205.820999
MtrCurrDaxRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxCog_Amp_M_f32	-100.035004
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxFinalRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxPrevIntg_Volt_M_f32	12.4246998
MtrCurrQaxRef_Amp_M_f32[0]	140.289001
MtrCurrQaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxRpl_Amp_M_f32[0]	-146.723007
MtrCurrQaxRpl_Amp_M_f32[1]	-121.943001
MtrPosComputationDelay_Rad_M_f32[0]	-3.15400004
MtrPosComputationDelay_Rad_M_f32[1]	2.54299998
MtrVoltDax_Volt_M_f32[0]	-15.9300003
MtrVoltDax_Volt_M_f32[1]	-19.7889996
MtrVoltQax_Volt_M_f32[0]	-15.9300003
MtrVoltQax_Volt_M_f32[1]	-19.7889996
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.592999995
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.638999999
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0196000002
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0350000001
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1838.12
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1611
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	1838.12
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.1611
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6671
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3327.94995
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.165999994
k_MtrCtrlVirtualResQax_Ohm_f32	0.196999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	13.8471003
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.69999981
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	4.8657999
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	12.7969999
k_VoltSatQaxPolyCoeff_Uls_f32	-5.26999998
k_deadtimeVScale_Uls_f32	0.968999982
t_CommOffsetTblX_Uls_u3p13[0]	3006
t_CommOffsetTblX_Uls_u3p13[1]	6971
t_CommOffsetTblY_Cnt_u16[0]	136
t_CommOffsetTblY_Cnt_u16[1]	593
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-96.3310013
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3660
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-124.758003
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	593	593	✓
MtrCntl_Write_ModlIdx_Uls_u16p16(val)	63504	63504 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-13.7191525	-13.7192001 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-15.4085112	-15.4084997 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	7458	7458 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	13.8471003	13.8471003 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-121.943001	-121.943001 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlIdx_Uls_u16p16	1	MtrCntl_Write_ModlIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.43 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00800000038
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.064000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.925000012
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.28699994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-467.540985
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	559.55603
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0729999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0450000018
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.53100002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.621999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	363.421997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-896.711975
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	3.87299991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2730007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.8899994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.02799988
MtrCtrl_Vecu_Volt_M_f32[0]	12.1129999
MtrCtrl_Vecu_Volt_M_f32[1]	14.4729996
MtrCurrDaxPrevIntg_Volt_M_f32	22.4890003
MtrCurrDaxRef_Amp_M_f32[0]	-69.0940018
MtrCurrDaxRef_Amp_M_f32[1]	161.973007
MtrCurrQaxCog_Amp_M_f32	-184.522003
MtrCurrQaxFinalRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxFinalRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxPrevIntg_Volt_M_f32	24.3127995

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[0]	91.8850021		
MtrCurrQaxRef_Amp_M_f32[1]	182.261002		
MtrCurrQaxRpl_Amp_M_f32[0]	-208.287994		
MtrCurrQaxRpl_Amp_M_f32[1]	-27.9839993		
MtrPosComputationDelay_Rad_M_f32[0]	-3.4059999		
MtrPosComputationDelay_Rad_M_f32[1]	1.83000004		
MtrVoltDax_Volt_M_f32[0]	18.8899994		
MtrVoltDax_Volt_M_f32[1]	6.02799988		
MtrVoltQax_Volt_M_f32[0]	18.8899994		
MtrVoltQax_Volt_M_f32[1]	6.02799988		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.287		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.202999994		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0443000011		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.128999993		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	30983.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.328200012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	30983.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.328200012		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	1324.80005		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	754.981018		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0610000007		
k_MtrCtrlVirtualResQax_Ohm_f32	0.112000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.0340996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.80000019		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.7854996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	0.541999996		
k_VoltSatQaxPolyCoeff_Uls_f32	5.81099987		
k_deadtimeVScale_Uls_f32	0.957000017		
t_CommOffsetTblX_Uls_u3p13[0]	205		
t_CommOffsetTblX_Uls_u3p13[1]	4096		
t_CommOffsetTblY_Cnt_u16[0]	34		
t_CommOffsetTblY_Cnt_u16[1]	96		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-168.113007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2573		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	96	96	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62717	62717 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-9.07771492	-9.07771015 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	7.2092185	7.20921993 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	20627	20627 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-27.9839993	-27.9839993 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.44 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.869000018
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.84599996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	181.75
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	166.714005
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.98500001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.287999988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-837.336975
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-656.465027
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.1380005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.920000017
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.16100025
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	24.6310005
MtrCtrl_Vecu_Volt_M_f32[0]	26.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	28.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-4.96700001
MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
MtrCurrDaxRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxCog_Amp_M_f32	-197.354996
MtrCurrQaxFinalRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxFinalRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxPrevIntg_Volt_M_f32	18.0771999
MtrCurrQaxRef_Amp_M_f32[0]	-218.035004
MtrCurrQaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxRpl_Amp_M_f32[0]	31.5869999
MtrCurrQaxRpl_Amp_M_f32[1]	-186.395996
MtrPosComputationDelay_Rad_M_f32[0]	-2.08400011
MtrPosComputationDelay_Rad_M_f32[1]	2.81699991
MtrVoltDax_Volt_M_f32[0]	-8.16100025
MtrVoltDax_Volt_M_f32[1]	24.6310005
MtrVoltQax_Volt_M_f32[0]	-8.16100025
MtrVoltQax_Volt_M_f32[1]	24.6310005

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.602999985		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.887000024		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.169799998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.649999976		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8419.69043		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0151000004		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	8419.69043		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.0151000004		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	771.372986		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	798.940002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0700000003		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0270000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.6986008		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.9000001		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	10.9334002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.1189995		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.09100008		
k_deadtimeVScale_Uls_f32	0.995999992		
t_CommOffsetTblX_Uls_u3p13[0]	2212		
t_CommOffsetTblX_Uls_u3p13[1]	4742		
t_CommOffsetTblY_Cnt_u16[0]	109		
t_CommOffsetTblY_Cnt_u16[1]	367		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-5.66300011		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1243		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	367	367	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	65273	65273 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-52.2670135	-52.2669983 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.133214787	0.133214995 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-26.254221	-26.2542 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	10978	10978 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-52.2670135	-52.2669983 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-186.395996	-186.395996 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.45 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0370000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80200005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.740999997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-489.436005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	938.341003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0199999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10899997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.479999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	916.997009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1002.97998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.1960001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-2.83699989
MtrCtrl_Vecu_Volt_M_f32[0]	5.33099985
MtrCtrl_Vecu_Volt_M_f32[1]	7.69099998
MtrCurrDaxPrevIntg_Volt_M_f32	6.17600012
MtrCurrDaxRef_Amp_M_f32[0]	-146.173996
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxCog_Amp_M_f32	152.016006
MtrCurrQaxFinalRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxPrevIntg_Volt_M_f32	1.08770001
MtrCurrQaxRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxRpl_Amp_M_f32[0]	-133.947006
MtrCurrQaxRpl_Amp_M_f32[1]	75.7020035
MtrPosComputationDelay_Rad_M_f32[0]	-6.23799992
MtrPosComputationDelay_Rad_M_f32[1]	2.11599994
MtrVoltDax_Volt_M_f32[0]	15.1960001
MtrVoltDax_Volt_M_f32[1]	-2.83699989
MtrVoltQax_Volt_M_f32[0]	15.1960001
MtrVoltQax_Volt_M_f32[1]	-2.83699989
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.432999998
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0109999999
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.335599989
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.851999998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12079.9004
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.620700002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12079.9004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.620700002
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	2475.81006
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	2645.06006
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.179000005
k_MtrCtrlVirtualResQax_Ohm_f32	0.0120000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	7.70650005

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.0999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.614899993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.26999998		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9449997		
k_deadtimeVScale_Uls_f32	0.962000012		
t_CommOffsetTblX_Uls_u3p13[0]	4809		
t_CommOffsetTblX_Uls_u3p13[1]	5553		
t_CommOffsetTblY_Cnt_u16[0]	663		
t_CommOffsetTblY_Cnt_u16[1]	905		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	114.946999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1956		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1956	1956	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	0.381894678	0.381895989 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-4.79481602	-4.79481983 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32410	32410 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035	75.7020035 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBiinearOut	1	CalLowPassFiltBiinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
CalLowPassFiltBiinearOut	1	CalLowPassFiltBiinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.46 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.579
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.407999992

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-297.562012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	435.532013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0189999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.878000021
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.247999996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	791.299988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-595.505981
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	10.0620003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	10.7410002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-4.92999983
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	14.6809998
MtrCtrl_Vecu_Volt_M_f32[0]	18.2229996
MtrCtrl_Vecu_Volt_M_f32[1]	20.5830002
MtrCurrDaxPrevIntg_Volt_M_f32	2.41400003
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	54.1119995
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	11.5314999
MtrCurrQaxRef_Amp_M_f32[0]	138.595001
MtrCurrQaxRef_Amp_M_f32[1]	-157.388
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	5.76999998
MtrPosComputationDelay_Rad_M_f32[1]	4.8210001
MtrVoltDax_Volt_M_f32[0]	-4.92999983
MtrVoltDax_Volt_M_f32[1]	14.6809998
MtrVoltQax_Volt_M_f32[0]	-4.92999983
MtrVoltQax_Volt_M_f32[1]	14.6809998
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.890999973
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.76700002
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.976100028
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.837000012
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47476.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.878300011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47476.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.878300011
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	4075.97998
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1135.18994
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0430000015
k_MtrCtrlVirualResQax_Ohm_f32	0.0289999992
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	6.86969995
k_MtrVoltDaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	0.205699995
k_MtrVoltQaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-11.4569998
k_VoltSatQaxPolyCoeff_Uls_f32	0.670000017
k_deadtimeVScale_Uls_f32	0.958000004
t_CommOffsetTblX_Uls_u3p13[0]	4424
t_CommOffsetTblX_Uls_u3p13[1]	7552
t_CommOffsetTblY_Cnt_u16[0]	1052
t_CommOffsetTblY_Cnt_u16[1]	1891
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	3.89299989
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1889
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	1889	1889	✓
MtrCntl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	7.32646561	7.32645988 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	28.7801018	28.7800999 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	52885	52885 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.47 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	24.6130009
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-20.9400005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.270000011
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.89100003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	898.598999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	416.613007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0199999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.699000001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	378.188995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	157.612
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	26.9950008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	7.13500023
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	30.9510002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-20.6159992
MtrCtrl_Vecu_Volt_M_f32[0]	28.0200005
MtrCtrl_Vecu_Volt_M_f32[1]	30.3799992
MtrCurrDaxPrevIntg_Volt_M_f32	17.8910007
MtrCurrDaxRef_Amp_M_f32[0]	171.485992
MtrCurrDaxRef_Amp_M_f32[1]	163.787003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxCog_Amp_M_f32	-17.6900005		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	10.2707996		
MtrCurrQaxRef_Amp_M_f32[0]	-100.282997		
MtrCurrQaxRef_Amp_M_f32[1]	-120.248001		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	5.76399994		
MtrPosComputationDelay_Rad_M_f32[1]	5.0999999		
MtrVoltDax_Volt_M_f32[0]	30.9510002		
MtrVoltDax_Volt_M_f32[1]	-20.6159992		
MtrVoltQax_Volt_M_f32[0]	30.9510002		
MtrVoltQax_Volt_M_f32[1]	-20.6159992		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.978999972		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.845000029		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.128399998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.428000003		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	3431.37012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.436399996		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	3431.37012		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.436399996		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6201.14014		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	194.557007		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0469999984		
k_MtrCtrlVirualResQax_Ohm_f32	0.164000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	26.1525002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	4.69950008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-12.8179998		
k_VoltSatQaxPolyCoeff_Uls_f32	12.3579998		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	705		
t_CommOffsetTblX_Uls_u3p13[1]	4996		
t_CommOffsetTblY_Cnt_u16[0]	1077		
t_CommOffsetTblY_Cnt_u16[1]	1690		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	45.3779984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4409		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1690	1690	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63700	63700 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-35.8349991	-35.8349991 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-28.6776619	-28.6777 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.04093504	-7.04092979 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	34300	34300 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-35.8349991	-35.8349991 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.48 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0469999984
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70599997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.759000003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-535.288025
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-568.218018
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0160000008
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.624000013
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.850000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-327.623993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	577.963989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	21.0270004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-7.53299999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	29.0550003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.6779995
MtrCtrl_Vecu_Volt_M_f32[0]	6.83099985
MtrCtrl_Vecu_Volt_M_f32[1]	9.19099998
MtrCurrDaxPrevIntg_Volt_M_f32	15.6529999
MtrCurrDaxRef_Amp_M_f32[0]	106.072998
MtrCurrDaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxCog_Amp_M_f32	-214.828995
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxPrevIntg_Volt_M_f32	5.7888999
MtrCurrQaxRef_Amp_M_f32[0]	-68.6760025
MtrCurrQaxRef_Amp_M_f32[1]	-96.776001
MtrCurrQaxRpl_Amp_M_f32[0]	-212.632996
MtrCurrQaxRpl_Amp_M_f32[1]	-205.085007
MtrPosComputationDelay_Rad_M_f32[0]	-0.913999975
MtrPosComputationDelay_Rad_M_f32[1]	3.25399995
MtrVoltDax_Volt_M_f32[0]	29.0550003
MtrVoltDax_Volt_M_f32[1]	-17.6779995
MtrVoltQax_Volt_M_f32[0]	29.0550003
MtrVoltQax_Volt_M_f32[1]	-17.6779995

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.610000014		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.959999979		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.593599975		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.291000009		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13842.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.35710001		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	13842.5		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.35710001		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	4211.16016		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3085.33008		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0430000015		
k_MtrCtrlVirtualResQax_Ohm_f32	0.119000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.83409977		
k_MtrVoltDaxIntegLoLim_Volt_f32	-12.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.2000999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-12.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3659992		
k_VoltSatQaxPolyCoeff_Uls_f32	8.98099995		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTblX_Uls_u3p13[0]	1580		
t_CommOffsetTblX_Uls_u3p13[1]	2671		
t_CommOffsetTblY_Cnt_u16[0]	161		
t_CommOffsetTblY_Cnt_u16[1]	1743		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	103.652		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4487		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1743	1743	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64946	64946 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	3.61272049	3.61272001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	8.36116123	8.36116028 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38195	38195 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996	-212.632996 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.49 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	140.289001
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	178.235992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0430000015
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.829
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.39300001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-515.534973
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.975006
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.120999999
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0909999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0199999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.954
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.512000024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	305.28299
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-513.950012
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.9390001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-24.3929996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.0189991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	13.2709999
MtrCtrl_Vecu_Volt_M_f32[0]	8.61200047
MtrCtrl_Vecu_Volt_M_f32[1]	10.9720001
MtrCurrDaxPrevIntg_Volt_M_f32	-19.3589993
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	-96.3310013
MtrCurrQaxFinalRef_Amp_M_f32[0]	205.820999
MtrCurrQaxFinalRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxPrevIntg_Volt_M_f32	14.2783003
MtrCurrQaxRef_Amp_M_f32[0]	-139.906998
MtrCurrQaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxRpl_Amp_M_f32[0]	205.820999
MtrCurrQaxRpl_Amp_M_f32[1]	-206.792007
MtrPosComputationDelay_Rad_M_f32[0]	2.22900009
MtrPosComputationDelay_Rad_M_f32[1]	-0.579999983
MtrVoltDax_Volt_M_f32[0]	-22.0189991
MtrVoltDax_Volt_M_f32[1]	13.2709999
MtrVoltQax_Volt_M_f32[0]	-22.0189991
MtrVoltQax_Volt_M_f32[1]	13.2709999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.791999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0799999982
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.619400024
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.783999979
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	865.320007
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	46503.6992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.198599994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46503.6992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.198599994
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	7813.02979
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	6623.8501
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.120999999
k_MtrCtrlVirtualResQax_Ohm_f32	0.100000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	30.1000996

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	5.77519989		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.5739994		
k_VoltSatQaxPolyCoeff_Uls_f32	-11.3669996		
k_deadtimeVScale_Uls_f32	0.952000022		
t_CommOffsetTblX_Uls_u3p13[0]	908		
t_CommOffsetTblX_Uls_u3p13[1]	5956		
t_CommOffsetTblY_Cnt_u16[0]	578		
t_CommOffsetTblY_Cnt_u16[1]	1247		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	99.348999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2730		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	2730	2730	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-4.1812501	-4.1812501 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	2.27480721	2.27481008 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	48299	48299 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	205.820999	205.820999 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.50 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	91.8850021
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	182.261002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.03099999995
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.03299999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.05799999983
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.545000017
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.884000003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-366.040009

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-870.554993
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0189999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.690999985
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.344000012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	592.877014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	559.130005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	5.68100023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.06599998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.32400036
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-26.5540009
MtrCtrl_Vecu_Volt_M_f32[0]	24.4610004
MtrCtrl_Vecu_Volt_M_f32[1]	26.8209991
MtrCurrDaxPrevIntg_Volt_M_f32	29.6800003
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	-168.113007
MtrCurrQaxFinalRef_Amp_M_f32[0]	-69.0940018
MtrCurrQaxFinalRef_Amp_M_f32[1]	161.973007
MtrCurrQaxPrevIntg_Volt_M_f32	18.2201996
MtrCurrQaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxRpl_Amp_M_f32[0]	-69.0940018
MtrCurrQaxRpl_Amp_M_f32[1]	161.973007
MtrPosComputationDelay_Rad_M_f32[0]	4.2750001
MtrPosComputationDelay_Rad_M_f32[1]	-2.16300011
MtrVoltDax_Volt_M_f32[0]	-8.32400036
MtrVoltDax_Volt_M_f32[1]	-26.5540009
MtrVoltQax_Volt_M_f32[0]	-8.32400036
MtrVoltQax_Volt_M_f32[1]	-26.5540009
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.785000026
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.55400002
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.91960001
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.871999979
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	20.7000008
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	42029.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0784000009
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	20.7000008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	42029.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0784000009
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	712.458008
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	303.729004
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.100000001
k_MtrCtrlVirtualResQax_Ohm_f32	0.0799999982
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	8.17039967
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	12.5521002
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-24.3269997
k_VoltSatQaxPolyCoeff_Uls_f32	-18.0820007
k_deadtimeVScale_Uls_f32	0.976999998
t_CommOffsetTblX_Uls_u3p13[0]	7
t_CommOffsetTblX_Uls_u3p13[1]	179
t_CommOffsetTblY_Cnt_u16[0]	128
t_CommOffsetTblY_Cnt_u16[1]	452
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	80.5459976
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4498
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	121.994003
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	452	452	✓
MtrCntl_Write_ModlDx_Uls_u16p16(val)	64028	64028 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	154.908997	154.908997 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-17.3200226	-17.3199997 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	16.4666386	16.4666004 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	36134	36134 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	154.908997	154.908997 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	161.973007	161.973007 ± 7.81E-03	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlDx_Uls_u16p16	1	MtrCntl_Write_ModlDx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.51 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-218.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	11.6370001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0489999987
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.17900002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46399999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-613.749023
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-825.028992
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0199999996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.202000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.88499999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	635.659973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-88.5709991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	19.6130009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.3180008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.87299991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	19.2730007
MtrCtrl_Vecu_Volt_M_f32[0]	21.3409996
MtrCtrl_Vecu_Volt_M_f32[1]	23.7010002
MtrCurrDaxPrevIntg_Volt_M_f32	-25.5009995
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	-5.66300011
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxPrevIntg_Volt_M_f32	16.0422993

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[0]	160.044006		
MtrCurrQaxRef_Amp_M_f32[1]	165.242004		
MtrCurrQaxRpl_Amp_M_f32[0]	-132.813004		
MtrCurrQaxRpl_Amp_M_f32[1]	-9.14299965		
MtrPosComputationDelay_Rad_M_f32[0]	-1.83500004		
MtrPosComputationDelay_Rad_M_f32[1]	0.768999994		
MtrVoltDax_Volt_M_f32[0]	3.87299991		
MtrVoltDax_Volt_M_f32[1]	19.2730007		
MtrVoltQax_Volt_M_f32[0]	3.87299991		
MtrVoltQax_Volt_M_f32[1]	19.2730007		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.472000003		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.796000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.4287		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.926999986		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17234.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0538000017		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17234.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0538000017		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	642.700012		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	7742.27002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0280000009		
k_MtrCtrlVirtualResQax_Ohm_f32	0.174999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.6893997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.8882008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	6.35500002		
k_VoltSatQaxPolyCoeff_Uls_f32	21.1189995		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	4506		
t_CommOffsetTblX_Uls_u3p13[1]	5381		
t_CommOffsetTblY_Cnt_u16[0]	1282		
t_CommOffsetTblY_Cnt_u16[1]	1346		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	41.1769981		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3770		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3770	3770	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.727851868	0.727851987 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.78495884	4.78495979 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	47971	47971 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965	-9.14299965 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.52 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0979999974
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.671
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.986000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	129.369003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-141.128998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0659999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0160000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.111000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83399999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	972.747009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	150.199997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	28.1019993
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.381000012
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-30.1380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.920000017
MtrCtrl_Vecu_Volt_M_f32[0]	28.2360001
MtrCtrl_Vecu_Volt_M_f32[1]	30.5960007
MtrCurrDaxPrevIntg_Volt_M_f32	28.816
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	114.946999
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxPrevIntg_Volt_M_f32	22.5016003
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxRpl_Amp_M_f32[0]	-146.173996
MtrCurrQaxRpl_Amp_M_f32[1]	-213.335007
MtrPosComputationDelay_Rad_M_f32[0]	5.08300018
MtrPosComputationDelay_Rad_M_f32[1]	3.41400003
MtrVoltDax_Volt_M_f32[0]	-30.1380005
MtrVoltDax_Volt_M_f32[1]	0.920000017

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-30.1380005		
MtrVoltQax_Volt_M_f32[1]	0.920000017		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.560000002		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.125		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.716899991		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.54400003		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20241.6992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.124300003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	20241.6992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.124300003		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	4748.89014		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5014.08008		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.109999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.0350000001		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.6864996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.9398003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-5.84200001		
k_VoltSatQaxPolyCoeff_Uls_f32	-16.993		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	3030		
t_CommOffsetTblX_Uls_u3p13[1]	5366		
t_CommOffsetTblY_Cnt_u16[0]	554		
t_CommOffsetTblY_Cnt_u16[1]	778		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-30.7789993		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4190		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	48.8400002		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	778	778	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63111	63111 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-33.9630051	-33.9630013 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	18.543644	18.5436001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-19.8871384	-19.8871002 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12422	12422 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-33.9630051	-33.9630013 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007	-213.335007 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.53 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.50699997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.64999998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	491.182007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	987.453979
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0520000011
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.00800000038
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.82099998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.995999992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-73.2539978
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-688.901978
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	4.64300013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.7069998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	4.36100006
MtrCtrl_Vecu_Volt_M_f32[0]	28.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	30.2600002
MtrCurrDaxPrevIntg_Volt_M_f32	-11.5699997
MtrCurrDaxRef_Amp_M_f32[0]	-208.287994
MtrCurrDaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxCog_Amp_M_f32	3.89299989
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993
MtrCurrQaxPrevIntg_Volt_M_f32	25.7052002
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRpl_Amp_M_f32[1]	133.692993
MtrPosComputationDelay_Rad_M_f32[0]	4.79699993
MtrPosComputationDelay_Rad_M_f32[1]	-0.542999983
MtrVoltDax_Volt_M_f32[0]	-26.5079994
MtrVoltDax_Volt_M_f32[1]	4.36100006
MtrVoltQax_Volt_M_f32[0]	-26.5079994
MtrVoltQax_Volt_M_f32[1]	4.36100006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.745000005
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.305999994
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.720300019
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0160000008
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.107100002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.107100002
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	5850.64014
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	2794.15991
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0399999991
k_MtrCtrlVirtualResQax_Ohm_f32	0.150999993
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.24860001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.75800037		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-12.8660002		
k_VoltSatQaxPolyCoeff_Uls_f32	6.20200014		
k_deadtimeVScale_Uls_f32	0.986999989		
t_CommOffsetTblX_Uls_u3p13[0]	4850		
t_CommOffsetTblX_Uls_u3p13[1]	6241		
t_CommOffsetTblY_Cnt_u16[0]	1044		
t_CommOffsetTblY_Cnt_u16[1]	1978		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	23		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	107.702003		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1044	1044	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	27056	27056 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-11.7069998	-11.7069998 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	4.36100006	4.36100006 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	47208	47208 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013	-91.4420013 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.54 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	160.044006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	165.242004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.81200004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.92799997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-519.974976

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-32.9770012
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.050999999
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.061999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.952000022
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.88499999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-957.802979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	641.666016
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	25.9820004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-23.0480003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	10.0620003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	10.7410002
MtrCtrl_Vecu_Volt_M_f32[0]	22.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	24.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	20.1009998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	45.3779984
MtrCurrQaxFinalRef_Amp_M_f32[0]	171.485992
MtrCurrQaxFinalRef_Amp_M_f32[1]	163.787003
MtrCurrQaxPrevIntg_Volt_M_f32	28.1571999
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	171.485992
MtrCurrQaxRpl_Amp_M_f32[1]	163.787003
MtrPosComputationDelay_Rad_M_f32[0]	-3.4230001
MtrPosComputationDelay_Rad_M_f32[1]	-2.05100012
MtrVoltDax_Volt_M_f32[0]	10.0620003
MtrVoltDax_Volt_M_f32[1]	10.7410002
MtrVoltQax_Volt_M_f32[0]	10.0620003
MtrVoltQax_Volt_M_f32[1]	10.7410002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.476999998
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.254999995
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0443000011
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.142000005
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.742399991
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.742399991
SlowDataAccessBufIndex_Cnt_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3971.34009
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5639.2998
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.00300000003
k_MtrCtrlVirualResQax_Ohm_f32	0.00899999961
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	17.7175007
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	4.11920023
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	24.934
k_VoltSatQaxPolyCoeff_Uls_f32	-16.5429993
k_deadtimeVScale_Uls_f32	0.959999979
t_CommOffsetTblX_Uls_u3p13[0]	2114
t_CommOffsetTblX_Uls_u3p13[1]	4735
t_CommOffsetTblY_Cnt_u16[0]	153
t_CommOffsetTblY_Cnt_u16[1]	914
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	177.046997
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4147
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	5.72399998
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	4147	4147	✓
MtrCntl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-4.35074472	-4.35073996 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	2.02756619	2.02757001 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32308	32308 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	171.485992	171.485992 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.55 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0839999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.6830000028
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.86699998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-334.098999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	800.172974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.119999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.77999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.85699999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	130.878998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-255.671997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	14.9390001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.4060001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.9950008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	7.13500023
MtrCtrl_Vecu_Volt_M_f32[0]	18.7189999
MtrCtrl_Vecu_Volt_M_f32[1]	21.0790005
MtrCurrDaxPrevIntg_Volt_M_f32	-24.684
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxCog_Amp_M_f32	103.652
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002		
MtrCurrQaxPrevIntg_Volt_M_f32	18.5097008		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32[0]	106.072998		
MtrCurrQaxRpl_Amp_M_f32[1]	-112.455002		
MtrPosComputationDelay_Rad_M_f32[0]	1.03199995		
MtrPosComputationDelay_Rad_M_f32[1]	4.79300022		
MtrVoltDax_Volt_M_f32[0]	26.9950008		
MtrVoltDax_Volt_M_f32[1]	7.13500023		
MtrVoltQax_Volt_M_f32[0]	26.9950008		
MtrVoltQax_Volt_M_f32[1]	7.13500023		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0390000008		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.84799999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.611599982		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.144999996		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25640.4004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.971499979		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	25640.4004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.971499979		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	4766.68994		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7056.62988		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.114		
k_MtrCtrlVirtualResQax_Ohm_f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.74440002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.79839993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-14.3280001		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.07999992		
k_deadtimeVScale_Uls_f32	0.99000001		
t_CommOffsetTblX_Uls_u3p13[0]	1498		
t_CommOffsetTblX_Uls_u3p13[1]	4940		
t_CommOffsetTblY_Cnt_u16[0]	125		
t_CommOffsetTblY_Cnt_u16[1]	898		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	155		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	898	898	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64880	64880 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-177.592987	-177.593002 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-20.2797813	-20.2798004 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.92063665	4.92063999 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	36092	36092 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998	106.072998 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-177.592987	-177.593002 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_Modldx_Uls_u16p16	1	MtrCtrl_Write_Modldx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.56 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.118000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.26400006
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.94200003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-771.768005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	61.4269981
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0489999987
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.657999992
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.64900005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-279.015015
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-333.037994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.6959991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-27.8540001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	21.0270004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-7.53299999
MtrCtrl_Vecu_Volt_M_f32[0]	22.3540001
MtrCtrl_Vecu_Volt_M_f32[1]	24.7140007
MtrCurrDaxPrevIntg_Volt_M_f32	-18.9759998
MtrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxCog_Amp_M_f32	99.348999
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993
MtrCurrQaxPrevIntg_Volt_M_f32	0.0860000029
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRpl_Amp_M_f32[1]	133.692993
MtrPosComputationDelay_Rad_M_f32[0]	1.44299996
MtrPosComputationDelay_Rad_M_f32[1]	4.22300005
MtrVoltDax_Volt_M_f32[0]	21.0270004
MtrVoltDax_Volt_M_f32[1]	-7.53299999
MtrVoltQax_Volt_M_f32[0]	21.0270004
MtrVoltQax_Volt_M_f32[1]	-7.53299999

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.941999972		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.619000018		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0478000008		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.801999986		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.570299983		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.570299983		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	7521.91016		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5032.43018		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0799999982		
k_MtrCtrlVirtualResQax_Ohm_f32	0.00600000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.1720009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	21.2935009		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.3940001		
k_VoltSatQaxPolyCoeff_Uls_f32	10.5640001		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	6110		
t_CommOffsetTblX_Uls_u3p13[1]	7324		
t_CommOffsetTblY_Cnt_u16[0]	940		
t_CommOffsetTblY_Cnt_u16[1]	1216		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1115		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	20.6149998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1216	1216	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63438	63438 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-157.339996	-157.339996 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-23.0935135	-23.0935001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.24554586	-6.24555016 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24909	24909 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013	-91.4420013 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-157.339996	-157.339996 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.57 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.199
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.528
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-96.7659988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-485.93399
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.1190000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0489999987
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.79999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.797999978
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	839.791992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-829.577026
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.9399996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	29.1739998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-14.9390001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.3929996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	1.27900004
MtrCurrDaxRef_Amp_M_f32[0]	-200.556
MtrCurrDaxRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxCog_Amp_M_f32	80.5459976
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	10.5852003
MtrCurrQaxRef_Amp_M_f32[0]	209.052002
MtrCurrQaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-3.0940001
MtrPosComputationDelay_Rad_M_f32[1]	1.25100005
MtrVoltDax_Volt_M_f32[0]	-14.9390001
MtrVoltDax_Volt_M_f32[1]	-24.3929996
MtrVoltQax_Volt_M_f32[0]	-14.9390001
MtrVoltQax_Volt_M_f32[1]	-24.3929996
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.374000013
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.744000018
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.797999978
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.699000001
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	36325.3984
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.287999988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	36325.3984
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.287999988
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	6822.06006
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	5157.0498
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.163000003
k_MtrCtrlVirtualResQax_Ohm_f32	0.0439999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	6.49800014

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.0617008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	11.9960003		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.5869999		
k_deadtimeVScale_Uls_f32	0.998000026		
t_CommOffsetTblX_Uls_u3p13[0]	220		
t_CommOffsetTblX_Uls_u3p13[1]	5037		
t_CommOffsetTblY_Cnt_u16[0]	980		
t_CommOffsetTblY_Cnt_u16[1]	1528		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1366		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	79.6729965		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1528	1528	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	65404	65404 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	61.0160065	61.0159988 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-3.96529174	-3.96529007 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-13.6866035	-13.6865997 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3438	3438 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	61.0160065	61.0159988 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.58 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	0
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10599995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.44500005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	308.303009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-313.46701
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0149999997		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.119000003		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0820000023		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.80400002		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.768000007		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-984.03302		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-670.601013		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-12.816		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-15.0170002		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	5.68100023		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.06599998		
MtrCtrl_Vecu_Volt_M_f32[0]	20.2549992		
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998		
MtrCurrDaxPrevIntg_Volt_M_f32	-9.55599976		
MtrCurrDaxRef_Amp_M_f32[0]	67.4899979		
MtrCurrDaxRef_Amp_M_f32[1]	119.721001		
MtrCurrQaxCog_Amp_M_f32	41.1769981		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-146.173996		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007		
MtrCurrQaxPrevIntg_Volt_M_f32	13.0853004		
MtrCurrQaxRef_Amp_M_f32[0]	-200.556		
MtrCurrQaxRef_Amp_M_f32[1]	-98.4449997		
MtrCurrQaxRpl_Amp_M_f32[0]	-146.173996		
MtrCurrQaxRpl_Amp_M_f32[1]	-213.335007		
MtrPosComputationDelay_Rad_M_f32[0]	5.96099997		
MtrPosComputationDelay_Rad_M_f32[1]	2.0480001		
MtrVoltDax_Volt_M_f32[0]	5.68100023		
MtrVoltDax_Volt_M_f32[1]	2.06599998		
MtrVoltQax_Volt_M_f32[0]	5.68100023		
MtrVoltQax_Volt_M_f32[1]	2.06599998		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.444000006		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.815999985		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.891499996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0939999968		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	10763.7002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.852599978		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	10763.7002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.852599978		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	1549.32996		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	6542.3501		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.119000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.0289999992		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.6065006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.5948		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	7.44799995		
k_VoltSatQaxPolyCoeff_Uls_f32	0.351999998		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	573		
t_CommOffsetTblX_Uls_u3p13[1]	7569		
t_CommOffsetTblY_Cnt_u16[0]	556		
t_CommOffsetTblY_Cnt_u16[1]	934		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3490		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	0.486999989		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	934	934	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_ModlIdx_Uls_u16p16(val)	63635	63635 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-95.5590057	-95.5589981 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	10.7777771	10.7777996 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-16.4515743	-16.4515991 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	23358	23358 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-95.5590057	-95.5589981 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-213.335007	-213.335007 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlIdx_Uls_u16p16	1	MtrCntl_Write_ModlIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.59 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-115.696999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-141.417007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.980000019
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.497000009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	550.754028
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-584.435974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.45700002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.78799999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	966.106995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	858.828003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	27.9379997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.50300026
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	19.6130009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.3180008
MtrCtrl_Vecu_Volt_M_f32[0]	13.085
MtrCtrl_Vecu_Volt_M_f32[1]	15.4449997
MtrCurrDaxPrevIntg_Volt_M_f32	23.0559998
MtrCurrDaxRef_Amp_M_f32[0]	37.4550018
MtrCurrDaxRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxCog_Amp_M_f32	-30.7789993
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxFinalRef_Amp_M_f32[1]	133.692993
MtrCurrQaxPrevIntg_Volt_M_f32	13.2370005
MtrCurrQaxRef_Amp_M_f32[0]	67.4899979

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[1]	119.721001		
MtrCurrQaxRpl_Amp_M_f32[0]	-91.4420013		
MtrCurrQaxRpl_Amp_M_f32[1]	133.692993		
MtrPosComputationDelay_Rad_M_f32[0]	2.34599996		
MtrPosComputationDelay_Rad_M_f32[1]	1.27100003		
MtrVoltDax_Volt_M_f32[0]	19.6130009		
MtrVoltDax_Volt_M_f32[1]	-29.3180008		
MtrVoltQax_Volt_M_f32[0]	19.6130009		
MtrVoltQax_Volt_M_f32[1]	-29.3180008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.208000004		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.451000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.251300007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.681999981		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	68.5733032		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.957700014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	68.5733032		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.957700014		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2384.97998		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3626.42993		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.108999997		
k_MtrCtrlVirtualResQax_Ohm_f32	0.108999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	25.8696003		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.0412998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-12.5480003		
k_VoltSatQaxPolyCoeff_Uls_f32	-9.86100006		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	1154		
t_CommOffsetTblX_Uls_u3p13[1]	5284		
t_CommOffsetTblY_Cnt_u16[0]	39		
t_CommOffsetTblY_Cnt_u16[1]	93		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-72.4260025		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2326		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2326	2326	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	16.8070068	16.8069992 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.37593937	8.37594032 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-28.8799	-28.8799 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43081	43081 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-91.4420013	-91.4420013 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	16.8070068	16.8069992 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.60 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	140.470001
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	93.5790024
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.012000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.93900001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.79499996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-876.190002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	798.229004
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.930000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.875
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	737.640991
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-190.210999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-9.61999989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-0.206
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	28.1019993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.381000012
MtrCtrl_Vecu_Volt_M_f32[0]	25.4869995
MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001
MtrCurrDaxPrevIntg_Volt_M_f32	15.3000002
MtrCurrDaxRef_Amp_M_f32[0]	94.3150024
MtrCurrDaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxCog_Amp_M_f32	-34.6189995
MtrCurrQaxFinalRef_Amp_M_f32[0]	171.485992
MtrCurrQaxFinalRef_Amp_M_f32[1]	163.787003
MtrCurrQaxPrevIntg_Volt_M_f32	1.81389999
MtrCurrQaxRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxRpl_Amp_M_f32[0]	171.485992
MtrCurrQaxRpl_Amp_M_f32[1]	163.787003
MtrPosComputationDelay_Rad_M_f32[0]	-0.107000001
MtrPosComputationDelay_Rad_M_f32[1]	-1.71000004
MtrVoltDax_Volt_M_f32[0]	28.1019993
MtrVoltDax_Volt_M_f32[1]	0.381000012

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	28.1019993		
MtrVoltQax_Volt_M_f32[1]	0.381000012		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.442000002		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.184		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.864700019		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.991999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50.7543983		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.536199987		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50.7543983		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.536199987		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2035.48999		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5391.29004		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.133000001		
k_MtrCtrlVirualResQax_Ohm_f32	0.159999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.5212002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.1000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.7397003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-13.3400002		
k_VoltSatQaxPolyCoeff_Uls_f32	12.0819998		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	1896		
t_CommOffsetTblY_Cnt_u16[1]	1952		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2340		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2340	2340	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-99.4119873	-99.4120026 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.531391025	-0.531391025 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.92642355	-4.92642021 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32773	32773 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-99.4119873	-99.4120026 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	163.787003	163.787003 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.61 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.07099998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.27900004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	650.622009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	557.583984
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.79900002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.624000013
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-125.525002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	839.142029
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-12.2150002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.874000013
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	4.64300013
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-11.7069998
MtrCtrl_Vecu_Volt_M_f32[0]	16.8080006
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993
MtrCurrDaxPrevIntg_Volt_M_f32	-18.566
MtrCurrDaxRef_Amp_M_f32[0]	212.455994
MtrCurrDaxRef_Amp_M_f32[1]	89.8619995
MtrCurrQaxCog_Amp_M_f32	177.046997
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220
MtrCurrQaxPrevIntg_Volt_M_f32	27.2450008
MtrCurrQaxRef_Amp_M_f32[0]	94.3150024
MtrCurrQaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxRpl_Amp_M_f32[0]	-220
MtrCurrQaxRpl_Amp_M_f32[1]	-220
MtrPosComputationDelay_Rad_M_f32[0]	4.64699984
MtrPosComputationDelay_Rad_M_f32[1]	2.27200007
MtrVoltDax_Volt_M_f32[0]	4.64300013
MtrVoltDax_Volt_M_f32[1]	-11.7069998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	4.64300013		
MtrVoltQax_Volt_M_f32[1]	-11.7069998		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.375999987		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0529999994		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.636099994		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.49000001		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	33.7361984		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.666299999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	33.7361984		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.666299999		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	1046.93005		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	1066.56006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.063000001		
k_MtrCtrlVirualResQax_Ohm_f32	0.119000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.9659004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.7127991		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-0.66900003		
k_VoltSatQaxPolyCoeff_Uls_f32	13.8260002		
k_deadtimeVScale_Uls_f32	0.959999979		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	730		
t_CommOffsetTblY_Cnt_u16[1]	1388		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3783		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3783	3783	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	80.4490051	80.4489975 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.357355237	0.357354999 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.78667927	-4.78668022 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55689	55689 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	13.9659004	13.9659004 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	80.4490051	80.4489975 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.62 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.070000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75300002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	524.809998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-986.283997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.02900004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.546000004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	613.835999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	556.35498
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	22.1809998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.0150003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	25.9820004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.0480003
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997
MtrCurrDaxPrevIntg_Volt_M_f32	-15.7600002
MtrCurrDaxRef_Amp_M_f32[0]	-108.124001
MtrCurrDaxRef_Amp_M_f32[1]	178.639008
MtrCurrQaxCog_Amp_M_f32	-9.31999969
MtrCurrQaxFinalRef_Amp_M_f32[0]	220
MtrCurrQaxFinalRef_Amp_M_f32[1]	220
MtrCurrQaxPrevIntg_Volt_M_f32	11.2662001
MtrCurrQaxRef_Amp_M_f32[0]	212.455994
MtrCurrQaxRef_Amp_M_f32[1]	89.8619995
MtrCurrQaxRpl_Amp_M_f32[0]	220
MtrCurrQaxRpl_Amp_M_f32[1]	220
MtrPosComputationDelay_Rad_M_f32[0]	2.65499997
MtrPosComputationDelay_Rad_M_f32[1]	-5.77899981
MtrVoltDax_Volt_M_f32[0]	25.9820004
MtrVoltDax_Volt_M_f32[1]	-23.0480003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	25.9820004		
MtrVoltQax_Volt_M_f32[1]	-23.0480003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.379999995		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.151999995		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.621800005		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.887000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	65.2260971		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.8046		
PICurrCntrl_MtrVoltQaxFFfilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFfilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFfilt_M_str.TermN_Uls_f32	65.2260971		
PICurrCntrl_MtrVoltQaxFFfilt_M_str.TermD_Uls_f32	0.8046		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6758.08008		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	7037.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0520000011		
k_MtrCtrlVirualResQax_Ohm_f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.4785004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.3000002		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.1450005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.6620007		
k_VoltSatQaxPolyCoeff_Uls_f32	-24.7110004		
k_deadtimeVScale_Uls_f32	0.986000001		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	24		
t_CommOffsetTblY_Cnt_u16[1]	47		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	80.8180008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4523		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4523	4523	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	1.77600098	1.77600002 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	19.8459988	19.8460007 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	23.2468662	23.2469006 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	35063	35063 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-11.3000002	-11.3000002 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	1.77600098	1.77600002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.63 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.412
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.523
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-834.685974
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-788.218994
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.50899994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	881.109009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1005.21997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-23.6089993
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-14.04
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	14.9390001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.4060001
MtrCtrl_Vecu_Volt_M_f32[0]	17.9899998
MtrCtrl_Vecu_Volt_M_f32[1]	20.3500004
MtrCurrDaxPrevIntg_Volt_M_f32	-14.0459995
MtrCurrDaxRef_Amp_M_f32[0]	-76.8769989
MtrCurrDaxRef_Amp_M_f32[1]	-153.238998
MtrCurrQaxCog_Amp_M_f32	-161.352005
MtrCurrQaxFinalRef_Amp_M_f32[0]	0
MtrCurrQaxFinalRef_Amp_M_f32[1]	0
MtrCurrQaxPrevIntg_Volt_M_f32	29.0646
MtrCurrQaxRef_Amp_M_f32[0]	-108.124001
MtrCurrQaxRef_Amp_M_f32[1]	178.639008
MtrCurrQaxRpl_Amp_M_f32[0]	0
MtrCurrQaxRpl_Amp_M_f32[1]	0
MtrPosComputationDelay_Rad_M_f32[0]	-3.51999998
MtrPosComputationDelay_Rad_M_f32[1]	3.33299994
MtrVoltDax_Volt_M_f32[0]	14.9390001
MtrVoltDax_Volt_M_f32[1]	-29.4060001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	14.9390001		
MtrVoltQax_Volt_M_f32[1]	-29.4060001		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.635999978		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.791999996		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.847599983		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.493999988		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.400299996		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	25.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.400299996		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3945.78003		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	7691.68994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.172999993		
k_MtrCtrlVirualResQax_Ohm_f32	0.0810000002		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.3655996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.3999996		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.514699996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.2639999		
k_VoltSatQaxPolyCoeff_Uls_f32	11.283		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTblX_Uls_u3p13[0]	2638		
t_CommOffsetTblX_Uls_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	310		
t_CommOffsetTblY_Cnt_u16[1]	1418		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	129		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	121.994003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1418	1418	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63504	63504 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	53.2280045	53.2280006 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.5495453	-16.5494995 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-5.47703886	-5.47703981 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	9103	9103 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	53.2280045	53.2280006 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	0	0 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.64 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.30700004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.85500002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	620.015015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	715.487
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.987999976
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.42899999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	811.825012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-796.757996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-21.1959991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-4.78100014
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.6959991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-27.8540001
MtrCtrl_Vecu_Volt_M_f32[0]	26.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	29.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	9.64799976
MtrCurrDaxRef_Amp_M_f32[0]	191.369003
MtrCurrDaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxCog_Amp_M_f32	-205.514999
MtrCurrQaxFinalRef_Amp_M_f32[0]	-115.696999
MtrCurrQaxFinalRef_Amp_M_f32[1]	-141.417007
MtrCurrQaxPrevIntg_Volt_M_f32	2.89910007
MtrCurrQaxRef_Amp_M_f32[0]	-76.8769989
MtrCurrQaxRef_Amp_M_f32[1]	-153.238998
MtrCurrQaxRpl_Amp_M_f32[0]	-115.696999
MtrCurrQaxRpl_Amp_M_f32[1]	-141.417007
MtrPosComputationDelay_Rad_M_f32[0]	3.77699995
MtrPosComputationDelay_Rad_M_f32[1]	4.99499989
MtrVoltDax_Volt_M_f32[0]	-17.6959991
MtrVoltDax_Volt_M_f32[1]	-27.8540001
MtrVoltQax_Volt_M_f32[0]	-17.6959991
MtrVoltQax_Volt_M_f32[1]	-27.8540001
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.713
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.726000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.289799988
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.129999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	70.1131973
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.631200016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	70.1131973
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.631200016
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	1611.48999
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5394.18018
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0710000023
k_MtrCtrlVirtualResQax_Ohm_f32	0.101000004
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpiEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.98559999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.9648991		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.1690006		
k_VoltSatQaxPolyCoeff_Uls_f32	-5.51499987		
k_deadtimeVScale_Uls_f32	0.995000005		
t_CommOffsetTblX_Uls_u3p13[0]	1212		
t_CommOffsetTblX_Uls_u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	1257		
t_CommOffsetTblY_Cnt_u16[1]	1842		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-40.9220009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	662		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1842	1842	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65208	65208 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	193.693008	193.692993 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	15.6482735	15.6483002 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-24.2919445	-24.2919006 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	13363	13363 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-115.696999	-115.696999 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	193.693008	193.692993 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.65 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.526000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53100002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	555.133972

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	919.028015
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.662
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.31599998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-876.906982
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-215.744003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.7129993
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.4500008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-11.9399996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	29.1739998
MtrCtrl_Vecu_Volt_M_f32[0]	16.882
MtrCtrl_Vecu_Volt_M_f32[1]	19.2420006
MtrCurrDaxPrevIntg_Volt_M_f32	6.12300014
MtrCurrDaxRef_Amp_M_f32[0]	-147.343002
MtrCurrDaxRef_Amp_M_f32[1]	127.972
MtrCurrQaxCog_Amp_M_f32	-118.848
MtrCurrQaxFinalRef_Amp_M_f32[0]	140.470001
MtrCurrQaxFinalRef_Amp_M_f32[1]	93.5790024
MtrCurrQaxPrevIntg_Volt_M_f32	24.7549992
MtrCurrQaxRef_Amp_M_f32[0]	191.369003
MtrCurrQaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxRpl_Amp_M_f32[0]	140.470001
MtrCurrQaxRpl_Amp_M_f32[1]	93.5790024
MtrPosComputationDelay_Rad_M_f32[0]	3.8440001
MtrPosComputationDelay_Rad_M_f32[1]	3.79399991
MtrVoltDax_Volt_M_f32[0]	-11.9399996
MtrVoltDax_Volt_M_f32[1]	29.1739998
MtrVoltQax_Volt_M_f32[0]	-11.9399996
MtrVoltQax_Volt_M_f32[1]	29.1739998
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.47299999
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00899999961
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0659999996
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.665000021
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8.62930012
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.434899986
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	8.62930012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.434899986
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2358.21997
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5388.91992
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0729999989
k_MtrCtrlVirtualResQax_Ohm_f32	0.023
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	8.81120014
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	25.0259991
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	22.6620007
k_VoltSatQaxPolyCoeff_Uls_f32	12.3109999
k_deadtimeVScale_Uls_f32	0.985000014
t_CommOffsetTblX_Uls_u3p13[0]	3808
t_CommOffsetTblX_Uls_u3p13[1]	7298
t_CommOffsetTblY_Cnt_u16[0]	181
t_CommOffsetTblY_Cnt_u16[1]	812
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	75.0830002
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	917
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	48.8400002
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	917	917	✓
MtrCntl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	169.747009	169.746994 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	4.90829039	4.90828991 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-30.1379299	-30.1378994 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	5642	5642 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	169.747009	169.746994 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	93.5790024	93.5790024 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.66 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	24.6130009
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-20.9400005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.013000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.44099998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.26900005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-818.776001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-274.428986
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.976000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-737.580994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	408.726013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	18.2380009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.3910007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-12.816
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-15.0170002
MtrCtrl_Vecu_Volt_M_f32[0]	5.70200014
MtrCtrl_Vecu_Volt_M_f32[1]	8.06200027
MtrCurrDaxPrevIntg_Volt_M_f32	15.0279999
MtrCurrDaxRef_Amp_M_f32[0]	6.18900013
MtrCurrDaxRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxCog_Amp_M_f32	-220

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-184.923996		
MtrCurrQaxPrevIntg_Volt_M_f32	1.02610004		
MtrCurrQaxRef_Amp_M_f32[0]	-147.343002		
MtrCurrQaxRef_Amp_M_f32[1]	127.972		
MtrCurrQaxRpl_Amp_M_f32[0]	-216.921997		
MtrCurrQaxRpl_Amp_M_f32[1]	-184.923996		
MtrPosComputationDelay_Rad_M_f32[0]	4.13000011		
MtrPosComputationDelay_Rad_M_f32[1]	-3.80699992		
MtrVoltDax_Volt_M_f32[0]	-12.816		
MtrVoltDax_Volt_M_f32[1]	-15.0170002		
MtrVoltQax_Volt_M_f32[0]	-12.816		
MtrVoltQax_Volt_M_f32[1]	-15.0170002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.657000005		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.432999998		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.185100004		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.515999973		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12.6120005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.790099978		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12.6120005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.790099978		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3275.26001		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2711.1499		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.165000007		
k_MtrCtrlVirualResQax_Ohm_f32	0.200000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	25.7908001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.1938992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	19.0259991		
k_VoltSatQaxPolyCoeff_Uls_f32	1.51499999		
k_deadtimeVScale_Uls_f32	1		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	174		
t_CommOffsetTblY_Cnt_u16[1]	589		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	74.0660019		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3897		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	107.702003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	589	589	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65535	65536 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.64351988	2.64352012 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-5.05218792	-5.05218983 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	5281	5281 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-10.5	-10.5 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-184.923996	-184.923996 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.67 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0630000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.165999994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.68499994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	41.1699982
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	456.949005
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.57700002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.83099997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-771.507996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	920.502991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.2689991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	15.2200003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	27.9379997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	8.50300026
MtrCtrl_Vecu_Volt_M_f32[0]	28.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	30.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-1.19400001
MtrCurrDaxRef_Amp_M_f32[0]	-105.246002
MtrCurrDaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxCog_Amp_M_f32	220
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxPrevIntg_Volt_M_f32	11.6451998
MtrCurrQaxRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxRpl_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008
MtrPosComputationDelay_Rad_M_f32[0]	1.53499997
MtrPosComputationDelay_Rad_M_f32[1]	2.22300005
MtrVoltDax_Volt_M_f32[0]	27.9379997
MtrVoltDax_Volt_M_f32[1]	8.50300026
MtrVoltQax_Volt_M_f32[0]	27.9379997
MtrVoltQax_Volt_M_f32[1]	8.50300026

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.930999994		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.893000007		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.76819998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.670000017		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	42.0777016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.450700015		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	42.0777016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.450700015		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	220.787994		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5103.45996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0370000005		
k_MtrCtrlVirtualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.48790002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.9482002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	7.15899992		
k_VoltSatQaxPolyCoeff_Uls_f32	6.94099998		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTblX_Uls_u3p13[0]	1532		
t_CommOffsetTblX_Uls_u3p13[1]	2851		
t_CommOffsetTblY_Cnt_u16[0]	158		
t_CommOffsetTblY_Cnt_u16[1]	544		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-149.003006		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4983		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	5.72399998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4983	4983	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-131.513	-131.513 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.275402665	0.275402993 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.83716631	4.83717012 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	16604	16604 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-131.513	-131.513 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008	46.8180008 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.68 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.598999977
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.00399995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	324.985992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-932.651978
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.528999984
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.03900003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	139.416
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-482.338013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.91699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.9359999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-9.61999989
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-0.206
MtrCtrl_Vecu_Volt_M_f32[0]	22.7800007
MtrCtrl_Vecu_Volt_M_f32[1]	25.1399994
MtrCurrDaxPrevIntg_Volt_M_f32	-18.1529999
MtrCurrDaxRef_Amp_M_f32[0]	-213.026993
MtrCurrDaxRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxCog_Amp_M_f32	0
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004
MtrCurrQaxPrevIntg_Volt_M_f32	29.9225006
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxRpl_Amp_M_f32[0]	160.044006
MtrCurrQaxRpl_Amp_M_f32[1]	165.242004
MtrPosComputationDelay_Rad_M_f32[0]	0.574999988
MtrPosComputationDelay_Rad_M_f32[1]	-5.59200001
MtrVoltDax_Volt_M_f32[0]	-9.61999989
MtrVoltDax_Volt_M_f32[1]	-0.206
MtrVoltQax_Volt_M_f32[0]	-9.61999989
MtrVoltQax_Volt_M_f32[1]	-0.206
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.907999992
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.222000003
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.957899988
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.731999993
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	42.2845001
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.870100021
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	42.2845001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.870100021
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	1457.72998
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	4901.47021
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0680000037
k_MtrCtrlVirtualResQax_Ohm_f32	0.0299999993
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.7781		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.2348995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	13.868		
k_VoltSatQaxPolyCoeff_Uls_f32	24.8209991		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	4162		
t_CommOffsetTblX_Uls_u3p13[1]	8053		
t_CommOffsetTblY_Cnt_u16[0]	565		
t_CommOffsetTblY_Cnt_u16[1]	1207		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-124.758003		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3029		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	59.3040009		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1207	1207	✔
MtrCntri_Write_Modldx_Uls_u16p16(val)	65339	65339 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	16.376791	16.3768005 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-15.7359533	-15.7360001 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	30365	30365 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004	165.242004 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.69 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	138.595001
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-157.388
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.1129999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.169
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.2349999999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	643.937012

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-774.807983
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.34399998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.00300002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	144.895996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	675.440002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-10.6440001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.8400002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-12.2150002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.874000013
MtrCtrl_Vecu_Volt_M_f32[0]	21.2910004
MtrCtrl_Vecu_Volt_M_f32[1]	23.6509991
MtrCurrDaxPrevIntg_Volt_M_f32	-6.90899992
MtrCurrDaxRef_Amp_M_f32[0]	-212.632996
MtrCurrDaxRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxCog_Amp_M_f32	70.6559982
MtrCurrQaxFinalRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.972
MtrCurrQaxPrevIntg_Volt_M_f32	24.0646
MtrCurrQaxRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxRpl_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRpl_Amp_M_f32[1]	-216.972
MtrPosComputationDelay_Rad_M_f32[0]	4.14599991
MtrPosComputationDelay_Rad_M_f32[1]	4.31799984
MtrVoltDax_Volt_M_f32[0]	-12.2150002
MtrVoltDax_Volt_M_f32[1]	0.874000013
MtrVoltQax_Volt_M_f32[0]	-12.2150002
MtrVoltQax_Volt_M_f32[1]	0.874000013
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0250000004
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.723999977
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.785399973
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0450000018
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	43.7542992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.50029999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	43.7542992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.50029999
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5655.9502
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	4407.62012
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0579999983
k_MtrCtrlVirualResQax_Ohm_f32	0.191
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	10.6548996
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	13.8830004
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-5.80900002
k_VoltSatQaxPolyCoeff_Uls_f32	1.51699996
k_deadtimeVScale_Uls_f32	0.986000001
t_CommOffsetTblX_Uls_u3p13[0]	5153
t_CommOffsetTblX_Uls_u3p13[1]	8027
t_CommOffsetTblY_Cnt_u16[0]	164
t_CommOffsetTblY_Cnt_u16[1]	921
target_MtrCntrl_Read_IvtrLoaMtgtEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2959
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	20.6149998
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	921	921	✓
MtrCntl_Write_ModlIdx_Uls_u16p16(val)	64618	64618 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	79.5930099	79.5930023 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	9.86156178	9.86155987 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	21.132122	21.1320992 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49593	49593 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-65.1900024	-65.1900024 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	79.5930099	79.5930023 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlIdx_Uls_u16p16	1	MtrCntl_Write_ModlIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.70 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-100.282997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-120.248001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.41799998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.275000006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	747.85199
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-144.074005
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.05900002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.105999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	675.771973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1006.70001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-12.7250004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-6.00099993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	22.1809998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-11.0150003
MtrCtrl_Vecu_Volt_M_f32[0]	12.1129999
MtrCtrl_Vecu_Volt_M_f32[1]	14.4729996
MtrCurrDaxPrevIntg_Volt_M_f32	-14.8070002
MtrCurrDaxRef_Amp_M_f32[0]	205.820999
MtrCurrDaxRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxCog_Amp_M_f32	-111.970001
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxFinalRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxPrevIntg_Volt_M_f32	11.6198997

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[0]	-212.632996		
MtrCurrQaxRef_Amp_M_f32[1]	-205.085007		
MtrCurrQaxRpl_Amp_M_f32[0]	-216.921997		
MtrCurrQaxRpl_Amp_M_f32[1]	-184.923996		
MtrPosComputationDelay_Rad_M_f32[0]	-1.23300004		
MtrPosComputationDelay_Rad_M_f32[1]	5.02600002		
MtrVoltDax_Volt_M_f32[0]	22.1809998		
MtrVoltDax_Volt_M_f32[1]	-11.0150003		
MtrVoltQax_Volt_M_f32[0]	22.1809998		
MtrVoltQax_Volt_M_f32[1]	-11.0150003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0820000023		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.128999993		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.530900002		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.677999973		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.2423992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.400099993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.2423992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.400099993		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	7814.10986		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3404.45996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0659999996		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0670000017		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.7416		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.2787991		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-21.8419991		
k_VoltSatQaxPolyCoeff_Uls_f32	-0.100000001		
k_deadtimeVScale_Uls_f32	0.978999972		
t_CommOffsetTblX_Uls_u3p13[0]	2802		
t_CommOffsetTblX_Uls_u3p13[1]	3899		
t_CommOffsetTblY_Cnt_u16[0]	12		
t_CommOffsetTblY_Cnt_u16[1]	15		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1011		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	79.6729965		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	15	15	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	56799	56799 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	91.8089752	91.8089981 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-6.00099993	-6.00099993 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-11.0150003	-11.0150003 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24858	24858 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-216.921997	-216.921997 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	91.8089752	91.8089981 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.71 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-68.6760025
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-96.776001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.023
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.14400005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.58899999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	734.922974
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	615.338989
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.216999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.522000015
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-153.945007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-664.44397
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0.779999971
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-18.3120003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.6089993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-14.04
MtrCtrl_Vecu_Volt_M_f32[0]	20.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	22.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-19.6690006
MtrCurrDaxRef_Amp_M_f32[0]	-69.0940018
MtrCurrDaxRef_Amp_M_f32[1]	161.973007
MtrCurrQaxCog_Amp_M_f32	-72.4260025
MtrCurrQaxFinalRef_Amp_M_f32[0]	138.595001
MtrCurrQaxFinalRef_Amp_M_f32[1]	-157.388
MtrCurrQaxPrevIntg_Volt_M_f32	28.9094009
MtrCurrQaxRef_Amp_M_f32[0]	-132.813004
MtrCurrQaxRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxRpl_Amp_M_f32[0]	138.595001
MtrCurrQaxRpl_Amp_M_f32[1]	-157.388
MtrPosComputationDelay_Rad_M_f32[0]	1.02199996
MtrPosComputationDelay_Rad_M_f32[1]	-3.79699993
MtrVoltDax_Volt_M_f32[0]	-23.6089993
MtrVoltDax_Volt_M_f32[1]	-14.04
MtrVoltQax_Volt_M_f32[0]	-23.6089993
MtrVoltQax_Volt_M_f32[1]	-14.04

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.261000007		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.728999972		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.386200011		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	83.4807968		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.745700002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	83.4807968		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.745700002		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2013.56995		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	944.638977		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.126000002		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0670000017		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.694800019		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	27.6896992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-8.64500046		
k_VoltSatQaxPolyCoeff_Uls_f32	-10.3520002		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	4686		
t_CommOffsetTblX_Uls_u3p13[1]	6119		
t_CommOffsetTblY_Cnt_u16[0]	557		
t_CommOffsetTblY_Cnt_u16[1]	678		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3142		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	0.486999989		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	678	678	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64749	64749 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-198.981995	-198.981995 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	20.0663643	20.0664005 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.40769327	1.40769005 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	26313	26313 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0.694800019	0.694800019 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-198.981995	-198.981995 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-157.388	-157.388 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.72 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-139.906998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	115.814003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0540000014
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.88999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.33099997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-329.475006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-304.359985
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.384000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.620999992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	794.978027
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-414.11499
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.04099989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-21.3549995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-21.1959991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-4.78100014
MtrCtrl_Vecu_Volt_M_f32[0]	5.33099985
MtrCtrl_Vecu_Volt_M_f32[1]	7.69099998
MtrCurrDaxPrevIntg_Volt_M_f32	24.066
MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
MtrCurrDaxRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxCog_Amp_M_f32	83.9489975
MtrCurrQaxFinalRef_Amp_M_f32[0]	-100.282997
MtrCurrQaxFinalRef_Amp_M_f32[1]	-120.248001
MtrCurrQaxPrevIntg_Volt_M_f32	19.3868999
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32[0]	-100.282997
MtrCurrQaxRpl_Amp_M_f32[1]	-120.248001
MtrPosComputationDelay_Rad_M_f32[0]	-4.38800001
MtrPosComputationDelay_Rad_M_f32[1]	-3.36400008
MtrVoltDax_Volt_M_f32[0]	-21.1959991
MtrVoltDax_Volt_M_f32[1]	-4.78100014
MtrVoltQax_Volt_M_f32[0]	-21.1959991
MtrVoltQax_Volt_M_f32[1]	-4.78100014
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.479000002
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.882000029
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.258100003
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	65.649498
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.843400002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	65.649498
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.843400002
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	7558.6001
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3548.41992
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.126000002
k_MtrCtrlVirtualResQax_Ohm_f32	0.179000005
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	24.4853992

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.72780037		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-4.80100012		
k_VoltSatQaxPolyCoeff_Uls_f32	19.4750004		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTblX_Uls_u3p13[0]	1139		
t_CommOffsetTblX_Uls_u3p13[1]	7438		
t_CommOffsetTblY_Cnt_u16[0]	268		
t_CommOffsetTblY_Cnt_u16[1]	1844		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4704		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1844	1844	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64552	64552 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-177.036011	-177.035995 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.15903664	7.15903997 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.47758746	-2.47759008 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50307	50307 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-100.282997	-100.282997 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-177.036011	-177.035995 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.73 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0410000011
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.03900003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.26999998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	964.854004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-233.382004
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0769999996		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0949999988		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0179999992		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.765999973		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.43099999		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	887.062988		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	250.690994		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	6.24700022		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.82500005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.7129993		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-20.4500008		
MtrCtrl_Vecu_Volt_M_f32[0]	18.2229996		
MtrCtrl_Vecu_Volt_M_f32[1]	20.5830002		
MtrCurrDaxPrevIntg_Volt_M_f32	-21.9330006		
MtrCurrDaxRef_Amp_M_f32[0]	-146.173996		
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007		
MtrCurrQaxCog_Amp_M_f32	-144.667007		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-68.6760025		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-96.776001		
MtrCurrQaxPrevIntg_Volt_M_f32	19.6061993		
MtrCurrQaxRef_Amp_M_f32[0]	-91.4420013		
MtrCurrQaxRef_Amp_M_f32[1]	133.692993		
MtrCurrQaxRpl_Amp_M_f32[0]	-68.6760025		
MtrCurrQaxRpl_Amp_M_f32[1]	-96.776001		
MtrPosComputationDelay_Rad_M_f32[0]	0.829999983		
MtrPosComputationDelay_Rad_M_f32[1]	2.41899991		
MtrVoltDax_Volt_M_f32[0]	-22.7129993		
MtrVoltDax_Volt_M_f32[1]	-20.4500008		
MtrVoltQax_Volt_M_f32[0]	-22.7129993		
MtrVoltQax_Volt_M_f32[1]	-20.4500008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.324000001		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.540000021		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.153999999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.930000007		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	95.354599		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.241500005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	95.354599		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.241500005		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2668.02002		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	791.747986		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0839999989		
k_MtrCtrlVirualResQax_Ohm_f32	0.128999993		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.6718006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.4462996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	1.19500005		
k_VoltSatQaxPolyCoeff_Uls_f32	-18.6650009		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	2449		
t_CommOffsetTblX_Uls_u3p13[1]	2875		
t_CommOffsetTblY_Cnt_u16[0]	135		
t_CommOffsetTblY_Cnt_u16[1]	1455		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	654		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	654	654	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_ModlDx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	1.64366305	1.64365995 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	4.53108931	4.53108978 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	28861	28861 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-68.6760025	-68.6760025 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlDx_Uls_u16p16	1	MtrCntl_Write_ModlDx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.74 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	160.044006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	165.242004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.967999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.12600005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-115.790001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	183.574997
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.62800002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.66900003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-689.698975
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	753.629028
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	18.1280003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.3299999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.2380009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.3910007
MtrCtrl_Vecu_Volt_M_f32[0]	25.7329998
MtrCtrl_Vecu_Volt_M_f32[1]	28.0930004
MtrCurrDaxPrevIntg_Volt_M_f32	-14.8500004
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	80.8180008
MtrCurrQaxFinalRef_Amp_M_f32[0]	-139.906998
MtrCurrQaxFinalRef_Amp_M_f32[1]	115.814003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxPrevIntg_Volt_M_f32	3.95619988		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32[0]	-139.906998		
MtrCurrQaxRpl_Amp_M_f32[1]	115.814003		
MtrPosComputationDelay_Rad_M_f32[0]	3.92700005		
MtrPosComputationDelay_Rad_M_f32[1]	5.99399996		
MtrVoltDax_Volt_M_f32[0]	18.2380009		
MtrVoltDax_Volt_M_f32[1]	27.3910007		
MtrVoltQax_Volt_M_f32[0]	18.2380009		
MtrVoltQax_Volt_M_f32[1]	27.3910007		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.538999975		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.671000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.321999997		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	68.892601		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.91109997		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	68.892601		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.91109997		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6474.75		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5821.56006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.187999994		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0480000004		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	28.8398991		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.0121994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.8379993		
k_VoltSatQaxPolyCoeff_Uls_f32	-10.9659996		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	4153		
t_CommOffsetTblX_Uls_u3p13[1]	8176		
t_CommOffsetTblY_Cnt_u16[0]	434		
t_CommOffsetTblY_Cnt_u16[1]	1438		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1246		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1246	1246	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-32.8450012	-32.8450012 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.55194139	-3.55193996 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.31712341	3.31712008 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	53971	53971 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	28.8398991	28.8398991 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-139.906998	-139.906998 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-32.8450012	-32.8450012 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.75 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.938000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-751.672974
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	758.984985
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87199998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.86800003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	881.539001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	971.434998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-1.92999995
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.432000011
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.2689991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.2200003
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-21.5599995
MtrCurrDaxRef_Amp_M_f32[0]	171.485992
MtrCurrDaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxCog_Amp_M_f32	-44.2579994
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxPrevIntg_Volt_M_f32	15.5335999
MtrCurrQaxRef_Amp_M_f32[0]	106.072998
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxRpl_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008
MtrPosComputationDelay_Rad_M_f32[0]	0.128000006
MtrPosComputationDelay_Rad_M_f32[1]	-2.62299991
MtrVoltDax_Volt_M_f32[0]	-17.2689991
MtrVoltDax_Volt_M_f32[1]	15.2200003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-17.2689991		
MtrVoltQax_Volt_M_f32[1]	15.2200003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.47299999		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0410000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.142000005		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	20.7000008		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	82.1283035		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.396600008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	20.7000008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	82.1283035		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.396600008		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6937.75977		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3932.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0869999975		
k_MtrCtrlVirualResQax_Ohm_f32	0.175999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.12940001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-12.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.5435009		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	14.7320004		
k_VoltSatQaxPolyCoeff_Uls_f32	9.23999977		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	4776		
t_CommOffsetTblX_Uls_u3p13[1]	7741		
t_CommOffsetTblY_Cnt_u16[0]	1756		
t_CommOffsetTblY_Cnt_u16[1]	1670		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1865		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1715	1715	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	49741	49741 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-115.014999	-115.014999 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.432000011	0.432000011 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	15.2200003	15.2200003 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38473	38473 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965	-82.2979965 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-115.014999	-115.014999 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.76 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.723007
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-121.943001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.68200004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.48800004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-133.520004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-739.294006
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.337000012
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.964999974
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	481.321014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	577.322998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.4989996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.1389999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-4.91699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.9359999
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	1.12
MtrCurrDaxRef_Amp_M_f32[0]	106.072998
MtrCurrDaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxCog_Amp_M_f32	-40.9220009
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004
MtrCurrQaxPrevIntg_Volt_M_f32	28.4825993
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpl_Amp_M_f32[0]	160.044006
MtrCurrQaxRpl_Amp_M_f32[1]	165.242004
MtrPosComputationDelay_Rad_M_f32[0]	3.76399994
MtrPosComputationDelay_Rad_M_f32[1]	3.68400002
MtrVoltDax_Volt_M_f32[0]	-4.91699982
MtrVoltDax_Volt_M_f32[1]	-13.9359999
MtrVoltQax_Volt_M_f32[0]	-4.91699982
MtrVoltQax_Volt_M_f32[1]	-13.9359999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.134000003
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0390000008
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.5352
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.524999976
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.8788986
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.45570001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.8788986
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.45570001
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	1146.66003
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	4987.56982
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.109999999
k_MtrCtrlVirtualResQax_Ohm_f32	0.0340000018
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	9.01119995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	20.6303005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	20.9039993		
k_VoltSatQaxPolyCoeff_Uls_f32	-4.77099991		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTblX_Uls_u3p13[0]	492		
t_CommOffsetTblX_Uls_u3p13[1]	7840		
t_CommOffsetTblY_Cnt_u16[0]	589		
t_CommOffsetTblY_Cnt_u16[1]	1202		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	121.994003		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	812		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-40.9220009		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	812	812	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-94.5090103	-94.5090027 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-4.26297998	-4.26297998 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-2.46629858	-2.46630001 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	17405	17405 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-94.5090103	-94.5090027 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004	165.242004 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.77 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.04700005

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.331999987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	965.18103
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	180.692001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.88900006
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.802999973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-940.226013
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-976.195007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.1280003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.3299999
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-22.2029991
MtrCurrDaxRef_Amp_M_f32[0]	24.6130009
MtrCurrDaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxCog_Amp_M_f32	-207.917999
MtrCurrQaxFinalRef_Amp_M_f32[0]	209.052002
MtrCurrQaxFinalRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxPrevIntg_Volt_M_f32	-31
MtrCurrQaxRef_Amp_M_f32[0]	-166.035004
MtrCurrQaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxRpl_Amp_M_f32[0]	209.052002
MtrCurrQaxRpl_Amp_M_f32[1]	-124.994003
MtrPosComputationDelay_Rad_M_f32[0]	4.39099979
MtrPosComputationDelay_Rad_M_f32[1]	1.33599997
MtrVoltDax_Volt_M_f32[0]	18.1280003
MtrVoltDax_Volt_M_f32[1]	-29.3299999
MtrVoltQax_Volt_M_f32[0]	18.1280003
MtrVoltQax_Volt_M_f32[1]	-29.3299999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.216000006
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.023
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.945299983
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.112999998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	94.2040024
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.323000014
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	94.2040024
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.323000014
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7138.00977
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7847.91016
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0930000022
k_MtrCtrlVirtualResQax_Ohm_f32	0.050999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	3.55049992
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	5.07770014
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-5.18900013
k_VoltSatQaxPolyCoeff_Uls_f32	-7.39099979
k_deadtimeVScale_Uls_f32	0.986000001
t_CommOffsetTblX_Uls_u3p13[0]	2834
t_CommOffsetTblX_Uls_u3p13[1]	3595
t_CommOffsetTblY_Cnt_u16[0]	1165
t_CommOffsetTblY_Cnt_u16[1]	1651
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-41.5750008
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	744

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnl_Read_MtrCurrQax_Amp_f32_Val	75.0830002		
target_MtrCntnl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntnl_Write_CommOffset_Cnt_u16(val)	744	744	✓
MtrCntnl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntnl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-167.169006	-167.169006 ± 7.81E-03	✓
MtrCntnl_Write_MtrDaxVoltage_Volt_f32(val)	-3.38360167	-3.3836 ± 4.88E-04	✓
MtrCntnl_Write_MtrQaxVoltage_Volt_f32(val)	3.58554578	3.58555007 ± 4.88E-04	✓
MtrCntnl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37910	37910 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	3.55049992	3.55049992 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-167.169006	-167.169006 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-124.994003	-124.994003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.78 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	37.4550018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-2.84500003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.41499996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.848999977
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-824.46698
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	454.670013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.196999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.954
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-729.622009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	640.599976
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-1.92999995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.432000011
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	26.6639996
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	183.065002		
MtrCurrQaxCog_Amp_M_f32	-198.285995		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-98.4449997		
MtrCurrQaxPrevIntg_Volt_M_f32	31		
MtrCurrQaxRef_Amp_M_f32[0]	140.289001		
MtrCurrQaxRef_Amp_M_f32[1]	178.235992		
MtrCurrQaxRpl_Amp_M_f32[0]	-200.556		
MtrCurrQaxRpl_Amp_M_f32[1]	-98.4449997		
MtrPosComputationDelay_Rad_M_f32[0]	-5.37699986		
MtrPosComputationDelay_Rad_M_f32[1]	-2.19300008		
MtrVoltDax_Volt_M_f32[0]	-1.92999995		
MtrVoltDax_Volt_M_f32[1]	0.432000011		
MtrVoltQax_Volt_M_f32[0]	-1.92999995		
MtrVoltQax_Volt_M_f32[1]	0.432000011		
PICurrCntrl_CurrSensFailSciFac_Uls_M_f32	0.810000002		
PICurrCntrl_InverterFailSciFac_Uls_M_f32	0.0489999987		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.932299972		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	6.96400023		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.331699997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.96400023		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.331699997		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalSciFacSlew_UlspS_f32	1416.70996		
k_ILOAFdbckSignalSciFacSlew_UlspS_f32	5107.18018		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.057		
k_MtrCtrlVirtualResQax_Ohm_f32	0.179000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.02270031		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	24.8586998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-11.7589998		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.63000011		
k_deadtimeVScale_Uls_f32	0.984000027		
t_CommOffsetTblX_Uls_u3p13[0]	2154		
t_CommOffsetTblX_Uls_u3p13[1]	6783		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	253		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1110	1110	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64487	64487 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.35975122	7.35974979 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.151220575	0.151221007 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	58832	58832 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556	-200.556 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.79 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	94.3150024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	37.4959984
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.896000028
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.76800001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-286.584991
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	831.651001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.927999973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-140.283005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	380.562012
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.4989996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	13.1389999
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.7919998
MtrCurrDaxRef_Amp_M_f32[0]	140.289001
MtrCurrDaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxCog_Amp_M_f32	136.341003
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	0
MtrCurrQaxRef_Amp_M_f32[0]	91.8850021
MtrCurrQaxRef_Amp_M_f32[1]	182.261002
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	3.58100009
MtrPosComputationDelay_Rad_M_f32[1]	4.95800018
MtrVoltDax_Volt_M_f32[0]	-8.4989996
MtrVoltDax_Volt_M_f32[1]	13.1389999
MtrVoltQax_Volt_M_f32[0]	-8.4989996
MtrVoltQax_Volt_M_f32[1]	13.1389999

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.93599999		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.967000008		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.99089998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.30399999		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-826.23999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12.8741999		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.75819999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-826.23999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12.8741999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.75819999		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5043.06982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1949.64001		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.181999996		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0189999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.3099003		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	5.74840021		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	19.0599995		
k_VoltSatQaxPolyCoeff_Uls_f32	14.7340002		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	418		
t_CommOffsetTblX_Uls_u3p13[1]	4570		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4760		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4760	4760	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-111.945992	-111.945999 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.95347309	-1.95346999 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.43919373	4.43918991 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33027	33027 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-111.945992	-111.945999 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.80 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	212.455994
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	89.8619995
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20599997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90699995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-304.572998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	299.334991
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.85399997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.39999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	778.853027
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-658.843994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-9.60900021
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	26.3260002
MtrCtrl_Vecu_Volt_M_f32[0]	14.243
MtrCtrl_Vecu_Volt_M_f32[1]	16.6030006
MtrCurrDaxPrevIntg_Volt_M_f32	-9.86299992
MtrCurrDaxRef_Amp_M_f32[0]	91.8850021
MtrCurrDaxRef_Amp_M_f32[1]	182.261002
MtrCurrQaxCog_Amp_M_f32	59.7319984
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	13.4132004
MtrCurrQaxRef_Amp_M_f32[0]	-218.035004
MtrCurrQaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	0.112999998
MtrPosComputationDelay_Rad_M_f32[1]	-5.44500017
MtrVoltDax_Volt_M_f32[0]	-9.60900021
MtrVoltDax_Volt_M_f32[1]	26.3260002
MtrVoltQax_Volt_M_f32[0]	-9.60900021
MtrVoltQax_Volt_M_f32[1]	26.3260002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.1449999996
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.995999992
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.702400029
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.208000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	5.05210018
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.2227
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	5.05210018
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.2227
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3857.37988
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	2438.91992
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0970000029
k_MtrCtrlVirtualResQax_Ohm_f32	0.0659999996
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.79429996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.95400047		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-12.4820004		
k_VoltSatQaxPolyCoeff_Uls_f32	10.7770004		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	2163		
t_CommOffsetTblX_Uls_u3p13[1]	5439		
t_CommOffsetTblY_Cnt_u16[0]	237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1062		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	383	383	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	63635	63635 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-45.2499962	-45.25 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-10.6555309	-10.6555004 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	12.0980501	12.0980997 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	1211	1211 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-45.2499962	-45.25 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.81 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-108.124001
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	178.639008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.880999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.54700005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-747.278992

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-161.845993
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0509999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.8360000025
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1011.37
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	886.40802
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.61900043
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.1560001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	23.4519997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-14.2340002
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6060009
MtrCurrDaxRef_Amp_M_f32[0]	-218.035004
MtrCurrDaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxCog_Amp_M_f32	1.62199998
MtrCurrQaxFinalRef_Amp_M_f32[0]	94.3150024
MtrCurrQaxFinalRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxPrevIntg_Volt_M_f32	-5.69140005
MtrCurrQaxRef_Amp_M_f32[0]	-216.921997
MtrCurrQaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxRpl_Amp_M_f32[0]	94.3150024
MtrCurrQaxRpl_Amp_M_f32[1]	37.4959984
MtrPosComputationDelay_Rad_M_f32[0]	-5.296
MtrPosComputationDelay_Rad_M_f32[1]	-0.995999992
MtrVoltDax_Volt_M_f32[0]	23.4519997
MtrVoltDax_Volt_M_f32[1]	-14.2340002
MtrVoltQax_Volt_M_f32[0]	23.4519997
MtrVoltQax_Volt_M_f32[1]	-14.2340002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.354999989
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.851999998
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.345699996
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0850000009
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	69.3029022
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.383899987
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	69.3029022
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.383899987
SlowDataAccessBufIndex_Cnt_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3045.54004
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	4138.33984
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.063000001
k_MtrCtrlVirtualResQax_Ohm_f32	0.101999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	18.9549007
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	16.6681995
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-2.83899999
k_VoltSatQaxPolyCoeff_Uls_f32	-14.1759996
k_deadtimeVScale_Uls_f32	0.990999997
t_CommOffsetTblX_Uls_u3p13[0]	1162
t_CommOffsetTblX_Uls_u3p13[1]	1932
t_CommOffsetTblY_Cnt_u16[0]	71
t_CommOffsetTblY_Cnt_u16[1]	676
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	4	4	✓
MtrCntl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	1.26505959	1.26505995 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-4.7907877	-4.79079008 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40372	40372 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	37.4959984	37.4959984 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.82 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-76.8769989
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-153.238998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70299995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.44000006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	317.347992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-582.065002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.21300006
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.16400003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	72.6969986
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-560.289978
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.3199997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	18.0170002
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-31
MtrCurrDaxRef_Amp_M_f32[0]	-216.921997
MtrCurrDaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxCog_Amp_M_f32	-126.640999

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	212.455994		
MtrCurrQaxFinalRef_Amp_M_f32[1]	89.8619995		
MtrCurrQaxPrevIntg_Volt_M_f32	0.200000003		
MtrCurrQaxRef_Amp_M_f32[0]	-82.2979965		
MtrCurrQaxRef_Amp_M_f32[1]	46.8180008		
MtrCurrQaxRpl_Amp_M_f32[0]	212.455994		
MtrCurrQaxRpl_Amp_M_f32[1]	89.8619995		
MtrPosComputationDelay_Rad_M_f32[0]	0.537999988		
MtrPosComputationDelay_Rad_M_f32[1]	0.358999997		
MtrVoltDax_Volt_M_f32[0]	26.3199997		
MtrVoltDax_Volt_M_f32[1]	18.0170002		
MtrVoltQax_Volt_M_f32[0]	26.3199997		
MtrVoltQax_Volt_M_f32[1]	18.0170002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.661000013		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.606999993		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.201399997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.564999998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	56.7700005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.200399995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	56.7700005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.200399995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	874.497986		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	1758.53003		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.156000003		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0890000015		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.24860001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.9195995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	13.8400002		
k_VoltSatQaxPolyCoeff_Uls_f32	7.34399986		
k_deadtimeVScale_Uls_f32	0.964999974		
t_CommOffsetTblX_Uls_u3p13[0]	1892		
t_CommOffsetTblX_Uls_u3p13[1]	4832		
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	20.6149998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2780		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1211	1211	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63242	63242 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-168.112991	-168.113007 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-18.3107929	-18.3108006 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	15.4814291	15.4813995 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	62084	62084 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-10.5	-10.5 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-168.112991	-168.113007 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	89.8619995	89.8619995 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_Modldx_Uls_u16p16	1	MtrCtrl_Write_Modldx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.83 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	191.369003
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	107.137001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.174999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.270999998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	317.493011
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	653.375977
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.89100003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.17499995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	345.561005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	325.127991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-24.052
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.3250008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.35299969
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.7590008
MtrCtrl_Vecu_Volt_M_f32[0]	29.0240002
MtrCtrl_Vecu_Volt_M_f32[1]	30.2299995
MtrCurrDaxPrevIntg_Volt_M_f32	31
MtrCurrDaxRef_Amp_M_f32[0]	138.595001
MtrCurrDaxRef_Amp_M_f32[1]	-157.388
MtrCurrQaxCog_Amp_M_f32	121.994003
MtrCurrQaxFinalRef_Amp_M_f32[0]	-108.124001
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.639008
MtrCurrQaxPrevIntg_Volt_M_f32	27.8124008
MtrCurrQaxRef_Amp_M_f32[0]	160.044006
MtrCurrQaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxRpl_Amp_M_f32[0]	-108.124001
MtrCurrQaxRpl_Amp_M_f32[1]	178.639008
MtrPosComputationDelay_Rad_M_f32[0]	-4.69399977
MtrPosComputationDelay_Rad_M_f32[1]	0.0949999988
MtrVoltDax_Volt_M_f32[0]	-8.35299969
MtrVoltDax_Volt_M_f32[1]	-29.7590008
MtrVoltQax_Volt_M_f32[0]	-8.35299969
MtrVoltQax_Volt_M_f32[1]	-29.7590008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.261999995		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.382999986		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.419499993		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.226999998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-304.940002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	95.180397		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.65170002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-304.940002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	95.180397		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.65170002		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6833.22998		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3531.6499		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0309999995		
k_MtrCtrlVirtualResQax_Ohm_f32	0.131999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.7777004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	27.8603001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-19.1399994		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.7269993		
k_deadtimeVScale_Uls_f32	0.981999993		
t_CommOffsetTblX_Uls_u3p13[0]	6349		
t_CommOffsetTblX_Uls_u3p13[1]	7225		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3088		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64356	64356 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	146.174011	146.173996 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	23.6980801	23.6980991 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	15.8347845	15.8347998 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	26816	26816 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	23.7777004	23.7777004 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	146.174011	146.173996 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	178.639008	178.639008 ± 7.81E-03	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.84 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.26699996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-645.427979
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	733.924988
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.03900003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.528
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	643.85498
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	55.9690018
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	21.2989998
MtrCtrl_Vecu_Volt_M_f32[1]	23.6590004
MtrCurrDaxPrevIntg_Volt_M_f32	0
MtrCurrDaxRef_Amp_M_f32[0]	-100.282997
MtrCurrDaxRef_Amp_M_f32[1]	-120.248001
MtrCurrQaxCog_Amp_M_f32	-41.5750008
MtrCurrQaxFinalRef_Amp_M_f32[0]	-76.8769989
MtrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998
MtrCurrQaxPrevIntg_Volt_M_f32	22.0902996
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxRpl_Amp_M_f32[0]	-76.8769989
MtrCurrQaxRpl_Amp_M_f32[1]	-153.238998
MtrPosComputationDelay_Rad_M_f32[0]	-1.20700002
MtrPosComputationDelay_Rad_M_f32[1]	-2.58599997
MtrVoltDax_Volt_M_f32[0]	-29.3959999
MtrVoltDax_Volt_M_f32[1]	-1.94000006
MtrVoltQax_Volt_M_f32[0]	-29.3959999
MtrVoltQax_Volt_M_f32[1]	-1.94000006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.181999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.349999994
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.615199983
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.521000028
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	78.1542969
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.47240001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	78.1542969
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.47240001
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	7388.58984
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	4638.1499
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.123000003
k_MtrCtrlVirtualResQax_Ohm_f32	0.0130000003
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	9.58860016

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	10.2995996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-11.0579996		
k_VoltSatQaxPolyCoeff_Uls_f32	14.974		
k_deadtimeVScale_Uls_f32	0.981999993		
t_CommOffsetTblX_Uls_u3p13[0]	3351		
t_CommOffsetTblX_Uls_u3p13[1]	5291		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4554		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4554	4554	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	53.2619934	53.262001 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.13420677	4.13421011 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.64885473	-2.64884996 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	9738	9738 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	53.2619934	53.262001 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998	-153.238998 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.85 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.07999999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.582000017
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.196999997

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	847.179993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-586.309021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.351999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83099997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	808.513977
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.0500031
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_Vecu_Volt_M_f32[0]	21.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	23.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	17.9769993
MtrCurrDaxRef_Amp_M_f32[0]	-68.6760025
MtrCurrDaxRef_Amp_M_f32[1]	-96.776001
MtrCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001
MtrCurrQaxPrevIntg_Volt_M_f32	24.0972004
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	191.369003
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001
MtrPosComputationDelay_Rad_M_f32[0]	-5.93400002
MtrPosComputationDelay_Rad_M_f32[1]	5.86899996
MtrVoltDax_Volt_M_f32[0]	-17.1070004
MtrVoltDax_Volt_M_f32[1]	15.9390001
MtrVoltQax_Volt_M_f32[0]	-17.1070004
MtrVoltQax_Volt_M_f32[1]	15.9390001
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.512000024
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.275000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.342900008
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	68.4229965
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.445100009
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	68.4229965
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.445100009
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	329.425995
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4506.12012
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0189999994
k_MtrCtrlVirualResQax_Ohm_f32	0.189999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	29.8101997
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	30.8836002
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	17.5830002
k_VoltSatQaxPolyCoeff_Uls_f32	3.3670001
k_deadtimeVScale_Uls_f32	0.986999989
t_CommOffsetTblX_Uls_u3p13[0]	1450
t_CommOffsetTblX_Uls_u3p13[1]	4529
t_CommOffsetTblY_Cnt_u16[0]	889
t_CommOffsetTblY_Cnt_u16[1]	1543
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3203
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	1543	1543	✓
MtrCntl_Write_ModlDx_Uls_u16p16(val)	64684	64684 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	10.0439243	10.0438995 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-18.5360146	-18.5359993 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	31231	31231 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlDx_Uls_u16p16	1	MtrCntl_Write_ModlDx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.86 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc(ptr)	target_MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.239999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.128999993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-873.200012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-251.832993
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.69299996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.763999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-639.518982
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	659.557007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_Vecu_Volt_M_f32[0]	22.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	24.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4580002
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
MtrCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972		
MtrCurrQaxPrevIntg_Volt_M_f32	25.1226997		
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994		
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993		
MtrCurrQaxRpl_Amp_M_f32[0]	-147.343002		
MtrCurrQaxRpl_Amp_M_f32[1]	127.972		
MtrPosComputationDelay_Rad_M_f32[0]	-1.05700004		
MtrPosComputationDelay_Rad_M_f32[1]	4.92199993		
MtrVoltDax_Volt_M_f32[0]	0.908999979		
MtrVoltDax_Volt_M_f32[1]	21.0249996		
MtrVoltQax_Volt_M_f32[0]	0.908999979		
MtrVoltQax_Volt_M_f32[1]	21.0249996		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.84799999		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.224000007		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.133100003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.143000007		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1048.76001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0.987200022		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.676100016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1048.76001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	0.987200022		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.676100016		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6782.70996		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1518.89001		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.180999994		
k_MtrCtrlVirtualResQax_Ohm_f32	0.171000004		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.4330006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	24.4650993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.5150003		
k_VoltSatQaxPolyCoeff_Uls_f32	-6.57200003		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	1663		
t_CommOffsetTblX_Uls_u3p13[1]	5979		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSriComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1472		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63700	63700 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-7.63010979	-7.63010979 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-22.7841721	-22.7842007 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	21941	21941 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002	-147.343002 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.87 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.25300002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0610000007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-237.227005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	186.412003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.61699998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.86600006
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	896.210999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-75.5360031
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	20.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	22.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4869995
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxPrevIntg_Volt_M_f32	25.5816994
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	6.18900013
MtrCurrQaxRpl_Amp_M_f32[1]	83.0540009
MtrPosComputationDelay_Rad_M_f32[0]	-3.98799992
MtrPosComputationDelay_Rad_M_f32[1]	2.56999993
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.768999994		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.68599999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.566999972		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.098999995		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	412.23999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	64.4531021		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.970099986		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	412.23999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	64.4531021		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.970099986		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2259.41992		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	6958.5498		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.021999999		
k_MtrCtrlVirtualResQax_Ohm_f32	0.014999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.9607		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.550499976		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.7080002		
k_VoltSatQaxPolyCoeff_Uls_f32	7.73099995		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	2195		
t_CommOffsetTblX_Uls_u3p13[1]	6013		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1694		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1371	1371	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	43962	43962 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33019	33019 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013	6.18900013 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.88 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16100001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	600.401001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-510.458008
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.709999979
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.37
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-807.60199
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-536.44397
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	14.243
MtrCtrl_Vecu_Volt_M_f32[1]	16.6030006
MtrCurrDaxPrevIntg_Volt_M_f32	29.1970005
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	3.13030005
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	6.22800016
MtrPosComputationDelay_Rad_M_f32[1]	-2.81399989
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.602999985
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0560999997
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	20.7000008
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	4.45230007
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.590499997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	20.7000008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	4.45230007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.590499997
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	3135.04004
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	6729.4502
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0460000001
k_MtrCtrlVirtualResQax_Ohm_f32	0.196999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	18.1431999

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.7143993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-15.9820004		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.8069992		
k_deadtimeVScale_Uls_f32	0.981000006		
t_CommOffsetTblX_Uls_u3p13[0]	3023		
t_CommOffsetTblX_Uls_u3p13[1]	3703		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4486		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1236	1236	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	64290	64290 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-88.0050049	-88.0049973 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	4.58217335	4.58217001 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-13.199666	-13.1997004 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	28707	28707 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-88.0050049	-88.0049973 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.89 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.09300005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	2.95000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-590.848999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0170000009		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0410000011		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.216999993		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.446999997		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	20.6189995		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-802.844971		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.55999994		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.8330002		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.61900043		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.1560001		
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999		
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996		
MtrCurrDaxPrevIntg_Volt_M_f32	-23.1609993		
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrDaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxCog_Amp_M_f32	20.6149998		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	1.44630003		
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	3.50099993		
MtrPosComputationDelay_Rad_M_f32[1]	-3.37899995		
MtrVoltDax_Volt_M_f32[0]	-8.61900043		
MtrVoltDax_Volt_M_f32[1]	-13.1560001		
MtrVoltQax_Volt_M_f32[0]	-8.61900043		
MtrVoltQax_Volt_M_f32[1]	-13.1560001		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0179999992		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0599999987		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0817999989		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1048.76001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	64.245903		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0513000004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1048.76001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	64.245903		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0513000004		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3946.5		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3683.88989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.00400000019		
k_MtrCtrlVirualResQax_Ohm_f32	0.145999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.50430012		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	23.5613003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.58000004		
k_VoltSatQaxPolyCoeff_Uls_f32	24.8470001		
k_deadtimeVScale_Uls_f32	0.962000012		
t_CommOffsetTblX_Uls_u3p13[0]	6128		
t_CommOffsetTblX_Uls_u3p13[1]	7397		
t_CommOffsetTblY_Cnt_u16[0]	1081		
t_CommOffsetTblY_Cnt_u16[1]	1779		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4823		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1779	1779	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_ModlIdx_Uls_u16p16(val)	63045	63045 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	87.7369995	87.7369995 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-12.7889309	-12.7889004 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-8.07618523	-8.07618999 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8033	8033 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	87.7369995	87.7369995 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlIdx_Uls_u16p16	1	MtrCntl_Write_ModlIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.90 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	79.6729965
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxPrevIntg_Volt_M_f32	21.3169994
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007		
MtrCurrQaxRpl_Amp_M_f32[0]	-212.632996		
MtrCurrQaxRpl_Amp_M_f32[1]	-205.085007		
MtrPosComputationDelay_Rad_M_f32[0]	1.52600002		
MtrPosComputationDelay_Rad_M_f32[1]	-2.68400002		
MtrVoltDax_Volt_M_f32[0]	-27.0669994		
MtrVoltDax_Volt_M_f32[1]	28.1070004		
MtrVoltQax_Volt_M_f32[0]	-27.0669994		
MtrVoltQax_Volt_M_f32[1]	28.1070004		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.400000006		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0590000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.231900007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5851002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.5851002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.887899995		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2663.65991		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5194.8999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0219999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.7327003		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.5590992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.368		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.9229965	-87.9229965 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70514989 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95905995 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996	-212.632996 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-87.9229965	-87.9229965 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.91 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-133.947006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	75.7020035
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0949999988
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0179999992
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.558000028
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.505
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-763.603027
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	830.864014
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0810000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.354999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	657.155029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-284.454987
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	22.7639999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	9.54300022
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-24.052
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-25.3250008
MtrCtrl_Vecu_Volt_M_f32[0]	14.243
MtrCtrl_Vecu_Volt_M_f32[1]	16.6030006
MtrCurrDaxPrevIntg_Volt_M_f32	-10.9969997
MtrCurrDaxRef_Amp_M_f32[0]	-208.287994
MtrCurrDaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxCog_Amp_M_f32	0.486999989
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004
MtrCurrQaxFinalRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxPrevIntg_Volt_M_f32	26.5330009
MtrCurrQaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxRpl_Amp_M_f32[0]	-132.813004
MtrCurrQaxRpl_Amp_M_f32[1]	-9.14299965
MtrPosComputationDelay_Rad_M_f32[0]	-2.81100011
MtrPosComputationDelay_Rad_M_f32[1]	-3.06399989
MtrVoltDax_Volt_M_f32[0]	-24.052
MtrVoltDax_Volt_M_f32[1]	-25.3250008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-24.052		
MtrVoltQax_Volt_M_f32[1]	-25.3250008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.533999979		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0460000001		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.339700013		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.492000014		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	51.8735008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.139899999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	51.8735008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.139899999		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	10		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4664.1001		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0250000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.151999995		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	26.3267994		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.5105991		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	11.9359999		
k_VoltSatQaxPolyCoeff_Uls_f32	-16.2380009		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTblX_Uls_u3p13[0]	1106		
t_CommOffsetTblX_Uls_u3p13[1]	4701		
t_CommOffsetTblY_Cnt_u16[0]	363		
t_CommOffsetTblY_Cnt_u16[1]	989		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3229		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	989	989	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	62324	62324 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	142.348999	142.348999 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	14.6598969	14.6598997 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-5.86465836	-5.86465979 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	53931	53931 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-132.813004	-132.813004 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	142.348999	142.348999 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.92 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	209.052002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-124.994003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.1129999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.03999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-632.612
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-39.875
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.1129999998
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.465999991
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.62100005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-201.291
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-817.749023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996
MtrCurrDaxPrevIntg_Volt_M_f32	-17.9279995
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-190.440994
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556
MtrCurrQaxFinalRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxPrevIntg_Volt_M_f32	26.2782993
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	-200.556
MtrCurrQaxRpl_Amp_M_f32[1]	-98.4449997
MtrPosComputationDelay_Rad_M_f32[0]	1.08399999
MtrPosComputationDelay_Rad_M_f32[1]	4.23000002
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0900000036
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.88499999
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.660399973
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.535000026
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-826.23999
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	15.0881996
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.349099994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-826.23999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	15.0881996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.349099994
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	8000
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5272.3999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.179000005
k_MtrCtrlVirtualResQax_Ohm_f32	0.0209999997
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.5879993		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	20.5517998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6410007		
k_VoltSatQaxPolyCoeff_Uls_f32	20.0030003		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	687		
t_CommOffsetTblX_Uls_u3p13[1]	7234		
t_CommOffsetTblY_Cnt_u16[0]	341		
t_CommOffsetTblY_Cnt_u16[1]	370		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	389		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-205.514999		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	389	389	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-0.112415425	-0.112415001 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-4.83869457	-4.8386898 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	11595	11595 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-200.556	-200.556 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.93 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.495
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.398999989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	184.223999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-915.817017
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.58099997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.776000023
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-189.419998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	896.187988
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxCog_Amp_M_f32	83.9489975
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	9.36159992
MtrCurrQaxRef_Amp_M_f32[0]	106.072998
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-2.69099998
MtrPosComputationDelay_Rad_M_f32[1]	-1.99899995
MtrVoltDax_Volt_M_f32[0]	-3.59500003
MtrVoltDax_Volt_M_f32[1]	-28.4209995
MtrVoltQax_Volt_M_f32[0]	-3.59500003
MtrVoltQax_Volt_M_f32[1]	-28.4209995
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.155000001
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.158000007
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.768999994
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.565999985
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	58.6325989
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.559199989
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	58.6325989
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.559199989
SlowDataAccessBufIndex_Cnt_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1848.06995
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6831.5
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0219999999
k_MtrCtrlVirtualResQax_Ohm_f32	0.0419999994
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	2.43009996
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	24.5324001
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	2.34999999
k_VoltSatQaxPolyCoeff_Uls_f32	21.7280006
k_deadtimeVScale_Uls_f32	0.957000017
t_CommOffsetTblX_Uls_u3p13[0]	474
t_CommOffsetTblX_Uls_u3p13[1]	6954
t_CommOffsetTblY_Cnt_u16[0]	434
t_CommOffsetTblY_Cnt_u16[1]	1438
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2090		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	2090	2090	✓
MtrCntri_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-45.3659973	-45.3660011 ± 7.81E-03	✓
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-4.73769665	-4.73769999 ± 4.88E-04	✓
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-0.671159685	-0.671159983 ± 4.88E-04	✓
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	19616	19616 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-45.3659973	-45.3660011 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.94 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.495
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.398999989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	184.223999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-915.817017
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.58099997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.776000023
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-189.419998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	896.187988
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxCog_Amp_M_f32	83.9489975		
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979		
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001		
MtrCurrQaxPrevIntg_Volt_M_f32	15.9664001		
MtrCurrQaxRef_Amp_M_f32[0]	106.072998		
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002		
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979		
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001		
MtrPosComputationDelay_Rad_M_f32[0]	-2.69099998		
MtrPosComputationDelay_Rad_M_f32[1]	-1.99899995		
MtrVoltDax_Volt_M_f32[0]	-3.59500003		
MtrVoltDax_Volt_M_f32[1]	-28.4209995		
MtrVoltQax_Volt_M_f32[0]	-3.59500003		
MtrVoltQax_Volt_M_f32[1]	-28.4209995		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.155000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.158000007		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.147200003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.565999985		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0.442999989		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.579999983		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	0.442999989		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.579999983		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	100		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6831.5		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0219999999		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0419999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.578599989		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.73509979		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	2.34999999		
k_VoltSatQaxPolyCoeff_Uls_f32	21.7280006		
k_deadtimeVScale_Uls_f32	0.957000017		
t_CommOffsetTblX_Uls_u3p13[0]	474		
t_CommOffsetTblX_Uls_u3p13[1]	6954		
t_CommOffsetTblY_Cnt_u16[0]	434		
t_CommOffsetTblY_Cnt_u16[1]	1438		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2090		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2090	2090	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-45.3659973	-45.3660011 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-25.3770008	-25.3770008 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.59500003	-3.59500003 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	19616	19616 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-45.3659973	-45.3660011 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.95 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.56099999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.21300006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	907.228027
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-851.888
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0930000022
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.467999995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-286.428009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	784.336975
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_Vecu_Volt_M_f32[0]	25.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	27.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-27.3339996
MtrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	21.0373001
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-2.78399992
MtrPosComputationDelay_Rad_M_f32[1]	-3.74300003
MtrVoltDax_Volt_M_f32[0]	-25.3770008
MtrVoltDax_Volt_M_f32[1]	21.3880005

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-25.3770008		
MtrVoltQax_Volt_M_f32[1]	21.3880005		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.178000003		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0496999994		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17.1812992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.386400014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17.1812992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.386400014		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	4653.20996		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1635.59998		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.171000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.180000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.0632		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.61609995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.6819992		
k_VoltSatQaxPolyCoeff_Uls_f32	2.70700002		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	4506		
t_CommOffsetTblX_Uls_u3p13[1]	5381		
t_CommOffsetTblY_Cnt_u16[0]	156		
t_CommOffsetTblY_Cnt_u16[1]	1570		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4809		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4809	4809	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.825012	131.824997 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	15.7353363	15.7353001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-25.6242886	-25.6243 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	63522	63522 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.825012	131.824997 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.96 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.841000021
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.24800003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-552.150024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-568.89502
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.921000004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.173999995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1018.71997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-471.221985
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	20.9669991
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
MtrCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	-41.5750008
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001
MtrCurrQaxPrevIntg_Volt_M_f32	23.1735001
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxRpl_Amp_M_f32[0]	191.369003
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001
MtrPosComputationDelay_Rad_M_f32[0]	-0.188999996
MtrPosComputationDelay_Rad_M_f32[1]	1.03799999
MtrVoltDax_Volt_M_f32[0]	-29.3959999
MtrVoltDax_Volt_M_f32[1]	-1.94000006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.94000006		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.662999988		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.952600002		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	865.320007		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	67.9733963		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.439500004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	67.9733963		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.439500004		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	1265.93005		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5888.85986		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.151999995		
k_MtrCtrlVirualResQax_Ohm_f32	0.0329999998		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	11.7454004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.04580021		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.3470001		
k_VoltSatQaxPolyCoeff_Uls_f32	8.97500038		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	3030		
t_CommOffsetTblX_Uls_u3p13[1]	5366		
t_CommOffsetTblY_Cnt_u16[0]	589		
t_CommOffsetTblY_Cnt_u16[1]	1202		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4196		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	589	589	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	10634	10634 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.61400008	2.61400008 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.94000006	-1.94000006 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33870	33870 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	11.7454004	11.7454004 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003	191.369003 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.97 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70099998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.287999988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1014.57001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	639.960022
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.42200005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.612999976
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-811.013
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-317.71701
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	10.2959995
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972
MtrCurrQaxPrevIntg_Volt_M_f32	12.8893003
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	-147.343002
MtrCurrQaxRpl_Amp_M_f32[1]	127.972
MtrPosComputationDelay_Rad_M_f32[0]	0.750999987
MtrPosComputationDelay_Rad_M_f32[1]	-1.45000005
MtrVoltDax_Volt_M_f32[0]	-17.1070004
MtrVoltDax_Volt_M_f32[1]	15.9390001
MtrVoltQax_Volt_M_f32[0]	-17.1070004
MtrVoltQax_Volt_M_f32[1]	15.9390001
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.640999973
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.88499999
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.120399997
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.143000007
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	74.1108017
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.654399991
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	74.1108017
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.654399991
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7251.52002
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5272.3999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.126000002
k_MtrCtrlVirtualResQax_Ohm_f32	0.128999993
k_MtrCurrQaxReffModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.9356995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.26469994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.3450003		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.0529995		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	4850		
t_CommOffsetTblX_Uls_u3p13[1]	6241		
t_CommOffsetTblY_Cnt_u16[0]	165		
t_CommOffsetTblY_Cnt_u16[1]	651		
target_MtrCntnl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntnl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntnl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntnl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntnl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3061		
target_MtrCntnl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntnl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntnl_Write_CommOffset_Cnt_u16(val)	3061	3061	✔
MtrCntnl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntnl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-48.2200012	-48.2200012 ± 7.81E-03	✔
MtrCntnl_Write_MtrDaxVoltage_Volt_f32(val)	-0.995374382	-0.995374024 ± 4.88E-04	✔
MtrCntnl_Write_MtrQaxVoltage_Volt_f32(val)	-4.73654222	-4.73653984 ± 4.88E-04	✔
MtrCntnl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42762	42762 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-48.2200012	-48.2200012 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972	127.972 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.98 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.74000001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.391000003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	382.878998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-891.598022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.207000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.145999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-192.985992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	708.689026
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	28.4400005
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxPrevIntg_Volt_M_f32	9.35029984
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	6.18900013
MtrCurrQaxRpl_Amp_M_f32[1]	83.0540009
MtrPosComputationDelay_Rad_M_f32[0]	-3.47099996
MtrPosComputationDelay_Rad_M_f32[1]	4.87400007
MtrVoltDax_Volt_M_f32[0]	0.908999979
MtrVoltDax_Volt_M_f32[1]	21.0249996
MtrVoltQax_Volt_M_f32[0]	0.908999979
MtrVoltQax_Volt_M_f32[1]	21.0249996
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.986999989
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.370000005
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.382499993
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0989999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.4266968
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.757300019
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.4266968
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.757300019
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5252.41016
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	10
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.192000002
k_MtrCtrlVirtualResQax_Ohm_f32	0.145999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	9.65880013
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	23.7485008
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	8.97799969
k_VoltSatQaxPolyCoeff_Uls_f32	18.2439995
k_deadtimeVScale_Uls_f32	0.975000024
t_CommOffsetTblX_Uls_u3p13[0]	2114
t_CommOffsetTblX_Uls_u3p13[1]	4735
t_CommOffsetTblY_Cnt_u16[0]	297
t_CommOffsetTblY_Cnt_u16[1]	1110
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2412

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntnrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntnrl_Write_CommOffset_Cnt_u16(val)	1110	1110	✓
MtrCntnrl_Write_Modldx_Uls_u16p16(val)	63897	63897 ± 1	✓
MtrCntnrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntnrl_Write_MtrDaxVoltage_Volt_f32(val)	19.6874123	19.6874008 ± 4.88E-04	✓
MtrCntnrl_Write_MtrQaxVoltage_Volt_f32(val)	6.8305316	6.83053017 ± 4.88E-04	✓
MtrCntnrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42233	42233 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009	83.0540009 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.99 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.15499997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.71800005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	953.320984
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-556.945007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.34200001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0759999976
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	41.5550003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	935.856018
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	1.82700002
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxPrevIntg_Volt_M_f32	7.8980999		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016		
MtrPosComputationDelay_Rad_M_f32[0]	6.27600002		
MtrPosComputationDelay_Rad_M_f32[1]	1.47300005		
MtrVoltDax_Volt_M_f32[0]	-28.2420006		
MtrVoltDax_Volt_M_f32[1]	12.6160002		
MtrVoltQax_Volt_M_f32[0]	-28.2420006		
MtrVoltQax_Volt_M_f32[1]	12.6160002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.818000019		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.61500001		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0132999998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	97.2235031		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.703100026		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	97.2235031		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.703100026		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	18.2099991		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	8000		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0500000007		
k_MtrCtrlVirualResQax_Ohm_f32	0.0469999984		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	16.8875999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.7059002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-14.7080002		
k_VoltSatQaxPolyCoeff_Uls_f32	3.90300012		
k_deadtimeVScale_Uls_f32	0.957000017		
t_CommOffsetTblX_Uls_u3p13[0]	1498		
t_CommOffsetTblX_Uls_u3p13[1]	4940		
t_CommOffsetTblY_Cnt_u16[0]	623		
t_CommOffsetTblY_Cnt_u16[1]	1212		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1116		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1116	1116	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.39211178	-2.39211011 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.1441555	-4.14415979 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38153	38153 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.100 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.335000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.61000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	676.015015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.322021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.521000028
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65699995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	40.1660004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	649.921021
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-11.698
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	3.4605999
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-1.23399997
MtrPosComputationDelay_Rad_M_f32[1]	-1.25100005
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.810000002		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.551999986		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.397899985		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	18.5506001		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.689499974		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18.5506001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.689499974		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5675.16992		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	7779.18994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.174999997		
k_MtrCtrlVirualResQax_Ohm_f32	0.0280000009		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	4.64909983		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.52950001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.73900008		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.03700018		
k_deadtimeVScale_Uls_f32	0.958000004		
t_CommOffsetTblX_Uls_u3p13[0]	6110		
t_CommOffsetTblX_Uls_u3p13[1]	7324		
t_CommOffsetTblY_Cnt_u16[0]	237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2039		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	62783	62783 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.3380833	-10.3381004 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.6049595	-17.6049995 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	25258	25258 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-30.2000008	-30.2000008 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.101 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.335000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.61000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	676.015015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.322021
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.521000028
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65699995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	40.1660004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	649.921021
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-11.698
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	10.2080002
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-1.23399997
MtrPosComputationDelay_Rad_M_f32[1]	-1.25100005
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.810000002
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.551999986
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.338800013
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-304.940002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21.2028008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0865999982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-304.940002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	21.2028008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0865999982
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5675.16992
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	100
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.174999997
k_MtrCtrlVirtualResQax_Ohm_f32	0.0280000009
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.178399995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.8720999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.73900008		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.03700018		
k_deadtimeVScale_Uls_f32	0.958000004		
t_CommOffsetTblX_Uls_u3p13[0]	6110		
t_CommOffsetTblX_Uls_u3p13[1]	7324		
t_CommOffsetTblY_Cnt_u16[0]	237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2039		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	383	383	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	62783	62783 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-13.7442369	-13.7441998 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	15.0965738	15.0965996 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	44784	44784 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-9.64999962	-9.64999962 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.102 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	79.6729965
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxPrevIntg_Volt_M_f32	0.771099985
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32[0]	-212.632996
MtrCurrQaxRpl_Amp_M_f32[1]	-205.085007
MtrPosComputationDelay_Rad_M_f32[0]	1.52600002
MtrPosComputationDelay_Rad_M_f32[1]	-2.68400002
MtrVoltDax_Volt_M_f32[0]	-27.0669994
MtrVoltDax_Volt_M_f32[1]	28.1070004
MtrVoltQax_Volt_M_f32[0]	-27.0669994
MtrVoltQax_Volt_M_f32[1]	28.1070004
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.400000006
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.375200003
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5851002
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.5851002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.887899995
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2663.65991
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5194.8999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.112000003
k_MtrCtrlVirtualResQax_Ohm_f32	0.0219999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	19.6758003
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	8.2833004
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001
k_VoltSatQaxPolyCoeff_Uls_f32	-21.368
k_deadtimeVScale_Uls_f32	0.958999991
t_CommOffsetTblX_Uls_u3p13[0]	6528
t_CommOffsetTblX_Uls_u3p13[1]	8192
t_CommOffsetTblY_Cnt_u16[0]	76
t_CommOffsetTblY_Cnt_u16[1]	211
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	65	65	✓
MtrCntl_Write_ModlDx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.9229965	-87.9229965 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70514989 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95905995 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996	-212.632996 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-87.9229965	-87.9229965 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlDx_Uls_u16p16	1	MtrCntl_Write_ModlDx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.103 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.73699999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.397000015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	692.312988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	147.145996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.29700005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0260000005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	381.019012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-514.21698
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-1.17400002
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxPrevIntg_Volt_M_f32	23.9027004		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016		
MtrPosComputationDelay_Rad_M_f32[0]	2.648		
MtrPosComputationDelay_Rad_M_f32[1]	-4.26800013		
MtrVoltDax_Volt_M_f32[0]	-28.2420006		
MtrVoltDax_Volt_M_f32[1]	12.6160002		
MtrVoltQax_Volt_M_f32[0]	-28.2420006		
MtrVoltQax_Volt_M_f32[1]	12.6160002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.109999999		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	1		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.289799988		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	39.4047012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.869400024		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	39.4047012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.869400024		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3550.11011		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5873.56006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.162		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0790000036		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	26.7257996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.8303003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.2010002		
k_VoltSatQaxPolyCoeff_Uls_f32	-9.57699966		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	573		
t_CommOffsetTblX_Uls_u3p13[1]	7569		
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSriComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	410		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	410	410	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.40960884	-2.40961003 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.17446756	-4.17446995 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	312	312 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.104 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.878000021
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.29400003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-295.479004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-442.687988
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.896000028
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.91700006
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1015.31
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-261.230011
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-16.7709999
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	17.1947002
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	0.994000018
MtrPosComputationDelay_Rad_M_f32[1]	-0.177000001
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.658999979		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.203999996		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.49939999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	96.5500031		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	56.0906982		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.676299989		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	96.5500031		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	56.0906982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.676299989		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6301.31982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3999.36011		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.129999995		
k_MtrCtrlVirualResQax_Ohm_f32	0.182999998		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.9897995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.6005001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.84200001		
k_VoltSatQaxPolyCoeff_Uls_f32	18.5489998		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	1154		
t_CommOffsetTblX_Uls_u3p13[1]	5284		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1520		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63176	63176 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.4028301	-10.4027996 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.7152195	-17.7152004 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	36460	36460 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-4.57000017	-4.57000017 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.105 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.76499999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.986999989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-48.4529991
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-844.020996
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.22599995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.58099997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	17.7269993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-203.524002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	7.91989994
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	4.15500021
MtrPosComputationDelay_Rad_M_f32[1]	1.90199995
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.944000006
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.294999987
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.936500013
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	5.45760012
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.833899975
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	5.45760012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.833899975
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	5388.75
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	3076.13989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.123999998
k_MtrCtrlVirtualResQax_Ohm_f32	0.163000003
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.2125001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.89789963		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-23.9650002		
k_VoltSatQaxPolyCoeff_Uls_f32	-16.2169991		
k_deadtimeVScale_Uls_f32	0.949999988		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3921		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	3921	3921	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-4.21121168	-4.21120977 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-2.19731569	-2.19731998 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	21938	21938 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.106 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.08099997

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.26699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	484.062988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	62.8199997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.40799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.17900002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	623.000977
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-937.359009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	2.18199992
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	1.20200002
MtrPosComputationDelay_Rad_M_f32[1]	-1.38100004
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.25999999
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.222000003
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.918299973
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	301.089996
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50.1682014
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.833800018
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	301.089996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50.1682014
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.833800018
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7174.7002
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	1940.90002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0850000009
k_MtrCtrlVirualResQax_Ohm_f32	0.118000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	30.3138008
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	15.4816999
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-14.8479996
k_VoltSatQaxPolyCoeff_Uls_f32	5.72399998
k_deadtimeVScale_Uls_f32	1
t_CommOffsetTblX_Uls_u3p13[0]	4611
t_CommOffsetTblX_Uls_u3p13[1]	5579
t_CommOffsetTblY_Cnt_u16[0]	889
t_CommOffsetTblY_Cnt_u16[1]	1543
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntnrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntnrl_Write_CommOffset_Cnt_u16(val)	3001	3001	✓
MtrCntnrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntnrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntnrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	✓
MtrCntnrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	✓
MtrCntnrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	57345	57345 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.107 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.42499995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.64400005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	980.661987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	771.224976
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.26199996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.75600004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	424.487
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	866.411987
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	13.4927998		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	2.78600001		
MtrPosComputationDelay_Rad_M_f32[1]	-0.129999995		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.1870003		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSciFac_Uls_M_f32	0.185000002		
PICurrCntrl_InverterFailSciFac_Uls_M_f32	0.737999976		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.543500006		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-200.740005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	85.9597015		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.842700005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-200.740005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	85.9597015		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.842700005		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSciFacSlew_UlspS_f32	3789.18994		
k_ILOAFdbackSignalSciFacSlew_UlspS_f32	3865.98999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.00700000022		
k_MtrCtrlVirtualResQax_Ohm_f32	0.149000004		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.9689007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.60000038		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.1835003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.0129995		
k_VoltSatQaxPolyCoeff_Uls_f32	-13.5450001		
k_deadtimeVScale_Uls_f32	0.995000005		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4728		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	65208	65208 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.7373619	-10.7374001 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-18.2849007	-18.2849007 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	36950	36950 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-9.60000038	-9.60000038 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.108 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.247999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.372999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	875.137024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-484.88501
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.29700005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.26699999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-49.7849998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	739.11499
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-7.71299982
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	2.60570002
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	5.8579998
MtrPosComputationDelay_Rad_M_f32[1]	2.852
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.723999977		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.800999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.335799992		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	19.6403008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.133300006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	19.6403008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.133300006		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3827.27002		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirtualResQax_Ohm_f32	0.147		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.43280005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.2376003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.5009995		
k_VoltSatQaxPolyCoeff_Uls_f32	6.51900005		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTblX_Uls_u3p13[0]	2638		
t_CommOffsetTblX_Uls_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	121.994003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	34		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-40.9220009		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	34	34	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.75824261	-4.75824022 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.06054318	-1.06053996 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42430	42430 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.109 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.474999994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.837000012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	673.796997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-165.348999
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0149999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.88800001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-32.8989983
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	814.530029
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	6.07289982
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	-2.54200006
MtrPosComputationDelay_Rad_M_f32[1]	4.63800001
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.296000004
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.281399995
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-861.580017
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	88.4244995
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.322100013
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-861.580017
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	88.4244995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.322100013
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	208.033005
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.061999999
k_MtrCtrlVirtualResQax_Ohm_f32	0.0989999995
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.790099978		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.3572006		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-6.70300007		
k_VoltSatQaxPolyCoeff_Uls_f32	-3.44300008		
k_deadtimeVScale_Uls_f32	0.977999985		
t_CommOffsetTblX_Uls_u3p13[0]	1212		
t_CommOffsetTblX_Uls_u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntnl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntnl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntnl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntnl_Read_MtrCurrDax_Amp_f32_Val	-41.5750008		
target_MtrCntnl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1147		
target_MtrCntnl_Read_MtrCurrQax_Amp_f32_Val	75.0830002		
target_MtrCntnl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntnl_Write_CommOffset_Cnt_u16(val)	1147	1147	✔
MtrCntnl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntnl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	✔
MtrCntnl_Write_MtrDaxVoltage_Volt_f32(val)	4.88757896	4.88757992 ± 4.88E-04	✔
MtrCntnl_Write_MtrQaxVoltage_Volt_f32(val)	-0.153855383	-0.153854996 ± 4.88E-04	✔
MtrCntnl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55734	55734 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0.790099978	0.790099978 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.110 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.22500002

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.88600004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	800.210022
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-622.848022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.82700002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.74600005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-301.071014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	368.852997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	11.2777004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	0.674000025
MtrPosComputationDelay_Rad_M_f32[1]	-3.648
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.448000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.565900028
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.700012
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	75.3476028
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.841799974
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	75.3476028
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.841799974
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0460000001
k_MtrCtrlVirtualResQax_Ohm_f32	0.101000004
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	0.3583
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	0.993200004
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	23.7310009
k_VoltSatQaxPolyCoeff_Uls_f32	10.2309999
k_deadtimeVScale_Uls_f32	0.986999989
t_CommOffsetTblX_Uls_u3p13[0]	3808
t_CommOffsetTblX_Uls_u3p13[1]	7298
t_CommOffsetTblY_Cnt_u16[0]	1237
t_CommOffsetTblY_Cnt_u16[1]	383
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	48.8400002
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2022

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnrl_Read_MtrCurrQax_Amp_f32_Val	-220		
target_MtrCntnrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntnrl_Write_CommOffset_Cnt_u16(val)	383	383	✓
MtrCntnrl_Write_Modldx_Uls_u16p16(val)	64684	64684 ± 1	✓
MtrCntnrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	✓
MtrCntnrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.73893094	-3.73892999 ± 4.88E-04	✓
MtrCntnrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.36711168	-6.36710978 ± 4.88E-04	✓
MtrCntnrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	256	256 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.111 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.62300003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23300004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	810.853027
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-988.492981
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.263000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.324999988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-344.360992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	396.108002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg_Volt_M_f32	3.09949994		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003		
MtrPosComputationDelay_Rad_M_f32[0]	-1.17200005		
MtrPosComputationDelay_Rad_M_f32[1]	5.78900003		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.777999997		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0450000018		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.95569998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	77.2248001		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.411900014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	77.2248001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.411900014		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6667.54004		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7823.27002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.00499999989		
k_MtrCtrlVirualResQax_Ohm_f32	0.0989999995		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.7106991		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.35650003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.0270004		
k_VoltSatQaxPolyCoeff_Uls_f32	19.8290005		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	107.702003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	233		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	233	233	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-29.7752876	-29.7752991 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.19967127	4.19966984 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38389	38389 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.112 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.859000027
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.911000013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-309.057007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-788.815002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.00399995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.43400002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-19.7409992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1021.15997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	14.243
MtrCtrl_Vecu_Volt_M_f32[1]	16.6030006
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	23.843399
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	3.53800011
MtrPosComputationDelay_Rad_M_f32[1]	-1.18900001
MtrVoltDax_Volt_M_f32[0]	-7.66699982

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.851999998		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.398999989		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.705799997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	49.1376991		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0364000015		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	49.1376991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0364000015		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	7980.1499		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0280000009		
k_MtrCtrlVirtualResQax_Ohm_f32	0.129999995		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.1104002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.7721004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-0.893999994		
k_VoltSatQaxPolyCoeff_Uls_f32	-24.9239998		
k_deadtimeVScale_Uls_f32	0.966000021		
t_CommOffsetTblX_Uls_u3p13[0]	1532		
t_CommOffsetTblX_Uls_u3p13[1]	2851		
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	5.72399998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2264		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	0		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1211	1211	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63307	63307 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-15.956131	-15.9561005 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.62337315	1.62337005 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37808	37808 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.113 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.624000013
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.05799997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	609.603027
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-912.517029
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.47599995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.801999986
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-292.941986
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	762.052002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996
MtrCurrDaxPrevIntg_Volt_M_f32	7.36499977
MtrCurrDaxRef_Amp_M_f32[0]	-100.282997
MtrCurrDaxRef_Amp_M_f32[1]	-120.248001
MtrCurrQaxCog_Amp_M_f32	-41.5750008
MtrCurrQaxFinalRef_Amp_M_f32[0]	-76.8769989
MtrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998
MtrCurrQaxPrevIntg_Volt_M_f32	8.32960033
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxRpl_Amp_M_f32[0]	-76.8769989
MtrCurrQaxRpl_Amp_M_f32[1]	-153.238998
MtrPosComputationDelay_Rad_M_f32[0]	1.32799995
MtrPosComputationDelay_Rad_M_f32[1]	-0.925000012
MtrVoltDax_Volt_M_f32[0]	-29.3959999
MtrVoltDax_Volt_M_f32[1]	-1.94000006
MtrVoltQax_Volt_M_f32[0]	-29.3959999
MtrVoltQax_Volt_M_f32[1]	-1.94000006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.349000007		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.111000001		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.824800014		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.521000028		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	82.828598		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.797699988		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	82.828598		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.797699988		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	1293.53003		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7361.14014		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirtualResQax_Ohm_f32	0.103		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.9596996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.6800003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	8.96500015		
k_VoltSatQaxPolyCoeff_Uls_f32	-13.8369999		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3317		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3317	3317	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	53.2619934	53.262001 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.7370646	-1.73705995 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.52290058	-4.5229001 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50444	50444 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	53.2619934	53.262001 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998	-153.238998 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.114 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.591000021
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	248.214996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-29.2189999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.46300006
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.03900003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	126.671997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-963.362976
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	0.851999998
MtrCurrDaxRef_Amp_M_f32[0]	-68.6760025
MtrCurrDaxRef_Amp_M_f32[1]	-96.776001
MtrCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001
MtrCurrQaxPrevIntg_Volt_M_f32	28.9717007
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	191.369003
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001
MtrPosComputationDelay_Rad_M_f32[0]	4.06799984
MtrPosComputationDelay_Rad_M_f32[1]	1.16100001
MtrVoltDax_Volt_M_f32[0]	-17.1070004

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltDax_Volt_M_f32[1]	15.9390001		
MtrVoltQax_Volt_M_f32[0]	-17.1070004		
MtrVoltQax_Volt_M_f32[1]	15.9390001		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.911000013		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.816999972		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.916199982		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-44.2799988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	95.5231018		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.219099998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-44.2799988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	95.5231018		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.219099998		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6616.02002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0810000002		
k_MtrCtrlVirualResQax_Ohm_f32	0.0829999968		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.9941998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.51160002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	19.1259995		
k_VoltSatQaxPolyCoeff_Uls_f32	1.05900002		
k_deadtimeVScale_Uls_f32	0.977999985		
t_CommOffsetTblX_Uls_u3p13[0]	2638		
t_CommOffsetTblX_Uls_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	20.6149998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	70		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1110	1110	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64094	64094 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-23.0419712	-23.0419998 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.72158337	-7.72159004 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22674	22674 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-10.5	-10.5 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.115 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.72399998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	376.216003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-802.426025
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.60300004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.42400002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	18.6140003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-320.81601
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_Vecu_Volt_M_f32[0]	28.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	30.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	1.579
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
MtrCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972
MtrCurrQaxPrevIntg_Volt_M_f32	0.0671999976
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	-147.343002
MtrCurrQaxRpl_Amp_M_f32[1]	127.972
MtrPosComputationDelay_Rad_M_f32[0]	-2.94000006
MtrPosComputationDelay_Rad_M_f32[1]	1.80400002
MtrVoltDax_Volt_M_f32[0]	0.908999979
MtrVoltDax_Volt_M_f32[1]	21.0249996
MtrVoltQax_Volt_M_f32[0]	0.908999979
MtrVoltQax_Volt_M_f32[1]	21.0249996

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.702000022		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.99180001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.143000007		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	44.7025986		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.123199999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	44.7025986		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.123199999		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5911.31982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	2332.93994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0179999992		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0500000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.83920002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.60000038		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.530899		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-9.26000023		
k_VoltSatQaxPolyCoeff_Uls_f32	12.5810003		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	1212		
t_CommOffsetTblX_Uls_u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3905		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	212	212	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	65339	65339 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-168.647003	-168.647003 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	28.2433357	28.2432995 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.33609736	1.33609998 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50762	50762 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	5.83920002	5.83920002 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-168.647003	-168.647003 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972	127.972 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.116 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.898000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.30599999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	806.749023
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-34.0489998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.221000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.49000001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	737.367004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	253.417999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	21.2989998
MtrCtrl_Vecu_Volt_M_f32[1]	23.6590004
MtrCurrDaxPrevIntg_Volt_M_f32	-8.56599998
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxPrevIntg_Volt_M_f32	2.58669996
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	6.18900013
MtrCurrQaxRpl_Amp_M_f32[1]	83.0540009
MtrPosComputationDelay_Rad_M_f32[0]	-2.94300008
MtrPosComputationDelay_Rad_M_f32[1]	0.898000002
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.662
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.499000013
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.1514
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0989999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.35469997
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.649999976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	1.35469997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.649999976
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	7083.27002
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	947.890015
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0540000014
k_MtrCtrlVirtualResQax_Ohm_f32	0.172000006
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	27.2376995

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	20.0447998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-13.0120001		
k_VoltSatQaxPolyCoeff_Uls_f32	6.00699997		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	220		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	794		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	794	794	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-57.1910019	-57.1910019 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-3.54663992	-3.54663992 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-3.29347062	-3.29346991 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	10649	10649 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013	6.18900013 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-57.1910019	-57.1910019 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.117 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.008999999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.009999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.65499997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.88900006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	392.079987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	734.911987
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.22399998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-333.980988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-661.781006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	21.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	23.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	22.5144005
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	-3.61800003
MtrPosComputationDelay_Rad_M_f32[1]	-4.93400002
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.851000011
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.757000029
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.070600003
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-826.23999
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	53.2509003
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.3134
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-826.23999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	53.2509003
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.3134
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2409.94995
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4854.70996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0359999985
k_MtrCtrlVirualResQax_Ohm_f32	0.163000003
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	20.7133999
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	12.3408003
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-10.0080004
k_VoltSatQaxPolyCoeff_Uls_f32	-22.6299992
k_deadtimeVScale_Uls_f32	0.97299999
t_CommOffsetTblX_Uls_u3p13[0]	4611
t_CommOffsetTblX_Uls_u3p13[1]	5579
t_CommOffsetTblY_Cnt_u16[0]	2000
t_CommOffsetTblY_Cnt_u16[1]	2000
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	0
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2515
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	2000	2000	✓
MtrCntl_Write_Modldx_Uls_u16p16(val)	63766	63766 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-88.0050049	-88.0049973 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	0.0192687921	0.0192687996 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	20.7832718	20.7833004 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27809	27809 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-88.0050049	-88.0049973 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.118 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(ptr)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(ptr)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_ptr
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(ptr)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_ptr
MtrCntl_Read_SysState_Cnt_Enum(ptr)	target_MtrCntl_Read_SysState_Cnt_Enum_ptr
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.703999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-685.018005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-140.973999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.24399996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83899999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	584.664978
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	935.218018
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.55999994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.8330002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.61900043
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.1560001
MtrCtrl_Vecu_Volt_M_f32[0]	17.2600002
MtrCtrl_Vecu_Volt_M_f32[1]	19.6200008
MtrCurrDaxPrevIntg_Volt_M_f32	14.2440004
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	20.6149998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	30.4113998		
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	6.09100008		
MtrPosComputationDelay_Rad_M_f32[1]	3.83599997		
MtrVoltDax_Volt_M_f32[0]	-8.61900043		
MtrVoltDax_Volt_M_f32[1]	-13.1560001		
MtrVoltQax_Volt_M_f32[0]	-8.61900043		
MtrVoltQax_Volt_M_f32[1]	-13.1560001		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.237000003		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.150000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.483900011		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50.986599		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.889199972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50.986599		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.889199972		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6110.83008		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7208.8501		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.172999993		
k_MtrCtrlVirtualResQax_Ohm_f32	0.189999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.9330006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	23.8327999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	17.1749992		
k_VoltSatQaxPolyCoeff_Uls_f32	8.79699993		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3557		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64946	64946 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.4397964	-16.4398003 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-10.3817005	-10.3816996 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	17752	17752 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.119 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.131999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.051
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	781.679016
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1013.59998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.495000005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.11000001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-786.575989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	755.47699
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	16.4099998
MtrCtrl_Vecu_Volt_M_f32[1]	18.7700005
MtrCurrDaxPrevIntg_Volt_M_f32	26.7269993
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	30.6539001
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	4.46799994
MtrPosComputationDelay_Rad_M_f32[1]	2.6400001
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.980000019		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.840300024		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	51.9599991		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.70389998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	51.9599991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.70389998		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	754.531982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	248.589005		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.179000005		
k_MtrCtrlVirtualResQax_Ohm_f32	0.158999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.8127003		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.2922001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-23.691		
k_VoltSatQaxPolyCoeff_Uls_f32	-14.6350002		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	220		
t_CommOffsetTblX_Uls_u3p13[1]	5037		
t_CommOffsetTblY_Cnt_u16[0]	671		
t_CommOffsetTblY_Cnt_u16[1]	876		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	256		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	876	876	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63635	63635 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.90307331	8.90305042 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	15.9031553	15.9032001 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32859	32859 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.120 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.77100003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.31400001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	4.21999979
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	387.277008
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.25800002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.36399996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	860.961975
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-245.580002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5
MtrCtrl_Vecu_Volt_M_f32[1]	5
MtrCurrDaxPrevIntg_Volt_M_f32	-1.17400002
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	23.6063004
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	4.70499992
MtrPosComputationDelay_Rad_M_f32[1]	-2.8670001
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.109999999
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00400000019
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.703999996
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	107.129997
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	71.2244034
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.953999996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	107.129997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	71.2244034
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.953999996
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3550.11011
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5873.56006
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0240000002
k_MtrCtrlVirtualResQax_Ohm_f32	0.0109999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHilim_Volt_f32	16.5436993

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.1534996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	17.4050007		
k_VoltSatQaxPolyCoeff_Uls_f32	-2.23099995		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	573		
t_CommOffsetTblX_Uls_u3p13[1]	7569		
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4516		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1211	1211	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	63111	63111 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	0.00506430957	0.00506431004 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	4.8149972	4.81500006 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49086	49086 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.121 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.975000024
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69599998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	585.619019
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-791.551025
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.125		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0529999994		
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0939999968		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.456999987		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.693000019		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1016.78003		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	915.791992		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003		
MtrCtrl_Vecu_Volt_M_f32[0]	31		
MtrCtrl_Vecu_Volt_M_f32[1]	31		
MtrCurrDaxPrevIntg_Volt_M_f32	-16.7709999		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	4.80830002		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	-2.35100007		
MtrPosComputationDelay_Rad_M_f32[1]	-1.33299994		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.1870003		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.658999979		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.824599981		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	96.5500031		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	27.4986992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.75029999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	96.5500031		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	27.4986992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.75029999		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6301.31982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3999.36011		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.00899999961		
k_MtrCtrlVirualResQax_Ohm_f32	0.0469999984		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.9073009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	21.2607994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	19.5340004		
k_VoltSatQaxPolyCoeff_Uls_f32	10.6110001		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	1154		
t_CommOffsetTblX_Uls_u3p13[1]	5284		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	153		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_ModlIdx_Uls_u16p16(val)	56845	56845 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-13.6160002	-13.6160002 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-23.1870003	-23.1870003 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24403	24403 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlIdx_Uls_u16p16	1	MtrCntl_Write_ModlIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.122 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.533999979
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.231999993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	322.946991
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	998.633972
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.869000018
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.70599997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	53.6450005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-178.399002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	18.9440002
MtrCurrQaxRef_Amp_M_f32[0]	171.485992

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003		
MtrPosComputationDelay_Rad_M_f32[0]	3.41799998		
MtrPosComputationDelay_Rad_M_f32[1]	3.54200006		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.944000006		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.465999991		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.403899997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13.2960997		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.640799999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	13.2960997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.640799999		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5388.75		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3076.13989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.178000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.181999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.5997009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	6.4369998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	16.1879997		
k_VoltSatQaxPolyCoeff_Uls_f32	0.165000007		
k_deadtimeVScale_Uls_f32	0.954999983		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	998		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	998	998	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	5.79688215	5.79687977 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-29.0319138	-29.0319004 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	2121	2121 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.123 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.317999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-952.169983
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-35.3190002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0149999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.439999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-452.992004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	297.122009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	14.243
MtrCtrl_Vecu_Volt_M_f32[1]	16.6030006
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	2.83229995
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	-5.51200008
MtrPosComputationDelay_Rad_M_f32[1]	3.42700005
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-28.2420006		
MtrVoltQax_Volt_M_f32[1]	12.6160002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.25999999		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0450000018		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.464599997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	99.8274994		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.052099999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	99.8274994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.052099999		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	7174.7002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1940.90002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0810000002		
k_MtrCtrlVirualResQax_Ohm_f32	0.159999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.89389992		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	6.79160023		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.066		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.36500001		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	926		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1543	1543	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	60159	60159 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	41958	41958 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Test Step 2.124 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.507000029
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	330.04599
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-179.259003
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.230000004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.53799999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-818.869995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	353.450989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	25.5028
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	2.39700007
MtrPosComputationDelay_Rad_M_f32[1]	2.73799992
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntl_CurrSensFailScfFac_Uls_M_f32	0.185000002
PICurrCntl_InverterFailScfFac_Uls_M_f32	0.398999989
PICurrCntl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0604999997
PICurrCntl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntl_MtrVecuFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntl_MtrVecuFilt_M_str.TermN_Uls_f32	1.48909998
PICurrCntl_MtrVecuFilt_M_str.TermD_Uls_f32	0.176699996
PICurrCntl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	1.48909998
PICurrCntl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.176699996
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3789.18994
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3865.98999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0710000023
k_MtrCtrlVirtualResQax_Ohm_f32	0.0489999987
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.557200015		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.7143993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-9.73099995		
k_VoltSatQaxPolyCoeff_Uls_f32	-24.3579998		
k_deadtimeVScale_Uls_f32	0.966000021		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	474		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-205.514999		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	474	474	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-8.04187298	-8.04187012 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	28.8459892	28.8460007 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	25723	25723 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.125 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.822000027
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.419999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-292.269989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-754.054993
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.871999979
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.22000003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	688.346985
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-854.249023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-7.71299982
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	17.9473
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-1.56599998
MtrPosComputationDelay_Rad_M_f32[1]	1.21399999
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.723999977
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.111000001
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0421999991
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	75.4738007
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.306199998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	75.4738007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.306199998
SlowDataAccessBufIndex_Cnt_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3827.27002
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2156.63989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.185000002
k_MtrCtrlVirtualResQax_Ohm_f32	0.0799999982
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	2.9461
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	19.4281006
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	17.6229992
k_VoltSatQaxPolyCoeff_Uls_f32	-20.6590004
k_deadtimeVScale_Uls_f32	0.968999982
t_CommOffsetTblX_Uls_u3p13[0]	2638
t_CommOffsetTblX_Uls_u3p13[1]	3628
t_CommOffsetTblY_Cnt_u16[0]	297
t_CommOffsetTblY_Cnt_u16[1]	1110
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2994		
target_MtrCntnrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntnrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntnrl_Write_CommOffset_Cnt_u16(val)	2994	2994	✓
MtrCntnrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntnrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntnrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.29543591	-4.2954402 ± 4.88E-04	✓
MtrCntnrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.24126196	-2.24126005 ± 4.88E-04	✓
MtrCntnrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27802	27802 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.126 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.90799999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.08500004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-899.770996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	697.859009
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.48399997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.526000023
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-99.262001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	138.542007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	28.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	30.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrDaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxCog_Amp_M_f32	5.72399998		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxPrevIntg_Volt_M_f32	7.55770016		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016		
MtrPosComputationDelay_Rad_M_f32[0]	1.403		
MtrPosComputationDelay_Rad_M_f32[1]	-4.98400021		
MtrVoltDax_Volt_M_f32[0]	-28.2420006		
MtrVoltDax_Volt_M_f32[1]	12.6160002		
MtrVoltQax_Volt_M_f32[0]	-28.2420006		
MtrVoltQax_Volt_M_f32[1]	12.6160002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.848999977		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.816999972		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.848399997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	52.3392982		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404900014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	52.3392982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.404900014		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	208.033005		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5517.5		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0909999982		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0199999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	9.36629963		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	10.1091003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	2.421		
k_VoltSatQaxPolyCoeff_Uls_f32	-11.9060001		
k_deadtimeVScale_Uls_f32	0.977999985		
t_CommOffsetTblX_Uls_u3p13[0]	1212		
t_CommOffsetTblX_Uls_u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_IvrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3747		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3747	3747	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	30.219759	30.2198009 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.43871307	2.43870997 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	30178	30178 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.127 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.98000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.097
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-313.263
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	882.630981
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.958000004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.437000006
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	89.8040009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	673.749023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	-22.7238007
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-2.704
MtrPosComputationDelay_Rad_M_f32[1]	-2.66799998
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.231999993		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.657599986		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	84.4263992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404500008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	84.4263992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.404500008		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6145.56982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.050999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.0270000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.1113005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.5658998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6089993		
k_VoltSatQaxPolyCoeff_Uls_f32	1.55499995		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4791		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	65339	65339 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	83.1210022	83.1210022 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.0475826	-13.0475998 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-15.1589346	-15.1589003 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12352	12352 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-8.68999958	-8.68999958 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.1210022	83.1210022 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.128 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.24300003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98800004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-222.714996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	849.533997
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.584999979
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.638000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-338.394012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	815.107971
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	27.7875996
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	4.24399996
MtrPosComputationDelay_Rad_M_f32[1]	1.44299996
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.777999997
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.499000013
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.389899999
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	76.5533981
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0997999981
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	76.5533981
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0997999981
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6667.54004
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7823.27002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.194999993
k_MtrCtrlVirtualResQax_Ohm_f32	0.142000005
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.06680012		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	24.2038002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-23.5930004		
k_VoltSatQaxPolyCoeff_Uls_f32	22.8640003		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	190		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	190	190	✓
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-4.29100323	-4.29099989 ± 4.88E-04	✓
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-2.2389493	-2.23895001 ± 4.88E-04	✓
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22866	22866 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.129 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.76400006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.99300003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	176.421997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-310.208008
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.71200001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	392.343994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	136.852005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	4.63049984
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	5.38999987
MtrPosComputationDelay_Rad_M_f32[1]	2.17600012
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.851999998
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.757000029
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.754400015
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50.1534996
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.319000006
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50.1534996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.319000006
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7980.1499
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	6489.7002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.00400000019
k_MtrCtrlVirtualResQax_Ohm_f32	0.108999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	7.02110004
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	18.0783997
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	21.5779991
k_VoltSatQaxPolyCoeff_Uls_f32	19.1660004
k_deadtimeVScale_Uls_f32	0.97299999
t_CommOffsetTblX_Uls_u3p13[0]	5022
t_CommOffsetTblX_Uls_u3p13[1]	7003
t_CommOffsetTblY_Cnt_u16[0]	63
t_CommOffsetTblY_Cnt_u16[1]	327
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3399

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntnrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntnrl_Write_CommOffset_Cnt_u16(val)	240	240	✓
MtrCntnrl_Write_Modldx_Uls_u16p16(val)	50820	50820 ± 1	✓
MtrCntnrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-26.7350006	-26.7350006 ± 7.81E-03	✓
MtrCntnrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	✓
MtrCntnrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	✓
MtrCntnrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	34820	34820 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-26.7350006	-26.7350006 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.130 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.28900003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.70599997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	383.354004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-411.454987
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.432999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	39.0110016
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-717.330017
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	7.36499977
MtrCurrDaxRef_Amp_M_f32[0]	-100.282997
MtrCurrDaxRef_Amp_M_f32[1]	-120.248001
MtrCurrQaxCog_Amp_M_f32	-41.5750008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-76.8769989		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-153.238998		
MtrCurrQaxPrevIntg_Volt_M_f32	9.1906004		
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpl_Amp_M_f32[0]	-76.8769989		
MtrCurrQaxRpl_Amp_M_f32[1]	-153.238998		
MtrPosComputationDelay_Rad_M_f32[0]	-0.195999995		
MtrPosComputationDelay_Rad_M_f32[1]	-0.303000003		
MtrVoltDax_Volt_M_f32[0]	-29.3959999		
MtrVoltDax_Volt_M_f32[1]	-1.94000006		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.94000006		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.349000007		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.150000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.203999996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.521000028		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	51.3003006		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.881699979		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	51.3003006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.881699979		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	1293.53003		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7361.14014		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.101000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.107000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.2988997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.7958002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	6.31400013		
k_VoltSatQaxPolyCoeff_Uls_f32	16.4209995		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4458		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4458	4458	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	129.623993	129.623993 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.25051808	-1.25051999 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.7946043	-4.79460001 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33385	33385 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-76.8769989	-76.8769989 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	129.623993	129.623993 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.131 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.07999999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.09899999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.111000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	757.447021
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-505.596985
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.07999999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.09399999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.08799999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.73800004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.727
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	841.114014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	815.677979
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	0.851999998
MtrCurrDaxRef_Amp_M_f32[0]	-68.6760025
MtrCurrDaxRef_Amp_M_f32[1]	-96.776001
MtrCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001
MtrCurrQaxPrevIntg_Volt_M_f32	17.5212994
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	191.369003
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001
MtrPosComputationDelay_Rad_M_f32[0]	-4.36899996
MtrPosComputationDelay_Rad_M_f32[1]	-4.83900023
MtrVoltDax_Volt_M_f32[0]	-17.1070004
MtrVoltDax_Volt_M_f32[1]	15.9390001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-17.1070004		
MtrVoltQax_Volt_M_f32[1]	15.9390001		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.911000013		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.68629998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	32.4859009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.175400004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	32.4859009		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.175400004		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6616.02002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.00200000009		
k_MtrCtrlVirualResQax_Ohm_f32	0.0410000011		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	28.8642998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.4000001		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	6.41629982		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	16.5690002		
k_VoltSatQaxPolyCoeff_Uls_f32	18.1630001		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1441		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63635	63635 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-18.0484562	-18.0485001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	10.1218939	10.1218996 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	4010	4010 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-2.4000001	-2.4000001 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.132 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0299999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.186000004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	161.147995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	308.806
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.43900001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.87399995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	259.894012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	103.217003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	1.579
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
MtrCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972
MtrCurrQaxPrevIntg_Volt_M_f32	13.7006998
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32[0]	-147.343002
MtrCurrQaxRpl_Amp_M_f32[1]	127.972
MtrPosComputationDelay_Rad_M_f32[0]	-5.46899986
MtrPosComputationDelay_Rad_M_f32[1]	-3.09299994
MtrVoltDax_Volt_M_f32[0]	0.908999979
MtrVoltDax_Volt_M_f32[1]	21.0249996
MtrVoltQax_Volt_M_f32[0]	0.908999979
MtrVoltQax_Volt_M_f32[1]	21.0249996
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.702000022
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00400000019
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.35589999
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.143000007
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.1921005
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.763000011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.1921005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.763000011
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5911.31982
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	2332.93994
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.169
k_MtrCtrlVirtualResQax_Ohm_f32	0.194000006
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.7931995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.1371994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	4.79199982		
k_VoltSatQaxPolyCoeff_Uls_f32	19.2709999		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	2638		
t_CommOffsetTblX_Uls_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3172		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	3172	3172	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-168.647003	-168.647003 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	18.6116123	18.6116009 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	23.341156	23.3411999 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	15513	15513 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-168.647003	-168.647003 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972	127.972 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.133 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.94200003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.00600004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-80.9769974
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-53.5390015
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.352999985
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.35800004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-261.626007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-882.955017
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-8.56599998
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxPrevIntg_Volt_M_f32	26.4118996
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	6.18900013
MtrCurrQaxRpl_Amp_M_f32[1]	83.0540009
MtrPosComputationDelay_Rad_M_f32[0]	2.9749999
MtrPosComputationDelay_Rad_M_f32[1]	0.486999989
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.116499998
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0989999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	3.26889992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.282200009
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	3.26889992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.282200009
SlowDataAccessBufIndex_Cnt_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7083.27002
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	947.890015
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.133000001
k_MtrCtrlVirtualResQax_Ohm_f32	0.131999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	12.9096003
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	29.3528004
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-6.11899996
k_VoltSatQaxPolyCoeff_Uls_f32	19.4669991
k_deadtimeVScale_Uls_f32	0.987999976
t_CommOffsetTblX_Uls_u3p13[0]	1212
t_CommOffsetTblX_Uls_u3p13[1]	1704
t_CommOffsetTblY_Cnt_u16[0]	23
t_CommOffsetTblY_Cnt_u16[1]	212
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	479		
target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	479	479	✓
MtrCntl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	19.6739998	19.6739998 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-2.46959901	-2.46959996 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-4.27839613	-4.27839994 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3723	3723 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	19.6739998	19.6739998 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	83.0540009	83.0540009 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.134 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20099998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.342000008
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	808.778992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-903.747009
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.101999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.838
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	827.307007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	398.522003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[0]	160.044006		
MtrCurrDaxRef_Amp_M_f32[1]	165.242004		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002		
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxPrevIntg_Volt_M_f32	8.14280033		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016		
MtrPosComputationDelay_Rad_M_f32[0]	4.70800018		
MtrPosComputationDelay_Rad_M_f32[1]	-2.68499994		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.1870003		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.851000011		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.465999991		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.591700017		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12.2650003		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.361299992		
PICurrCntrl_MtrVoltQaxFFfilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFfilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFfilt_M_str.TermN_Uls_f32	12.2650003		
PICurrCntrl_MtrVoltQaxFFfilt_M_str.TermD_Uls_f32	0.361299992		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2409.94995		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4854.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0390000008		
k_MtrCtrlVirtualResQax_Ohm_f32	0.158000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.4475994		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	24.7383003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	20.7450008		
k_VoltSatQaxPolyCoeff_Uls_f32	23.7810001		
k_deadtimeVScale_Uls_f32	0.954999983		
t_CommOffsetTblX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2036		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	62586	62586 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-25.2309952	-25.2310009 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	17.3926353	17.3925991 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-10.5688381	-10.5688 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	59610	59610 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-8.02652168	-8.02651978 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-25.2309952	-25.2310009 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.135 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.296
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.25800002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-261.467987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-308.463013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.09599996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.614000022
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-333.243011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	115.543999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.55999994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.8330002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.61900043
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.1560001
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	14.2440004
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	20.6149998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	9.33899975
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	0.157000005
MtrPosComputationDelay_Rad_M_f32[1]	-5.01100016
MtrVoltDax_Volt_M_f32[0]	-8.61900043
MtrVoltDax_Volt_M_f32[1]	-13.1560001
MtrVoltQax_Volt_M_f32[0]	-8.61900043
MtrVoltQax_Volt_M_f32[1]	-13.1560001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.237000003		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.00650000013		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	20.7000008		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	66.3365021		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.644699991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	20.7000008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	66.3365021		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.644699991		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6110.83008		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	7208.8501		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.187000006		
k_MtrCtrlVirtualResQax_Ohm_f32	0.112999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.2907009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.4712		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	19.9039993		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9039993		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1357		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1357	1357	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	87.1659851	87.1660004 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.55999994	-4.55999994 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-8.61900043	-8.61900043 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	39481	39481 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	87.1659851	87.1660004 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
InplVarXY_u16_u16Xu16Y_Cnt	1	InplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.136 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.51800001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.48300004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-99.1699982
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-632.38501
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.074000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.1669999996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1003.84003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	15.3310003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	6.73180008
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-2.15700006
MtrPosComputationDelay_Rad_M_f32[1]	4.67700005
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.944000006
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.294999987
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.167500004
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	71.040802
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.209299996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	71.040802
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.209299996
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5388.75
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3076.13989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.145999998
k_MtrCtrlVirtualResQax_Ohm_f32	0.0710000023
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.0139999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.2535		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-4.86499977		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.41699982		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	826		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	676	676	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	63569	63569 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-19.3592358	-19.3591995 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	1.96960425	1.96959996 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33456	33456 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	20.0139999	20.0139999 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.137 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.675999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.14600003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	63.882

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-0.995000005
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.638999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.104000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-406.304993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-469.421997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	14.2131004
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	2.36999989
MtrPosComputationDelay_Rad_M_f32[1]	2.67000008
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.25999999
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.222000003
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.763899982
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	44.2909012
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.210999995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	44.2909012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.210999995
SlowDataAccessBufIndex_Cnt_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7174.7002
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	1940.90002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.159999996
k_MtrCtrlVirualResQax_Ohm_f32	0.0109999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	14.0096998
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	19.7115993
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	14.1599998
k_VoltSatQaxPolyCoeff_Uls_f32	-17.3999996
k_deadtimeVScale_Uls_f32	0.967999995
t_CommOffsetTblX_Uls_u3p13[0]	1532
t_CommOffsetTblX_Uls_u3p13[1]	2851
t_CommOffsetTblY_Cnt_u16[0]	912
t_CommOffsetTblY_Cnt_u16[1]	1211
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	827
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	827	827	✓
MtrCntl_Write_ModlDx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-15.7660007	-15.7659998 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	-2.4196074	-2.41961002 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-4.19178963	-4.1917901 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	62948	62948 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002	-105.246002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-15.7660007	-15.7659998 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlDx_Uls_u16p16	1	MtrCntl_Write_ModlDx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.138 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.33800006
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.481999993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	406.850006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	57.3230019
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.810000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.185000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	222.291
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1002.88
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxPrevIntg_Volt_M_f32	17.0116005		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	2.68400002		
MtrPosComputationDelay_Rad_M_f32[1]	5.81400013		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.1870003		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.185000002		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.737999976		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.89200002		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.9187012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.882499993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.9187012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.882499993		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3789.18994		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3865.98999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.159999996		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0689999983		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.20769978		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.04409981		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	7.02699995		
k_VoltSatQaxPolyCoeff_Uls_f32	-15.4709997		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTblX_Uls_u3p13[0]	0		
t_CommOffsetTblX_Uls_u3p13[1]	0		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4450		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	57194	57194 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	19.7759857	19.7759991 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.736000001	-0.736000001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	18.6380005	18.6380005 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27583	27583 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	19.7759857	19.7759991 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_Modldx_Uls_u16p16	1	MtrCtrl_Write_Modldx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.139 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10300004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.991999984
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	841.302979
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-775.062012
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.25199997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.245
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	582.953003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	578.156982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-7.71299982
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	15.2893
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	2.86899996
MtrPosComputationDelay_Rad_M_f32[1]	0.839999974
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.723999977		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.800999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.752200007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13.1514997		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.821500003		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	13.1514997		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.821500003		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3827.27002		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0219999999		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0960000008		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.5093002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.39999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.0611992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	20.5559998		
k_VoltSatQaxPolyCoeff_Uls_f32	-5.53700018		
k_deadtimeVScale_Uls_f32	0.957000017		
t_CommOffsetTblX_Uls_u3p13[0]	8192		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1730		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1730	1730	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.7741425	-1.77414 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.44394493	4.44394016 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	25963	25963 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.140 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.62699997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.414000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-64.4329987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	243.455002
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.94700003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.409000009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	648.445007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-828.104004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	14.2027998
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	5.21500015
MtrPosComputationDelay_Rad_M_f32[1]	0.550000012
MtrVoltDax_Volt_M_f32[0]	-28.2420006
MtrVoltDax_Volt_M_f32[1]	12.6160002
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.848999977
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.296000004
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.194800004
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	60.8638
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.472600013
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	60.8638
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.472600013
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	208.033005
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5517.5
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.180000007
k_MtrCtrlVirtualResQax_Ohm_f32	0.0839999989
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.5352001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	5.54530001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	24.8269997		
k_VoltSatQaxPolyCoeff_Uls_f32	-17.3369999		
k_deadtimeVScale_Uls_f32	0.958000004		
t_CommOffsetTblX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3235		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1273	1273	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	46868	46868 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-86.8739929	-86.8740005 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	11950	11950 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-30.2000008	-30.2000008 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-86.8739929	-86.8740005 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.141 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.028
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.828000009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	670.815002

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-276.028992
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.136000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.861999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	360.989014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	26.3950005
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	-20.8540993
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-1.46899998
MtrPosComputationDelay_Rad_M_f32[1]	-5.42999983
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.448000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0320999995
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	9.46790028
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.962000012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	9.46790028
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.962000012
SlowDataAccessBufIndex_Cnt_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6145.56982
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.131999999
k_MtrCtrlVirualResQax_Ohm_f32	0.112999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	25.1942005
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	29.8994007
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	6.06099987
k_VoltSatQaxPolyCoeff_Uls_f32	-12.2449999
k_deadtimeVScale_Uls_f32	0.963999987
t_CommOffsetTblX_Uls_u3p13[0]	5022
t_CommOffsetTblX_Uls_u3p13[1]	7003
t_CommOffsetTblY_Cnt_u16[0]	0
t_CommOffsetTblY_Cnt_u16[1]	0
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4296
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	4296	4296	✓
MtrCntl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-126.528008	-126.528 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	4.47866297	4.47866011 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	-1.78156698	-1.78156996 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	5011	5011 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-126.528008	-126.528 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.142 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.010999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.45799994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.934021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.754000008
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.338013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg_Volt_M_f32	29.6310005		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003		
MtrPosComputationDelay_Rad_M_f32[0]	-0.344999999		
MtrPosComputationDelay_Rad_M_f32[1]	0.467999995		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.777999997		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0450000018		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0951000005		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	20.7000008		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	10.3985996		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.630500019		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	20.7000008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	10.3985996		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.630500019		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6667.54004		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7823.27002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0820000023		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0120000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.4818001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.8833008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-7.19700003		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9090004		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	2000		
t_CommOffsetTblY_Cnt_u16[1]	2000		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	674		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	674	674	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40537	40537 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.143 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.949
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.935974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.30399999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.403015
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	0.371499985
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-2.48799992
MtrPosComputationDelay_Rad_M_f32[1]	0.00100000005
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.851999998		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.398999989		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.208499998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	865.320007		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	69.4054031		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0452999994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	69.4054031		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0452999994		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	7980.1499		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.00999999978		
k_MtrCtrlVirualResQax_Ohm_f32	0.163000003		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	16.9069004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.2285004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-18.3610001		
k_VoltSatQaxPolyCoeff_Uls_f32	22.7819996		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63176	63176 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	182.914001	182.914001 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-20.4382992	-20.4382992 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.0793879	2.07939005 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50220	50220 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	14.9324188	14.9323997 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	182.914001	182.914001 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.144 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.349999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.873001
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.5
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.738007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.5599998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	14.3669996
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-5.50199986
MtrPosComputationDelay_Rad_M_f32[1]	4.21099997
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.448000014
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.254799992
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	22.2399998
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.5784988
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.966000021
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.5784988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.966000021
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0
k_MtrCtrlVirtualResQax_Ohm_f32	0.0949999988
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.8994007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.4134998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	2.44199991		
k_VoltSatQaxPolyCoeff_Uls_f32	19.2689991		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	671		
t_CommOffsetTblY_Cnt_u16[1]	876		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	2125	2125	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-126.528008	-126.528 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-17.5821819	-17.5821991 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	24.1644421	24.1644001 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	1587	1587 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-126.528008	-126.528 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.145 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16600001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.221985
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.232999995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.27301
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	1.06570005
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-3.16700006
MtrPosComputationDelay_Rad_M_f32[1]	3.09599996
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.777999997
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.837800026
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	75.4597015
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.830900013
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	75.4597015
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.830900013
SlowDataAccessBufIndex_Cnt_u16	0
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6667.54004
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7823.27002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.200000003
k_MtrCtrlVirtualResQax_Ohm_f32	0.0109999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	26.6909008
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	20.4568005
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	20.2509995
k_VoltSatQaxPolyCoeff_Uls_f32	18.1280003
k_deadtimeVScale_Uls_f32	0.963999987
t_CommOffsetTblX_Uls_u3p13[0]	4611
t_CommOffsetTblX_Uls_u3p13[1]	5579
t_CommOffsetTblY_Cnt_u16[0]	912
t_CommOffsetTblY_Cnt_u16[1]	1211
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1468		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1468	1468	✓
MtrCntri_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	✓
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	✓
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	11103	11103 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.146 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.93900001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.36699998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	826.950989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-163.621994
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.26100004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60300004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-401.145996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-278.5
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	-144.667007		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg_Volt_M_f32	13.5303001		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003		
MtrPosComputationDelay_Rad_M_f32[0]	3.42499995		
MtrPosComputationDelay_Rad_M_f32[1]	-0.836000025		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851999998		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.398999989		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.633599997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17.0797005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.727199972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17.0797005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.727199972		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	7980.1499		
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.182999998		
k_MtrCtrlVirtualResQax_Ohm_f32	0.169		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	16.2366009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	22.8831997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	3.96000004		
k_VoltSatQaxPolyCoeff_Uls_f32	7.87699986		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_IvrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2363		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63176	63176 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	182.914001	182.914001 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-20.4382992	-20.4382992 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.0793879	2.07939005 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	41490	41490 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-10.5	-10.5 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	182.914001	182.914001 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.147 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.527999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.268999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-311.075012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-305.570007
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.12899995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.556999981
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	354.154999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	556.525024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-7.71299982
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	30.4999008
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	5.14300013
MtrPosComputationDelay_Rad_M_f32[1]	0.453999996
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.723999977		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.111000001		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.233400002		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	52.7086983		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.584299982		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	52.7086983		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.584299982		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	3827.27002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.131999999		
k_MtrCtrlVirtualResQax_Ohm_f32	0		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.3010998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.0423002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	7.04799986		
k_VoltSatQaxPolyCoeff_Uls_f32	6.82399988		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	672		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	672	672	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.05482388	-2.05482006 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.38767862	4.38768005 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49075	49075 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	220	220 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.148 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.75999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	979.52301
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	987.510986
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10699999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.653999984
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-420.446991
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	525.913025
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	29.0240002
MtrCtrl_Vecu_Volt_M_f32[1]	30.3600006
MtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	-16.7549
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	-2.02900004
MtrPosComputationDelay_Rad_M_f32[1]	-1.17700005
MtrVoltDax_Volt_M_f32[0]	-28.2420006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltDax_Volt_M_f32[1]	12.6160002		
MtrVoltQax_Volt_M_f32[0]	-28.2420006		
MtrVoltQax_Volt_M_f32[1]	12.6160002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.848999977		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.816999972		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.9375		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.7649002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.605599999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.7649002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.605599999		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	208.033005		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5517.5		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0179999992		
k_MtrCtrlVirtualResQax_Ohm_f32	0.200000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.5195007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	4.98169994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.4769993		
k_VoltSatQaxPolyCoeff_Uls_f32	4.35599995		
k_deadtimeVScale_Uls_f32	0.977999985		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1798		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1798	1798	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.109009	131.108994 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-28.4440193	-28.4440002 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-10.4937534	-10.4938002 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24302	24302 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.109009	131.108994 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.149 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.99100006
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.046
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-57.7280006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	352.100006
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.634000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.12699997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-489.911011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1007.60999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	30.4363995
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-5.35500002
MtrPosComputationDelay_Rad_M_f32[1]	1.125
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.231999993		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.83099997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	58.6543999		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.82130003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	58.6543999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.82130003		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6145.56982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0329999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.0590000004		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	27.2359009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	27.6382999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	2.04900002		
k_VoltSatQaxPolyCoeff_Uls_f32	-6.98999977		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2210		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	65339	65339 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.1278772	-10.1279001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.246994	-17.2469997 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50040	50040 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-9.64999962	-9.64999962 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.150 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.79400003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.268000007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1.13
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	893.153992
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.472000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.03400004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-816.005981
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-233.619995
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxCog_Amp_M_f32	83.9489975
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	20.3910999
MtrCurrQaxRef_Amp_M_f32[0]	106.072998
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-6.19099998
MtrPosComputationDelay_Rad_M_f32[1]	0.512000024
MtrVoltDax_Volt_M_f32[0]	-3.59500003
MtrVoltDax_Volt_M_f32[1]	-28.4209995
MtrVoltQax_Volt_M_f32[0]	-3.59500003
MtrVoltQax_Volt_M_f32[1]	-28.4209995
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.911000013
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.513000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0749000013
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.565999985
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1118
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40.2612
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.925599992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	40.2612
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.925599992
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6616.02002
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5777.70996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.140000001
k_MtrCtrlVirtualResQax_Ohm_f32	0.0560000017
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.4277		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	8.11270046		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-25		
k_VoltSatQaxPolyCoeff_Uls_f32	0.398000002		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1099		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1099	1099	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-97.5970001	-97.5970001 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.00629377319	-0.00629376993 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.8549962	4.85500002 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	948	948 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979	67.4899979 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-97.5970001	-97.5970001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.151 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.684000015

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.978999972
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-984.268005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	119.455002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.566999972
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83399999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	762.239014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-528.901978
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-27.3339996
MtrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	2.85220003
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	3.82500005
MtrPosComputationDelay_Rad_M_f32[1]	2.33800006
MtrVoltDax_Volt_M_f32[0]	-25.3770008
MtrVoltDax_Volt_M_f32[1]	21.3880005
MtrVoltQax_Volt_M_f32[0]	-25.3770008
MtrVoltQax_Volt_M_f32[1]	21.3880005
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.702000022
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00400000019
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.716700017
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	60.2319984
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.522199988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	60.2319984
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.522199988
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5911.31982
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2332.93994
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.0209999997
k_MtrCtrlVirtualResQax_Ohm_f32	0.150999993
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	13.2849998
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	11.2693005
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	25
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995
k_deadtimeVScale_Uls_f32	0.963
t_CommOffsetTblX_Uls_u3p13[0]	4611
t_CommOffsetTblX_Uls_u3p13[1]	5579
t_CommOffsetTblY_Cnt_u16[0]	23
t_CommOffsetTblY_Cnt_u16[1]	212
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	665

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	665	665	✓
MtrCntri_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.825012	131.824997 ± 7.81E-03	✓
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-16.1349812	-16.1350002 ± 4.88E-04	✓
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-25.1170063	-25.1170006 ± 4.88E-04	✓
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	13084	13084 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.825012	131.824997 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.152 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.904999971
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.704999983
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-822.044006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-449.259003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.922999978
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.349999994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	161.807007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	765.385986
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	20.9669991
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	115.814003		
MtrCurrQaxCog_Amp_M_f32	-41.5750008		
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001		
MtrCurrQaxPrevIntg_Volt_M_f32	30.6926994		
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpl_Amp_M_f32[0]	191.369003		
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001		
MtrPosComputationDelay_Rad_M_f32[0]	4.6960001		
MtrPosComputationDelay_Rad_M_f32[1]	2.56500006		
MtrVoltDax_Volt_M_f32[0]	-29.3959999		
MtrVoltDax_Volt_M_f32[1]	-1.94000006		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.94000006		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.662		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.65079999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	76.1873016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0882999972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	76.1873016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0882999972		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	7083.27002		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	947.890015		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.126000002		
k_MtrCtrlVirtualResQax_Ohm_f32	0.115999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHILim_Volt_f32	5.48570013		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHILim_Volt_f32	13.9652004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	0		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1724		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	739	739	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	28516	28516 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.61400008	2.61400008 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.94000006	-1.94000006 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49797	49797 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	5.48570013	5.48570013 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220	-220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.153 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.273999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.469999999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-538.278992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-363.735992
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.36099994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0199999996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-870.234009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	991.184998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	10.2959995
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002
MtrCurrQaxFinalRef_Amp_M_f32[1]	127.972
MtrCurrQaxPrevIntg_Volt_M_f32	7.01200008
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32[0]	-147.343002
MtrCurrQaxRpl_Amp_M_f32[1]	127.972
MtrPosComputationDelay_Rad_M_f32[0]	4.78000021
MtrPosComputationDelay_Rad_M_f32[1]	-2.88599992
MtrVoltDax_Volt_M_f32[0]	-17.1070004
MtrVoltDax_Volt_M_f32[1]	15.9390001
MtrVoltQax_Volt_M_f32[0]	-17.1070004
MtrVoltQax_Volt_M_f32[1]	15.9390001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.851000011		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.465999991		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.371499985		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.143000007		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	99.3730011		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1426		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	99.3730011		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.1426		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2409.94995		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4854.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0939999968		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0769999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.2031002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	3.43330002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.0909996		
k_VoltSatQaxPolyCoeff_Uls_f32	20.9540005		
k_deadtimeVScale_Uls_f32	0.954999983		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	568		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	568	568	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.55785704	4.5578599 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.42357612	1.42358005 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	63084	63084 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-147.343002	-147.343002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.154 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.648
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.782000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-530.372009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-420.145996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.47300005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.944000006
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	736.344971
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	379.115997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	7.36499977
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	-41.5750008
MtrCurrQaxFinalRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxPrevIntg_Volt_M_f32	11.7653999
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32[0]	-76.8769989
MtrCurrQaxRpl_Amp_M_f32[1]	-153.238998
MtrPosComputationDelay_Rad_M_f32[0]	-0.195999995
MtrPosComputationDelay_Rad_M_f32[1]	-0.303000003
MtrVoltDax_Volt_M_f32[0]	-28.2420006

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltDax_Volt_M_f32[1]	12.6160002		
MtrVoltQax_Volt_M_f32[0]	-28.2420006		
MtrVoltQax_Volt_M_f32[1]	12.6160002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.237000003		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.982200027		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	64.5255966		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.514999986		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	64.5255966		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.514999986		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6110.83008		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7208.8501		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0909999982		
k_MtrCtrlVirualResQax_Ohm_f32	0.172999993		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	27.6355991		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.4790993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	8.55000019		
k_VoltSatQaxPolyCoeff_Uls_f32	-12.8280001		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2927		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2927	2927	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	150.039001	150.039001 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.24673235	-1.24672997 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.78008938	-4.78008986 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33385	33385 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	150.039001	150.039001 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	41.6290016	41.6290016 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.155 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.07999999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.09899999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.01700000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10399997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.256000012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	458.355011
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1015.39001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.07999999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.09399999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.08799999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.77700001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.27199996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	309.817993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-766.486023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	0.851999998
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	24.9650002
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	191.369003
MtrCurrQaxRpl_Amp_M_f32[1]	107.137001
MtrPosComputationDelay_Rad_M_f32[0]	-4.36899996
MtrPosComputationDelay_Rad_M_f32[1]	-4.83900023
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.231999993		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.287900001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	70.1921005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.3741		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	70.1921005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.3741		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6145.56982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.8564997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.1163998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	-25		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1789		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	65339	65339 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-164.506989	-164.507004 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.4447479	-17.4447002 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	9.78332424	9.78332043 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	4010	4010 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-5.81056309	-5.81056023 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-164.506989	-164.507004 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996	-66.7229996 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.156 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	79.6729965
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxPrevIntg_Volt_M_f32	-26.8785992
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32[0]	-212.632996
MtrCurrQaxRpl_Amp_M_f32[1]	-205.085007
MtrPosComputationDelay_Rad_M_f32[0]	1.52600002
MtrPosComputationDelay_Rad_M_f32[1]	-2.68400002
MtrVoltDax_Volt_M_f32[0]	-27.0669994
MtrVoltDax_Volt_M_f32[1]	28.1070004
MtrVoltQax_Volt_M_f32[0]	-27.0669994
MtrVoltQax_Volt_M_f32[1]	28.1070004
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.400000006
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0590000004
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.967899978
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5851002
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.5851002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.887899995
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	2663.65991
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	5194.8999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.112000003
k_MtrCtrlVirtualResQax_Ohm_f32	0.0219999999
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpiEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.98460007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5142002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	25		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	65	65	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.9229965	-87.9229965 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70514989 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95905995 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996	-212.632996 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-87.9229965	-87.9229965 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.157 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.397000015

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.980000019
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	161.654999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-253.688004
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.057
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.633000016
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-170.535004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-25.7549992
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-8.56599998
MtrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	12.8451004
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpl_Amp_M_f32[0]	6.18900013
MtrCurrQaxRpl_Amp_M_f32[1]	83.0540009
MtrPosComputationDelay_Rad_M_f32[0]	2.9749999
MtrPosComputationDelay_Rad_M_f32[1]	0.486999989
MtrVoltDax_Volt_M_f32[0]	-25.3770008
MtrVoltDax_Volt_M_f32[1]	21.3880005
MtrVoltQax_Volt_M_f32[0]	-25.3770008
MtrVoltQax_Volt_M_f32[1]	21.3880005
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.702000022
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00400000019
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.853799999
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.3075027
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.461600006
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.3075027
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.461600006
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5911.31982
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2332.93994
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	5.5145998
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	6.05779982
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003
k_VoltSatQaxPolyCoeff_Uls_f32	0
k_deadtimeVScale_Uls_f32	0.963
t_CommOffsetTbIX_Uls_u3p13[0]	4611
t_CommOffsetTbIX_Uls_u3p13[1]	5579
t_CommOffsetTbIY_Cnt_u16[0]	163
t_CommOffsetTbIY_Cnt_u16[1]	1236
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1357

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntnrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntnrl_Write_CommOffset_Cnt_u16(val)	1357	1357	✓
MtrCntnrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntnrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	12.7000008	12.6999998 ± 7.81E-03	✓
MtrCntnrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.4071095	-2.40710998 ± 4.88E-04	✓
MtrCntnrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.17013788	-4.17013979 ± 4.88E-04	✓
MtrCntnrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3723	3723 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	12.7000008	12.6999998 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.158 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.490000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.741999984
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-517.109009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-593.112976
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.15900004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60500002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	323.631989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-319.569
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[1]	115.814003		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003		
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001		
MtrCurrQaxPrevIntg_Volt_M_f32	20.2117004		
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016		
MtrPosComputationDelay_Rad_M_f32[0]	4.70800018		
MtrPosComputationDelay_Rad_M_f32[1]	-2.68499994		
MtrVoltDax_Volt_M_f32[0]	-29.3959999		
MtrVoltDax_Volt_M_f32[1]	-1.94000006		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.94000006		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.662		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.189799994		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	97.3968964		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.662699997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	97.3968964		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.662699997		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	7083.27002		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	947.890015		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.023		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0179999992		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.8029995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.0914993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.1960001		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.0979996		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	1081		
t_CommOffsetTblY_Cnt_u16[1]	1779		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	903		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1779	1779	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	64749	64749 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-171.029999	-171.029999 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.74271894	-3.74271899 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.37356234	-6.37355995 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	10301	10301 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-171.029999	-171.029999 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_Modldx_Uls_u16p16	1	MtrCtrl_Write_Modldx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.159 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	79.6729965
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxFinalRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxPrevIntg_Volt_M_f32	11.4363003
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32[0]	-212.632996
MtrCurrQaxRpl_Amp_M_f32[1]	-205.085007
MtrPosComputationDelay_Rad_M_f32[0]	1.52600002
MtrPosComputationDelay_Rad_M_f32[1]	-2.68400002
MtrVoltDax_Volt_M_f32[0]	-27.0669994
MtrVoltDax_Volt_M_f32[1]	28.1070004
MtrVoltQax_Volt_M_f32[0]	-27.0669994
MtrVoltQax_Volt_M_f32[1]	28.1070004

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.400000006		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0590000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.827000022		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5851002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	16.5851002		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.887899995		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2663.65991		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5194.8999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0219999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.4445		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	23.5652008		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	14.658		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.9229965	-87.9229965 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70514989 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95905995 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-212.632996	-212.632996 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-87.9229965	-87.9229965 ± 7.81E-03	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
InplVarXY_u16_u16Xu16Y_Cnt	1	InplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.160 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.142000005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.272000015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	243.257004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-253.089996
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.713999987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.870999992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	49.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-366.458008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001
MtrCurrQaxPrevIntg_Volt_M_f32	30.3402004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-2.15700006
MtrPosComputationDelay_Rad_M_f32[1]	4.67700005
MtrVoltDax_Volt_M_f32[0]	-3.59500003
MtrVoltDax_Volt_M_f32[1]	-28.4209995
MtrVoltQax_Volt_M_f32[0]	-3.59500003
MtrVoltQax_Volt_M_f32[1]	-28.4209995
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.499500006
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	78.8641968
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.388500005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	78.8641968
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.388500005
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	6145.56982
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.193000004
k_MtrCtrlVirtualResQax_Ohm_f32	0.114
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.67009997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.1749001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1636	1636	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	65339	65339 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	182.914001	182.914001 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-19.8980999	-19.8980999 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	2.02442813	2.02443004 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33456	33456 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-4.57000017	-4.57000017 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	182.914001	182.914001 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	119.721001	119.721001 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.161 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.51999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.203999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-569.184021

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-867
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.852999985
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	442.492004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-332.345001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	26.4762993
MtrCurrQaxRef_Amp_M_f32[0]	106.072998
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxRpl_Amp_M_f32[0]	-105.246002
MtrCurrQaxRpl_Amp_M_f32[1]	41.6290016
MtrPosComputationDelay_Rad_M_f32[0]	2.36999989
MtrPosComputationDelay_Rad_M_f32[1]	2.67000008
MtrVoltDax_Volt_M_f32[0]	-25.3770008
MtrVoltDax_Volt_M_f32[1]	21.3880005
MtrVoltQax_Volt_M_f32[0]	-25.3770008
MtrVoltQax_Volt_M_f32[1]	21.3880005
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.911000013
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.513000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.1329
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.565999985
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	43.3250008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.744499981
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	43.3250008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.744499981
SlowDataAccessBufIndex_Cnt_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6616.02002
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	5777.70996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.191
k_MtrCtrlVirualResQax_Ohm_f32	0.0529999994
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	21.1630993
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	26.3924999
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003
k_deadtimeVScale_Uls_f32	0.971000016
t_CommOffsetTblX_Uls_u3p13[0]	5022
t_CommOffsetTblX_Uls_u3p13[1]	7003
t_CommOffsetTblY_Cnt_u16[0]	365
t_CommOffsetTblY_Cnt_u16[1]	1530
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	5000
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCntl_Write_CommOffset_Cnt_u16(val)	5000	5000	✓
MtrCntl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	58.7199974	58.7200012 ± 7.81E-03	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32(val)	22.5505848	22.5506001 ± 4.88E-04	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32(val)	19.9384365	19.9384003 ± 4.88E-04	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33552	33552 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	58.7199974	58.7200012 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.162 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.74399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.96099997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	756.674988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	76.4720001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.591000021
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.175999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	730.219971
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1004.94
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxFinalRef_Amp_M_f32[0]	191.369003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001		
MtrCurrQaxPrevIntg_Volt_M_f32	17.6383991		
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009		
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996		
MtrPosComputationDelay_Rad_M_f32[0]	2.68400002		
MtrPosComputationDelay_Rad_M_f32[1]	5.81400013		
MtrVoltDax_Volt_M_f32[0]	-29.3959999		
MtrVoltDax_Volt_M_f32[1]	-1.94000006		
MtrVoltQax_Volt_M_f32[0]	-29.3959999		
MtrVoltQax_Volt_M_f32[1]	-1.94000006		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.702000022		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00400000019		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.756799996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	44.6861992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.246199995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	44.6861992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.246199995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5911.31982		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2332.93994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVrualResDax_Ohm_f32	0.144999996		
k_MtrCtrlVrualResQax_Ohm_f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.42719996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	4.96659994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	20.9540005		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	841		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	841	841	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	178.335999	178.335999 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.189992487	-0.189991996 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.81125021	4.81125021 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27583	27583 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	178.335999	178.335999 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	107.137001	107.137001 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.163 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.45799994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.934021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.754000008
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.338013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-27.3339996
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	23.1366997
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84500003
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00600000005
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.702000022		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00400000019		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.155699998		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	29.8064003		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.197799996		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	29.8064003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.197799996		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5911.31982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	2332.93994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	27.6096992		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	20.0156002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	-12.2449999		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4296		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4296	4296	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.60241628	-2.60242009 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.05112982	-4.05112982 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	7965	7965 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.164 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.949
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.935974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.30399999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.403015
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-42.6814995
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	2.99090004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.331
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.911000013		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.195500001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	90.7209015		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.617500007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	90.7209015		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.617500007		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6616.02002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.191		
k_MtrCtrlVirualResQax_Ohm_f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.4471998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.4596996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9090004		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	674		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	674	674	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-210.986511	-210.985992 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.37544227	2.37543988 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.23418236	4.23417997 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	29688	29688 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-210.986511	-210.985992 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.165 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.873001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.5
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.738007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	25.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	27.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-37.3235016
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7229996
MtrCurrQaxPrevIntg_Volt_M_f32	2.34949994
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005
MtrPosComputationDelay_Rad_M_f32[1]	3.19000006
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1870003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.911000013		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.211199999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	6.4671998		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0860000029		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.4671998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0860000029		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6616.02002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.191		
k_MtrCtrlVirualResQax_Ohm_f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.2147999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.0927		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	22.7819996		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4608	4608	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-216.344513	-216.343994 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	6.19107008	6.19107008 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	23.8335838	23.8335991 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	62638	62638 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-216.344513	-216.343994 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.166 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16600001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.221985
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.232999995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.27301
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-31.9654999
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	0.29429999
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7229996
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00600000005
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.1822		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	57.8652992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.414700001		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	57.8652992		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.414700001		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	6145.56982		
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.6170006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5634995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	19.2689991		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	65339	65339 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.52427006	-2.52427006 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.29863739	-4.29863977 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38369	38369 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.167 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.722
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0399999991
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0179999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.45899999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.934998
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.7549999995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.338989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55200005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3889999
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-0.216499999
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.397003
MtrCurrQaxCog_Amp_M_f32	-26.6075001
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	24.4969997
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787994
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84599996
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00700000022
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1870003
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.851999998
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.513000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.953199983
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.778999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	96.5500031
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	69.4054031
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.3134
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	76.1873016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.821500003
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6616.02002
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3865.98999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.140000001
k_MtrCtrlVirtualResQax_Ohm_f32	0.0769999996
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.3593998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.2693005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-31		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	-12.2449999		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	665		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	665	665	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	160.638489	160.639008 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	16.7303963	16.7304001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-25.987236	-25.9871998 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61579	61579 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.638489	160.639008 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.168 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.937012
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.085000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.085000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.305000007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.403992
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	5.26809978
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-21.2495003
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	15.5079002
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.331999987
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.0040000019
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.486799985
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.76819998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.5784988
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.889199972
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	99.3730011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.472600013
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	5911.31982
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2156.63989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0209999997
k_MtrCtrlVirtualResQax_Ohm_f32	0.172999993
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	0.465799987
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	13.9652004
k_MtrVoltQaxIntegLoLim_Volt_f32	0
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9090004
k_deadtimeVScale_Uls_f32	0.958999991
t_CommOffsetTblX_Uls_u3p13[0]	459
t_CommOffsetTblX_Uls_u3p13[1]	5775
t_CommOffsetTblY_Cnt_u16[0]	1237
t_CommOffsetTblY_Cnt_u16[1]	383
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1724

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntnl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntnl_Write_CommOffset_Cnt_u16(val)	383	383	✓
MtrCntnl_Write_Modldx_Uls_u16p16(val)	62849	62849 ± 1	✓
MtrCntnl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	29.4615059	29.4615002 ± 7.81E-03	✓
MtrCntnl_Write_MtrDaxVoltage_Volt_f32(val)	2.88322926	2.88322997 ± 4.88E-04	✓
MtrCntnl_Write_MtrQaxVoltage_Volt_f32(val)	-3.83131933	-3.83132005 ± 4.88E-04	✓
MtrCntnl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22576	22576 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-4.57000017	-4.57000017 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	29.4615059	29.4615002 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.169 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(ptr)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.723
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0189999994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46000004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.935974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.755999982
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.339996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55300045
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3899994
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	21.7219009
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.397995

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxCog_Amp_M_f32	-5.17549992		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg_Volt_M_f32	7.74660015		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.789001		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84699988		
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988		
MtrPosComputationDelay_Rad_M_f32[1]	0.00800000038		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	-7.66699982		
MtrVoltQax_Volt_M_f32[1]	2.61400008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.723999977		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.807699978		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.735800028		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	52.7086983		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.75029999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.5851002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0452999994		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6110.83008		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7823.27002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0909999982		
k_MtrCtrlVirtualResQax_Ohm_f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	11.4057999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.1163998		
k_MtrVoltQaxIntegLoLim_Volt_f32	11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	18.1280003		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1789		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62849	62849 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	131.509491	131.509995 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.6636548	2.66365004 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	6.66147232	6.66146994 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	4051	4051 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-11.6000004	-11.6000004 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	131.509491	131.509995 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.170 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95099998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.937988
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.305999994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.405029
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	27.2064991
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	0.182500005
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	14.6610003
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.333000004
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.848999977		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.398999989		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.203600004		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.7649002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.640799999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.3075027		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.966000021		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6145.56982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirtualResQax_Ohm_f32	0.0179999992		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.3612995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5142002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-14.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	7.87699986		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-44.2014999	-44.2014999 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	18.5843525	18.5844002 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-24.6954308	-24.6954002 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22566	22566 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-44.2014999	-44.2014999 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
InplVarXY_u16_u16Xu16Y_Cnt	1	InplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.171 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.875
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.70000005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.73999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	5.54050016
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7249985
MtrCurrQaxPrevIntg_Volt_M_f32	12.7981997
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005
MtrPosComputationDelay_Rad_M_f32[1]	3.21000004
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1889992
MtrVoltQax_Volt_M_f32[0]	-7.66699982
MtrVoltQax_Volt_M_f32[1]	2.61400008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.111000001
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.398900002
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.71420002
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	58.6543999
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.052099999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	97.3968964
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.830900013
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	2663.65991
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.112000003
k_MtrCtrlVirtualResQax_Ohm_f32	0.0219999999
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	22.8146992		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	0		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	6.82399988		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1357		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1357	1357	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-206.977509	-206.977005 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.8397541	-17.8397999 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.52724576	-2.52725005 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42135	42135 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-206.977509	-206.977005 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7249985	-66.7249985 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.172 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.087006
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16799998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.223999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.127000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0960000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.234999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.274994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.618
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1889992
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.397995
MtrCurrQaxCog_Amp_M_f32	10.8985004
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988
MtrCurrQaxPrevIntg_Volt_M_f32	15.8292999
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.789001
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7249985
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00800000038
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61599994
MtrVoltQax_Volt_M_f32[0]	-28.2420006
MtrVoltQax_Volt_M_f32[1]	12.6160002
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.911000013
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.816999972
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.489399999
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.703400016
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40.2612
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.176699996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.5851002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.727199972
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5911.31982
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	7823.27002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996
k_MtrCtrlVirualResQax_Ohm_f32	0.114
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	28.3733006
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	31
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003
k_VoltSatQaxPolyCoeff_Uls_f32	4.35599995
k_deadtimeVScale_Uls_f32	0.963
t_CommOffsetTblX_Uls_u3p13[0]	459
t_CommOffsetTblX_Uls_u3p13[1]	5775
t_CommOffsetTblY_Cnt_u16[0]	1081
t_CommOffsetTblY_Cnt_u16[1]	1779
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	903
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1779	1779	✔
MtrCntri_Write_Modldx_Uls_u16p16(val)	63111	63111 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	1.44797277	1.44797003 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	4.59212399	4.59212017 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3269	3269 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988	-2.84699988 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.173 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.723999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0199999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46099997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.937012
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0309999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0309999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.757000029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.341003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.5539999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3910007
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.399002
MtrCurrQaxCog_Amp_M_f32	16.2565002

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003		
MtrCurrQaxPrevIntg_Volt_M_f32	15.6167002		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.789993		
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018		
MtrCurrQaxRpl_Amp_M_f32[1]	-2.84800005		
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988		
MtrPosComputationDelay_Rad_M_f32[1]	0.00899999961		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61400008		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.702000022		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.446700007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.692600012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	60.2319984		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.306199998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	78.8641968		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.584299982		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7083.27002		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.023		
k_MtrCtrlVirualResQax_Ohm_f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.67889977		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.5		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	-6.98999977		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	150.3815	150.382004 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.62001944	7.62001991 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	19.0553932	19.0554008 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	4062	4062 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	150.3815	150.382004 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.174 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.085000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.028000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95200002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.939026
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.085000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.085000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.307000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.406006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	21.6145
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxPrevIntg_Volt_M_f32	18.8419991
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.333999991
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61400008
MtrVoltQax_Volt_M_f32[0]	-3.59500003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[1]	-28.4209995		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.662		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.499000013		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.716799974		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.681800008		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	76.1873016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404900014		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	43.3250008		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.605599999		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	2663.65991		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.7716999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.1749001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	0.398000002		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63635	63635 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-13.4024963	-13.4025002 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.73418069	-3.73417997 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.10272765	-3.10273004 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38437	38437 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	21.7716999	21.7716999 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-13.4024963	-13.4025002 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84500003	-2.84500003 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.175 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10099995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.875999
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.79999995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.740997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	29.0240002
MtrCtrl_Vecu_Volt_M_f32[1]	30.3600006
MtrCurrDaxPrevIntg_Volt_M_f32	-27.3339996
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	26.9724998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7259979
MtrCurrQaxPrevIntg_Volt_M_f32	21.7777996
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005
MtrPosComputationDelay_Rad_M_f32[1]	3.22000003
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1900005
MtrVoltQax_Volt_M_f32[0]	-25.3770008
MtrVoltQax_Volt_M_f32[1]	21.3880005
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851000011
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.757000029
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.476399988
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.671000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	99.3730011
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404500008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	44.6861992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.82130003
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.193000004
k_MtrCtrlVirtualResQax_Ohm_f32	0.155000001
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.9853992		
k_MtrVoltDaxIntegLoLim_Volt_f32	-31		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.3924999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4000		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4000	4000	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.42178202	4.42178011 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	29.774456	29.7744999 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61525	61525 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.176 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.087997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0120000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.109999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16900003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.225006
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.128000006
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.236000001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.276001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6190004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1900005
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.399002
MtrCurrQaxCog_Amp_M_f32	32.3305016
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84800005
MtrCurrQaxPrevIntg_Volt_M_f32	2.29920006
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.789993
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7259979
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00899999961
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.6170001
MtrVoltQax_Volt_M_f32[0]	-29.3959999
MtrVoltQax_Volt_M_f32[1]	-1.94000006
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.237000003
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.150000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.844699979
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.6602
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	64.5255966
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0997999981
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	29.8064003
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.925599992
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	6616.02002
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.191
k_MtrCtrlVirtualResQax_Ohm_f32	0.0529999994
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	15.4042997
k_MtrVoltDaxIntegLoLim_Volt_f32	0
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	4.96659994
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003
k_deadtimeVScale_Uls_f32	0.996999979
t_CommOffsetTblX_Uls_u3p13[0]	4611
t_CommOffsetTblX_Uls_u3p13[1]	5579
t_CommOffsetTblY_Cnt_u16[0]	163
t_CommOffsetTblY_Cnt_u16[1]	1236
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	656

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntnrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntnrl_Write_CommOffset_Cnt_u16(val)	656	656	✓
MtrCntnrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntnrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntnrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.6963588	-0.696358979 ± 4.88E-04	✓
MtrCntnrl_Write_MtrQaxVoltage_Volt_f32(val)	17.6341515	17.6341991 ± 4.88E-04	✓
MtrCntnrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	34365	34365 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84800005	-2.84800005 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.177 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.102
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.876999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.89999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.742004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-0.216499999
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxCog_Amp_M_f32	48.4044991		
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7269974		
MtrCurrQaxPrevIntg_Volt_M_f32	14.2393999		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979		
MtrCurrQaxRpl_Amp_M_f32[1]	119.721001		
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005		
MtrPosComputationDelay_Rad_M_f32[1]	3.23000002		
MtrVoltDax_Volt_M_f32[0]	18.6380005		
MtrVoltDax_Volt_M_f32[1]	-23.191		
MtrVoltQax_Volt_M_f32[0]	18.6380005		
MtrVoltQax_Volt_M_f32[1]	-23.1870003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.749800026		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.627799988		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.3075027		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.175400004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	57.8652992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.1426		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	947.890015		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.191		
k_MtrCtrlVirtualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	27.0930996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.0927		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4608	4608	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.96718693	2.96719003 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	30.4839344	30.4839001 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	60999	60999 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993	-213.026993 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.178 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.089005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.1199999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.226013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.128999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.237000003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.277008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6199999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.191
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.399994
MtrCurrQaxCog_Amp_M_f32	53.7625008
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84899998
MtrCurrQaxPrevIntg_Volt_M_f32	11.8942003
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.791
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7269974
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00999999978
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61800003

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-27.0669994		
MtrVoltQax_Volt_M_f32[1]	28.1070004		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.662		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.465999991		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.498699993		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.616999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	97.3968964		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.763000011		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	44.6861992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.514999986		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	6616.02002		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	4854.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.7719002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5634995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_Igc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.1960001		
k_VoltSatQaxPolyCoeff_Uls_f32	20.9540005		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	62849	62849 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.3498068	-10.3498001 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.6227875	-17.6228008 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38411	38411 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	21.7719002	21.7719002 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84899998	-2.84899998 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.179 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.725998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0439999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0219999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46300006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.939026
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0329999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.759000003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.342987
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55599976
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3929996
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3120003
MtrCurrDaxPrevIntg_Volt_M_f32	-5.69950008
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.401001
MtrCurrQaxCog_Amp_M_f32	62.6925011
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84599996
MtrCurrQaxPrevIntg_Volt_M_f32	17.4958
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.792007
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.8499999
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.0109999999
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61500001
MtrVoltQax_Volt_M_f32[0]	-17.1070004
MtrVoltQax_Volt_M_f32[1]	15.9399996
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.962000012
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.439666986
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.74940002
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.598999977
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	23.1455994
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	954.236023
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	98.3255997
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.532559991
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-424.399994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	45.25
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18.3894005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.623560011
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7085.81982
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2937.1001
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.160332993
k_MtrCtrlVirtualResQax_Ohm_f32	0.154667005
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	0		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.4384995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.3633003		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	0.320666999		
k_VoltSatQaxPolyCoeff_Uls_f32	10.2349997		
k_deadtimeVScale_Uls_f32	0.968666971		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5587		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1237		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-125.123001		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1505		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.7156982		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1505	1505	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	111.643494	111.643997 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-30.021286	-30.0212994 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.666092694	0.666091979 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18624	18624 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	111.643494	111.643997 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.180 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4459991
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10300004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.877998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.085000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.085000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.10000002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.743011
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3889999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4220009
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48199987
MtrCurrDaxPrevIntg_Volt_M_f32	-5.39400005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.944
MtrCurrQaxCog_Amp_M_f32	70.7294998
MtrCurrQaxFinalRef_Amp_M_f32[0]	-213.026993
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7279968
MtrCurrQaxPrevIntg_Volt_M_f32	3.1875
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7030029
MtrCurrQaxRpl_Amp_M_f32[0]	67.4899979
MtrCurrQaxRpl_Amp_M_f32[1]	119.722
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005
MtrPosComputationDelay_Rad_M_f32[1]	3.24000001
MtrVoltDax_Volt_M_f32[0]	18.6380005
MtrVoltDax_Volt_M_f32[1]	-23.1919994
MtrVoltQax_Volt_M_f32[0]	18.6380005
MtrVoltQax_Volt_M_f32[1]	-23.1879997
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0769999996
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.416166991
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.968200028
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.582799971
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.5478001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	256.125
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	45.6320992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.235679999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1002.79999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	110.253998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	11.2356005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.51244998
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7438.18018
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2475.6001
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.160332993
k_MtrCtrlVirtualResQax_Ohm_f32	0.185167
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	31
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.69999981
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	30.2145004
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.7682991
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-3.72033
k_VoltSatQaxPolyCoeff_Uls_f32	11.6547003
k_deadtimeVScale_Uls_f32	0.968666971
t_CommOffsetTbIX_Uls_u3p13[0]	5022
t_CommOffsetTbIX_Uls_u3p13[1]	7003
t_CommOffsetTbIY_Cnt_u16[0]	365
t_CommOffsetTbIY_Cnt_u16[1]	1531
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-129.938995
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	419

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	129.727005		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	1531	1531	✓
MtrCntri_Write_Modldx_Uls_u16p16(val)	63482	63482 ± 1	✓
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-62.5164948	-62.5164986 ± 7.81E-03	✓
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	4.35798788	4.35798645	✓
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-5.79095459	-5.79095316	✓
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	59833	59833	✓
MtrCurrDaxPrevIntg_Volt_M_f32	31	31 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-62.5164948	-62.5164986 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-66.7279968	-66.7279968 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.181 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.089996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0140000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.171
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.22699
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.238000005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.278015
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6210003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1919994
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3120003
MtrCurrDaxPrevIntg_Volt_M_f32	-5.08850002
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.401001
MtrCurrQaxCog_Amp_M_f32	78.7665024

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.8499999		
MtrCurrQaxPrevIntg_Volt_M_f32	12.9105997		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.792007		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7279968		
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988		
MtrPosComputationDelay_Rad_M_f32[1]	0.010999999		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61899996		
MtrVoltQax_Volt_M_f32[0]	-27.0669994		
MtrVoltQax_Volt_M_f32[1]	28.1079998		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0120000001		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.392666996		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.642499983		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.566600025		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	65.3214035		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	144.326004		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	44.2155991		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.13256		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1250.13		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	125.012001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	10.2364998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.142560005		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7790.52979		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	2014.09998		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.160332993		
k_MtrCtrlVirualResQax_Ohm_f32	0.156669945		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	11.7658005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.80000019		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.2355995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-20.5599995		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-7.76133013		
k_VoltSatQaxPolyCoeff_Uls_f32	21.3213997		
k_deadtimeVScale_Uls_f32	0.968666971		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1637		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-134.755005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	175.738007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	159.447479	159.447006 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	29.9311619	29.9312 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.41802621	2.4175899 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50320	50320 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018	37.4550018 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	159.447479	159.447006 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.182 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.726997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0450000018
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46399999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.940002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0340000018
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0340000018
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.75999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.343994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55700016
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3939991
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3129997
MtrCurrDaxPrevIntg_Volt_M_f32	-4.78299999
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.401993
MtrCurrQaxCog_Amp_M_f32	86.8034973
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988
MtrCurrQaxPrevIntg_Volt_M_f32	3.9934001
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.792999
MtrCurrQaxRpl_Amp_M_f32[0]	37.4550018
MtrCurrQaxRpl_Amp_M_f32[1]	-2.85100007
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.0120000001
MtrVoltDax_Volt_M_f32[0]	-7.66699982
MtrVoltDax_Volt_M_f32[1]	2.61599994

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[0]	-17.1070004		
MtrVoltQax_Volt_M_f32[1]	15.941		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.0599999987		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.369167		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.936200023		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.550400019		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	14.2356005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	112.364998		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25.2145004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.432559997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1125.02002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	113.021004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	0.532540023		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.235599995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	122.320999		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	1552.59998		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.160332993		
k_MtrCtrlVirualResQax_Ohm_f32	0.123400003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	9		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.9000001		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	10.2356005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.2360001		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-11.8023005		
k_VoltSatQaxPolyCoeff_Uls_f32	24.5214005		
k_deadtimeVScale_Uls_f32	0.968666971		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5594		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1238		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-139.570999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	201.748993		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1238	1238	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63482	63482 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	39.5345001	39.5345001 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.5138645	-17.5138569	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	10.9310913	10.9310942	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55097	55097	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	39.5345001	39.5345001 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988	-2.84699988 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 2.183 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-110.000999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	100.021004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0225000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0425000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0421000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0282000005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.214499995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.321449995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	102.325996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	102.214996
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0320999995
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0313999988
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0124000004
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0214000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.321399987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.321399987
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	102.021004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	102.320999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-1.02139997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.32139993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.0214005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	11.0214005
MtrCtrl_Vecu_Volt_M_f32[0]	11.0214005
MtrCtrl_Vecu_Volt_M_f32[1]	20.1452007
MtrCurrDaxPrevIntg_Volt_M_f32	-3.21420002
MtrCurrDaxRef_Amp_M_f32[0]	10.2356005
MtrCurrDaxRef_Amp_M_f32[1]	25.3255997
MtrCurrQaxCog_Amp_M_f32	22.3255997
MtrCurrQaxFinalRef_Amp_M_f32[0]	33.2355995
MtrCurrQaxFinalRef_Amp_M_f32[1]	31.2145004
MtrCurrQaxPrevIntg_Volt_M_f32	2.36540008
MtrCurrQaxRef_Amp_M_f32[0]	12.3255997
MtrCurrQaxRef_Amp_M_f32[1]	25.2145004
MtrCurrQaxRpl_Amp_M_f32[0]	35.0214005
MtrCurrQaxRpl_Amp_M_f32[1]	110.021004
MtrPosComputationDelay_Rad_M_f32[0]	-2.0144999
MtrPosComputationDelay_Rad_M_f32[1]	5.02139997
MtrVoltDax_Volt_M_f32[0]	2.65440011
MtrVoltDax_Volt_M_f32[1]	3.21449995
MtrVoltQax_Volt_M_f32[0]	-10.3249998
MtrVoltQax_Volt_M_f32[1]	11.3255997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	9.99999975e-005
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0122999996
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.214499995
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.654100001
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	10.2356005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	110.236
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20.2145004
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.214499995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	110.325996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	112.320999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	10.3213997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0122999996
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10.0214005
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	100.213997
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	1
k_MtrCtrlVirtualResDax_Ohm_f32	0.00120000006
k_MtrCtrlVirtualResQax_Ohm_f32	0.132499993
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	10.2356005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.36540008		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.3255997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.32559967		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.2356005		
k_VoltSatQaxPolyCoeff_Uls_f32	20.3213997		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	10		
t_CommOffsetTblX_Uls_u3p13[1]	1757		
t_CommOffsetTblY_Cnt_u16[0]	160		
t_CommOffsetTblY_Cnt_u16[1]	260		
target_MtrCntri_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	23.0214005		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	10		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	100.214996		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	10	10	✔
MtrCntri_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-120.021004	-120.021004 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	-1.10058939	-1.10058916	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	-22.7908764	-22.7908802	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12259	12259 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	-2.36540008	-2.36540008 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	33.2355995	33.2355995 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-120.021004	-120.021004 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 2.184 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	110.200996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	99.0123978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112300001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0214000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0125000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.02139997

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23559999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	10.2356005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	21.2145004
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.021400009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0122999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00124999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	9.99999975e-005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.214499995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.421499997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	90.2141037
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	254.320999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-2.02139997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.21449995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-10.0214005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	5.02139997
MtrCtrl_Vecu_Volt_M_f32[0]	6.32140017
MtrCtrl_Vecu_Volt_M_f32[1]	21.2014008
MtrCurrDaxPrevIntg_Volt_M_f32	-2.36540008
MtrCurrDaxRef_Amp_M_f32[0]	32.1245003
MtrCurrDaxRef_Amp_M_f32[1]	21.0214005
MtrCurrQaxCog_Amp_M_f32	11.2545996
MtrCurrQaxFinalRef_Amp_M_f32[0]	14.2563
MtrCurrQaxFinalRef_Amp_M_f32[1]	12.3654003
MtrCurrQaxPrevIntg_Volt_M_f32	10.3249998
MtrCurrQaxRef_Amp_M_f32[0]	10.2356005
MtrCurrQaxRef_Amp_M_f32[1]	32.2145004
MtrCurrQaxRpl_Amp_M_f32[0]	22.0123997
MtrCurrQaxRpl_Amp_M_f32[1]	33.2014008
MtrPosComputationDelay_Rad_M_f32[0]	-1.0244
MtrPosComputationDelay_Rad_M_f32[1]	3.02139997
MtrVoltDax_Volt_M_f32[0]	1.25460005
MtrVoltDax_Volt_M_f32[1]	0.214499995
MtrVoltQax_Volt_M_f32[0]	23.3255997
MtrVoltQax_Volt_M_f32[1]	23.0125008
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	9.99999975e-005
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.231399998
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.145300001
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.74119997
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	11.3255997
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	111.325996
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	11.3255997
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.145600006
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	145.320999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	123.250999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12.3255997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.123600014
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	122.320999
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	200.214005
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.123599999
k_MtrCtrlVirtualResQax_Ohm_f32	0.112300001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	11.3255997
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.02559996
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	12.3255997
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.25689983
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-5.02139997
k_VoltSatQaxPolyCoeff_Uls_f32	10.2356005
k_deadtimeVScale_Uls_f32	0.971000016
t_CommOffsetTblX_Uls_u3p13[0]	8192
t_CommOffsetTblX_Uls_u3p13[1]	2633
t_CommOffsetTblY_Cnt_u16[0]	110
t_CommOffsetTblY_Cnt_u16[1]	365
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	22.3213997
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	12

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	90.2145004		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	110	110	✓
MtrCntri_Write_Modldx_Uls_u16p16(val)	18429	18429	✓
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-1.05249786	-1.05250001 ± 7.81E-03	✓
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	3.21449995	3.21449995	✓
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	5.02139997	5.02139997	✓
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	37454	37454	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-1.05249786	-1.05250001 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	12.3654003	12.3654003 ± 7.81E-03	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlIdx_Uls_u16p16	1	MtrCntl_Write_ModlIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Case 3: Path Test

Description	Vector Description:
	<p>TS 3.1((k_MtrCurrQaxRefModifRplEn_Cnt_Igc == TRUE)=False&&(MtrCurrQaxRefModif_Amp_T_f32>=220)=True&&(MtrCurrQaxFinalRef_Amp_T_f32>=220)=True&&(MtrCurrDaxRefModif_Amp_k_MtrVoltQaxIntegLoLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegHiLim_Volt_f32)=True&&(MtrCurrQaxIntg_Volt_T_f32>=k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrCtrlFeedbackControlDisable_Cnt_Igc == TRUE)=False&&(k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc == FALSE)=False&&(k_MtrVoltQaxFiltFFEnable_Cnt_Igc==TRUE)=False&&(VoltSatnRatio_Uls_T_f32 > D_ONE_ULS_F32)=True&&(ModlIdxSrlComSvcDft_Cnt_T_Igc==TRUE)=False&&(k_MtrCurrQaxRefModifDsb_Cnt_Igc == FALSE)=True</p> <p>TS 3.2((k_MtrCurrQaxRefModifRplEn_Cnt_Igc == TRUE)=True&&(MtrCurrQaxRefModif_Amp_T_f32>=220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<=-220)=True&&(MtrCurrQaxFinalRef_Amp_T_f32>=220)=True&&(MtrCurrQaxFiltFFEnable_Cnt_Igc==TRUE)=True &&(IvtrLoaMtgnEn_Cnt_T_Igc==FALSE)=False&&(ModlIdxSrlComSvcDft_Cnt_T_Igc==TRUE)=True&&(k_MtrCurrQaxRefModifDsb_Cnt_Igc == FALSE)=False</p> <p>TS 3.3&&(MtrCurrQaxRefModif_Amp_T_f32<=-220)=False&&(MtrCurrQaxFinalRef_Amp_T_f32<=-220)=False&&(MtrCurrQaxIntg_Volt_T_f32>=k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&(MtrCurrQaxIntg_Volt_T_f32>=k_MtrVoltQaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegLoLim_Volt_f32)=True&&(k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc == FALSE)=True&&(VoltSatnRatio_Uls_T_f32 > D_ONE_ULS_F32)=False</p> <p>TS 3.4</p> <p>((MtrCurrDaxRefModif_Amp_T_f32<=0)=False&&&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegLoLim_Volt_f32)=True&&(MtrCurrLoaMtgnEn_Cnt_T_Igc==FALSE) =True&&(MotCurrLoaMtgnEn_Cnt_T_Igc==FALSE) =True&&(k_MtrCtrlFeedbackControlDisable_Cnt_Igc == FALSE)=False</p> <p>TS 3.5((k_MtrVoltQaxFiltFFEnable_Cnt_Igc==TRUE) =True&&(IvtrLoaMtgnEn_Cnt_T_Igc==FALSE) =True&&(MotCurrLoaMtgnEn_Cnt_T_Igc==FALSE) =True&&(k_MtrCtrlFeedbackControlDisable_Cnt_Igc == FALSE)=True</p> <p>TS 3.6((k_MtrVoltQaxFiltFFEnable_Cnt_Igc==TRUE) =True&&(IvtrLoaMtgnEn_Cnt_T_Igc==FALSE) =True&&(MotCurrLoaMtgnEn_Cnt_T_Igc==FALSE)=False</p> <p>TS 3.7((MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegHiLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegLoLim_Volt_f32)=False</p> <p>TS 3.8((MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegHiLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegLoLim_Volt_f32)=False</p> <p>TS 3.9</p> <p>((MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False</p>

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModlIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.004999999989
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.004999999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.004999999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.004999999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.004999999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.004999999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.004999999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.004999999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5
MtrCtrl_Vecu_Volt_M_f32[1]	5
MtrCurrDaxPrevIntg_Volt_M_f32	-31
MtrCurrDaxRef_Amp_M_f32[0]	-220
MtrCurrDaxRef_Amp_M_f32[1]	-220
MtrCurrQaxCog_Amp_M_f32	-220
MtrCurrQaxFinalRef_Amp_M_f32[0]	-220
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220
MtrCurrQaxPrevIntg_Volt_M_f32	-31
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220
MtrCurrQaxRpl_Amp_M_f32[0]	-220
MtrCurrQaxRpl_Amp_M_f32[1]	-220
MtrPosComputationDelay_Rad_M_f32[0]	-3.89064002
MtrPosComputationDelay_Rad_M_f32[1]	-6.28318977
MtrVoltDax_Volt_M_f32[0]	-31
MtrVoltDax_Volt_M_f32[1]	-31
MtrVoltQax_Volt_M_f32[0]	-31
MtrVoltQax_Volt_M_f32[1]	-31
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.996816993
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.96346009e-005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996816993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.96346009e-005
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	10
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	10
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0
k_MtrCtrlVirtualResQax_Ohm_f32	0
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	0
k_MtrVoltDaxIntegLoLim_Volt_f32	-31
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	0
k_MtrVoltQaxIntegLoLim_Volt_f32	-31
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-25
k_VoltSatQaxPolyCoeff_Uls_f32	-25

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_deadtimeVScale_Uls_f32	0.949999988		
t_CommOffsetTblX_Uls_u3p13[0]	0		
t_CommOffsetTblX_Uls_u3p13[1]	0		
t_CommOffsetTblY_Cnt_u16[0]	0		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	62259	62259 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.33995581	-3.3399601 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.37745404	-3.37744999 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	321	321 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 3.2 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

	Input Value		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	31		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	31		
MtrCtrl_Vecu_Volt_M_f32[0]	31		
MtrCtrl_Vecu_Volt_M_f32[1]	31		
MtrCurrDaxPrevIntg_Volt_M_f32	31		
MtrCurrDaxRef_Amp_M_f32[0]	220		
MtrCurrDaxRef_Amp_M_f32[1]	220		
MtrCurrQaxCog_Amp_M_f32	220		
MtrCurrQaxFinalRef_Amp_M_f32[0]	220		
MtrCurrQaxFinalRef_Amp_M_f32[1]	220		
MtrCurrQaxPrevIntg_Volt_M_f32	31		
MtrCurrQaxRef_Amp_M_f32[0]	220		
MtrCurrQaxRef_Amp_M_f32[1]	220		
MtrCurrQaxRpl_Amp_M_f32[0]	220		
MtrCurrQaxRpl_Amp_M_f32[1]	220		
MtrPosComputationDelay_Rad_M_f32[0]	6.28318977		
MtrPosComputationDelay_Rad_M_f32[1]	6.28318977		
MtrVoltDax_Volt_M_f32[0]	31		
MtrVoltDax_Volt_M_f32[1]	31		
MtrVoltQax_Volt_M_f32[0]	31		
MtrVoltQax_Volt_M_f32[1]	31		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	1		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1350		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1350		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50928.6016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996827006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1350		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1350		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50928.6016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996827006		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	8000		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	8000		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.200000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.200000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	31		
k_MtrVoltDaxIntegLoLim_Volt_f32	0		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	31		
k_MtrVoltQaxIntegLoLim_Volt_f32	0		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	25		
k_VoltSatQaxPolyCoeff_Uls_f32	25		
k_deadtimeVScale_Uls_f32	1		
t_CommOffsetTblX_Uls_u3p13[0]	8192		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	2000		
t_CommOffsetTblY_Cnt_u16[1]	2000		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	5000		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	5000	5000	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	21.9203072	21.9202995 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	21.9203072	21.9202995 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8192	8192 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✔

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Actual Value	Expected Value	Result
MtrCurrQaxFinalRef_Amp_M_f32[1]	-220	-220 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModlDx_Uls_u16p16	1	MtrCntl_Write_ModlDx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModlDxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0320000015
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.227
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	537.232971
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	67.9840012
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.703000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.75199997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	462.437012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-685.195984
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.6930008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.219999999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.45499992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.1830006
MtrCtrl_Vecu_Volt_M_f32[0]	22.3540001
MtrCtrl_Vecu_Volt_M_f32[1]	24.7140007
MtrCurrDaxPrevIntg_Volt_M_f32	-23.0620003
MtrCurrDaxRef_Amp_M_f32[0]	37.4550018
MtrCurrDaxRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxCog_Amp_M_f32	-55.5390015
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006
MtrCurrQaxFinalRef_Amp_M_f32[1]	165.242004
MtrCurrQaxPrevIntg_Volt_M_f32	8.08899975
MtrCurrQaxRef_Amp_M_f32[0]	220
MtrCurrQaxRef_Amp_M_f32[1]	220
MtrCurrQaxRpl_Amp_M_f32[0]	160.044006
MtrCurrQaxRpl_Amp_M_f32[1]	165.242004
MtrPosComputationDelay_Rad_M_f32[0]	-5.08799982
MtrPosComputationDelay_Rad_M_f32[1]	-4.26300001

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltDax_Volt_M_f32[0]	3.45499992		
MtrVoltDax_Volt_M_f32[1]	-19.1830006		
MtrVoltQax_Volt_M_f32[0]	3.45499992		
MtrVoltQax_Volt_M_f32[1]	-19.1830006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.638000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.880900025		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.978999972		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47050.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0229000002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47050.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0229000002		
SlowDataAccessBufIndex_Cnt_M_u16	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7088.3501		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirtualResDax_Ohm_f32	0.194999993		
k_MtrCtrlVirtualResQax_Ohm_f32	0.142000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.9123993		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.699999988		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.4449997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.699999988		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-19.4559994		
k_VoltSatQaxPolyCoeff_Uls_f32	-18.6200008		
k_deadtimeVScale_Uls_f32	0.95599997		
t_CommOffsetTblX_Uls_u3p13[0]	4170		
t_CommOffsetTblX_Uls_u3p13[1]	6749		
t_CommOffsetTblY_Cnt_u16[0]	177		
t_CommOffsetTblY_Cnt_u16[1]	340		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	335		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-145.169006		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	315	315	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	50872	50872 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	110.296997	110.296997 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.219999999	0.219999999 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-19.1830006	-19.1830006 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	53720	53720 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-0.699999988	-0.699999988 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	160.044006	160.044006 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	110.296997	110.296997 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_Modldx_Uls_u16p16	1	MtrCtrl_Write_Modldx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0179999992
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.167999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62100005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	720.525024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-487.845001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0649999976
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.60500002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.33500004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-418.748993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	590.754028
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.3319998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.40700006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_Vecu_Volt_M_f32[0]	16.8080006
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993
MtrCurrDaxPrevIntg_Volt_M_f32	14.7060003
MtrCurrDaxRef_Amp_M_f32[0]	220
MtrCurrDaxRef_Amp_M_f32[1]	220
MtrCurrQaxCog_Amp_M_f32	177.763
MtrCurrQaxFinalRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxPrevIntg_Volt_M_f32	12.4979
MtrCurrQaxRef_Amp_M_f32[0]	160.044006
MtrCurrQaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxRpl_Amp_M_f32[0]	-133.947006
MtrCurrQaxRpl_Amp_M_f32[1]	75.7020035
MtrPosComputationDelay_Rad_M_f32[0]	0.277999997
MtrPosComputationDelay_Rad_M_f32[1]	-5.2579999
MtrVoltDax_Volt_M_f32[0]	-22.3460007
MtrVoltDax_Volt_M_f32[1]	-17.8169994
MtrVoltQax_Volt_M_f32[0]	-22.3460007
MtrVoltQax_Volt_M_f32[1]	-17.8169994

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.426999986		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.469999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.194700003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.860000014		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	31081.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.797699988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	31081.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.797699988		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5764.10986		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	3350.96997		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.175999999		
k_MtrCtrlVirtualResQax_Ohm_f32	0.061999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.2978001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.2735996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.7950001		
k_VoltSatQaxPolyCoeff_Uls_f32	21.1380005		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	4432		
t_CommOffsetTblX_Uls_u3p13[1]	5751		
t_CommOffsetTblY_Cnt_u16[0]	132		
t_CommOffsetTblY_Cnt_u16[1]	216		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	75.0830002		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3800		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	54.1119995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3800	3800	✔
MtrCntrl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	116.228012	116.227997 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.68477249	4.68477011 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.02222037	-1.02222002 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	21524	21524 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	116.228012	116.227997 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	75.7020035	75.7020035 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
InplVarXY_u16_u16Xu16Y_Cnt	1	InplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 3.5 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0370000005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.8240000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.0160000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.286000013
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.41499996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-730.362
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-412.898987
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	14.4589996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.13000011
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	22.5750008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	22.8969994
MtrCtrl_Vecu_Volt_M_f32[0]	18.7189999
MtrCtrl_Vecu_Volt_M_f32[1]	21.0790005
MtrCurrDaxPrevIntg_Volt_M_f32	15.9169998
MtrCurrDaxRef_Amp_M_f32[0]	-69.0940018
MtrCurrDaxRef_Amp_M_f32[1]	161.973007
MtrCurrQaxCog_Amp_M_f32	-152.050995
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxPrevIntg_Volt_M_f32	20.0867996
MtrCurrQaxRef_Amp_M_f32[0]	-200.556
MtrCurrQaxRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxRpl_Amp_M_f32[0]	106.072998
MtrCurrQaxRpl_Amp_M_f32[1]	-112.455002
MtrPosComputationDelay_Rad_M_f32[0]	-0.430000007
MtrPosComputationDelay_Rad_M_f32[1]	-5.92700005
MtrVoltDax_Volt_M_f32[0]	22.5750008
MtrVoltDax_Volt_M_f32[1]	22.8969994
MtrVoltQax_Volt_M_f32[0]	22.5750008
MtrVoltQax_Volt_M_f32[1]	22.8969994
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.418000013
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.300000012
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.902100027
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.675000012
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8419.69043
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.634800017
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	8419.69043
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.634800017
SlowDataAccessBufIndex_Cnt_M_u16	0
k_CLOAFdbckSignalSclFacSlew_UlspS_f32	6857.12012
k_ILOAFdbckSignalSclFacSlew_UlspS_f32	2799.87988
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	1
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0289999992
k_MtrCtrlVirtualResQax_Ohm_f32	0.188999996
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrCurrQaxRefModifRpIEn_Cnt_Igc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.2152004		
k_MtrVoltDaxIntegLoLim_Volt_f32	3.5		
k_MtrVoltQaxFiltFFEnable_Cnt_Igc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.2434006		
k_MtrVoltQaxIntegLoLim_Volt_f32	3.5		
k_MtrVoltVecuFiltEnable_Cnt_Igc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.026		
k_VoltSatQaxPolyCoeff_Uls_f32	-23.2660007		
k_deadtimeVScale_Uls_f32	0.999000013		
t_CommOffsetTblX_Uls_u3p13[0]	4342		
t_CommOffsetTblX_Uls_u3p13[1]	7724		
t_CommOffsetTblY_Cnt_u16[0]	1124		
t_CommOffsetTblY_Cnt_u16[1]	1178		
target_MtrCntnl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntnl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntnl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr	0		
target_MtrCntnl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntnl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3317		
target_MtrCntnl_Read_MtrCurrQax_Amp_f32_Val	3.89299989		
target_MtrCntnl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntnl_Write_CommOffset_Cnt_u16(val)	3317	3317	✔
MtrCntnl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntnl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-154.578003	-154.578003 ± 7.81E-03	✔
MtrCntnl_Write_MtrDaxVoltage_Volt_f32(val)	0.454691678	0.454690993 ± 4.88E-04	✔
MtrCntnl_Write_MtrQaxVoltage_Volt_f32(val)	-4.97426176	-4.97425985 ± 4.88E-04	✔
MtrCntnl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27332	27332 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	-154.578003	-154.578003 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002	-112.455002 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc	1	✓
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_ModIdx_Uls_u16p16	1	MtrCntl_Write_ModIdx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 3.6 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_IvtrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_Igc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.057

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.15900004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.762000024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.120999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0909999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87699997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.648999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-603.161987
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-712.994019
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-11.3319998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.40700006
MtrCtrl_Vecu_Volt_M_f32[0]	16.4099998
MtrCtrl_Vecu_Volt_M_f32[1]	18.7700005
MtrCurrDaxPrevIntg_Volt_M_f32	-24.1620007
MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
MtrCurrDaxRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxCog_Amp_M_f32	-51.1100006
MtrCurrQaxFinalRef_Amp_M_f32[0]	205.820999
MtrCurrQaxFinalRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxPrevIntg_Volt_M_f32	13.3757
MtrCurrQaxRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxRef_Amp_M_f32[1]	119.721001
MtrCurrQaxRpl_Amp_M_f32[0]	205.820999
MtrCurrQaxRpl_Amp_M_f32[1]	-206.792007
MtrPosComputationDelay_Rad_M_f32[0]	-3.62700009
MtrPosComputationDelay_Rad_M_f32[1]	-5.73500013
MtrVoltDax_Volt_M_f32[0]	-11.3319998
MtrVoltDax_Volt_M_f32[1]	-5.40700006
MtrVoltQax_Volt_M_f32[0]	-11.3319998
MtrVoltQax_Volt_M_f32[1]	-5.40700006
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.423999995
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.395000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.712199986
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.651000023
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12079.9004
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.298200011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12079.9004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.298200011
SlowDataAccessBufIndex_Cnt_u16	0
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	3678.44995
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	7603.6001
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0439999998
k_MtrCtrlVirtualResQax_Ohm_f32	0.166999996
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	30.1203003
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	8.95559978
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1
k_VoltSatDaxPolyCoeff_Uls_f32	24.5209999
k_VoltSatQaxPolyCoeff_Uls_f32	-20.1860008
k_deadtimeVScale_Uls_f32	0.990000001
t_CommOffsetTblX_Uls_u3p13[0]	1516
t_CommOffsetTblX_Uls_u3p13[1]	5882
t_CommOffsetTblY_Cnt_u16[0]	1813
t_CommOffsetTblY_Cnt_u16[1]	183
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
target_MtrCntnl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3803		
target_MtrCntnl_Read_MtrCurrQax_Amp_f32_Val	45.3779984		
target_MtrCntnl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntnl_Write_CommOffset_Cnt_u16(val)	3803	3803	✓
MtrCntnl_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntnl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-87.2210007	-87.2210007 ± 7.81E-03	✓
MtrCntnl_Write_MtrDaxVoltage_Volt_f32(val)	-2.68405461	-2.68405008 ± 4.88E-04	✓
MtrCntnl_Write_MtrQaxVoltage_Volt_f32(val)	-4.15912867	-4.1591301 ± 4.88E-04	✓
MtrCntnl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	915	915 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	-87.2210007	-87.2210007 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-206.792007	-206.792007 ± 7.81E-03	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_lvtrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc(ptr)	target_MtrCntl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr
MtrCntl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrDax_Amp_f32_Val
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntl_Read_MtrCurrQax_Amp_f32_Val
MtrCntl_Read_SysState_Cnt_Enum(Val)	target_MtrCntl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.087006
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16799998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.223999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.127000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0960000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.234999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.274994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.618
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1889992
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrDaxRef_Amp_M_f32[1]	-186.397995		
MtrCurrQaxCog_Amp_M_f32	10.8985004		
MtrCurrQaxFinalRef_Amp_M_f32[0]	37.4550018		
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988		
MtrCurrQaxPrevIntg_Volt_M_f32	15.8292999		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.789001		
MtrCurrQaxRpl_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRpl_Amp_M_f32[1]	-66.7249985		
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988		
MtrPosComputationDelay_Rad_M_f32[1]	0.00800000038		
MtrVoltDax_Volt_M_f32[0]	-7.66699982		
MtrVoltDax_Volt_M_f32[1]	2.61599994		
MtrVoltQax_Volt_M_f32[0]	-28.2420006		
MtrVoltQax_Volt_M_f32[1]	12.6160002		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.911000013		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.816999972		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.489399999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.703400016		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40.2612		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.176699996		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	16.5851002		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.727199972		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbckSignalScfFacSlew_UlspS_f32	5911.31982		
k_ILOAFdbckSignalScfFacSlew_UlspS_f32	7823.27002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	28.3733006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	31		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	4.35599995		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	1081		
t_CommOffsetTblY_Cnt_u16[1]	1779		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	903		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1779	1779	✓
MtrCntrl_Write_Modldx_Uls_u16p16(val)	63111	63111 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1.44797277	1.44797003 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.59212399	4.59212017 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3269	3269 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998 ± 0.0625	✓
MtrCurrQaxFinalRef_Amp_M_f32[0]	220	220 ± 7.81E-03	✓
MtrCurrQaxFinalRef_Amp_M_f32[1]	-2.84699988	-2.84699988 ± 7.81E-03	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCtrl_Per1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCtrl_Read_MtrCurrQax_Amp_f32	1	MtrCtrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCtrl_Read_MtrCurrDax_Amp_f32	1	MtrCtrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCtrl_Read_SysState_Cnt_Enum	1	MtrCtrl_Read_SysState_Cnt_Enum	1	✓
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCtrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCtrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCtrl_Write_ModIdx_Uls_u16p16	1	MtrCtrl_Write_ModIdx_Uls_u16p16	1	✓
MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCtrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCtrl_Write_CommOffset_Cnt_u16	1	MtrCtrl_Write_CommOffset_Cnt_u16	1	✓

Test Step 3.8 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCtrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCtrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCtrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrDax_Amp_f32_Val
MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCtrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCtrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCtrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCtrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0430000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.43400002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.70599997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	362.112
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	65.1259995
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.064000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0209999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.356999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.65200001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-894.130005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-888.995972
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.9890003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	29.243
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	19.7509995
MtrCurrDaxRef_Amp_M_f32[0]	67.4899979
MtrCurrDaxRef_Amp_M_f32[1]	119.721001
MtrCurrQaxCog_Amp_M_f32	-181.929001
MtrCurrQaxFinalRef_Amp_M_f32[0]	-82.2979965
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008
MtrCurrQaxPrevIntg_Volt_M_f32	7.82140017
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220
MtrCurrQaxRpl_Amp_M_f32[0]	-82.2979965
MtrCurrQaxRpl_Amp_M_f32[1]	46.8180008
MtrPosComputationDelay_Rad_M_f32[0]	-0.541999996
MtrPosComputationDelay_Rad_M_f32[1]	6.08400011
MtrVoltDax_Volt_M_f32[0]	-29.7110004
MtrVoltDax_Volt_M_f32[1]	3.61899996
MtrVoltQax_Volt_M_f32[0]	-29.7110004

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Name	Input Value		
MtrVoltQax_Volt_M_f32[1]	3.61899996		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.416999996		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.787		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.190799996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.708000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	48410.1016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0835999995		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermN_Uls_f32	48410.1016		
PICurrCntrl_MtrVoltQaxFFilt_M_str.TermD_Uls_f32	0.0835999995		
SlowDataAccessBufIndex_Cnt_M_u16	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	10.1009998		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	4233.2002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirtualResDax_Ohm_f32	0.0879999995		
k_MtrCtrlVirtualResQax_Ohm_f32	0.00999999978		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.9371996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.1975002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.59399998		
k_VoltSatQaxPolyCoeff_Uls_f32	8.35700035		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTblX_Uls_u3p13[0]	4914		
t_CommOffsetTblX_Uls_u3p13[1]	7782		
t_CommOffsetTblY_Cnt_u16[0]	1099		
t_CommOffsetTblY_Cnt_u16[1]	1672		
target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-72.4260025		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4932		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	77.189003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1672	1672	✔
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62324	62324 ± 1	✔
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	44.2269897	44.2270012 ± 7.81E-03	✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	9.26780319	9.26780033 ± 4.88E-04	✔
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-9.9237175	-9.92372036 ± 4.88E-04	✔
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	19279	19279 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	12.9371996	12.9371996 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	44.2269897	44.2270012 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	46.8180008	46.8180008 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc	1	✓
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgnScfFac	1	LoaMtgnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntrl_Per1

Test Step 3.9 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-133.947006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	75.7020035
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0320000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.34800005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.11099994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-942.771973
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-380.85199
MtrCtrl_MtrlImpedDax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrlImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrlImpedQax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrlImpedQax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.35000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.749000013
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.3530006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.3040009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.4720001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.0999999
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997
MtrCurrDaxPrevIntg_Volt_M_f32	15.0869999
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004
MtrCurrDaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxCog_Amp_M_f32	114.531998
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998
MtrCurrQaxFinalRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxPrevIntg_Volt_M_f32	5.42920017
MtrCurrQaxRef_Amp_M_f32[0]	191.369003
MtrCurrQaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxRpl_Amp_M_f32[0]	106.072998
MtrCurrQaxRpl_Amp_M_f32[1]	-112.455002
MtrPosComputationDelay_Rad_M_f32[0]	2.61800003
MtrPosComputationDelay_Rad_M_f32[1]	-1.04299998
MtrVoltDax_Volt_M_f32[0]	26.4720001
MtrVoltDax_Volt_M_f32[1]	6.0999999
MtrVoltQax_Volt_M_f32[0]	26.4720001
MtrVoltQax_Volt_M_f32[1]	6.0999999
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.600000024
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.899999976
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.179199994
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.257999986
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	404.899994
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.578299999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	404.899994
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.578299999
SlowDataAccessBufIndex_Cnt_M_u16	1
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	2048.80005
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	3548.88989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	0
k_MtrCtrlFeedbackControlDisable_Cnt_Igc	0
k_MtrCtrlVirtualResDax_Ohm_f32	0.0790000036
k_MtrCtrlVirtualResQax_Ohm_f32	0.177000001
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0
k_MtrCurrQaxRefModifRplEn_Cnt_Igc	0

TEST DETAILS REPORT

2016-04-22, 16:28:30+0530



PICurrCntl_Per1

Name	Input Value		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.39529991		
k_MtrVoltDaxIntegLoLim_Volt_f32	-13.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.2297001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.0030003		
k_VoltSatQaxPolyCoeff_Uls_f32	-9.26399994		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTblX_Uls_u3p13[0]	1810		
t_CommOffsetTblX_Uls_u3p13[1]	2335		
t_CommOffsetTblY_Cnt_u16[0]	157		
t_CommOffsetTblY_Cnt_u16[1]	712		
target_MtrCntri_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntri_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntri_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntri_Read_MtrCurrDax_Amp_f32_Val	107.702003		
target_MtrCntri_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4540		
target_MtrCntri_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntri_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntri_Write_CommOffset_Cnt_u16(val)	4540	4540	✔
MtrCntri_Write_Modldx_Uls_u16p16(val)	0	0 ± 1	✔
MtrCntri_Write_MtrCurrQaxFinalRef_Amp_f32(val)	105.060005	105.059998 ± 7.81E-03	✔
MtrCntri_Write_MtrDaxVoltage_Volt_f32(val)	2.34449911	2.34450006 ± 4.88E-04	✔
MtrCntri_Write_MtrQaxVoltage_Volt_f32(val)	29.3876286	29.3875999 ± 4.88E-04	✔
MtrCntri_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55487	55487 ± 1.52588E-05	✔
MtrCurrDaxPrevIntg_Volt_M_f32	0.910027981	0.910028994 ± 0.0625	✔
MtrCurrQaxFinalRef_Amp_M_f32[0]	106.072998	106.072998 ± 7.81E-03	✔
MtrCurrQaxFinalRef_Amp_M_f32[1]	105.060005	105.059998 ± 7.81E-03	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntl_Read_MtrCurrQax_Amp_f32	1	MtrCntl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntl_Read_MtrCurrDax_Amp_f32	1	MtrCntl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntl_Read_SysState_Cnt_Enum	1	MtrCntl_Read_SysState_Cnt_Enum	1	✓
MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
CallLowPassFiltVecuOut	1	CallLowPassFiltVecuOut	1	✓
CallLowPassFiltBilinearOut	1	CallLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnScfFac	1	LoaMtgtnScfFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntl_Write_Modldx_Uls_u16p16	1	MtrCntl_Write_Modldx_Uls_u16p16	1	✓
MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntl_Write_CommOffset_Cnt_u16	1	MtrCntl_Write_CommOffset_Cnt_u16	1	✓

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScfFac



Project	Ap_PICurrCntrl
Module	PICurrCntrl
Test Object	LoaMtgtnScfFac

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Picurrentcontrol
Configuration File	D:\Synergy_Work_Area\Picurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS=-D__sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D__sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'PICurrCntrl'	*****Unit Test Information***** Name of Tester: Priyanka Shitole Code File(s) Under Test: Ap_PICurrCntrl.c Code File(s) Version: 15 Module Design Document: PICurrentControl.doc Module Design Document Version: 11 Data Dictionary Version: 14 Unit Test Plan Version: 3 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): NA Total RAM Used (Bytes): NA Total CALS Used (Bytes): NA Special Test Requirements: Test Date: 4/22/2016 Comments: "Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2 : Out of range value is given in function ""LoaMtgtnScfFac"" for variables ""k_CLOAFdbackSignalScfFacSlew_UlspS_f32,k_ILOAFdbackSignalScfFacSlew_UlspS_f32, PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 and PICurrCntrl_InverterFailScfFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PICurrCntrl_Per1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScfFac



Test Case 1: Metric Test

Description Vector Description:

TS 1.1Shortest Path==>(MotCurrLoaMtgtnEn_Cnt_T_lgc == TRUE)=False&&(1>=((D_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=True&&(1>=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32))=True
TS 1.2Longest Path==>(MotCurrLoaMtgtnEn_Cnt_T_lgc == TRUE)=True&&(0>=((D_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=False&&(0<=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=False&&(0>=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32))=false&&(0<=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32))=false

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.00125000009	0.00124999997 ± 4.88E-04	✓
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00125000009	0.00124999997 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0	0 ± 4.88E-04	✓
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0	0 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScfFac



Test Case 2: Boundary Test

Description Vector Description:

TS 2.1All_Min
TS 2.2All_Max
TS 2.3MotCurrLoaMtgtnEn_Cnt_T_lgc==>Min
TS 2.4MotCurrLoaMtgtnEn_Cnt_T_lgc==>Max
TS 2.5lvtrLoaMtgtnEn_Cnt_T_lgc==>Min
TS 2.6lvtrLoaMtgtnEn_Cnt_T_lgc==>Max
TS 2.7PICurrCntrl_CurrSensFailScfFac_Uls_M_f32==>Min
TS 2.8PICurrCntrl_CurrSensFailScfFac_Uls_M_f32==>Max
TS 2.9PICurrCntrl_CurrSensFailScfFac_Uls_M_f32==>Mid
TS 2.10k_CLOAFdbackSignalScfFacSlew_UlspS_f32==>Min
TS 2.11k_CLOAFdbackSignalScfFacSlew_UlspS_f32==>Max
TS 2.12k_CLOAFdbackSignalScfFacSlew_UlspS_f32==>default
TS 2.13k_CLOAFdbackSignalScfFacSlew_UlspS_f32==>Mid
TS 2.14k_ILOAFdbackSignalScfFacSlew_UlspS_f32==>Min
TS 2.15k_ILOAFdbackSignalScfFacSlew_UlspS_f32==>Max
TS 2.16k_ILOAFdbackSignalScfFacSlew_UlspS_f32==>default
TS 2.17k_ILOAFdbackSignalScfFacSlew_UlspS_f32==>Mid
TS 2.18PICurrCntrl_InverterFailScfFac_Uls_M_f32==>Min
TS 2.19PICurrCntrl_InverterFailScfFac_Uls_M_f32==>Max
TS 2.20PICurrCntrl_InverterFailScfFac_Uls_M_f32==>Mid

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.00125000009	0.00125 ± 4.88E-04	✔
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00125000009	0.00125 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0	0 ± 4.88E-04	✔
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0	0 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.2356		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.2356		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7702.76		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	6599.26		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1	1 ± 4.88E-04	✔
PICurrCntrl_InverterFailScfFac_Uls_M_f32	1	1 ± 4.88E-04	✔

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScIFac



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.3256		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.325		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	7466.02		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	5753.88		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0	0 ± 4.88E-04	✔
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.5 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.1425		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.1245		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	6719.13		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	1788.25		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.982391238	0.982392 ± 4.88E-04	✔
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.348031253	0.348032 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.6 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.3256		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.3256		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	3280.9		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	2855.33		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.735712528	0.735713 ± 4.88E-04	✔
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0	0 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.7 (Repeat Count = 1)

Name	Input Value
IvtrLoaMtgtnEn_Cnt_T_lgc	0
MotCurrLoaMtgtnEn_Cnt_T_lgc	0
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.2256
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	7916.52

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScIFac



Name	Input Value		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	2851.42		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.989565074	0.989565 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.582027555	0.582027 ± 4.88E-04	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.8 (Repeat Count = 1) ✓

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.3256		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	2075.21		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	1642.61		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.740598738	0.740599 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.530926228	0.530926 ± 4.88E-04	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.9 (Repeat Count = 1) ✓

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.5		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.3244		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	1404.3		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	4636.46		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.324462473	0.324462265 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.903957546	0.903957 ± 4.88E-04	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.10 (Repeat Count = 1) ✓

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.5326		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.1256		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	5044.23		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.531349957	0.53135 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.756128728	0.756129 ± 4.88E-04	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScIFac



Test Step 2.11 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.3214		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.12356		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	8000		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	1124.25		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0	0 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.12 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.2356		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.3256		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	100		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	4636.46		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.223099992	0.2231 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.905157566	0.905157 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.13 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.8332		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.1425		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	4217		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	497.261		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.204657629	0.204657659 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.14 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.2461		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.7888		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	5544.15		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0	0 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.79005003	0.79005 ± 4.88E-04	✓

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScIFac



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.15 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.87426		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	892.101		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.888487339	0.888487 ± 4.88E-04	✔
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.16 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.2145		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.6125		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	892.101		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	100		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.326012611	0.326012671 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.600000024	0.6 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.17 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.3214		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.5362		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	886.92		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	1071.5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.432264984	0.432265 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.670137525	0.670138 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.18 (Repeat Count = 1)

Name	Input Value
IvtrLoaMtgtnEn_Cnt_T_lgc	0
MotCurrLoaMtgtnEn_Cnt_T_lgc	1
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.3698
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	2332

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScIFac



Name	Input Value		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	3405.61		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.0782999992	0.0782994 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.42570129	0.425701 ± 4.88E-04	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.19 (Repeat Count = 1) ✓

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.8032		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	3197.44		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	1505.18		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.403519988	0.40352 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.20 (Repeat Count = 1) ✓

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.0325		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0.654		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	4477.15		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	3969.39		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.592143774	0.592143 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScfFac



Test Case 3: Path test

Description Vector Description:

```
TS 3.1(MotCurrLoaMtgtnEn_Cnt_T_Igc == TRUE)=False&&(1>=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=True&&(1>=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32 ))=True
TS 3.2(MotCurrLoaMtgtnEn_Cnt_T_Igc == TRUE)=True&&(0>=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=False&&(0<=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=False&&(0>=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32 ))=false
TS 3.3(1>=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=False&&(1<=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_InverterFailScfFac_Uls_M_f32))=False&&(1>=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32 ))=False
TS 3.4(1<=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=true
TS 3.5(0>=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=False
TS 3.6&&(0<=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalScfFacSlew_UlspS_f32)+ PICurrCntrl_CurrSensFailScfFac_Uls_M_f32))=True
TS 3.7(1<=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32 ))=True
TS 3.8(0>=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32 ))=True
TS 3.9(0<=((D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalScfFacSlew_UlspS_f32)+PICurrCntrl_InverterFailScfFac_Uls_M_f32 ))=True
```

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_Igc	0		
MotCurrLoaMtgtnEn_Cnt_T_Igc	0		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.00125000009	0.00125 ± 4.88E-04	✔
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.00125000009	0.00125 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	1		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0	0 ± 4.88E-04	✔
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0	0 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.3 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_Igc	0		
MotCurrLoaMtgtnEn_Cnt_T_Igc	0		
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	0.2356		
PICurrCntrl_InverterFailScfFac_Uls_M_f32	0.2356		
k_CLOAFdbackSignalScfFacSlew_UlspS_f32	7702.76		
k_ILOAFdbackSignalScfFacSlew_UlspS_f32	6599.26		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScfFac_Uls_M_f32	1	1 ± 4.88E-04	✔
PICurrCntrl_InverterFailScfFac_Uls_M_f32	1	1 ± 4.88E-04	✔

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgtnScIFac



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.4 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	2		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	0		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	6599.26		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	2	2 ± 4.88E-04	✔
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.5 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	-1		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	0		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	-1	-1 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0	0 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.6 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1		
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	0		
k_ILOAFdbckSignalScIFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	0	0 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.7 (Repeat Count = 1)

Name	Input Value
IvtrLoaMtgtnEn_Cnt_T_lgc	0
MotCurrLoaMtgtnEn_Cnt_T_lgc	0
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0.2356
PICurrCntrl_InverterFailScIFac_Uls_M_f32	2
k_CLOAFdbckSignalScIFacSlew_UlspS_f32	7702.76

TEST DETAILS REPORT

2016-04-22, 16:27:52+0530

LoaMtgnScIFac



Name	Input Value		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	0		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	2	2 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.8 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgnEn_Cnt_T_lgc	1		
MotCurrLoaMtgnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	-1		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	0		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0	0 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	-1	-1 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.9 (Repeat Count = 1)

Name	Input Value		
IvtrLoaMtgnEn_Cnt_T_lgc	1		
MotCurrLoaMtgnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	1		
PICurrCntrl_InverterFailScIFac_Uls_M_f32	2		
k_CLOAFdbackSignalScIFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32	0	0 ± 4.88E-04	✓
PICurrCntrl_InverterFailScIFac_Uls_M_f32	1	1 ± 4.88E-04	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2016-04-22, 16:27:43+0530

IntegralStateVarNonOperState



Project	Ap_PICurrCntrl
Module	PICurrCntrl
Test Object	IntegralStateVarNonOperState

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\Picurrentcontrol
Configuration File	D:\Synergy_Work_Area\Picurrentcontrol\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	GNU GCC GNU GVD (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS=-D__sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D__sqrtf=sqrtf -Dconst=-I\$(SOURCEROOT)\utp\contract -I\$(SOURCEROOT)\utp\contract\Ap_PICurrCntrl -I\$(SOURCEROOT)\include -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -IC:\Program Files (x86)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description/Specification

Name	Text
Module 'PICurrCntrl'	*****Unit Test Information***** Name of Tester: Priyanka Shitole Code File(s) Under Test: Ap_PICurrCntrl.c Code File(s) Version: 15 Module Design Document: PICurrentControl.doc Module Design Document Version: 11 Data Dictionary Version: 14 Unit Test Plan Version: 3 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS570_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes): NA Total RAM Used (Bytes): NA Total CALS Used (Bytes): NA Special Test Requirements: Test Date: 4/22/2016 Comments: "Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2 : Out of range value is given in function ""LoaMtgtnScfFac"" for variables ""k_CLOAFdbckSignalScfFacSlew_UlspS_f32,k_ILOAFdbckSignalScfFacSlew_UlspS_f32, PICurrCntrl_CurrSensFailScfFac_Uls_M_f32 and PICurrCntrl_InverterFailScfFac_Uls_M_f32"" to achieve 100% path coverage. NOTE 3: In function PICurrCntrl_Per1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

2016-04-22, 16:27:43+0530

IntegralStateVarNonOperState



Test Case 1: Metric Test

Description Vector Description:

TS 1.1Longest Path==>(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=True
TS 1.2Shortest Path==>(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=False

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✔
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391		
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723		
SysState_Cnt_T_Enum	2		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	✓
MtrCurrQaxPrevIntg_Volt_M_f32	-6.72300005	-6.72300005	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Case 2: Boundary test

Description Vector Description:

TS 2.1All Min
TS 2.2All Max
TS 2.3SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_DISABLE
TS 2.4SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OFF
TS 2.5SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OPERATE
TS 2.6SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_WARMINIT
TS 2.7SysState_Cnt_T_Enum=>RTE_TRANSITION_StaMd_Mode

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✔
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value
MtrCurrDaxPrevIntg_Volt_M_f32	31
MtrCurrQaxPrevIntg_Volt_M_f32	31

TEST DETAILS REPORT

2016-04-22, 16:27:43+0530

IntegralStateVarNonOperState



Name	Input Value		
SysState_Cnt_T_Enum	4		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	7.8214		
MtrCurrQaxPrevIntg_Volt_M_f32	19.751		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	8.089		
MtrCurrQaxPrevIntg_Volt_M_f32	-23.062		
SysState_Cnt_T_Enum	1		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.5 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391		
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723		
SysState_Cnt_T_Enum	2		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	✓
MtrCurrQaxPrevIntg_Volt_M_f32	-6.72300005	-6.72300005	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.6 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	22.3449993		
MtrCurrQaxPrevIntg_Volt_M_f32	25.7600002		
SysState_Cnt_T_Enum	3		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

TEST DETAILS REPORT

2016-04-22, 16:27:43+0530

IntegralStateVarNonOperState



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.7 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	12.4499998		
MtrCurrQaxPrevIntg_Volt_M_f32	11.3400002		
SysState_Cnt_T_Enum	4		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✔
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Case 3: Path Test

Description Vector Description:

TS 3.1(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=True
TS 3.2(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=False

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723
SysState_Cnt_T_Enum	2

Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	✓
MtrCurrQaxPrevIntg_Volt_M_f32	-6.72300005	-6.72300005	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓