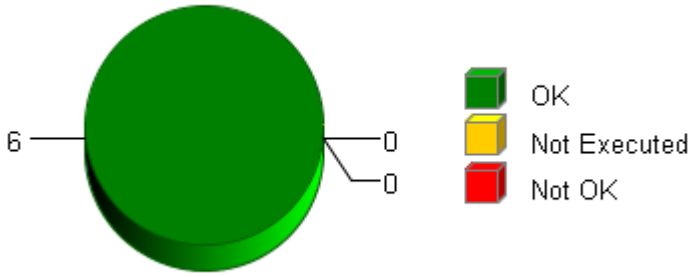
Summary

Total Test Objects:	6
Successful:	6
Failed:	0
Not Executed:	0
Date:	2015-04-13
Time:	16:56:42+0530

Overall Test Object Results (including Coverage)



Selected Project Items

Test Collection "CBD_UnitTest"

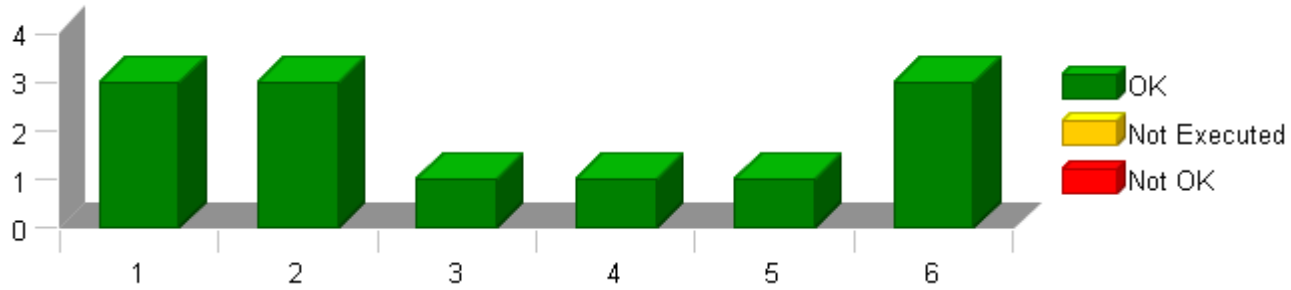
Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

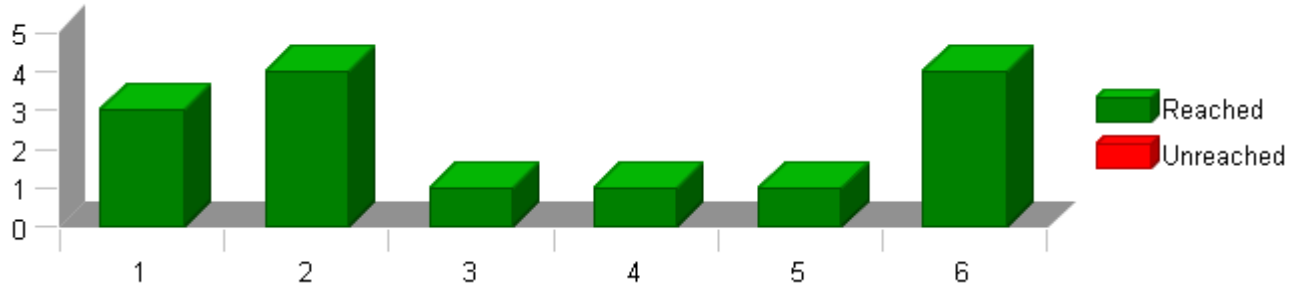
Check Interface:	No
Generate Driver:	Yes
Execute Test:	Yes
Create New Test Run:	No
Instrumentation:	Test Object Only
Coverage:	Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)



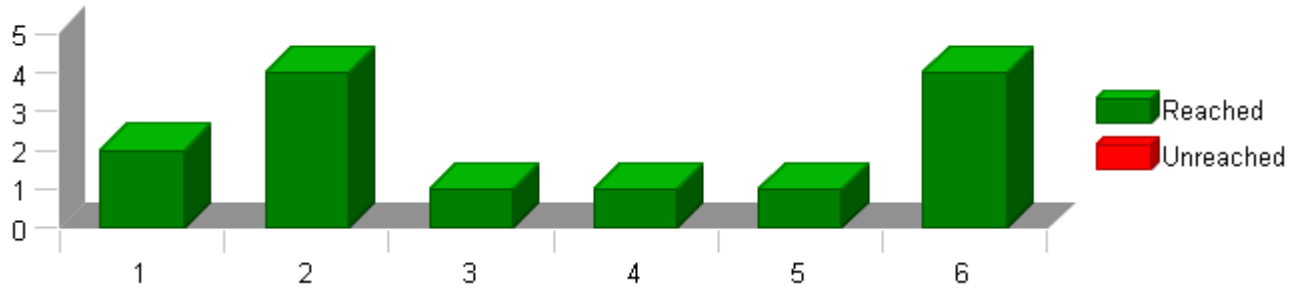
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



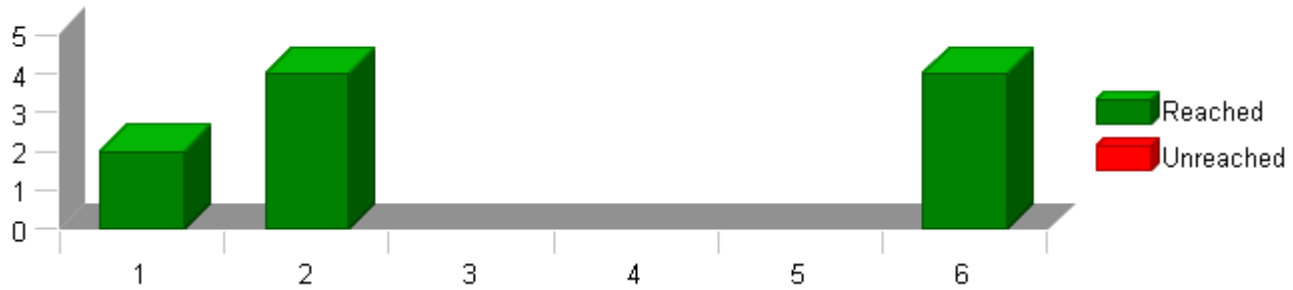
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

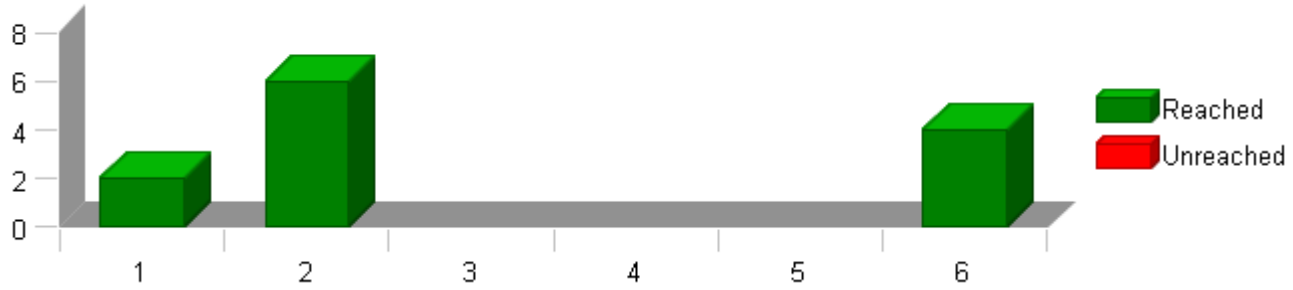
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

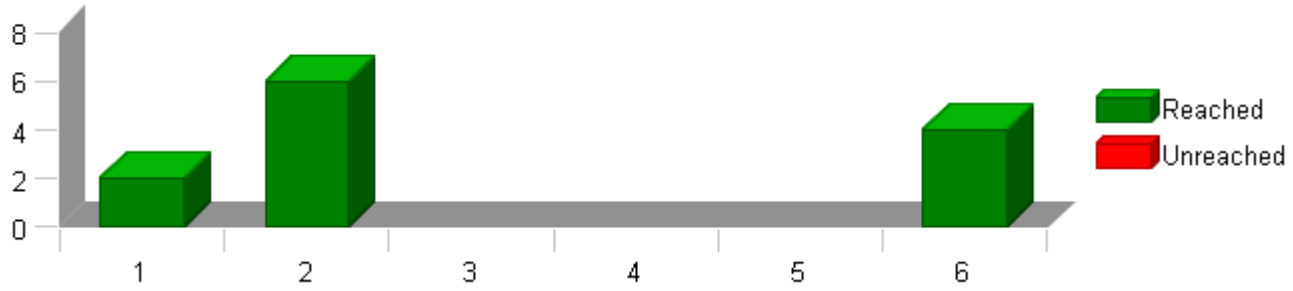
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	CustPerSrvcs	100 %	100 %	100 %	100 %	100 %	12 of 12 passed	✔
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	12 of 12 passed	✔
	CustPerSrvcs	100 %	100 %	100 %	100 %	100 %	12 of 12 passed	✔
1	CustPerSrvcs_Init1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
2	CustPerSrvcs_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
3	CustPerSrvcs_SCom_ReadActivePullParam	100 %	100 %	-	-	-	1 of 1 passed	✔
4	CustPerSrvcs_SCom_ReadLrnEOTParam	100 %	100 %	-	-	-	1 of 1 passed	✔
5	CustPerSrvcs_SCom_ResetThrmlCntr	100 %	100 %	-	-	-	1 of 1 passed	✔
6	CustPerSrvcs_Trns1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔

TEST DETAILS REPORT

CustPerSrvcs_Per1

2015-04-13, 16:53:11+0530

Project	CustPerSrvcs
Module	CustPerSrvcs
Test Object	CustPerSrvcs_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CustPerSrvcs_C1xx
Configuration File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\CustPerSrvcs\src\Ap_CustPerSrvcs.c
Compiler Options	-I$(PROJECTROOT)\CustPerSrvcs\utp\contract -I$(PROJECTROOT)\CustPerSrvcs\utp\contract\Ap_CustPerSrvcs -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'CustPerSrvcs'	**********Unit Test Description********** Name of Tester:Spoorti Mali Code File(s) Under Test:Ap_CustPerSrvcs.c Code File(s) Version:2 Module Design Document:Customer_Periodic_Services_MDD.docx Module Design Document Version:2 Data Dictionary Version:1 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):394 Total RAM Used (Bytes):19 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:4/13/2015 Comments:"NOTE1: Inline function defined in globalmacro.h is not unittested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false

Attributes

Name	Value
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS1.1  1953.00 Cycles
TS1.2  2930.00 Cycles
```

Description

Vector Description:

TS1.1Shortest Execution Path:

```
(
 (FALSE == ThermalLimitFlagCntrIncremented_Cnt_M_lgc) &&
 ((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTFAILED_CNT_U08) == D_TESTFAILED_CNT_U08) &&
 ((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTNOTCOMPTHISOPCYCLE_CNT_U08) != D_TESTNOTCOMPTHISOPCYCLE_CNT_U08)
  ) =False"
```

TS1.2 Longest Execution Path:

```
(
 (FALSE == ThermalLimitFlagCntrIncremented_Cnt_M_lgc) &&
 ((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTFAILED_CNT_U08) == D_TESTFAILED_CNT_U08) &&
 ((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTNOTCOMPTHISOPCYCLE_CNT_U08) != D_TESTNOTCOMPTHISOPCYCLE_CNT_U08)
  ) =True,(ThermalLimitFlagCntr_Cnt_M_u08 < 255U)=True"
```

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	0
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	0
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	0
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	14
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	124
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	25
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	125	125	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	125	125	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Case 2: Boundary Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS2.1  1953.00 Cycles
TS2.2  1705.00 Cycles
TS2.3  1953.00 Cycles
TS2.4  2923.00 Cycles
TS2.5  2930.00 Cycles
TS2.6  2204.00 Cycles
TS2.7  1705.00 Cycles
TS2.8  1953.00 Cycles
TS2.9  1705.00 Cycles
TS2.10  1953.00 Cycles
```

Description

Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 ThermalLimitFlagCntr_Cnt_M_u08=Min
TS2.4 ThermalLimitFlagCntr_Cnt_M_u08=Max
TS2.5 ThermalLimitFlagCntr_Cnt_M_u08=Pos
TS2.6 ThermalLimitFlagCntrIncremented_Cnt_M_lgc=Min
TS2.7 ThermalLimitFlagCntrIncremented_Cnt_M_lgc=Max
TS2.8 Rte_Call_NxtrDiagMgr_GetNTCStatus=Min
TS2.9 Rte_Call_NxtrDiagMgr_GetNTCStatus=Max
TS2.10 Rte_Call_NxtrDiagMgr_GetNTCStatus=Pos

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	0
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	0
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	0
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.2 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	50
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1
ThermalLimitFlagCntr_Cnt_M_u08	255
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	255
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	50	50	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	255	255	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	255	255	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.3 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	1
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	0
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	36
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	1	1	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.4 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	25
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	255
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	49
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	255	255	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	255	255	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.5 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	14
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	124
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	25
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	125	125	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	125	125	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.6 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	14
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	111
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	255
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	14	14	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	111	111	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	111	111	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.7 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	20
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1
ThermalLimitFlagCntr_Cnt_M_u08	125
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	45
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	20	20	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	125	125	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	125	125	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.8 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	29
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	136
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	0
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	29	29	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	136	136	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	136	136	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.9 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	36
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1
ThermalLimitFlagCntr_Cnt_M_u08	142
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	255
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	36	36	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	142	142	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	142	142	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 2.10 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	47
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	152
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	122
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	47	47	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	152	152	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	152	152	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Case 3: Path Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS3.1  1953.00 Cycles
TS3.2  2930.00 Cycles
TS3.3  2923.00 Cycles
TS3.4  2204.00 Cycles
TS3.5  1705.00 Cycles
```

Description

Vector Description:

TS3.1 "((FALSE == ThermalLimitFlagCntrIncremented_Cnt_M_lgc)=True &&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTFAILED_CNT_U08) == D_TESTFAILED_CNT_U08) =False&&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTNOTCOMPTHISOPCYCLE_CNT_U08) != D_TESTNOTCOMPTHISOPCYCLE_CNT_U08)) "
TS3.2 "((FALSE == ThermalLimitFlagCntrIncremented_Cnt_M_lgc)=True &&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTFAILED_CNT_U08) == D_TESTFAILED_CNT_U08) =True&&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTNOTCOMPTHISOPCYCLE_CNT_U08) != D_TESTNOTCOMPTHISOPCYCLE_CNT_U08))
=True,(ThermalLimitFlagCntr_Cnt_M_u08 < 255U)=True"
TS3.3 "((FALSE == ThermalLimitFlagCntrIncremented_Cnt_M_lgc)=False &&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTFAILED_CNT_U08) == D_TESTFAILED_CNT_U08) =&&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTNOTCOMPTHISOPCYCLE_CNT_U08) != D_TESTNOTCOMPTHISOPCYCLE_CNT_U08))
=True,(ThermalLimitFlagCntr_Cnt_M_u08 < 255U)=False"
TS3.4 "((FALSE == ThermalLimitFlagCntrIncremented_Cnt_M_lgc) =True&&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTFAILED_CNT_U08) == D_TESTFAILED_CNT_U08)=True&&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTNOTCOMPTHISOPCYCLE_CNT_U08) !=
D_TESTNOTCOMPTHISOPCYCLE_CNT_U08))=False"
TS3.5 "((FALSE == ThermalLimitFlagCntrIncremented_Cnt_M_lgc)=False &&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTFAILED_CNT_U08) == D_TESTFAILED_CNT_U08) =&&
((ThermalLimitFlagStatus_Cnt_T_u08 & D_TESTNOTCOMPTHISOPCYCLE_CNT_U08) != D_TESTNOTCOMPTHISOPCYCLE_CNT_U08)) "

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	0
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	0
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	0
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 3.2 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	14
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	124
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	25
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	125	125	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	125	125	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 3.3 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	25
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	255
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	49
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	255	255	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	255	255	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 3.4 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	14
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0
ThermalLimitFlagCntr_Cnt_M_u08	111
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	255
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	14	14	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	111	111	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	111	111	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

Test Step 3.5 (Repeat Count = 1)

Name	Input Value
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus(Status_Ptr_T_u08)	tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt
Rte_Inst_Ap_CustPerSrvcs	tgt_Rte_Inst_Ap_CustPerSrvcs
ThermalLimitFlagClearCntr_Cnt_M_u08	20
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1
ThermalLimitFlagCntr_Cnt_M_u08	125
tgt_Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus_Status_Pt	45
tgt_Rte_Inst_Ap_CustPerSrvcs.CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08	tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	20	20	✔
ThermalLimitFlagCntrIncremented_Cnt_M_lgc	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	125	125	✔
tgt_CustPerSrvcs_Per1_ThermalLimitFlagCntr_Cnt_u08.value	125	125	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP0_CheckpointReached	1	✔
Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	Rte_Call_Ap_CustPerSrvcs_NxtrDiagMgr_GetNTCStatus	1	✔
Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	Rte_Call_CustPerSrvcs_Per1_CP1_CheckpointReached	1	✔

TEST DETAILS REPORT

CustPerSrvcs_Trns1

2015-04-13, 16:56:33+0530

Project	CustPerSrvcs
Module	CustPerSrvcs
Test Object	CustPerSrvcs_Trns1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CustPerSrvcs_C1xx
Configuration File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\CustPerSrvcs\src\Ap_CustPerSrvcs.c
Compiler Options	-I$(PROJECTROOT)\CustPerSrvcs\utp\contract -I$(PROJECTROOT)\CustPerSrvcs\utp\contract\Ap_CustPerSrvcs -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'CustPerSrvcs'	**********Unit Test Description********** Name of Tester:Spoorti Mali Code File(s) Under Test:Ap_CustPerSrvcs.c Code File(s) Version:2 Module Design Document:Customer_Periodic_Services_MDD.docx Module Design Document Version:2 Data Dictionary Version:1 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):394 Total RAM Used (Bytes):19 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:4/13/2015 Comments:"NOTE1: Inline function defined in globalmacro.h is not unittested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false

Attributes

Name	Value
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS1.1  1650.00 Cycles
TS1.2  1922.00 Cycles
```

Description

Vector Description:

TS1.1 Shortest Execution Path:

(WriteLTCompValAftRst_Cnt_M_lgc == TRUE)=False,(WriteEOTValAftRst_Cnt_M_lgc == TRUE)=False

TS1.2 Longest Execution Path:

(WriteLTCompValAftRst_Cnt_M_lgc == TRUE)=True,(WriteEOTValAftRst_Cnt_M_lgc == TRUE)=True

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-1440.11
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1440.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	0	0 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1440.10999	-1440.11 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	1
CCWEOTPosBefReset_HwDeg_M_f32	0
CWEOTFndBefReset_Cnt_M_lgc	1
CWEOTPosBefReset_HwDeg_M_f32	1440.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1440.11
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	1
WriteLTCompValAftRst_Cnt_M_lgc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1440.10999	1440.11 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	0	0 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1	1	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Case 2: Boundary Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS2.1  1650.00 Cycles
TS2.2  1922.00 Cycles
TS2.3  1650.00 Cycles
TS2.4  1922.00 Cycles
TS2.5  1650.00 Cycles
TS2.6  1922.00 Cycles
TS2.7  1650.00 Cycles
TS2.8  1922.00 Cycles
TS2.9  1671.00 Cycles
TS2.10  1901.00 Cycles
TS2.11  1671.00 Cycles
TS2.12  1901.00 Cycles
TS2.13  1671.00 Cycles
TS2.14  1901.00 Cycles
TS2.15  1901.00 Cycles
TS2.16  1671.00 Cycles
```

Description

Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 WriteLTCompValAftRst_Cnt_M_lgc =Min
TS2.4 WriteLTCompValAftRst_Cnt_M_lgc=Max
TS2.5 WriteEOTValAftRst_Cnt_M_lgc=Min
TS2.6 WriteEOTValAftRst_Cnt_M_lgc=Max
TS2.7 CCWEOTFndBefReset_Cnt_M_lgc=Min
TS2.8 CCWEOTFndBefReset_Cnt_M_lgc=Max
TS2.9 CCWEOTPosBefReset_HwDeg_M_f32=Min
TS2.10 CCWEOTPosBefReset_HwDeg_M_f32=Max
TS2.11 CCWEOTPosBefReset_HwDeg_M_f32=Mid
TS2.12 CWEOTPosBefReset_HwDeg_M_f32=Min
TS2.13 CWEOTPosBefReset_HwDeg_M_f32=Max
TS2.14 CWEOTPosBefReset_HwDeg_M_f32=Mid
TS2.15 CWEOTFndBefReset_Cnt_M_lgc=Min
TS2.16 CWEOTFndBefReset_Cnt_M_lgc=Max

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-1440.11
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1440.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	0	0 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1440.10999	-1440.11 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.2 (Repeat Count = 1)

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	1
CCWEOTPosBefReset_HwDeg_M_f32	0
CWEOTFndBefReset_Cnt_M_lgc	1
CWEOTPosBefReset_HwDeg_M_f32	1440.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1440.11
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	1
WriteLTCompValAftRst_Cnt_M_lgc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1440.10999	1440.11 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	0	0 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Step 2.3 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	1
CCWEOTPosBefReset_HwDeg_M_f32	-1214.32
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	586.24
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.3
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1123.01
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.299988	682.3 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1123.01001	-1123.01 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-805.14
CWEOTFndBefReset_Cnt_M_lgc	1
CWEOTPosBefReset_HwDeg_M_f32	613.14
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	352.4
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-123.02
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0
WriteEOTValAftRst_Cnt_M_lgc	1
WriteLTCompValAftRst_Cnt_M_lgc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	613.140015	613.14 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-805.140015	-805.14 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Step 2.5 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	1
CCWEOTPosBefReset_HwDeg_M_f32	-20.2
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	1254.2
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1123.01
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-825.1
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1123.01001	1123.01 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-825.099976	-825.1 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.6 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-1120.01
CWEOTFndBefReset_Cnt_M_lgc	1
CWEOTPosBefReset_HwDeg_M_f32	123.02
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	123.02
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-682.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0
WriteEOTValAftRst_Cnt_M_lgc	1
WriteLTCompValAftRst_Cnt_M_lgc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	123.019997	123.02 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1120.01001	-1120.01 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Step 2.7 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-1321.2
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	825.1
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	825.1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-586.24
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	825.099976	825.1 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-586.23999	-586.24 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.8 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_Igc	1
CCWEOTPosBefReset_HwDeg_M_f32	-1214.2
CWEOTFndBefReset_Cnt_M_Igc	1
CWEOTPosBefReset_HwDeg_M_f32	682.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.11
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-613.14
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_Igc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_Igc	1
WriteEOTValAftRst_Cnt_M_Igc	1
WriteLTCompValAftRst_Cnt_M_Igc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.109985	682.11 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1214.19995	-1214.2 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_Igc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_Igc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Step 2.9 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_Igc	0
CCWEOTPosBefReset_HwDeg_M_f32	-1440.11
CWEOTFndBefReset_Cnt_M_Igc	0
CWEOTPosBefReset_HwDeg_M_f32	124.2
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	586.24
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1254.2
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_Igc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_Igc	1
WriteEOTValAftRst_Cnt_M_Igc	1
WriteLTCompValAftRst_Cnt_M_Igc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	124.199997	124.2 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1440.10999	-1440.11 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_Igc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_Igc	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.10 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_Igc	0
CCWEOTPosBefReset_HwDeg_M_f32	0
CWEOTFndBefReset_Cnt_M_Igc	1
CWEOTPosBefReset_HwDeg_M_f32	586.24
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	613.14
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1120.01
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_Igc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_Igc	0
WriteEOTValAftRst_Cnt_M_Igc	0
WriteLTCompValAftRst_Cnt_M_Igc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	613.140015	613.14 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1120.01001	-1120.01 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_Igc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_Igc	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Step 2.11 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-500.25
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	613.14
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1254.2
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1321.2
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0
WriteEOTValAftRst_Cnt_M_lgc	1
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	613.140015	613.14 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-500.25	-500.25 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.12 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	1
CCWEOTPosBefReset_HwDeg_M_f32	-123.02
CWEOTFndBefReset_Cnt_M_lgc	1
CWEOTPosBefReset_HwDeg_M_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	123.02
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-825.1
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	123.019997	123.02 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-825.099976	-825.1 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Step 2.13 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-1256.2
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	1440.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	825.1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-682.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0
WriteEOTValAftRst_Cnt_M_lgc	1
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1440.10999	1440.11 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1256.19995	-1256.2 ± 0.0625	✔

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 2.14 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	1
CCWEOTPosBefReset_HwDeg_M_f32	-566.2
CWEOTFndBefReset_Cnt_M_lgc	1
CWEOTPosBefReset_HwDeg_M_f32	450.23
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.11
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-123.02
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.109985	682.11 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-123.019997	-123.02 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Step 2.15 (Repeat Count = 1) ✔

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	1
CCWEOTPosBefReset_HwDeg_M_f32	-455.3
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	352.4
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	124.2
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1214.2
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	124.199997	124.2 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1214.19995	-1214.2 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

Test Step 2.16 (Repeat Count = 1)

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-754.021
CWEOTFndBefReset_Cnt_M_lgc	1
CWEOTPosBefReset_HwDeg_M_f32	1123.01
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.11
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-502.3
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	1
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1123.01001	1123.01 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-754.020996	-754.021 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Case 3: Path Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS3.1  1650.00 Cycles
TS3.2  1922.00 Cycles
```

Description

Vector Description:

TS3.1 (WriteLTCompValAftRst_Cnt_M_lgc == TRUE)=False,(WriteEOTValAftRst_Cnt_M_lgc == TRUE)=False
TS3.2 (WriteLTCompValAftRst_Cnt_M_lgc == TRUE)=True,(WriteEOTValAftRst_Cnt_M_lgc == TRUE)=True

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	1
CCWEOTPosBefReset_HwDeg_M_f32	-1214.32
CWEOTFndBefReset_Cnt_M_lgc	0
CWEOTPosBefReset_HwDeg_M_f32	586.24
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.3
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1123.01
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1
WriteEOTValAftRst_Cnt_M_lgc	0
WriteLTCompValAftRst_Cnt_M_lgc	0

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.299988	682.3 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1123.01001	-1123.01 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0	0	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1	1	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✔

Test Step 3.2 (Repeat Count = 1)

Name	Input Value
CCWEOTFndBefReset_Cnt_M_lgc	0
CCWEOTPosBefReset_HwDeg_M_f32	-805.14
CWEOTFndBefReset_Cnt_M_lgc	1
CWEOTPosBefReset_HwDeg_M_f32	613.14
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	352.4
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-123.02
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1

TEST DETAILS REPORT

CustPerSrvcs_Trns1

2015-04-13, 16:56:33+0530

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0
WriteEOTValAftRst_Cnt_M_lgc	1
WriteLTCompValAftRst_Cnt_M_lgc	1

Name	Actual Value	Expected Value	Result
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	613.140015	613.14 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-805.140015	-805.14 ± 0.0625	✔
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1	1	✔
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0	0	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_SetLTComp	1	ActivePull_SCom_SetLTComp	1	✔

TEST DETAILS REPORT

CustPerSrvcs_SCom_ReadActivePullParam

2015-04-13, 16:53:51+0530

Project	CustPerSrvcs
Module	CustPerSrvcs
Test Object	CustPerSrvcs_SCom_ReadActivePullParam

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CustPerSrvcs_C1xx
Configuration File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\CustPerSrvcs\src\Ap_CustPerSrvcs.c
Compiler Options	-I$(PROJECTROOT)\CustPerSrvcs\utp\contract -I$(PROJECTROOT)\CustPerSrvcs\utp\contract\Ap_CustPerSrvcs -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'CustPerSrvcs'	**********Unit Test Description********** Name of Tester:Spoorti Mali Code File(s) Under Test:Ap_CustPerSrvcs.c Code File(s) Version:2 Module Design Document:Customer_Periodic_Services_MDD.docx Module Design Document Version:2 Data Dictionary Version:1 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):394 Total RAM Used (Bytes):19 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:4/13/2015 Comments:"NOTE1: Inline function defined in globalmacro.h is not unittested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Workspace File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS1.1  494.00 Cycles
TS1.2  494.00 Cycles
TS1.3  494.00 Cycles
TS1.4  494.00 Cycles
TS1.5  494.00 Cycles
```

Description

Vector Description:

TS1.1 ActivePull_SCom_ReadParam=Min
TS1.2 ActivePull_SCom_ReadParam=Max
TS1.3 ActivePull_SCom_ReadParam=Zero
TS1.4 ActivePull_SCom_ReadParam=Pos
TS1.5 ActivePull_SCom_ReadParam=Neg

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
ActivePull_SCom_ReadParam(LTComp_HwNm_f32)	tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32
tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32	-4

Name	Actual Value	Expected Value	Result
LTCompValBefReset_HwNm_M_f32	-4	-4 ± 0.000488	✔
WriteLTCompValAftRst_Cnt_M_lgc	1	1	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_ReadParam	1	ActivePull_SCom_ReadParam	1	✔

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
ActivePull_SCom_ReadParam(LTComp_HwNm_f32)	tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32
tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32	4

Name	Actual Value	Expected Value	Result
LTCompValBefReset_HwNm_M_f32	4	4 ± 0.000488	✔
WriteLTCompValAftRst_Cnt_M_lgc	1	1	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_ReadParam	1	ActivePull_SCom_ReadParam	1	✔

Test Step 1.3 (Repeat Count = 1)

Name	Input Value
ActivePull_SCom_ReadParam(LTComp_HwNm_f32)	tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32
tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32	0

Name	Actual Value	Expected Value	Result
LTCompValBefReset_HwNm_M_f32	0	0 ± 0.000488	✔
WriteLTCompValAftRst_Cnt_M_lgc	1	1	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_ReadParam	1	ActivePull_SCom_ReadParam	1	✔

Test Step 1.4 (Repeat Count = 1)

Name	Input Value
ActivePull_SCom_ReadParam(LTComp_HwNm_f32)	tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32
tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32	2.1

Name	Actual Value	Expected Value	Result
LTCompValBefReset_HwNm_M_f32	2.0999999	2.1 ± 0.000488	✔
WriteLTCompValAftRst_Cnt_M_lgc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_ReadParam	1	ActivePull_SCom_ReadParam	1	✔

Test Step 1.5 (Repeat Count = 1) ✔

Name	Input Value
ActivePull_SCom_ReadParam(LTComp_HwNm_f32)	tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32
tgt_ActivePull_SCom_ReadParam_LTComp_HwNm_f32	-2.3

Name	Actual Value	Expected Value	Result
LTCompValBefReset_HwNm_M_f32	-2.29999995	-2.3 ± 0.000488	✔
WriteLTCompValAftRst_Cnt_M_lgc	1	1	✔

Test Step Call Trace ✔

Actual Function	Count	Expected Function	Count	Result
ActivePull_SCom_ReadParam	1	ActivePull_SCom_ReadParam	1	✔

TEST DETAILS REPORT

CustPerSrvcs_SCom_ResetThrmlCntr

2015-04-13, 16:55:14+0530

Project	CustPerSrvcs
Module	CustPerSrvcs
Test Object	CustPerSrvcs_SCom_ResetThrmlCntr

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CustPerSrvcs_C1xx
Configuration File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\CustPerSrvcs\src\Ap_CustPerSrvcs.c
Compiler Options	-I$(PROJECTROOT)\CustPerSrvcs\utp\contract -I$(PROJECTROOT)\CustPerSrvcs\utp\contract\Ap_CustPerSrvcs -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'CustPerSrvcs'	**********Unit Test Description********** Name of Tester:Spoorti Mali Code File(s) Under Test:Ap_CustPerSrvcs.c Code File(s) Version:2 Module Design Document:Customer_Periodic_Services_MDD.docx Module Design Document Version:2 Data Dictionary Version:1 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):394 Total RAM Used (Bytes):19 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:4/13/2015 Comments:"NOTE1: Inline function defined in globalmacro.h is not unittested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Workspace File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Bounadary Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS1.1  228.00 Cycles
```

Description

Vector Description:

TS1.1 Tested for Expected Output

Test Step 1.1 (Repeat Count = 1)

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔

TEST DETAILS REPORT

CustPerSrvcs_Init1

2015-04-13, 16:51:57+0530

Project	CustPerSrvcs
Module	CustPerSrvcs
Test Object	CustPerSrvcs_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CustPerSrvcs_C1xx
Configuration File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\CustPerSrvcs\src\Ap_CustPerSrvcs.c
Compiler Options	-I$(PROJECTROOT)\CustPerSrvcs\utp\contract -I$(PROJECTROOT)\CustPerSrvcs\utp\contract\Ap_CustPerSrvcs -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'CustPerSrvcs'	**********Unit Test Description********** Name of Tester:Spoorti Mali Code File(s) Under Test:Ap_CustPerSrvcs.c Code File(s) Version:2 Module Design Document:Customer_Periodic_Services_MDD.docx Module Design Document Version:2 Data Dictionary Version:1 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):394 Total RAM Used (Bytes):19 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:4/13/2015 Comments:"NOTE1: Inline function defined in globalmacro.h is not unittested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false

Attributes

Name	Value
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS1.1  940.00 Cycles
TS1.2  943.00 Cycles
```

Description

Vector Description:

TS1.1 Shortest Execution Path:

(ThermalLimitFlagClearCntr_Cnt_M_u08 >= D_MAXCLEARCOUNT_CNT_U08)=False

TS1.2 Longest Execution Path:

(ThermalLimitFlagClearCntr_Cnt_M_u08 >= D_MAXCLEARCOUNT_CNT_U08)=True

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	0
ThermalLimitFlagCntr_Cnt_M_u08	0

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	50
ThermalLimitFlagCntr_Cnt_M_u08	255

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔

Test Case 2: Boundary Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS2.1  940.00 Cycles
TS2.2  943.00 Cycles
TS2.3  940.00 Cycles
TS2.4  943.00 Cycles
TS2.5  940.00 Cycles
TS2.6  940.00 Cycles
TS2.7  940.00 Cycles
TS2.8  940.00 Cycles
```

Description

Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 ThermalLimitResetCntr_Cnt_M_u08=Min
TS2.4 ThermalLimitResetCntr_Cnt_M_u08=Max
TS2.5 ThermalLimitResetCntr_Cnt_M_u08=Mid
TS2.6 ThermalLimitFlagCntr_Cnt_M_u08=Min
TS2.7 ThermalLimitFlagCntr_Cnt_M_u08=Max
TS2.8 ThermalLimitFlagCntr_Cnt_M_u08=Mid

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	0
ThermalLimitFlagCntr_Cnt_M_u08	0

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔

Test Step 2.2 (Repeat Count = 1)

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	50

Name	Input Value
ThermalLimitFlagCntr_Cnt_M_u08	255

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔

Test Step 2.3 (Repeat Count = 1) ✔

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	0
ThermalLimitFlagCntr_Cnt_M_u08	125

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	125	125	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	50
ThermalLimitFlagCntr_Cnt_M_u08	185

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔

Test Step 2.5 (Repeat Count = 1) ✔

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	24
ThermalLimitFlagCntr_Cnt_M_u08	63

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	25	25	✔
ThermalLimitFlagCntr_Cnt_M_u08	63	63	✔

Test Step 2.6 (Repeat Count = 1) ✔

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	14
ThermalLimitFlagCntr_Cnt_M_u08	0

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	15	15	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔

Test Step 2.7 (Repeat Count = 1) ✔

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	18
ThermalLimitFlagCntr_Cnt_M_u08	255

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	19	19	✔
ThermalLimitFlagCntr_Cnt_M_u08	255	255	✔

Test Step 2.8 (Repeat Count = 1) ✔

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	32
ThermalLimitFlagCntr_Cnt_M_u08	124

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	33	33	✔
ThermalLimitFlagCntr_Cnt_M_u08	124	124	✔

Test Case 3: Path Test

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS3.1  940.00 Cycles
TS3.2  943.00 Cycles
```

Description

Vector Description:

TS3.1 (ThermalLimitFlagClearCntr_Cnt_M_u08 >= D_MAXCLEARCOUNT_CNT_U08)=False
TS3.2 (ThermalLimitFlagClearCntr_Cnt_M_u08 >= D_MAXCLEARCOUNT_CNT_U08)=True

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	0
ThermalLimitFlagCntr_Cnt_M_u08	125

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	1	1	✔
ThermalLimitFlagCntr_Cnt_M_u08	125	125	✔

Test Step 3.2 (Repeat Count = 1)

Name	Input Value
ThermalLimitFlagClearCntr_Cnt_M_u08	50
ThermalLimitFlagCntr_Cnt_M_u08	185

Name	Actual Value	Expected Value	Result
ThermalLimitFlagClearCntr_Cnt_M_u08	0	0	✔
ThermalLimitFlagCntr_Cnt_M_u08	0	0	✔

TEST DETAILS REPORT

CustPerSrvcs_SCom_ReadLrnEOTParam

2015-04-13, 16:54:41+0530

Project	CustPerSrvcs
Module	CustPerSrvcs
Test Object	CustPerSrvcs_SCom_ReadLrnEOTParam

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CustPerSrvcs_C1xx
Configuration File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\CustPerSrvcs\src\Ap_CustPerSrvcs.c
Compiler Options	-I$(PROJECTROOT)\CustPerSrvcs\utp\contract -I$(PROJECTROOT)\CustPerSrvcs\utp\contract\Ap_CustPerSrvcs -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'CustPerSrvcs'	**********Unit Test Description********** Name of Tester:Spoorti Mali Code File(s) Under Test:Ap_CustPerSrvcs.c Code File(s) Version:2 Module Design Document:Customer_Periodic_Services_MDD.docx Module Design Document Version:2 Data Dictionary Version:1 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):394 Total RAM Used (Bytes):19 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:4/13/2015 Comments:"NOTE1: Inline function defined in globalmacro.h is not unittested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **********

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Workspace File	D:\Synergy_Work_Area\CustPerSrvcs_C1xx\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test ✔

Specification

```
Performance Metrics:
(With "None"  Instrumentation and "WithPS"
Enviroment)

CPU Cycles:

TS1.1  246.00 Cycles
TS1.2  246.00 Cycles
TS1.3  246.00 Cycles
TS1.4  246.00 Cycles
TS1.5  246.00 Cycles
TS1.6  246.00 Cycles
TS1.7  246.00 Cycles
TS1.8  246.00 Cycles
TS1.9  246.00 Cycles
TS1.10  246.00 Cycles
TS1.11  246.00 Cycles
TS1.12  246.00 Cycles
```

Description

Vector Description:

TS1.1 All Min
TS1.2 All Max
TS1.3 LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc==>Min
TS1.4 LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc==>Max
TS1.5 LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32==>Min
TS1.6 LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32==>Max
TS1.7 LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32==>Mid
TS1.8 LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc==>Min
TS1.9 LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc==>Max
TS1.10 LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32==>Min
TS1.11 LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32==>Max
TS1.12 LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32==>Mid

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1440.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CCWEOTPosBefReset_HwDeg_M_f32	-1440.10999	-1440.11 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CWEOTPosBefReset_HwDeg_M_f32	0	0 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1440.11
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CCWEOTPosBefReset_HwDeg_M_f32	0	0 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CWEOTPosBefReset_HwDeg_M_f32	1440.10999	1440.11 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.3 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1123.01
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-700.02
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CCWEOTPosBefReset_HwDeg_M_f32	-700.02002	-700.02 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CWEOTPosBefReset_HwDeg_M_f32	1123.01001	1123.01 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.4 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	123.02
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-850.12
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CCWEOTPosBefReset_HwDeg_M_f32	-850.119995	-850.12 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CWEOTPosBefReset_HwDeg_M_f32	123.019997	123.02 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.5 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	825.1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1440.11
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CCWEOTPosBefReset_HwDeg_M_f32	-1440.10999	-1440.11 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CWEOTPosBefReset_HwDeg_M_f32	825.099976	825.1 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.6 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	682.11
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CCWEOTPosBefReset_HwDeg_M_f32	0	0 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CWEOTPosBefReset_HwDeg_M_f32	682.109985	682.11 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.7 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	586.24
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1225.3
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CCWEOTPosBefReset_HwDeg_M_f32	-1225.30005	-1225.3 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CWEOTPosBefReset_HwDeg_M_f32	586.23999	586.24 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.8 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	613.14
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-450.12
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CCWEOTPosBefReset_HwDeg_M_f32	-450.119995	-450.12 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	0	0	✔

Name	Actual Value	Expected Value	Result
CWEOTPosBefReset_HwDeg_M_f32	613.140015	613.14 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.9 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1254.2
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-960.14
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CCWEOTPosBefReset_HwDeg_M_f32	-960.140015	-960.14 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CWEOTPosBefReset_HwDeg_M_f32	1254.19995	1254.2 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.10 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1230.04
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CCWEOTPosBefReset_HwDeg_M_f32	-1230.04004	-1230.04 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CWEOTPosBefReset_HwDeg_M_f32	0	0 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.11 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1440.11
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1426.3
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	0
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	0

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CCWEOTPosBefReset_HwDeg_M_f32	-1426.30005	-1426.3 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	0	0	✔
CWEOTPosBefReset_HwDeg_M_f32	1440.10999	1440.11 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔

Test Step 1.12 (Repeat Count = 1) ✔

Name	Input Value
Rte_Ap_LrnEOT_LearnedEOT.CWEOTPosition_HwDeg_f32	1155.3
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTPosition_HwDeg_f32	-1263.2
Rte_Ap_LrnEOT_LearnedEOT.CWEOTFound_Cnt_lgc	1
Rte_Ap_LrnEOT_LearnedEOT.CCWEOTFound_Cnt_lgc	1

Name	Actual Value	Expected Value	Result
CCWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CCWEOTPosBefReset_HwDeg_M_f32	-1263.19995	-1263.2 ± 0.0625	✔
CWEOTFndBefReset_Cnt_M_lgc	1	1	✔
CWEOTPosBefReset_HwDeg_M_f32	1155.30005	1155.3 ± 0.0625	✔
WriteEOTValAftRst_Cnt_M_lgc	1	1	✔