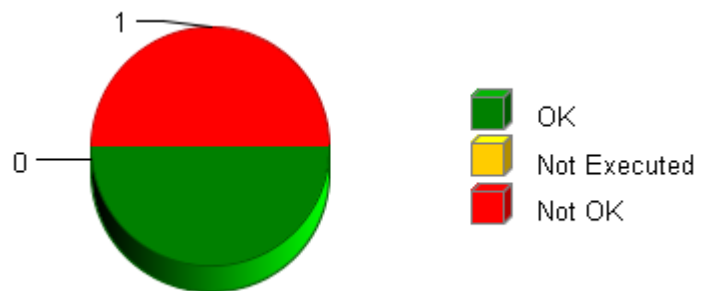


Summary

Total Test Objects: 2
Successful: 1
Failed: 1
Not Executed: 0
Date: 2014-09-16
Time: 12:22:24+0530

Overall Test Object Results (including Coverage)



Selected Project Items

Test Object "CBD_UnitTest/TrqArblim/TrqArblim_Init"
Test Object "CBD_UnitTest/TrqArblim/TrqArblim_Per1"

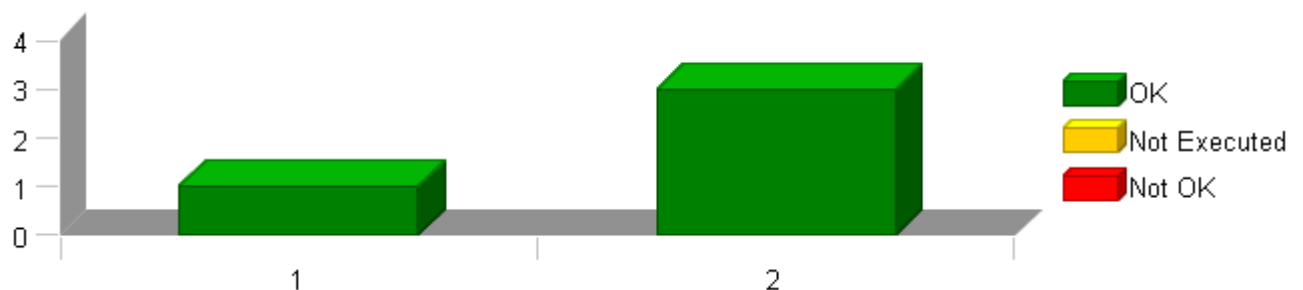
Used Test Environments

TI TMS 570 PLS UDE (Default)

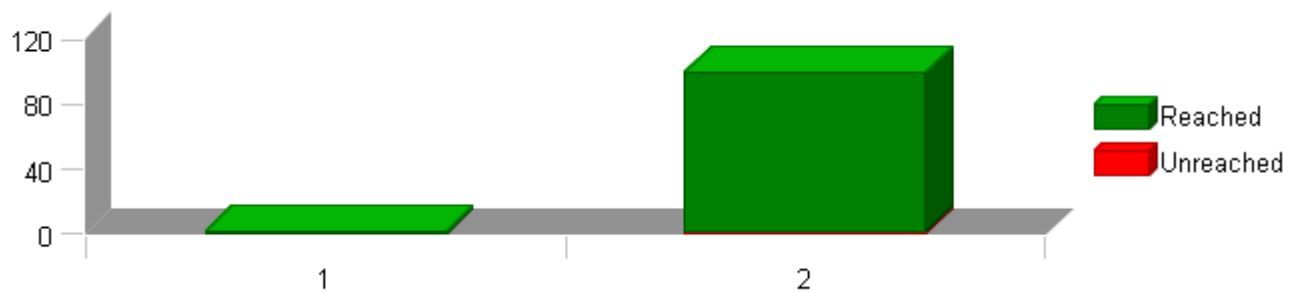
Batch Operation Settings

Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No
Instrumentation: Test Object Only
Coverage: Statement Coverage, Branch Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

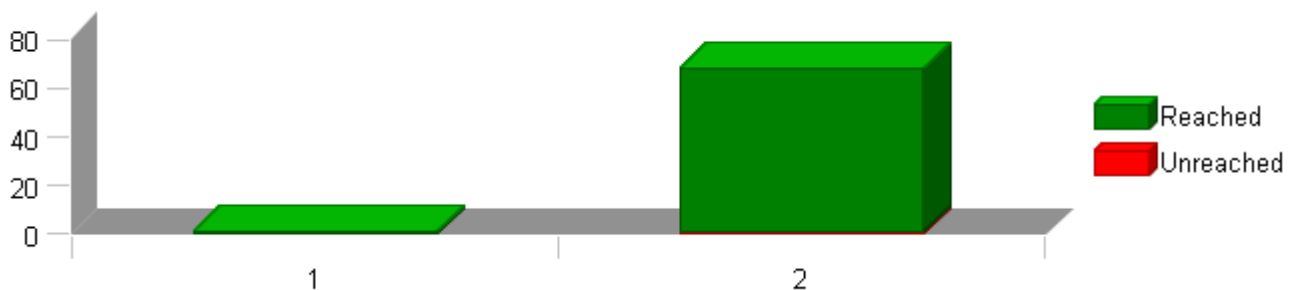
Test Case Results for Each Test Object (without Coverage)



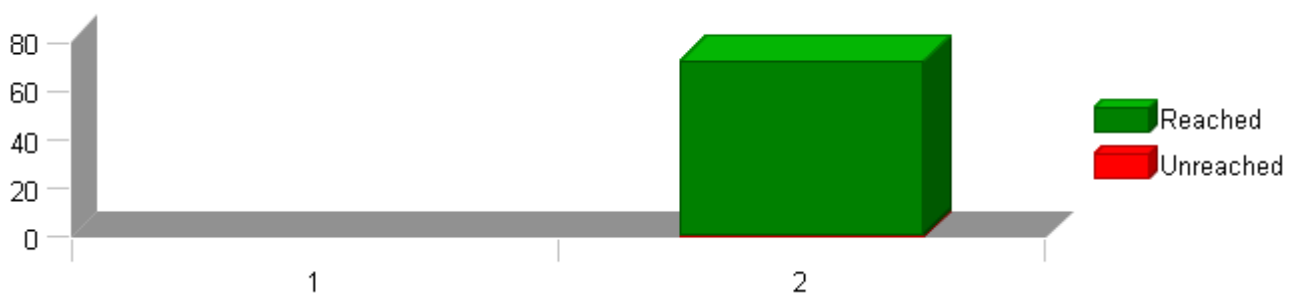
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object

The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object

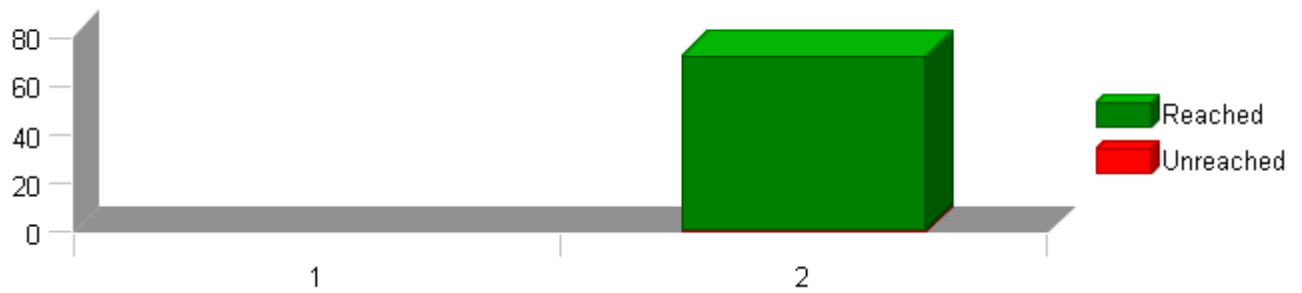
The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

MC/DC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

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Project TrqArblim



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	MC/DC	MCC	Test Cases	Result
	TrqArblim	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed	✘
	CBD_UnitTest	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed	✘
	TrqArblim	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed	✘
1	TrqArblim_Init	100 %	100 %	-	-	1 of 1 passed	✔
2	TrqArblim_Per1	99 %	98.52 %	98.61 %	98.61 %	3 of 3 passed	✘

TEST DETAILS REPORT

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TrqArblim_Per1

Project	TrqArblim
Module	TrqArblim
Test Object	TrqArblim_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	99 %
Branch (C1) Coverage	98.52 %
MCC Coverage	98.61 %
MC/DC Coverage	98.61 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_TrqArblim
Configuration File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqArblim\src\Ap_TrqArblim.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_OFF -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utpl\contract \Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utpl\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_OFF -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utpl\contract \Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utpl\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'TrqArblim'	*****Unit Test Report***** Name of Tester:Priti Mangalekar Code File(s) Under Test: Ap_TrqArblim.c Code File(s) Version:5 Module Design Document:TrqArblim_MDD.docx Module Design Document Version:6 Data Dictionary Version:5 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):1456 Total RAM Used (Bytes):33 Total CALS Used (Bytes):104 Special Test Requirements: Test Date:9/16/2014 Comments: Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested. Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 3:In function ""TrqArblim_Per1"" path ""(D_ONE_ULS_F32 <= ((D_2MS_SEC_F32 * APALowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" cannot be covered as ""((D_2MS_SEC_F32 * APALowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32)"" always gets updated with a value less than 1." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

TEST DETAILS REPORT

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TrqArblim_Per1



Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Time Enabled	false
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TEST DETAILS REPORT

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TrqArblim_Per1

Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS1.1 2665.00 Cycles TS1.2 2522.00 Cycles
Description	Vector Description: TS 1.1Shortest execution Path: (APALowDisableRate_pSec_T_f32 >= -0.2F)=True,(PosServEnable_Cnt_T_lgc == TRUE)=False,(GMOSHOscillate_State_T_enum == Haptic_Active)=False,(k_APAUseAsstScale_lgc == TRUE)=True,(k_APAUseRetScale_lgc == TRUE)=True,(k_APAUseADmpScale_lgc == TRUE)=True,((LKA_State_T_enum != LKA_Active) ((GMOSHOscillate_State_T_enum != Haptic_Inactive) (PosServEnable_Cnt_T_lgc == TRUE)))=False,(k_LKAUseSlewCal_lgc == FALSE)=False,(FinalSlew_NmpSec_T_f32 < D_FINALSLEWLMT_NMPSEC_F32)=False,(Abs_f32_m(ESCCmd_HwNm_T_f32) > k_ESCMax_HwNm_f32)=False,((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active) ((TrqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=False,"" TS1.2 Longest Execution Path: (APALowDisableRate_pSec_T_f32 >= -0.2F)=False,(PosServEnable_Cnt_T_lgc == TRUE)=false,(GMOSHOscillate_State_T_enum == Haptic_Active)=True,(k_APAUseAsstScale_lgc == TRUE)=False,(k_APAUseRetScale_lgc == TRUE)=false,(k_APAUseADmpScale_lgc == TRUE)=False,((LKA_State_T_enum != LKA_Active) ((GMOSHOscillate_State_T_enum != Haptic_Inactive) (PosServEnable_Cnt_T_lgc == TRUE)))=True,(k_LKAUseSlewCal_lgc == FALSE)=True,(FinalSlew_NmpSec_T_f32 < D_FINALSLEWLMT_NMPSEC_F32)=False,((ESC_State_T_enum != ESC_Active)&&(ESC_State_T_enum != ESC_TemporarilyLimited))=False,(Abs_f32_m(ESCCmd_HwNm_T_f32) > k_ESCMax_HwNm_f32)=true,((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active) (TrqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=True"

Test Case 2: Path Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS2.1 2392.00 Cycles TS2.2 2725.00 Cycles TS2.3 2732.00 Cycles TS2.4 2423.00 Cycles TS2.5 2459.00 Cycles TS2.6 2309.00 Cycles TS2.7 2608.00 Cycles TS2.8 2472.00 Cycles TS2.9 2301.00 Cycles TS2.10 2425.00 Cycles TS2.11 2457.00 Cycles TS2.12 2239.00 Cycles TS2.13 2374.00 Cycles TS2.14 2425.00 Cycles
Description	Vector Description: TS2.1""(APALowDisableRate_pSec_T_f32 >= -0.2F)=False,((LKA_State_T_enum != LKA_Active)=True ((GMOSHOscillate_State_T_enum != Haptic_Inactive) (PosServEnable_Cnt_T_lgc == TRUE))),((k_LKAUseSlewCal_lgc == FALSE)=True" TS2.2(k_APAUseAsstScale_lgc == TRUE)=True,(k_APAUseRetScale_lgc == TRUE)=True,(k_APAUseADmpScale_lgc == TRUE)=True TS2.3(PosServEnable_Cnt_T_lgc == TRUE)=True,(GMOSHOscillate_State_T_enum == Haptic_Active)=True TS2.4""(LKA_State_T_enum != LKA_Active) =False ((GMOSHOscillate_State_T_enum != Haptic_Inactive)=False (PosServEnable_Cnt_T_lgc == TRUE)))=True" TS2.5OpTrqOv_MtrNm_T_f32 = Limit_m(OpTrqOv_MtrNm_T_f32,-D_OPTRQOVLMT_MTRNM_F32,D_OPTRQOVLMT_MTRNM_F32)=False TS2.6LKATrqReq_HwNm_T_f32 = Limit_m(LKADesired_HwNm_T_f32, ((D_2MS_SEC_F32 * -FinalSlew_NmpSec_T_f32) + TrqArblim_PreLKATrqReq_HwNm_M_f32), ((D_2MS_SEC_F32 * FinalSlew_NmpSec_T_f32)+ TrqArblim_PreLKATrqReq_HwNm_M_f32))=True TS2.7LKADesired_HwNm_T_f32 = Limit_m(LKACmd_HwNm_T_f32, -D_DSRTQLMT_HWNM_F32,D_DSRTQLMT_HWNM_F32)=False TS2.8(APALowDisableRate_pSec_T_f32 >= -0.2F)=True TS2.9((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active) ((TrqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=True TS2.10((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active) ((TrqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=False TS2.11((ESC_State_T_enum != ESC_Active)&&(ESC_State_T_enum != ESC_TemporarilyLimited))=True TS2.12APADisableRate_Uls_T_f32 = Limit_m(D_ZERO_ULS_F32, ((D_2MS_SEC_F32 * APALowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32), ((D_2MS_SEC_F32 * k_APAEnableRate_pSec_f32)+ TrqArblim_APADisableRate_Uls_M_f32))=False TS2.13APADisableRate_Uls_T_f32 = Limit_m(D_ONE_ULS_F32, ((D_2MS_SEC_F32 * APALowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32), ((D_2MS_SEC_F32 * k_APAEnableRate_pSec_f32)+ TrqArblim_APADisableRate_Uls_M_f32))=True TS2.14(Abs_f32_m(ESCCmd_HwNm_T_f32) > k_ESCMax_HwNm_f32)=False

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.00999999978
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85999966
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-10.2200003
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.60000038
k_APAEnableRate_pSec_f32	1.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	2.24000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	0

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[1]	410		
t_APADisableRateX_HwNm_u4p12[2]	819		
t_APADisableRateX_HwNm_u4p12[3]	1229		
t_APADisableRateX_HwNm_u4p12[4]	1638		
t_APADisableRateX_HwNm_u4p12[5]	2048		
t_APADisableRateY_pSec_u7p9[0]	0		
t_APADisableRateY_pSec_u7p9[1]	512		
t_APADisableRateY_pSec_u7p9[2]	1024		
t_APADisableRateY_pSec_u7p9[3]	1536		
t_APADisableRateY_pSec_u7p9[4]	2048		
t_APADisableRateY_pSec_u7p9[5]	2560		
t_APASmoothX_Uls_u2p14[0]	0		
t_APASmoothX_Uls_u2p14[1]	164		
t_APASmoothX_Uls_u2p14[2]	328		
t_APASmoothX_Uls_u2p14[3]	492		
t_APASmoothX_Uls_u2p14[4]	655		
t_APASmoothX_Uls_u2p14[5]	819		
t_APASmoothX_Uls_u2p14[6]	983		
t_APASmoothX_Uls_u2p14[7]	1147		
t_APASmoothX_Uls_u2p14[8]	1311		
t_APASmoothX_Uls_u2p14[9]	1475		
t_APASmoothY_Uls_u2p14[0]	12780		
t_APASmoothY_Uls_u2p14[1]	12943		
t_APASmoothY_Uls_u2p14[2]	13107		
t_APASmoothY_Uls_u2p14[3]	13271		
t_APASmoothY_Uls_u2p14[4]	13435		
t_APASmoothY_Uls_u2p14[5]	13599		
t_APASmoothY_Uls_u2p14[6]	13763		
t_APASmoothY_Uls_u2p14[7]	13926		
t_APASmoothY_Uls_u2p14[8]	14090		
t_APASmoothY_Uls_u2p14[9]	14254		
t_LKASlewX_Kph_u8p8[0]	0		
t_LKASlewX_Kph_u8p8[1]	512		
t_LKASlewX_Kph_u8p8[2]	1024		
t_LKASlewX_Kph_u8p8[3]	1536		
t_LKASlewX_Kph_u8p8[4]	2048		
t_LKASlewX_Kph_u8p8[5]	2560		
t_LKASlewY_NmpSec_u4p12[0]	0		
t_LKASlewY_NmpSec_u4p12[1]	410		
t_LKASlewY_NmpSec_u4p12[2]	819		
t_LKASlewY_NmpSec_u4p12[3]	1229		
t_LKASlewY_NmpSec_u4p12[4]	1638		
t_LKASlewY_NmpSec_u4p12[5]	2048		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCoscillate_State_enum	target_TrqArblim_Per1_GMOSHOCoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.82000017		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOCoscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.64000034		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.23999998		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	2		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.209999993		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	5.90000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	✓

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85956001	-9.85956001 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-10.2200003	-10.2200003 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.5732336	-8.57322979 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.2 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.023
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.004199999985
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-9.02000046
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038
k_APAEnableRate_pSec_f32	6
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.8499999
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	410
t_APADisableRateX_HwNm_u4p12[1]	819
t_APADisableRateX_HwNm_u4p12[2]	1229
t_APADisableRateX_HwNm_u4p12[3]	1638
t_APADisableRateX_HwNm_u4p12[4]	2048
t_APADisableRateX_HwNm_u4p12[5]	2458
t_APADisableRateY_pSec_u7p9[0]	512
t_APADisableRateY_pSec_u7p9[1]	1024
t_APADisableRateY_pSec_u7p9[2]	1536
t_APADisableRateY_pSec_u7p9[3]	2048
t_APADisableRateY_pSec_u7p9[4]	2560
t_APADisableRateY_pSec_u7p9[5]	3072
t_APASmoothX_Uls_u2p14[0]	164
t_APASmoothX_Uls_u2p14[1]	328
t_APASmoothX_Uls_u2p14[2]	492
t_APASmoothX_Uls_u2p14[3]	655
t_APASmoothX_Uls_u2p14[4]	819
t_APASmoothX_Uls_u2p14[5]	983
t_APASmoothX_Uls_u2p14[6]	1147
t_APASmoothX_Uls_u2p14[7]	1311
t_APASmoothX_Uls_u2p14[8]	1475
t_APASmoothX_Uls_u2p14[9]	1638
t_APASmoothY_Uls_u2p14[0]	12943
t_APASmoothY_Uls_u2p14[1]	13107
t_APASmoothY_Uls_u2p14[2]	13271
t_APASmoothY_Uls_u2p14[3]	13435
t_APASmoothY_Uls_u2p14[4]	13599
t_APASmoothY_Uls_u2p14[5]	13763
t_APASmoothY_Uls_u2p14[6]	13926
t_APASmoothY_Uls_u2p14[7]	14090
t_APASmoothY_Uls_u2p14[8]	14254
t_APASmoothY_Uls_u2p14[9]	14418
t_LKASlewX_Kph_u8p8[0]	512
t_LKASlewX_Kph_u8p8[1]	1024
t_LKASlewX_Kph_u8p8[2]	1536
t_LKASlewX_Kph_u8p8[3]	2048

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TrqArblim_Per1

Name	Input Value		
t_LKASlewX_Kph_u8p8[4]	2560		
t_LKASlewX_Kph_u8p8[5]	3072		
t_LKASlewY_NmpSec_u4p12[0]	410		
t_LKASlewY_NmpSec_u4p12[1]	819		
t_LKASlewY_NmpSec_u4p12[2]	1229		
t_LKASlewY_NmpSec_u4p12[3]	1638		
t_LKASlewY_NmpSec_u4p12[4]	2048		
t_LKASlewY_NmpSec_u4p12[5]	2458		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.22999954		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	5.86000013		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	0.560000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0109999999	0.0109999999 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.24571609	-9.24571991 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-9.02000046	-9.02000046 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.09400082	-8.09399986 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4.8499999	-4.8499999 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0784785151	-0.0784785002 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0359999985
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.64000034
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-7.82000017
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.5999999
k_APAEnableRate_pSec_f32	10.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1

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TrqArblim_Per1

Name	Input Value
k_ESCMax_HwNm_f32	3.30999994
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	819
t_APADisableRateX_HwNm_u4p12[1]	1229
t_APADisableRateX_HwNm_u4p12[2]	1638
t_APADisableRateX_HwNm_u4p12[3]	2048
t_APADisableRateX_HwNm_u4p12[4]	2458
t_APADisableRateX_HwNm_u4p12[5]	2867
t_APADisableRateY_pSec_u7p9[0]	1024
t_APADisableRateY_pSec_u7p9[1]	1536
t_APADisableRateY_pSec_u7p9[2]	2048
t_APADisableRateY_pSec_u7p9[3]	2560
t_APADisableRateY_pSec_u7p9[4]	3072
t_APADisableRateY_pSec_u7p9[5]	3584
t_APASmoothX_Uls_u2p14[0]	328
t_APASmoothX_Uls_u2p14[1]	492
t_APASmoothX_Uls_u2p14[2]	655
t_APASmoothX_Uls_u2p14[3]	819
t_APASmoothX_Uls_u2p14[4]	983
t_APASmoothX_Uls_u2p14[5]	1147
t_APASmoothX_Uls_u2p14[6]	1311
t_APASmoothX_Uls_u2p14[7]	1475
t_APASmoothX_Uls_u2p14[8]	1638
t_APASmoothX_Uls_u2p14[9]	1802
t_APASmoothY_Uls_u2p14[0]	13107
t_APASmoothY_Uls_u2p14[1]	13271
t_APASmoothY_Uls_u2p14[2]	13435
t_APASmoothY_Uls_u2p14[3]	13599
t_APASmoothY_Uls_u2p14[4]	13763
t_APASmoothY_Uls_u2p14[5]	13926
t_APASmoothY_Uls_u2p14[6]	14090
t_APASmoothY_Uls_u2p14[7]	14254
t_APASmoothY_Uls_u2p14[8]	14418
t_APASmoothY_Uls_u2p14[9]	14582
t_LKASlewX_Kph_u8p8[0]	1024
t_LKASlewX_Kph_u8p8[1]	1536
t_LKASlewX_Kph_u8p8[2]	2048
t_LKASlewX_Kph_u8p8[3]	2560
t_LKASlewX_Kph_u8p8[4]	3072
t_LKASlewX_Kph_u8p8[5]	3584
t_LKASlewY_NmpSec_u4p12[0]	819
t_LKASlewY_NmpSec_u4p12[1]	1229
t_LKASlewY_NmpSec_u4p12[2]	1638
t_LKASlewY_NmpSec_u4p12[3]	2048
t_LKASlewY_NmpSec_u4p12[4]	2458
t_LKASlewY_NmpSec_u4p12[5]	2867
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.42000008
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.82000017
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	9.43000031
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.910000026

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.057	0.057 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.62835217	-8.62835026 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.910000026	0.910000026 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-7.59399986	-7.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.30999994	-3.30999994 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	6.34341431	6.34341002 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.4 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.101000004
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.82000005
TrqArblim_PrelKATrqReq_HwNm_M_f32	-5.0999999
k_APAEnableRate_pSec_f32	33
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.337000012
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	2867
t_APADisableRateX_HwNm_u4p12[1]	3277
t_APADisableRateX_HwNm_u4p12[2]	3686
t_APADisableRateX_HwNm_u4p12[3]	4096
t_APADisableRateX_HwNm_u4p12[4]	4506
t_APADisableRateX_HwNm_u4p12[5]	4915
t_APADisableRateY_pSec_u7p9[0]	3584
t_APADisableRateY_pSec_u7p9[1]	4096
t_APADisableRateY_pSec_u7p9[2]	4608
t_APADisableRateY_pSec_u7p9[3]	5120
t_APADisableRateY_pSec_u7p9[4]	5632
t_APADisableRateY_pSec_u7p9[5]	6144
t_APASmoothX_Uls_u2p14[0]	1147
t_APASmoothX_Uls_u2p14[1]	1311
t_APASmoothX_Uls_u2p14[2]	1475
t_APASmoothX_Uls_u2p14[3]	1638
t_APASmoothX_Uls_u2p14[4]	1802
t_APASmoothX_Uls_u2p14[5]	1966
t_APASmoothX_Uls_u2p14[6]	2130
t_APASmoothX_Uls_u2p14[7]	2294
t_APASmoothX_Uls_u2p14[8]	2458
t_APASmoothX_Uls_u2p14[9]	2621
t_APASmoothY_Uls_u2p14[0]	13926
t_APASmoothY_Uls_u2p14[1]	14090
t_APASmoothY_Uls_u2p14[2]	14254
t_APASmoothY_Uls_u2p14[3]	14418
t_APASmoothY_Uls_u2p14[4]	14582
t_APASmoothY_Uls_u2p14[5]	14746
t_APASmoothY_Uls_u2p14[6]	14909
t_APASmoothY_Uls_u2p14[7]	15073
t_APASmoothY_Uls_u2p14[8]	15237
t_APASmoothY_Uls_u2p14[9]	15401
t_LKASlewX_Kph_u8p8[0]	3584

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[1]	4096
t_LKASlewX_Kph_u8p8[2]	4608
t_LKASlewX_Kph_u8p8[3]	5120
t_LKASlewX_Kph_u8p8[4]	5632
t_LKASlewX_Kph_u8p8[5]	6144
t_LKASlewY_NmpSec_u4p12[0]	2867
t_LKASlewY_NmpSec_u4p12[1]	3277
t_LKASlewY_NmpSec_u4p12[2]	3686
t_LKASlewY_NmpSec_u4p12[3]	4096
t_LKASlewY_NmpSec_u4p12[4]	4506
t_LKASlewY_NmpSec_u4p12[5]	4915
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.230000004
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.11999989
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	256.339996
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	2.66000009
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.30000019

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.166999996	0.166999996 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.48873234	-5.48872995 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	2.66000009	2.66000009 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.09399986	-5.09399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.337000012	0.337000012 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.337000012	0.337000012 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.417567909	0.417567998 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.152999997
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002
TrqArblim_PosSrvcCmd_MtrNm_M_f32	2.98000002
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.0999999
k_APAEnableRate_pSec_f32	51

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TrqArblim_Per1

Name	Input Value
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.20000005
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	4506
t_APADisableRateX_HwNm_u4p12[1]	4915
t_APADisableRateX_HwNm_u4p12[2]	5325
t_APADisableRateX_HwNm_u4p12[3]	5734
t_APADisableRateX_HwNm_u4p12[4]	6144
t_APADisableRateX_HwNm_u4p12[5]	6554
t_APADisableRateY_pSec_u7p9[0]	5632
t_APADisableRateY_pSec_u7p9[1]	6144
t_APADisableRateY_pSec_u7p9[2]	6656
t_APADisableRateY_pSec_u7p9[3]	7168
t_APADisableRateY_pSec_u7p9[4]	7680
t_APADisableRateY_pSec_u7p9[5]	8192
t_APASmoothX_Uls_u2p14[0]	1802
t_APASmoothX_Uls_u2p14[1]	1966
t_APASmoothX_Uls_u2p14[2]	2130
t_APASmoothX_Uls_u2p14[3]	2294
t_APASmoothX_Uls_u2p14[4]	2458
t_APASmoothX_Uls_u2p14[5]	2621
t_APASmoothX_Uls_u2p14[6]	2785
t_APASmoothX_Uls_u2p14[7]	2949
t_APASmoothX_Uls_u2p14[8]	3113
t_APASmoothX_Uls_u2p14[9]	3277
t_APASmoothY_Uls_u2p14[0]	14582
t_APASmoothY_Uls_u2p14[1]	14746
t_APASmoothY_Uls_u2p14[2]	14909
t_APASmoothY_Uls_u2p14[3]	15073
t_APASmoothY_Uls_u2p14[4]	15237
t_APASmoothY_Uls_u2p14[5]	15401
t_APASmoothY_Uls_u2p14[6]	15565
t_APASmoothY_Uls_u2p14[7]	15729
t_APASmoothY_Uls_u2p14[8]	15892
t_APASmoothY_Uls_u2p14[9]	16056
t_LKASlewX_Kph_u8p8[0]	5632
t_LKASlewX_Kph_u8p8[1]	6144
t_LKASlewX_Kph_u8p8[2]	6656
t_LKASlewX_Kph_u8p8[3]	7168
t_LKASlewX_Kph_u8p8[4]	7680
t_LKASlewX_Kph_u8p8[5]	8192
t_LKASlewY_NmpSec_u4p12[0]	4506
t_LKASlewY_NmpSec_u4p12[1]	4915
t_LKASlewY_NmpSec_u4p12[2]	5325
t_LKASlewY_NmpSec_u4p12[3]	5734
t_LKASlewY_NmpSec_u4p12[4]	6144
t_LKASlewY_NmpSec_u4p12[5]	6554
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.86999989
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.15999985
target_TrqArblim_Per1_LKASState_State_enum.value	0

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	38.2933006		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.10000038		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.254999995	0.254999995 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.91367602	-2.91368008 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.20000005	1.20000005 ± 0.000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	1.20000005	1.20000005 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.244000003
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.12
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0416000001
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-10
TrqArblim_PrelKATrqReq_HwNm_M_f32	0.400000006
k_APAEnableRate_pSec_f32	82.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	7373
t_APADisableRateX_HwNm_u4p12[1]	7782
t_APADisableRateX_HwNm_u4p12[2]	8192
t_APADisableRateX_HwNm_u4p12[3]	8602
t_APADisableRateX_HwNm_u4p12[4]	9011
t_APADisableRateX_HwNm_u4p12[5]	9421
t_APADisableRateY_pSec_u7p9[0]	9216
t_APADisableRateY_pSec_u7p9[1]	9728
t_APADisableRateY_pSec_u7p9[2]	10240
t_APADisableRateY_pSec_u7p9[3]	10752
t_APADisableRateY_pSec_u7p9[4]	11264
t_APADisableRateY_pSec_u7p9[5]	11776
t_APASmoothX_Uls_u2p14[0]	2949
t_APASmoothX_Uls_u2p14[1]	3113
t_APASmoothX_Uls_u2p14[2]	3277
t_APASmoothX_Uls_u2p14[3]	3441
t_APASmoothX_Uls_u2p14[4]	3604
t_APASmoothX_Uls_u2p14[5]	3768
t_APASmoothX_Uls_u2p14[6]	3932
t_APASmoothX_Uls_u2p14[7]	4096
t_APASmoothX_Uls_u2p14[8]	4260
t_APASmoothX_Uls_u2p14[9]	4424
t_APASmoothY_Uls_u2p14[0]	0
t_APASmoothY_Uls_u2p14[1]	164
t_APASmoothY_Uls_u2p14[2]	328
t_APASmoothY_Uls_u2p14[3]	492
t_APASmoothY_Uls_u2p14[4]	655
t_APASmoothY_Uls_u2p14[5]	819
t_APASmoothY_Uls_u2p14[6]	983
t_APASmoothY_Uls_u2p14[7]	1147

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TrqArblim_Per1

Name	Input Value
t_APASmoothY_Uls_u2p14[8]	1311
t_APASmoothY_Uls_u2p14[9]	1475
t_LKASlewX_Kph_u8p8[0]	9216
t_LKASlewX_Kph_u8p8[1]	9728
t_LKASlewX_Kph_u8p8[2]	10240
t_LKASlewX_Kph_u8p8[3]	10752
t_LKASlewX_Kph_u8p8[4]	11264
t_LKASlewX_Kph_u8p8[5]	11776
t_LKASlewY_NmpSec_u4p12[0]	7373
t_LKASlewY_NmpSec_u4p12[1]	7782
t_LKASlewY_NmpSec_u4p12[2]	8192
t_LKASlewY_NmpSec_u4p12[3]	8602
t_LKASlewY_NmpSec_u4p12[4]	9011
t_LKASlewY_NmpSec_u4p12[5]	9421
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSH Oscillate_State_enum	target_TrqArblim_Per1_GMOSH Oscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSH Oscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.34000015
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-2.73000002
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	83.4432983
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.70000005
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.409000009	0.409000009 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.29555202	1.29554999 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.70000005	-3.70000005 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	0.382734507	0.382735014 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.382734507	0.382734507 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.382734507	0.382734507 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.86376238	1.86375999 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.34799999
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592

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TrqArblim_Per1

Name	Input Value
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-6.15999985
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.4000001
k_APAEnableRate_pSec_f32	118.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	10650
t_APADisableRateX_HwNm_u4p12[1]	11059
t_APADisableRateX_HwNm_u4p12[2]	11469
t_APADisableRateX_HwNm_u4p12[3]	11878
t_APADisableRateX_HwNm_u4p12[4]	12288
t_APADisableRateX_HwNm_u4p12[5]	12698
t_APADisableRateY_pSec_u7p9[0]	13312
t_APADisableRateY_pSec_u7p9[1]	13824
t_APADisableRateY_pSec_u7p9[2]	14336
t_APADisableRateY_pSec_u7p9[3]	14848
t_APADisableRateY_pSec_u7p9[4]	15360
t_APADisableRateY_pSec_u7p9[5]	15872
t_APASmoothX_Uls_u2p14[0]	4260
t_APASmoothX_Uls_u2p14[1]	4424
t_APASmoothX_Uls_u2p14[2]	4588
t_APASmoothX_Uls_u2p14[3]	4751
t_APASmoothX_Uls_u2p14[4]	4915
t_APASmoothX_Uls_u2p14[5]	5079
t_APASmoothX_Uls_u2p14[6]	5243
t_APASmoothX_Uls_u2p14[7]	5407
t_APASmoothX_Uls_u2p14[8]	5571
t_APASmoothX_Uls_u2p14[9]	5734
t_APASmoothY_Uls_u2p14[0]	1311
t_APASmoothY_Uls_u2p14[1]	1475
t_APASmoothY_Uls_u2p14[2]	1638
t_APASmoothY_Uls_u2p14[3]	1802
t_APASmoothY_Uls_u2p14[4]	1966
t_APASmoothY_Uls_u2p14[5]	2130
t_APASmoothY_Uls_u2p14[6]	2294
t_APASmoothY_Uls_u2p14[7]	2458
t_APASmoothY_Uls_u2p14[8]	2621
t_APASmoothY_Uls_u2p14[9]	2785
t_LKASlewX_Kph_u8p8[0]	13312
t_LKASlewX_Kph_u8p8[1]	13824
t_LKASlewX_Kph_u8p8[2]	14336
t_LKASlewX_Kph_u8p8[3]	14848
t_LKASlewX_Kph_u8p8[4]	15360
t_LKASlewX_Kph_u8p8[5]	15872
t_LKASlewY_NmpSec_u4p12[0]	10650
t_LKASlewY_NmpSec_u4p12[1]	11059
t_LKASlewY_NmpSec_u4p12[2]	11469
t_LKASlewY_NmpSec_u4p12[3]	11878
t_LKASlewY_NmpSec_u4p12[4]	12288
t_LKASlewY_NmpSec_u4p12[5]	12698
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.19999981
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHocillate_State_enum.value	0

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.59999999		
target_TrqArblim_Per1_LKASate_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	135.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.09999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.560000002		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.285999984	0.286000013 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.27776003	5.27776003 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985	-6.15999985 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.39379978	4.39379978 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.000390625	✔
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.186563313	-0.186563 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0.000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.774999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.386000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004
TrqArblim_PrelKATrqReq_HwNm_M_f32	2.59999999
k_APAEnableRate_pSec_f32	411.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	29491
t_APADisableRateX_HwNm_u4p12[1]	29901
t_APADisableRateX_HwNm_u4p12[2]	30310
t_APADisableRateX_HwNm_u4p12[3]	30720
t_APADisableRateX_HwNm_u4p12[4]	31130
t_APADisableRateX_HwNm_u4p12[5]	31539
t_APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	0
t_APADisableRateY_pSec_u7p9[2]	0
t_APADisableRateY_pSec_u7p9[3]	0
t_APADisableRateY_pSec_u7p9[4]	0
t_APADisableRateY_pSec_u7p9[5]	0
t_APASmoothX_Uls_u2p14[0]	12124
t_APASmoothX_Uls_u2p14[1]	12288
t_APASmoothX_Uls_u2p14[2]	12452
t_APASmoothX_Uls_u2p14[3]	12616
t_APASmoothX_Uls_u2p14[4]	12780
t_APASmoothX_Uls_u2p14[5]	12943
t_APASmoothX_Uls_u2p14[6]	13107
t_APASmoothX_Uls_u2p14[7]	13271
t_APASmoothX_Uls_u2p14[8]	13435
t_APASmoothX_Uls_u2p14[9]	13599
t_APASmoothY_Uls_u2p14[0]	9175
t_APASmoothY_Uls_u2p14[1]	9339
t_APASmoothY_Uls_u2p14[2]	9503
t_APASmoothY_Uls_u2p14[3]	9667
t_APASmoothY_Uls_u2p14[4]	9830

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TrqArblim_Per1

Name	Input Value
t_APASmoothY_Uls_u2p14[5]	9994
t_APASmoothY_Uls_u2p14[6]	10158
t_APASmoothY_Uls_u2p14[7]	10322
t_APASmoothY_Uls_u2p14[8]	10486
t_APASmoothY_Uls_u2p14[9]	10650
t_LKASlewX_Kph_u8p8[0]	36352
t_LKASlewX_Kph_u8p8[1]	36864
t_LKASlewX_Kph_u8p8[2]	37376
t_LKASlewX_Kph_u8p8[3]	37888
t_LKASlewX_Kph_u8p8[4]	38400
t_LKASlewX_Kph_u8p8[5]	38912
t_LKASlewY_NmpSec_u4p12[0]	29082
t_LKASlewY_NmpSec_u4p12[1]	29491
t_LKASlewY_NmpSec_u4p12[2]	29901
t_LKASlewY_NmpSec_u4p12[3]	30310
t_LKASlewY_NmpSec_u4p12[4]	30720
t_LKASlewY_NmpSec_u4p12[5]	31130
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.94999981
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	444.643005
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.29999995

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.774599969	0.774600029 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.72650003	-4.72650003 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59399986	2.5940001 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.47177887	4.47177982 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.200000003

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TrqArblim_Per1

Name	Input Value
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.92999983
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133000001
TrqArblim_PosSrvcCmd_MtrNm_M_f32	3.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	0
k_APAEnableRate_pSec_f32	231
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	20890
t_APADisableRateX_HwNm_u4p12[1]	21299
t_APADisableRateX_HwNm_u4p12[2]	21709
t_APADisableRateX_HwNm_u4p12[3]	22118
t_APADisableRateX_HwNm_u4p12[4]	22528
t_APADisableRateX_HwNm_u4p12[5]	22938
t_APADisableRateY_pSec_u7p9[0]	26112
t_APADisableRateY_pSec_u7p9[1]	26624
t_APADisableRateY_pSec_u7p9[2]	27136
t_APADisableRateY_pSec_u7p9[3]	27648
t_APADisableRateY_pSec_u7p9[4]	28160
t_APADisableRateY_pSec_u7p9[5]	28672
t_APASmoothX_Uls_u2p14[0]	8356
t_APASmoothX_Uls_u2p14[1]	8520
t_APASmoothX_Uls_u2p14[2]	8684
t_APASmoothX_Uls_u2p14[3]	8847
t_APASmoothX_Uls_u2p14[4]	9011
t_APASmoothX_Uls_u2p14[5]	9175
t_APASmoothX_Uls_u2p14[6]	9339
t_APASmoothX_Uls_u2p14[7]	9503
t_APASmoothX_Uls_u2p14[8]	9667
t_APASmoothX_Uls_u2p14[9]	9830
t_APASmoothY_Uls_u2p14[0]	5407
t_APASmoothY_Uls_u2p14[1]	5571
t_APASmoothY_Uls_u2p14[2]	5734
t_APASmoothY_Uls_u2p14[3]	5898
t_APASmoothY_Uls_u2p14[4]	6062
t_APASmoothY_Uls_u2p14[5]	6226
t_APASmoothY_Uls_u2p14[6]	6390
t_APASmoothY_Uls_u2p14[7]	6554
t_APASmoothY_Uls_u2p14[8]	6717
t_APASmoothY_Uls_u2p14[9]	6881
t_LKASlewX_Kph_u8p8[0]	26112
t_LKASlewX_Kph_u8p8[1]	26624
t_LKASlewX_Kph_u8p8[2]	27136
t_LKASlewX_Kph_u8p8[3]	27648
t_LKASlewX_Kph_u8p8[4]	28160
t_LKASlewX_Kph_u8p8[5]	28672
t_LKASlewY_NmpSec_u4p12[0]	20890
t_LKASlewY_NmpSec_u4p12[1]	21299
t_LKASlewY_NmpSec_u4p12[2]	21709
t_LKASlewY_NmpSec_u4p12[3]	22118
t_LKASlewY_NmpSec_u4p12[4]	22528
t_LKASlewY_NmpSec_u4p12[5]	22938
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_ESCcmd_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.05000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.12		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	296.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.662	0.662 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.07895947	8.07896042 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.9000001	2.9000001 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	0	0 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.75	-0.75 ± 0000390625	✔
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-0.75	-0.75 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.493716717	-0.493717015 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.375
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.30000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.209999993
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.0999999
k_APAEnableRate_pSec_f32	255.75
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	23757
t_APADisableRateX_HwNm_u4p12[1]	24166
t_APADisableRateX_HwNm_u4p12[2]	24576
t_APADisableRateX_HwNm_u4p12[3]	24986
t_APADisableRateX_HwNm_u4p12[4]	25395
t_APADisableRateX_HwNm_u4p12[5]	25805
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	9503
t_APASmoothX_Uls_u2p14[1]	9667
t_APASmoothX_Uls_u2p14[2]	9830
t_APASmoothX_Uls_u2p14[3]	9994
t_APASmoothX_Uls_u2p14[4]	10158
t_APASmoothX_Uls_u2p14[5]	10322
t_APASmoothX_Uls_u2p14[6]	10486
t_APASmoothX_Uls_u2p14[7]	10650
t_APASmoothX_Uls_u2p14[8]	10813
t_APASmoothX_Uls_u2p14[9]	10977
t_APASmoothY_Uls_u2p14[0]	6554
t_APASmoothY_Uls_u2p14[1]	6717

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[2]	6881		
t_APASmoothY_Uls_u2p14[3]	7045		
t_APASmoothY_Uls_u2p14[4]	7209		
t_APASmoothY_Uls_u2p14[5]	7373		
t_APASmoothY_Uls_u2p14[6]	7537		
t_APASmoothY_Uls_u2p14[7]	7700		
t_APASmoothY_Uls_u2p14[8]	7864		
t_APASmoothY_Uls_u2p14[9]	8028		
t_LKASlewX_Kph_u8p8[0]	29696		
t_LKASlewX_Kph_u8p8[1]	30208		
t_LKASlewX_Kph_u8p8[2]	30720		
t_LKASlewX_Kph_u8p8[3]	31232		
t_LKASlewX_Kph_u8p8[4]	31744		
t_LKASlewX_Kph_u8p8[5]	32256		
t_LKASlewY_NmpSec_u4p12[0]	23757		
t_LKASlewY_NmpSec_u4p12[1]	24166		
t_LKASlewY_NmpSec_u4p12[2]	24576		
t_LKASlewY_NmpSec_u4p12[3]	24986		
t_LKASlewY_NmpSec_u4p12[4]	25395		
t_LKASlewY_NmpSec_u4p12[5]	25805		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.45000005		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.34999999		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	341.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.100000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.258355469	0.248999998 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.83250046	-5.83249998 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017	5.07000017 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.08839989	-4.08739996 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.523976862	0.505002975 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 2.11 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.59999999
k_APAEnableRate_pSec_f32	470.25
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22938
t_APADisableRateX_HwNm_u4p12[1]	23347
t_APADisableRateX_HwNm_u4p12[2]	23757
t_APADisableRateX_HwNm_u4p12[3]	24166
t_APADisableRateX_HwNm_u4p12[4]	24576
t_APADisableRateX_HwNm_u4p12[5]	24986
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	10813
t_APASmoothX_Uls_u2p14[1]	10977
t_APASmoothX_Uls_u2p14[2]	11141
t_APASmoothX_Uls_u2p14[3]	11305
t_APASmoothX_Uls_u2p14[4]	11469
t_APASmoothX_Uls_u2p14[5]	11633
t_APASmoothX_Uls_u2p14[6]	11796
t_APASmoothX_Uls_u2p14[7]	11960
t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_Uls_u2p14[9]	12288
t_APASmoothY_Uls_u2p14[0]	6881
t_APASmoothY_Uls_u2p14[1]	7045
t_APASmoothY_Uls_u2p14[2]	7209
t_APASmoothY_Uls_u2p14[3]	7373
t_APASmoothY_Uls_u2p14[4]	7537
t_APASmoothY_Uls_u2p14[5]	7700
t_APASmoothY_Uls_u2p14[6]	7864
t_APASmoothY_Uls_u2p14[7]	8028
t_APASmoothY_Uls_u2p14[8]	8192
t_APASmoothY_Uls_u2p14[9]	8356
t_LKASlewX_Kph_u8p8[0]	35328
t_LKASlewX_Kph_u8p8[1]	35840
t_LKASlewX_Kph_u8p8[2]	36352
t_LKASlewX_Kph_u8p8[3]	36864
t_LKASlewX_Kph_u8p8[4]	37376
t_LKASlewX_Kph_u8p8[5]	37888
t_LKASlewY_NmpSec_u4p12[0]	28262
t_LKASlewY_NmpSec_u4p12[1]	28672
t_LKASlewY_NmpSec_u4p12[2]	29082
t_LKASlewY_NmpSec_u4p12[3]	29491
t_LKASlewY_NmpSec_u4p12[4]	29901
t_LKASlewY_NmpSec_u4p12[5]	30310
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	8.69999981
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.809000015	0.809000015 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.97300005	-2.97300005 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58520031	5.58519983 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.618896842	0.618897021 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-10.2399998
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.80000019
k_APAEnableRate_pSec_f32	0
k_APAUseADmpScale_Igc	0
k_APAUseAsstScale_Igc	0
k_APAUseRetScale_Igc	0
k_ESCMax_HwNm_f32	0
k_LKAUseSlewCal_Igc	0
t_APADisableRateX_HwNm_u4p12[0]	0
t_APADisableRateX_HwNm_u4p12[1]	0
t_APADisableRateX_HwNm_u4p12[2]	0
t_APADisableRateX_HwNm_u4p12[3]	0
t_APADisableRateX_HwNm_u4p12[4]	0
t_APADisableRateX_HwNm_u4p12[5]	0
t_APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	0
t_APADisableRateY_pSec_u7p9[2]	0
t_APADisableRateY_pSec_u7p9[3]	0
t_APADisableRateY_pSec_u7p9[4]	0
t_APADisableRateY_pSec_u7p9[5]	0
t_APASmoothX_Uls_u2p14[0]	0
t_APASmoothX_Uls_u2p14[1]	0
t_APASmoothX_Uls_u2p14[2]	0
t_APASmoothX_Uls_u2p14[3]	0
t_APASmoothX_Uls_u2p14[4]	0
t_APASmoothX_Uls_u2p14[5]	0
t_APASmoothX_Uls_u2p14[6]	0
t_APASmoothX_Uls_u2p14[7]	0

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TrqArblim_Per1

Name	Input Value
t_APASmoothX_Uls_u2p14[8]	0
t_APASmoothX_Uls_u2p14[9]	0
t_APASmoothY_Uls_u2p14[0]	0
t_APASmoothY_Uls_u2p14[1]	0
t_APASmoothY_Uls_u2p14[2]	0
t_APASmoothY_Uls_u2p14[3]	0
t_APASmoothY_Uls_u2p14[4]	0
t_APASmoothY_Uls_u2p14[5]	0
t_APASmoothY_Uls_u2p14[6]	0
t_APASmoothY_Uls_u2p14[7]	0
t_APASmoothY_Uls_u2p14[8]	0
t_APASmoothY_Uls_u2p14[9]	0
t_LKASlewX_Kph_u8p8[0]	0
t_LKASlewX_Kph_u8p8[1]	0
t_LKASlewX_Kph_u8p8[2]	0
t_LKASlewX_Kph_u8p8[3]	0
t_LKASlewX_Kph_u8p8[4]	0
t_LKASlewX_Kph_u8p8[5]	0
t_LKASlewY_NmpSec_u4p12[0]	0
t_LKASlewY_NmpSec_u4p12[1]	0
t_LKASlewY_NmpSec_u4p12[2]	0
t_LKASlewY_NmpSec_u4p12[3]	0
t_LKASlewY_NmpSec_u4p12[4]	0
t_LKASlewY_NmpSec_u4p12[5]	0
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10.2299995
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-8.80000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10	-10 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-10.2399998	-10.2399998 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-8.77299976	-8.77299976 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	1
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390027
TrqArblim_PosSrvcCmd_MtrNm_M_f32	10.2299995
TrqArblim_PrelKATrqReq_HwNm_M_f32	8.80000019
k_APAEnableRate_pSec_f32	500
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	5
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	40960
t_APADisableRateX_HwNm_u4p12[1]	40960
t_APADisableRateX_HwNm_u4p12[2]	40960
t_APADisableRateX_HwNm_u4p12[3]	40960
t_APADisableRateX_HwNm_u4p12[4]	40960
t_APADisableRateX_HwNm_u4p12[5]	40960
t_APADisableRateY_pSec_u7p9[0]	65535
t_APADisableRateY_pSec_u7p9[1]	65535
t_APADisableRateY_pSec_u7p9[2]	65535
t_APADisableRateY_pSec_u7p9[3]	65535
t_APADisableRateY_pSec_u7p9[4]	65535
t_APADisableRateY_pSec_u7p9[5]	65535
t_APASmoothX_Uls_u2p14[0]	16384
t_APASmoothX_Uls_u2p14[1]	16384
t_APASmoothX_Uls_u2p14[2]	16384
t_APASmoothX_Uls_u2p14[3]	16384
t_APASmoothX_Uls_u2p14[4]	16384
t_APASmoothX_Uls_u2p14[5]	16384
t_APASmoothX_Uls_u2p14[6]	16384
t_APASmoothX_Uls_u2p14[7]	16384
t_APASmoothX_Uls_u2p14[8]	16384
t_APASmoothX_Uls_u2p14[9]	16384
t_APASmoothY_Uls_u2p14[0]	16384
t_APASmoothY_Uls_u2p14[1]	16384
t_APASmoothY_Uls_u2p14[2]	16384
t_APASmoothY_Uls_u2p14[3]	16384
t_APASmoothY_Uls_u2p14[4]	16384
t_APASmoothY_Uls_u2p14[5]	16384
t_APASmoothY_Uls_u2p14[6]	16384
t_APASmoothY_Uls_u2p14[7]	16384
t_APASmoothY_Uls_u2p14[8]	16384
t_APASmoothY_Uls_u2p14[9]	16384
t_LKASlewX_Kph_u8p8[0]	56320
t_LKASlewX_Kph_u8p8[1]	56320
t_LKASlewX_Kph_u8p8[2]	56320
t_LKASlewX_Kph_u8p8[3]	56320
t_LKASlewX_Kph_u8p8[4]	56320
t_LKASlewX_Kph_u8p8[5]	56320
t_LKASlewY_NmpSec_u4p12[0]	55296
t_LKASlewY_NmpSec_u4p12[1]	55296
t_LKASlewY_NmpSec_u4p12[2]	55296
t_LKASlewY_NmpSec_u4p12[3]	55296
t_LKASlewY_NmpSec_u4p12[4]	55296
t_LKASlewY_NmpSec_u4p12[5]	55296
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	10
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	511
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	10 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	8.77299976	8.77299976 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0	0 ± 0.000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.270000011
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.03999996
TrqArblim_PrelKATrqReq_HwNm_M_f32	1.39999998
k_APAEnableRate_pSec_f32	91.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	8192
t_APADisableRateX_HwNm_u4p12[1]	8602
t_APADisableRateX_HwNm_u4p12[2]	9011
t_APADisableRateX_HwNm_u4p12[3]	9421
t_APADisableRateX_HwNm_u4p12[4]	9830
t_APADisableRateX_HwNm_u4p12[5]	10240
t_APADisableRateY_pSec_u7p9[0]	10240
t_APADisableRateY_pSec_u7p9[1]	10752
t_APADisableRateY_pSec_u7p9[2]	11264
t_APADisableRateY_pSec_u7p9[3]	11776
t_APADisableRateY_pSec_u7p9[4]	12288
t_APADisableRateY_pSec_u7p9[5]	12800
t_APASmoothX_Uls_u2p14[0]	3277
t_APASmoothX_Uls_u2p14[1]	3441
t_APASmoothX_Uls_u2p14[2]	3604
t_APASmoothX_Uls_u2p14[3]	3768
t_APASmoothX_Uls_u2p14[4]	3932
t_APASmoothX_Uls_u2p14[5]	4096

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TrqArblim_Per1

Name	Input Value		
t_APASmoothX_Uls_u2p14[6]	4260		
t_APASmoothX_Uls_u2p14[7]	4424		
t_APASmoothX_Uls_u2p14[8]	4588		
t_APASmoothX_Uls_u2p14[9]	4751		
t_APASmoothY_Uls_u2p14[0]	328		
t_APASmoothY_Uls_u2p14[1]	492		
t_APASmoothY_Uls_u2p14[2]	655		
t_APASmoothY_Uls_u2p14[3]	819		
t_APASmoothY_Uls_u2p14[4]	983		
t_APASmoothY_Uls_u2p14[5]	1147		
t_APASmoothY_Uls_u2p14[6]	1311		
t_APASmoothY_Uls_u2p14[7]	1475		
t_APASmoothY_Uls_u2p14[8]	1638		
t_APASmoothY_Uls_u2p14[9]	1802		
t_LKASlewX_Kph_u8p8[0]	10240		
t_LKASlewX_Kph_u8p8[1]	10752		
t_LKASlewX_Kph_u8p8[2]	11264		
t_LKASlewX_Kph_u8p8[3]	11776		
t_LKASlewX_Kph_u8p8[4]	12288		
t_LKASlewX_Kph_u8p8[5]	12800		
t_LKASlewY_NmpSec_u4p12[0]	8192		
t_LKASlewY_NmpSec_u4p12[1]	8602		
t_LKASlewY_NmpSec_u4p12[2]	9011		
t_LKASlewY_NmpSec_u4p12[3]	9421		
t_LKASlewY_NmpSec_u4p12[4]	9830		
t_LKASlewY_NmpSec_u4p12[5]	10240		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.85999966		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.5		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	96.3432999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.230000004	0.223203003 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	0	0	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.77880001	1.77880001 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-9.03999996	-9.03999996 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.39399993	1.39400005 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.39399993	1.39400005 ± 0.000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.39399993	1.39400005 ± 0.000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0.000390625	✔

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TrqArblim_Per1



Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Case 3: Boundary Test

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TrqArblim_Per1

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment)
	CPU Cycles:
	TS3.1 2392.00 Cycles
	TS3.2 2725.00 Cycles
	TS3.3 2732.00 Cycles
	TS3.4 2550.00 Cycles
	TS3.5 2793.00 Cycles
	TS3.6 2391.00 Cycles
	TS3.7 2509.00 Cycles
	TS3.8 2361.00 Cycles
	TS3.9 2674.00 Cycles
	TS3.10 2325.00 Cycles
	TS3.11 2345.00 Cycles
	TS3.12 2402.00 Cycles
	TS3.13 2267.00 Cycles
	TS3.14 2504.00 Cycles
	TS3.15 2256.00 Cycles
	TS3.16 2503.00 Cycles
	TS3.17 2342.00 Cycles
	TS3.18 2554.00 Cycles
	TS3.19 2264.00 Cycles
	TS3.20 2344.00 Cycles
	TS3.21 2399.00 Cycles
	TS3.22 2390.00 Cycles
	TS3.23 2439.00 Cycles
	TS3.24 2389.00 Cycles
	TS3.25 2290.00 Cycles
	TS3.26 2246.00 Cycles
	TS3.27 2550.00 Cycles
	TS3.28 2350.00 Cycles
	TS3.29 2444.00 Cycles
	TS3.30 2290.00 Cycles
	TS3.31 2514.00 Cycles
	TS3.32 2302.00 Cycles
	TS3.33 2469.00 Cycles
	TS3.34 2412.00 Cycles
	TS3.35 2336.00 Cycles
	TS3.36 2229.00 Cycles
	TS3.37 2457.00 Cycles
	TS3.38 2412.00 Cycles
	TS3.39 2308.00 Cycles
	TS3.40 2426.00 Cycles
	TS3.41 2308.00 Cycles
	TS3.42 2224.00 Cycles
	TS3.43 2322.00 Cycles
	TS3.44 2254.00 Cycles
	TS3.45 2248.00 Cycles
	TS3.46 2456.00 Cycles
	TS3.47 2537.00 Cycles
	TS3.48 2506.00 Cycles
	TS3.49 2303.00 Cycles
	TS3.50 2324.00 Cycles
	TS3.51 2293.00 Cycles
	TS3.52 2281.00 Cycles
	TS3.53 2241.00 Cycles
	TS3.54 2334.00 Cycles
	TS3.55 2217.00 Cycles
	TS3.56 2307.00 Cycles
	TS3.57 2343.00 Cycles
	TS3.58 2333.00 Cycles
	TS3.59 2377.00 Cycles
	TS3.60 2236.00 Cycles
	TS3.61 2238.00 Cycles
	TS3.62 2245.00 Cycles
	TS3.63 2238.00 Cycles
	TS3.64 2170.00 Cycles
	TS3.65 2266.00 Cycles
	TS3.66 2202.00 Cycles
	TS3.67 2305.00 Cycles
	TS3.68 2599.00 Cycles
	TS3.69 2292.00 Cycles
	TS3.70 2235.00 Cycles
	TS3.71 2264.00 Cycles
	TS3.72 2390.00 Cycles
	TS3.73 2187.00 Cycles
	TS3.74 2449.00 Cycles
	TS3.75 2409.00 Cycles
	TS3.76 2229.00 Cycles
	TS3.77 2228.00 Cycles
	TS3.78 2370.00 Cycles
	TS3.79 2207.00 Cycles
	TS3.80 2307.00 Cycles
	TS3.81 2373.00 Cycles
	TS3.82 2339.00 Cycles
	TS3.83 2193.00 Cycles
	TS3.84 2198.00 Cycles
	TS3.85 2347.00 Cycles
	TS3.86 2216.00 Cycles
	TS3.87 2498.00 Cycles
	TS3.88 2218.00 Cycles
	TS3.89 2375.00 Cycles
	TS3.90 2255.00 Cycles
	TS3.91 2245.00 Cycles
	TS3.92 2256.00 Cycles
	TS3.93 2191.00 Cycles
	TS3.94 2255.00 Cycles
	TS3.95 2306.00 Cycles
	TS3.96 2177.00 Cycles
	TS3.97 2215.00 Cycles
	TS3.98 2274.00 Cycles
	TS3.99 2231.00 Cycles
	TS3.100 2184.00 Cycles
	TS3.101 2338.00 Cycles
	TS3.102 2215.00 Cycles
	TS3.103 2318.00 Cycles

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TrqArblim_Per1

Description	Vector Description:
	TS3.1LKACmd_HwNm_f32 = Min
	TS3.2LKACmd_HwNm_f32 = Max
	TS3.3LKACmd_HwNm_f32 = Zero
	TS3.4LKACmd_HwNm_f32 = Pos
	TS3.5LKACmd_HwNm_f32 = Neg
	TS3.6SecureVehicleSpeed_Kph_f32 = Min
	TS3.7SecureVehicleSpeed_Kph_f32 = Max
	TS3.8SecureVehicleSpeed_Kph_f32 = Pos
	TS3.9PosServEnable_Cnt_lgc = False
	TS3.10PosServEnable_Cnt_lgc = True
	TS3.11PosSrvoCmd_MtrNm_f32 = Min
	TS3.12PosSrvoCmd_MtrNm_f32 = Max
	TS3.13PosSrvoCmd_MtrNm_f32 = Zero
	TS3.14PosSrvoCmd_MtrNm_f32 = Pos
	TS3.15PosSrvoCmd_MtrNm_f32 = Neg
	TS3.16HwTorque_HwNm_f32 = Min
	TS3.17HwTorque_HwNm_f32 = Max
	TS3.18HwTorque_HwNm_f32 = Zero
	TS3.19HwTorque_HwNm_f32 = Pos
	TS3.20HwTorque_HwNm_f32 = Neg
	TS3.21TrqOscCmd_MtrNm_f32 = Min
	TS3.22TrqOscCmd_MtrNm_f32 = Max
	TS3.23TrqOscCmd_MtrNm_f32 = Zero
	TS3.24TrqOscCmd_MtrNm_f32 = Pos
	TS3.25TrqOscCmd_MtrNm_f32 = Neg
	TS3.26LKASState_State_enum = LKA_Inactive
	TS3.27LKASState_State_enum = LKA_Active
	TS3.28LKASState_State_enum = LKA_TemporarilyLimited
	TS3.29LKASState_State_enum = LKA_PermanentlyFailed
	TS3.30LKASState_State_enum = LKA_ManufacturingDisable
	TS3.31GMOSHOscillate_State_enum = Haptic_Inactive
	TS3.32GMOSHOscillate_State_enum = Haptic_Active
	TS3.33GMOSHOscillate_State_enum = Haptic_Waiting
	TS3.34TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Min
	TS3.35TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Max
	TS3.36TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Zero
	TS3.37TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Pos
	TS3.38TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Neg
	TS3.39TrqArblim_HwTorqueSV_HwNm_M_Str.K = Min
	TS3.40TrqArblim_HwTorqueSV_HwNm_M_Str.K = Max
	TS3.41TrqArblim_HwTorqueSV_HwNm_M_Str.K = Pos
	TS3.42TrqArblim_APADisableRate_Uls_M_f32 = Min
	TS3.43TrqArblim_APADisableRate_Uls_M_f32 = Max
	TS3.44TrqArblim_APADisableRate_Uls_M_f32 = Pos
	TS3.45TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Min
	TS3.46TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Max
	TS3.47TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Zero
	TS3.48TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Pos
	TS3.49TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Neg
	TS3.50TrqArblim_PrelKATrqDel_HwNm_M_f32 = Min
	TS3.51TrqArblim_PrelKATrqDel_HwNm_M_f32 = Max
	TS3.52TrqArblim_PrelKATrqDel_HwNm_M_f32 = Zero
	TS3.53TrqArblim_PrelKATrqDel_HwNm_M_f32 = Pos
	TS3.54TrqArblim_PrelKATrqDel_HwNm_M_f32 = Neg
	TS3.55k_APAEnableRate_pSec_f32 = Min
	TS3.56k_APAEnableRate_pSec_f32 = Max
	TS3.57k_APAEnableRate_pSec_f32 = Pos
	TS3.58k_APAUseAsstScale_lgc = False
	TS3.59k_APAUseAsstScale_lgc = True
	TS3.60k_APAUseRetScale_lgc = False
	TS3.61k_APAUseRetScale_lgc = True
	TS3.62k_APAUseADmpScale_lgc = False
	TS3.63k_APAUseADmpScale_lgc = True
	TS3.64k_LKAUseSlewCal_lgc = False
	TS3.65k_LKAUseSlewCal_lgc = True
	TS3.66t_LKASlewX_Kph_u8p8[6] = Min
	TS3.67t_LKASlewX_Kph_u8p8[6] = Max
	TS3.68t_LKASlewX_Kph_u8p8[6] = Pos
	TS3.69t_LKASlewX_NmpSec_u4p12[6] = Min
	TS3.70t_LKASlewY_NmpSec_u4p12[6] = Max
	TS3.71t_LKASlewY_NmpSec_u4p12[6] = Pos
	TS3.72t_APADisableRateX_HwNm_u4p12[6] = Min
	TS3.73t_APADisableRateX_HwNm_u4p12[6] = Max
	TS3.74t_APADisableRateX_HwNm_u4p12[6] = Pos
	TS3.75t_APADisableRateY_pSec_u7p9[6] = Min
	TS3.76t_APADisableRateY_pSec_u7p9[6] = Max
	TS3.77t_APADisableRateY_pSec_u7p9[6] = Pos
	TS3.78t_APASmoothX_Uls_u2p14[10] = Min
	TS3.79t_APASmoothX_Uls_u2p14[10] = Max
	TS3.80t_APASmoothX_Uls_u2p14[10] = Pos
	TS3.81t_APASmoothY_Uls_u2p14[10] = Min
	TS3.82t_APASmoothY_Uls_u2p14[10] = Max
	TS3.83t_APASmoothY_Uls_u2p14[10] = Pos
	TS3.84ESCCmd_HwNm_f32=Min
	TS3.85ESCCmd_HwNm_f32 = Max
	TS3.86ESCCmd_HwNm_f32 = Zero
	TS3.87ESCCmd_HwNm_f32 = Pos
	TS3.88ESCCmd_HwNm_f32 = Neg
	TS3.89ESCState_State_enum=ESC_Inactive
	TS3.90ESCState_State_enum=ESC_Active
	TS3.91ESCState_State_enum=ESC_TemporarilyLimited
	TS3.92ESCState_State_enum=ESC_PermanentlyFailed
	TS3.93ESCState_State_enum=ESC_ManufacturingDisable
	TS3.94k_ESCMax_HwNm_f32=Min

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TS3.95k_ESCMax_HwNm_f32=Max
 TS3.96k_ESCMax_HwNm_f32=Pos
 TS3.97TrqArblim_ESC_State_Cnt_M_enum=ESC_Inactive
 TS3.98TrqArblim_ESC_State_Cnt_M_enum=ESC_Active
 TS3.99TrqArblim_ESC_State_Cnt_M_enum=ESC_TemporarilyLimited
 TS3.100TrqArblim_ESC_State_Cnt_M_enum=ESC_PermanentlyFailed
 TS3.101TrqArblim_ESC_State_Cnt_M_enum=ESC_ManufacturingDisable
 TS3.102All Min
 TS3.103All Max

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.00999999978
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85999966
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-10.2200003
TrqArblim_PrelKATrqReq_HwNm_M_f32	-8.60000038
k_APAEnableRate_pSec_f32	1.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	2.24000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	0
t_APADisableRateX_HwNm_u4p12[1]	410
t_APADisableRateX_HwNm_u4p12[2]	819
t_APADisableRateX_HwNm_u4p12[3]	1229
t_APADisableRateX_HwNm_u4p12[4]	1638
t_APADisableRateX_HwNm_u4p12[5]	2048
t_APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	512
t_APADisableRateY_pSec_u7p9[2]	1024
t_APADisableRateY_pSec_u7p9[3]	1536
t_APADisableRateY_pSec_u7p9[4]	2048
t_APADisableRateY_pSec_u7p9[5]	2560
t_APASmoothX_Uls_u2p14[0]	0
t_APASmoothX_Uls_u2p14[1]	164
t_APASmoothX_Uls_u2p14[2]	328
t_APASmoothX_Uls_u2p14[3]	492
t_APASmoothX_Uls_u2p14[4]	655
t_APASmoothX_Uls_u2p14[5]	819
t_APASmoothX_Uls_u2p14[6]	983
t_APASmoothX_Uls_u2p14[7]	1147
t_APASmoothX_Uls_u2p14[8]	1311
t_APASmoothX_Uls_u2p14[9]	1475
t_APASmoothY_Uls_u2p14[0]	12780
t_APASmoothY_Uls_u2p14[1]	12943
t_APASmoothY_Uls_u2p14[2]	13107
t_APASmoothY_Uls_u2p14[3]	13271
t_APASmoothY_Uls_u2p14[4]	13435
t_APASmoothY_Uls_u2p14[5]	13599
t_APASmoothY_Uls_u2p14[6]	13763
t_APASmoothY_Uls_u2p14[7]	13926
t_APASmoothY_Uls_u2p14[8]	14090
t_APASmoothY_Uls_u2p14[9]	14254
t_LKASlewX_Kph_u8p8[0]	0
t_LKASlewX_Kph_u8p8[1]	512
t_LKASlewX_Kph_u8p8[2]	1024
t_LKASlewX_Kph_u8p8[3]	1536
t_LKASlewX_Kph_u8p8[4]	2048
t_LKASlewX_Kph_u8p8[5]	2560
t_LKASlewY_NmpSec_u4p12[0]	0
t_LKASlewY_NmpSec_u4p12[1]	410
t_LKASlewY_NmpSec_u4p12[2]	819
t_LKASlewY_NmpSec_u4p12[3]	1229
t_LKASlewY_NmpSec_u4p12[4]	1638
t_LKASlewY_NmpSec_u4p12[5]	2048
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum

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Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.82000017
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.64000034
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	2
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.209999993
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	5.9000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85956001	-9.85956001 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003	-10.2200003 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.5732336	-8.57322979 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.023
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038
k_APAEnableRate_pSec_f32	6
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.8499999
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	410
t_APADisableRateX_HwNm_u4p12[1]	819
t_APADisableRateX_HwNm_u4p12[2]	1229
t_APADisableRateX_HwNm_u4p12[3]	1638
t_APADisableRateX_HwNm_u4p12[4]	2048
t_APADisableRateX_HwNm_u4p12[5]	2458
t_APADisableRateY_pSec_u7p9[0]	512
t_APADisableRateY_pSec_u7p9[1]	1024
t_APADisableRateY_pSec_u7p9[2]	1536
t_APADisableRateY_pSec_u7p9[3]	2048
t_APADisableRateY_pSec_u7p9[4]	2560
t_APADisableRateY_pSec_u7p9[5]	3072
t_APASmoothX_Uls_u2p14[0]	164

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Name	Input Value		
t_APASmoothX_Uls_u2p14[1]	328		
t_APASmoothX_Uls_u2p14[2]	492		
t_APASmoothX_Uls_u2p14[3]	655		
t_APASmoothX_Uls_u2p14[4]	819		
t_APASmoothX_Uls_u2p14[5]	983		
t_APASmoothX_Uls_u2p14[6]	1147		
t_APASmoothX_Uls_u2p14[7]	1311		
t_APASmoothX_Uls_u2p14[8]	1475		
t_APASmoothX_Uls_u2p14[9]	1638		
t_APASmoothY_Uls_u2p14[0]	12943		
t_APASmoothY_Uls_u2p14[1]	13107		
t_APASmoothY_Uls_u2p14[2]	13271		
t_APASmoothY_Uls_u2p14[3]	13435		
t_APASmoothY_Uls_u2p14[4]	13599		
t_APASmoothY_Uls_u2p14[5]	13763		
t_APASmoothY_Uls_u2p14[6]	13926		
t_APASmoothY_Uls_u2p14[7]	14090		
t_APASmoothY_Uls_u2p14[8]	14254		
t_APASmoothY_Uls_u2p14[9]	14418		
t_LKASlewX_Kph_u8p8[0]	512		
t_LKASlewX_Kph_u8p8[1]	1024		
t_LKASlewX_Kph_u8p8[2]	1536		
t_LKASlewX_Kph_u8p8[3]	2048		
t_LKASlewX_Kph_u8p8[4]	2560		
t_LKASlewX_Kph_u8p8[5]	3072		
t_LKASlewY_NmpSec_u4p12[0]	410		
t_LKASlewY_NmpSec_u4p12[1]	819		
t_LKASlewY_NmpSec_u4p12[2]	1229		
t_LKASlewY_NmpSec_u4p12[3]	1638		
t_LKASlewY_NmpSec_u4p12[4]	2048		
t_LKASlewY_NmpSec_u4p12[5]	2458		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.22999954		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	5.86000013		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	0.560000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0109999999	0.0109999999 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.24571609	-9.24571991 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-9.02000046	-9.02000046 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.09400082	-8.09399986 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4.8499999	-4.8499999 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0784785151	-0.0784785002 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	✔

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TrqArblim_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0359999985
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.64000034
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-7.82000017
TrqArblim_PrelKATrqReq_HwNm_M_f32	-7.59999999
k_APAEnableRate_pSec_f32	10.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.30999994
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	819
t_APADisableRateX_HwNm_u4p12[1]	1229
t_APADisableRateX_HwNm_u4p12[2]	1638
t_APADisableRateX_HwNm_u4p12[3]	2048
t_APADisableRateX_HwNm_u4p12[4]	2458
t_APADisableRateX_HwNm_u4p12[5]	2867
t_APADisableRateY_pSec_u7p9[0]	1024
t_APADisableRateY_pSec_u7p9[1]	1536
t_APADisableRateY_pSec_u7p9[2]	2048
t_APADisableRateY_pSec_u7p9[3]	2560
t_APADisableRateY_pSec_u7p9[4]	3072
t_APADisableRateY_pSec_u7p9[5]	3584
t_APASmoothX_Uls_u2p14[0]	328
t_APASmoothX_Uls_u2p14[1]	492
t_APASmoothX_Uls_u2p14[2]	655
t_APASmoothX_Uls_u2p14[3]	819
t_APASmoothX_Uls_u2p14[4]	983
t_APASmoothX_Uls_u2p14[5]	1147
t_APASmoothX_Uls_u2p14[6]	1311
t_APASmoothX_Uls_u2p14[7]	1475
t_APASmoothX_Uls_u2p14[8]	1638
t_APASmoothX_Uls_u2p14[9]	1802
t_APASmoothY_Uls_u2p14[0]	13107
t_APASmoothY_Uls_u2p14[1]	13271
t_APASmoothY_Uls_u2p14[2]	13435
t_APASmoothY_Uls_u2p14[3]	13599
t_APASmoothY_Uls_u2p14[4]	13763
t_APASmoothY_Uls_u2p14[5]	13926
t_APASmoothY_Uls_u2p14[6]	14090
t_APASmoothY_Uls_u2p14[7]	14254
t_APASmoothY_Uls_u2p14[8]	14418
t_APASmoothY_Uls_u2p14[9]	14582
t_LKASlewX_Kph_u8p8[0]	1024
t_LKASlewX_Kph_u8p8[1]	1536
t_LKASlewX_Kph_u8p8[2]	2048
t_LKASlewX_Kph_u8p8[3]	2560
t_LKASlewX_Kph_u8p8[4]	3072
t_LKASlewX_Kph_u8p8[5]	3584
t_LKASlewY_NmpSec_u4p12[0]	819
t_LKASlewY_NmpSec_u4p12[1]	1229
t_LKASlewY_NmpSec_u4p12[2]	1638
t_LKASlewY_NmpSec_u4p12[3]	2048
t_LKASlewY_NmpSec_u4p12[4]	2458
t_LKASlewY_NmpSec_u4p12[5]	2867
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCcmd_HwNm_f32.value	-5.42000008
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.82000017
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	9.43000031
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	0.910000026
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.30000019

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.057	0.057 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.62835217	-8.62835026 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	0.910000026	0.910000026 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.59399986	-7.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.30999994	-3.30999994 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	6.34341431	6.34341002 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.4 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0489999987
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-6.61999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.09999999
k_APAEnableRate_pSec_f32	15
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.20000005
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	1229
t_APADisableRateX_HwNm_u4p12[1]	1638
t_APADisableRateX_HwNm_u4p12[2]	2048
t_APADisableRateX_HwNm_u4p12[3]	2458
t_APADisableRateX_HwNm_u4p12[4]	2867
t_APADisableRateX_HwNm_u4p12[5]	3277
t_APADisableRateY_pSec_u7p9[0]	1536
t_APADisableRateY_pSec_u7p9[1]	2048
t_APADisableRateY_pSec_u7p9[2]	2560
t_APADisableRateY_pSec_u7p9[3]	3072
t_APADisableRateY_pSec_u7p9[4]	3584

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	4096		
t_APASmoothX_Uls_u2p14[0]	492		
t_APASmoothX_Uls_u2p14[1]	655		
t_APASmoothX_Uls_u2p14[2]	819		
t_APASmoothX_Uls_u2p14[3]	983		
t_APASmoothX_Uls_u2p14[4]	1147		
t_APASmoothX_Uls_u2p14[5]	1311		
t_APASmoothX_Uls_u2p14[6]	1475		
t_APASmoothX_Uls_u2p14[7]	1638		
t_APASmoothX_Uls_u2p14[8]	1802		
t_APASmoothX_Uls_u2p14[9]	1966		
t_APASmoothY_Uls_u2p14[0]	13271		
t_APASmoothY_Uls_u2p14[1]	13435		
t_APASmoothY_Uls_u2p14[2]	13599		
t_APASmoothY_Uls_u2p14[3]	13763		
t_APASmoothY_Uls_u2p14[4]	13926		
t_APASmoothY_Uls_u2p14[5]	14090		
t_APASmoothY_Uls_u2p14[6]	14254		
t_APASmoothY_Uls_u2p14[7]	14418		
t_APASmoothY_Uls_u2p14[8]	14582		
t_APASmoothY_Uls_u2p14[9]	14746		
t_LKASlewX_Kph_u8p8[0]	1536		
t_LKASlewX_Kph_u8p8[1]	2048		
t_LKASlewX_Kph_u8p8[2]	2560		
t_LKASlewX_Kph_u8p8[3]	3072		
t_LKASlewX_Kph_u8p8[4]	3584		
t_LKASlewX_Kph_u8p8[5]	4096		
t_LKASlewY_NmpSec_u4p12[0]	1229		
t_LKASlewY_NmpSec_u4p12[1]	1638		
t_LKASlewY_NmpSec_u4p12[2]	2048		
t_LKASlewY_NmpSec_u4p12[3]	2458		
t_LKASlewY_NmpSec_u4p12[4]	2867		
t_LKASlewY_NmpSec_u4p12[5]	3277		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.21999979		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.40999985		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.61999989		
target_TrqArblim_Per1_LKASState_State_enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	13.1933002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.25999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0789999962	0.0790000036 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.00746727	-8.00747013 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.25999999	1.25999999 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.07453918	-7.07454014 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.0854996592	0.0854996964 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.5 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.061999999
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.42000008
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.42000008
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.5999999
k_APAEnableRate_pSec_f32	19.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.73500001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	1638
t_APADisableRateX_HwNm_u4p12[1]	2048
t_APADisableRateX_HwNm_u4p12[2]	2458
t_APADisableRateX_HwNm_u4p12[3]	2867
t_APADisableRateX_HwNm_u4p12[4]	3277
t_APADisableRateX_HwNm_u4p12[5]	3686
t_APADisableRateY_pSec_u7p9[0]	2048
t_APADisableRateY_pSec_u7p9[1]	2560
t_APADisableRateY_pSec_u7p9[2]	3072
t_APADisableRateY_pSec_u7p9[3]	3584
t_APADisableRateY_pSec_u7p9[4]	4096
t_APADisableRateY_pSec_u7p9[5]	4608
t_APASmoothX_Uls_u2p14[0]	655
t_APASmoothX_Uls_u2p14[1]	819
t_APASmoothX_Uls_u2p14[2]	983
t_APASmoothX_Uls_u2p14[3]	1147
t_APASmoothX_Uls_u2p14[4]	1311
t_APASmoothX_Uls_u2p14[5]	1475
t_APASmoothX_Uls_u2p14[6]	1638
t_APASmoothX_Uls_u2p14[7]	1802
t_APASmoothX_Uls_u2p14[8]	1966
t_APASmoothX_Uls_u2p14[9]	2130
t_APASmoothY_Uls_u2p14[0]	13435
t_APASmoothY_Uls_u2p14[1]	13599
t_APASmoothY_Uls_u2p14[2]	13763
t_APASmoothY_Uls_u2p14[3]	13926
t_APASmoothY_Uls_u2p14[4]	14090
t_APASmoothY_Uls_u2p14[5]	14254
t_APASmoothY_Uls_u2p14[6]	14418
t_APASmoothY_Uls_u2p14[7]	14582
t_APASmoothY_Uls_u2p14[8]	14746
t_APASmoothY_Uls_u2p14[9]	14909
t_LKASlewX_Kph_u8p8[0]	2048
t_LKASlewX_Kph_u8p8[1]	2560
t_LKASlewX_Kph_u8p8[2]	3072
t_LKASlewX_Kph_u8p8[3]	3584
t_LKASlewX_Kph_u8p8[4]	4096
t_LKASlewX_Kph_u8p8[5]	4608
t_LKASlewY_NmpSec_u4p12[0]	1638
t_LKASlewY_NmpSec_u4p12[1]	2048
t_LKASlewY_NmpSec_u4p12[2]	2458
t_LKASlewY_NmpSec_u4p12[3]	2867
t_LKASlewY_NmpSec_u4p12[4]	3277
t_LKASlewY_NmpSec_u4p12[5]	3686
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.01999998
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-4
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.42000008
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	16.9083004
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	1.61000001
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.69999981

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0439999998	0.0439999998 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.38306427	-7.38305998 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-5.42000008	-5.42000008 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.59399986	-6.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.963745117	0.963744998 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.963745117	0.963744998 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	6.50349855	6.50349998 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.963745117	0.963744998 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.6 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.075000003
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-4.21999979
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.09999999
k_APAEnableRate_pSec_f32	24
k_APAUseADmpScale_Igc	1
k_APAUseAsstScale_Igc	1
k_APAUseRetScale_Igc	1
k_ESCMax_HwNm_f32	1.26900005
k_LKAUseSlewCal_Igc	0
t_APADisableRateX_HwNm_u4p12[0]	2048
t_APADisableRateX_HwNm_u4p12[1]	2458
t_APADisableRateX_HwNm_u4p12[2]	2867
t_APADisableRateX_HwNm_u4p12[3]	3277
t_APADisableRateX_HwNm_u4p12[4]	3686
t_APADisableRateX_HwNm_u4p12[5]	4096
t_APADisableRateY_pSec_u7p9[0]	2560
t_APADisableRateY_pSec_u7p9[1]	3072

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	3584		
t_APADisableRateY_pSec_u7p9[3]	4096		
t_APADisableRateY_pSec_u7p9[4]	4608		
t_APADisableRateY_pSec_u7p9[5]	5120		
t_APASmoothX_Uls_u2p14[0]	819		
t_APASmoothX_Uls_u2p14[1]	983		
t_APASmoothX_Uls_u2p14[2]	1147		
t_APASmoothX_Uls_u2p14[3]	1311		
t_APASmoothX_Uls_u2p14[4]	1475		
t_APASmoothX_Uls_u2p14[5]	1638		
t_APASmoothX_Uls_u2p14[6]	1802		
t_APASmoothX_Uls_u2p14[7]	1966		
t_APASmoothX_Uls_u2p14[8]	2130		
t_APASmoothX_Uls_u2p14[9]	2294		
t_APASmoothY_Uls_u2p14[0]	13599		
t_APASmoothY_Uls_u2p14[1]	13763		
t_APASmoothY_Uls_u2p14[2]	13926		
t_APASmoothY_Uls_u2p14[3]	14090		
t_APASmoothY_Uls_u2p14[4]	14254		
t_APASmoothY_Uls_u2p14[5]	14418		
t_APASmoothY_Uls_u2p14[6]	14582		
t_APASmoothY_Uls_u2p14[7]	14746		
t_APASmoothY_Uls_u2p14[8]	14909		
t_APASmoothY_Uls_u2p14[9]	15073		
t_LKASlewX_Kph_u8p8[0]	2560		
t_LKASlewX_Kph_u8p8[1]	3072		
t_LKASlewX_Kph_u8p8[2]	3584		
t_LKASlewX_Kph_u8p8[3]	4096		
t_LKASlewX_Kph_u8p8[4]	4608		
t_LKASlewX_Kph_u8p8[5]	5120		
t_LKASlewY_NmpSec_u4p12[0]	2048		
t_LKASlewY_NmpSec_u4p12[1]	2458		
t_LKASlewY_NmpSec_u4p12[2]	2867		
t_LKASlewY_NmpSec_u4p12[3]	3277		
t_LKASlewY_NmpSec_u4p12[4]	3686		
t_LKASlewY_NmpSec_u4p12[5]	4096		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHocillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-2.589999991		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-9.10000038		
target_TrqArblim_Per1_LKASState_State_enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.960000004		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.90000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0549999997	0.0549999997 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	0	0	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.75513983	-6.75513983 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.21999979	-4.21999979 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.07299995	-6.07299995 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.954073787	0.954074025 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.954073787	0.954074025 ± 0000390625	✔
target TrqArblim Per1 ESCIsLimited Cnt lgc.value	0	0	

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Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.1938086	-0.193809003 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.954073787	0.954074025 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntrlVarXY_u16_u16Xu16Y_Cnt	2	IntrlVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0879999995
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0152000003
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-3.01999998
TrqArblim_PrelKATrqReq_HwNm_M_f32	-5.59999999
k_APAEnableRate_pSec_f32	28.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.802999973
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	2458
t_APADisableRateX_HwNm_u4p12[1]	2867
t_APADisableRateX_HwNm_u4p12[2]	3277
t_APADisableRateX_HwNm_u4p12[3]	3686
t_APADisableRateX_HwNm_u4p12[4]	4096
t_APADisableRateX_HwNm_u4p12[5]	4506
t_APADisableRateY_pSec_u7p9[0]	3072
t_APADisableRateY_pSec_u7p9[1]	3584
t_APADisableRateY_pSec_u7p9[2]	4096
t_APADisableRateY_pSec_u7p9[3]	4608
t_APADisableRateY_pSec_u7p9[4]	5120
t_APADisableRateY_pSec_u7p9[5]	5632
t_APASmoothX_Uls_u2p14[0]	983
t_APASmoothX_Uls_u2p14[1]	1147
t_APASmoothX_Uls_u2p14[2]	1311
t_APASmoothX_Uls_u2p14[3]	1475
t_APASmoothX_Uls_u2p14[4]	1638
t_APASmoothX_Uls_u2p14[5]	1802
t_APASmoothX_Uls_u2p14[6]	1966
t_APASmoothX_Uls_u2p14[7]	2130
t_APASmoothX_Uls_u2p14[8]	2294
t_APASmoothX_Uls_u2p14[9]	2458
t_APASmoothY_Uls_u2p14[0]	13763
t_APASmoothY_Uls_u2p14[1]	13926
t_APASmoothY_Uls_u2p14[2]	14090
t_APASmoothY_Uls_u2p14[3]	14254
t_APASmoothY_Uls_u2p14[4]	14418
t_APASmoothY_Uls_u2p14[5]	14582
t_APASmoothY_Uls_u2p14[6]	14746
t_APASmoothY_Uls_u2p14[7]	14909
t_APASmoothY_Uls_u2p14[8]	15073
t_APASmoothY_Uls_u2p14[9]	15237
t_LKASlewX_Kph_u8p8[0]	3072
t_LKASlewX_Kph_u8p8[1]	3584
t_LKASlewX_Kph_u8p8[2]	4096
t_LKASlewX_Kph_u8p8[3]	4608
t_LKASlewX_Kph_u8p8[4]	5120
t_LKASlewX_Kph_u8p8[5]	5632
t_LKASlewY_NmpSec_u4p12[0]	2458
t_LKASlewY_NmpSec_u4p12[1]	2867
t_LKASlewY_NmpSec_u4p12[2]	3277
t_LKASlewY_NmpSec_u4p12[3]	3686
t_LKASlewY_NmpSec_u4p12[4]	4096

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[5]	4506
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-0.620000005
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-1.17999995
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.60999966
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	511
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.30999994
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.0999999

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.145000011	0.144999996 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.12369585	-6.12370014 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.30999994	2.30999994 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.59399986	-5.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.865885615	0.865885973 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.865885615	0.865885973 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.620000005	-0.620000005 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.620000005	-0.620000005 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	7.40980434	7.40980005 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.865885615	0.865885973 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.8 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.101000004
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.82000005
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.0999999
k_APAEnableRate_pSec_f32	33
k_APAUseADmpScale_Igc	0
k_APAUseAsstScale_Igc	0
k_APAUseRetScale_Igc	0
k_ESCMax_HwNm_f32	0.337000012
k_LKAUseSlewCal_Igc	0
t_APADisableRateX_HwNm_u4p12[0]	2867
t_APADisableRateX_HwNm_u4p12[1]	3277
t_APADisableRateX_HwNm_u4p12[2]	3686
t_APADisableRateX_HwNm_u4p12[3]	4096
t_APADisableRateX_HwNm_u4p12[4]	4506

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	4915		
t_APADisableRateY_pSec_u7p9[0]	3584		
t_APADisableRateY_pSec_u7p9[1]	4096		
t_APADisableRateY_pSec_u7p9[2]	4608		
t_APADisableRateY_pSec_u7p9[3]	5120		
t_APADisableRateY_pSec_u7p9[4]	5632		
t_APADisableRateY_pSec_u7p9[5]	6144		
t_APASmoothX_Uls_u2p14[0]	1147		
t_APASmoothX_Uls_u2p14[1]	1311		
t_APASmoothX_Uls_u2p14[2]	1475		
t_APASmoothX_Uls_u2p14[3]	1638		
t_APASmoothX_Uls_u2p14[4]	1802		
t_APASmoothX_Uls_u2p14[5]	1966		
t_APASmoothX_Uls_u2p14[6]	2130		
t_APASmoothX_Uls_u2p14[7]	2294		
t_APASmoothX_Uls_u2p14[8]	2458		
t_APASmoothX_Uls_u2p14[9]	2621		
t_APASmoothY_Uls_u2p14[0]	13926		
t_APASmoothY_Uls_u2p14[1]	14090		
t_APASmoothY_Uls_u2p14[2]	14254		
t_APASmoothY_Uls_u2p14[3]	14418		
t_APASmoothY_Uls_u2p14[4]	14582		
t_APASmoothY_Uls_u2p14[5]	14746		
t_APASmoothY_Uls_u2p14[6]	14909		
t_APASmoothY_Uls_u2p14[7]	15073		
t_APASmoothY_Uls_u2p14[8]	15237		
t_APASmoothY_Uls_u2p14[9]	15401		
t_LKASlewX_Kph_u8p8[0]	3584		
t_LKASlewX_Kph_u8p8[1]	4096		
t_LKASlewX_Kph_u8p8[2]	4608		
t_LKASlewX_Kph_u8p8[3]	5120		
t_LKASlewX_Kph_u8p8[4]	5632		
t_LKASlewX_Kph_u8p8[5]	6144		
t_LKASlewY_NmpSec_u4p12[0]	2867		
t_LKASlewY_NmpSec_u4p12[1]	3277		
t_LKASlewY_NmpSec_u4p12[2]	3686		
t_LKASlewY_NmpSec_u4p12[3]	4096		
t_LKASlewY_NmpSec_u4p12[4]	4506		
t_LKASlewY_NmpSec_u4p12[5]	4915		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.230000004		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.11999989		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	256.339996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	2.66000009		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.166999996	0.166999996 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.48873234	-5.48872995 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	2.66000009	2.66000009 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.09399986	-5.09399986 ± 0.03125	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.337000012	0.337000012 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	0.337000012	0.337000012 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.417567909	0.417567998 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.9 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.114	
TrqArblim_ESC_State_Cnt_M_enum	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.98000002	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0196000002	
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-0.620000005	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.5999999	
k_APAEnableRate_pSec_f32	37.5	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	2.24000001	
k_LKAUseSlewCal_lgc	1	
t_APADisableRateX_HwNm_u4p12[0]	3277	
t_APADisableRateX_HwNm_u4p12[1]	3686	
t_APADisableRateX_HwNm_u4p12[2]	4096	
t_APADisableRateX_HwNm_u4p12[3]	4506	
t_APADisableRateX_HwNm_u4p12[4]	4915	
t_APADisableRateX_HwNm_u4p12[5]	5325	
t_APADisableRateY_pSec_u7p9[0]	4096	
t_APADisableRateY_pSec_u7p9[1]	4608	
t_APADisableRateY_pSec_u7p9[2]	5120	
t_APADisableRateY_pSec_u7p9[3]	5632	
t_APADisableRateY_pSec_u7p9[4]	6144	
t_APADisableRateY_pSec_u7p9[5]	6656	
t_APASmoothX_Uls_u2p14[0]	1311	
t_APASmoothX_Uls_u2p14[1]	1475	
t_APASmoothX_Uls_u2p14[2]	1638	
t_APASmoothX_Uls_u2p14[3]	1802	
t_APASmoothX_Uls_u2p14[4]	1966	
t_APASmoothX_Uls_u2p14[5]	2130	
t_APASmoothX_Uls_u2p14[6]	2294	
t_APASmoothX_Uls_u2p14[7]	2458	
t_APASmoothX_Uls_u2p14[8]	2621	
t_APASmoothX_Uls_u2p14[9]	2785	
t_APASmoothY_Uls_u2p14[0]	14090	
t_APASmoothY_Uls_u2p14[1]	14254	
t_APASmoothY_Uls_u2p14[2]	14418	
t_APASmoothY_Uls_u2p14[3]	14582	
t_APASmoothY_Uls_u2p14[4]	14746	
t_APASmoothY_Uls_u2p14[5]	14909	
t_APASmoothY_Uls_u2p14[6]	15073	
t_APASmoothY_Uls_u2p14[7]	15237	
t_APASmoothY_Uls_u2p14[8]	15401	
t_APASmoothY_Uls_u2p14[9]	15565	
t_LKASlewX_Kph_u8p8[0]	4096	
t_LKASlewX_Kph_u8p8[1]	4608	
t_LKASlewX_Kph_u8p8[2]	5120	
t_LKASlewX_Kph_u8p8[3]	5632	
t_LKASlewX_Kph_u8p8[4]	6144	
t_LKASlewX_Kph_u8p8[5]	6656	
t_LKASlewY_NmpSec_u4p12[0]	3277	
t_LKASlewY_NmpSec_u4p12[1]	3686	

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[2]	4096
t_LKASlewY_NmpSec_u4p12[3]	4506
t_LKASlewY_NmpSec_u4p12[4]	4915
t_LKASlewY_NmpSec_u4p12[5]	5325
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	1.77999997
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.63999999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.63000011
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	19.6399994
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.00999999
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0879999995	0.0879999995 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.85024786	-4.85024977 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.620000005	-0.620000005 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-4.59399986	-4.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.923623025	0.923623025 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.923623025	0.923623025 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	7.45264626	7.45265007 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.923623025	0.923623025 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.10 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.127000004
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.579999983
TrqArblim_PrelKATrqReq_HwNm_M_f32	-4.0999999
k_APAEnableRate_pSec_f32	42
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	4.8499999
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	3686
t_APADisableRateX_HwNm_u4p12[1]	4096

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	4506		
t_APADisableRateX_HwNm_u4p12[3]	4915		
t_APADisableRateX_HwNm_u4p12[4]	5325		
t_APADisableRateX_HwNm_u4p12[5]	5734		
t_APADisableRateY_pSec_u7p9[0]	4608		
t_APADisableRateY_pSec_u7p9[1]	5120		
t_APADisableRateY_pSec_u7p9[2]	5632		
t_APADisableRateY_pSec_u7p9[3]	6144		
t_APADisableRateY_pSec_u7p9[4]	6656		
t_APADisableRateY_pSec_u7p9[5]	7168		
t_APASmoothX_Uls_u2p14[0]	1475		
t_APASmoothX_Uls_u2p14[1]	1638		
t_APASmoothX_Uls_u2p14[2]	1802		
t_APASmoothX_Uls_u2p14[3]	1966		
t_APASmoothX_Uls_u2p14[4]	2130		
t_APASmoothX_Uls_u2p14[5]	2294		
t_APASmoothX_Uls_u2p14[6]	2458		
t_APASmoothX_Uls_u2p14[7]	2621		
t_APASmoothX_Uls_u2p14[8]	2785		
t_APASmoothX_Uls_u2p14[9]	2949		
t_APASmoothY_Uls_u2p14[0]	14254		
t_APASmoothY_Uls_u2p14[1]	14418		
t_APASmoothY_Uls_u2p14[2]	14582		
t_APASmoothY_Uls_u2p14[3]	14746		
t_APASmoothY_Uls_u2p14[4]	14909		
t_APASmoothY_Uls_u2p14[5]	15073		
t_APASmoothY_Uls_u2p14[6]	15237		
t_APASmoothY_Uls_u2p14[7]	15401		
t_APASmoothY_Uls_u2p14[8]	15565		
t_APASmoothY_Uls_u2p14[9]	15729		
t_LKASlewX_Kph_u8p8[0]	4608		
t_LKASlewX_Kph_u8p8[1]	5120		
t_LKASlewX_Kph_u8p8[2]	5632		
t_LKASlewX_Kph_u8p8[3]	6144		
t_LKASlewX_Kph_u8p8[4]	6656		
t_LKASlewX_Kph_u8p8[5]	7168		
t_LKASlewY_NmpSec_u4p12[0]	3686		
t_LKASlewY_NmpSec_u4p12[1]	4096		
t_LKASlewY_NmpSec_u4p12[2]	4506		
t_LKASlewY_NmpSec_u4p12[3]	4915		
t_LKASlewY_NmpSec_u4p12[4]	5325		
t_LKASlewY_NmpSec_u4p12[5]	5734		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	2.98000002		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.04999995		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.13999987		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	24		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	3.3599999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.69999981		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.21100001	0.21099995 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20824385	-4.20824003 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.3599999	3.3599999 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.07579994	-4.07579994 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.680617154	0.680616975 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.11 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.140000001
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75999999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0240000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.77999997
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.5999999
k_APAEnableRate_pSec_f32	46.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.30999994
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	4096
t_APADisableRateX_HwNm_u4p12[1]	4506
t_APADisableRateX_HwNm_u4p12[2]	4915
t_APADisableRateX_HwNm_u4p12[3]	5325
t_APADisableRateX_HwNm_u4p12[4]	5734
t_APADisableRateX_HwNm_u4p12[5]	6144
t_APADisableRateY_pSec_u7p9[0]	5120
t_APADisableRateY_pSec_u7p9[1]	5632
t_APADisableRateY_pSec_u7p9[2]	6144
t_APADisableRateY_pSec_u7p9[3]	6656
t_APADisableRateY_pSec_u7p9[4]	7168
t_APADisableRateY_pSec_u7p9[5]	7680
t_APASmoothX_Uls_u2p14[0]	1638
t_APASmoothX_Uls_u2p14[1]	1802
t_APASmoothX_Uls_u2p14[2]	1966
t_APASmoothX_Uls_u2p14[3]	2130
t_APASmoothX_Uls_u2p14[4]	2294
t_APASmoothX_Uls_u2p14[5]	2458
t_APASmoothX_Uls_u2p14[6]	2621
t_APASmoothX_Uls_u2p14[7]	2785
t_APASmoothX_Uls_u2p14[8]	2949
t_APASmoothX_Uls_u2p14[9]	3113
t_APASmoothY_Uls_u2p14[0]	14418
t_APASmoothY_Uls_u2p14[1]	14582
t_APASmoothY_Uls_u2p14[2]	14746
t_APASmoothY_Uls_u2p14[3]	14909
t_APASmoothY_Uls_u2p14[4]	15073
t_APASmoothY_Uls_u2p14[5]	15237
t_APASmoothY_Uls_u2p14[6]	15401
t_APASmoothY_Uls_u2p14[7]	15565
t_APASmoothY_Uls_u2p14[8]	15729
t_APASmoothY_Uls_u2p14[9]	15892
t_LKASlewX_Kph_u8p8[0]	5120
t_LKASlewX_Kph_u8p8[1]	5632
t_LKASlewX_Kph_u8p8[2]	6144
t_LKASlewX_Kph_u8p8[3]	6656
t_LKASlewX_Kph_u8p8[4]	7168

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[5]	7680
t_LKASlewY_NmpSec_u4p12[0]	4096
t_LKASlewY_NmpSec_u4p12[1]	4506
t_LKASlewY_NmpSec_u4p12[2]	4915
t_LKASlewY_NmpSec_u4p12[3]	5325
t_LKASlewY_NmpSec_u4p12[4]	5734
t_LKASlewY_NmpSec_u4p12[5]	6144
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	4.17999983
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.46000004
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.6500001
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	32.5400009
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.80000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.9000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.23300001	0.232999995 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.56272006	-3.56272006 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-3.59399986	-3.5940001 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.77399683	0.773997009 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.77399683	0.773997009 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-1.98882806	-1.98882997 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.77399683	0.773997009 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.12 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.152999997
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.026200002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.98000002
TrqArblim_PrelKATrqReq_HwNm_M_f32	-3.0999999
k_APAEnableRate_pSec_f32	51
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.20000005

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TrqArblim_Per1

Name	Input Value
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	4506
t_APADisableRateX_HwNm_u4p12[1]	4915
t_APADisableRateX_HwNm_u4p12[2]	5325
t_APADisableRateX_HwNm_u4p12[3]	5734
t_APADisableRateX_HwNm_u4p12[4]	6144
t_APADisableRateX_HwNm_u4p12[5]	6554
t_APADisableRateY_pSec_u7p9[0]	5632
t_APADisableRateY_pSec_u7p9[1]	6144
t_APADisableRateY_pSec_u7p9[2]	6656
t_APADisableRateY_pSec_u7p9[3]	7168
t_APADisableRateY_pSec_u7p9[4]	7680
t_APADisableRateY_pSec_u7p9[5]	8192
t_APASmoothX_Uls_u2p14[0]	1802
t_APASmoothX_Uls_u2p14[1]	1966
t_APASmoothX_Uls_u2p14[2]	2130
t_APASmoothX_Uls_u2p14[3]	2294
t_APASmoothX_Uls_u2p14[4]	2458
t_APASmoothX_Uls_u2p14[5]	2621
t_APASmoothX_Uls_u2p14[6]	2785
t_APASmoothX_Uls_u2p14[7]	2949
t_APASmoothX_Uls_u2p14[8]	3113
t_APASmoothX_Uls_u2p14[9]	3277
t_APASmoothY_Uls_u2p14[0]	14582
t_APASmoothY_Uls_u2p14[1]	14746
t_APASmoothY_Uls_u2p14[2]	14909
t_APASmoothY_Uls_u2p14[3]	15073
t_APASmoothY_Uls_u2p14[4]	15237
t_APASmoothY_Uls_u2p14[5]	15401
t_APASmoothY_Uls_u2p14[6]	15565
t_APASmoothY_Uls_u2p14[7]	15729
t_APASmoothY_Uls_u2p14[8]	15892
t_APASmoothY_Uls_u2p14[9]	16056
t_LKASlewX_Kph_u8p8[0]	5632
t_LKASlewX_Kph_u8p8[1]	6144
t_LKASlewX_Kph_u8p8[2]	6656
t_LKASlewX_Kph_u8p8[3]	7168
t_LKASlewX_Kph_u8p8[4]	7680
t_LKASlewX_Kph_u8p8[5]	8192
t_LKASlewY_NmpSec_u4p12[0]	4506
t_LKASlewY_NmpSec_u4p12[1]	4915
t_LKASlewY_NmpSec_u4p12[2]	5325
t_LKASlewY_NmpSec_u4p12[3]	5734
t_LKASlewY_NmpSec_u4p12[4]	6144
t_LKASlewY_NmpSec_u4p12[5]	6554
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.86999989
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.15999985
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	38.2933006
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.10000038

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.254999995	0.254999995 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.91367602	-2.91368008 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.20000005	1.20000005 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	1.20000005	1.20000005 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.13 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.165999994	
TrqArblim_ESC_State_Cnt_M_enum	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53999996	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0284000002	
TrqArblim_PosSrvcCmd_MtrNm_M_f32	4.17999983	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.59999999	
k_APAEnableRate_pSec_f32	55.5	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	1.73500001	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	4915	
t_APADisableRateX_HwNm_u4p12[1]	5325	
t_APADisableRateX_HwNm_u4p12[2]	5734	
t_APADisableRateX_HwNm_u4p12[3]	6144	
t_APADisableRateX_HwNm_u4p12[4]	6554	
t_APADisableRateX_HwNm_u4p12[5]	6963	
t_APADisableRateY_pSec_u7p9[0]	6144	
t_APADisableRateY_pSec_u7p9[1]	6656	
t_APADisableRateY_pSec_u7p9[2]	7168	
t_APADisableRateY_pSec_u7p9[3]	7680	
t_APADisableRateY_pSec_u7p9[4]	8192	
t_APADisableRateY_pSec_u7p9[5]	8704	
t_APASmoothX_Uls_u2p14[0]	1966	
t_APASmoothX_Uls_u2p14[1]	2130	
t_APASmoothX_Uls_u2p14[2]	2294	
t_APASmoothX_Uls_u2p14[3]	2458	
t_APASmoothX_Uls_u2p14[4]	2621	
t_APASmoothX_Uls_u2p14[5]	2785	
t_APASmoothX_Uls_u2p14[6]	2949	
t_APASmoothX_Uls_u2p14[7]	3113	
t_APASmoothX_Uls_u2p14[8]	3277	
t_APASmoothX_Uls_u2p14[9]	3441	
t_APASmoothY_Uls_u2p14[0]	14746	
t_APASmoothY_Uls_u2p14[1]	14909	
t_APASmoothY_Uls_u2p14[2]	15073	
t_APASmoothY_Uls_u2p14[3]	15237	
t_APASmoothY_Uls_u2p14[4]	15401	
t_APASmoothY_Uls_u2p14[5]	15565	
t_APASmoothY_Uls_u2p14[6]	15729	
t_APASmoothY_Uls_u2p14[7]	15892	
t_APASmoothY_Uls_u2p14[8]	16056	
t_APASmoothY_Uls_u2p14[9]	16220	
t_LKASlewX_Kph_u8p8[0]	6144	
t_LKASlewX_Kph_u8p8[1]	6656	
t_LKASlewX_Kph_u8p8[2]	7168	

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[3]	7680
t_LKASlewX_Kph_u8p8[4]	8192
t_LKASlewX_Kph_u8p8[5]	8704
t_LKASlewY_NmpSec_u4p12[0]	4915
t_LKASlewY_NmpSec_u4p12[1]	5325
t_LKASlewY_NmpSec_u4p12[2]	5734
t_LKASlewY_NmpSec_u4p12[3]	6144
t_LKASlewY_NmpSec_u4p12[4]	6554
t_LKASlewY_NmpSec_u4p12[5]	6963
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.28000021
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	44.7433014
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.30000019

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.27700001	0.27700001 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.26111197	-2.26111007 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0	0 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-2.57821965	-2.57821989 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.73500001	1.73500001 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.73500001	1.73500001 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.30000019	8.30000019 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.14 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.179000005
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.92999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.38000011
TrqArblim_PrelKATrqReq_HwNm_M_f32	-2.0999999
k_APAEnableRate_pSec_f32	60
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1

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TrqArblim_Per1

Name	Input Value
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.26900005
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	5325
t_APADisableRateX_HwNm_u4p12[1]	5734
t_APADisableRateX_HwNm_u4p12[2]	6144
t_APADisableRateX_HwNm_u4p12[3]	6554
t_APADisableRateX_HwNm_u4p12[4]	6963
t_APADisableRateX_HwNm_u4p12[5]	7373
t_APADisableRateY_pSec_u7p9[0]	6656
t_APADisableRateY_pSec_u7p9[1]	7168
t_APADisableRateY_pSec_u7p9[2]	7680
t_APADisableRateY_pSec_u7p9[3]	8192
t_APADisableRateY_pSec_u7p9[4]	8704
t_APADisableRateY_pSec_u7p9[5]	9216
t_APASmoothX_Uls_u2p14[0]	2130
t_APASmoothX_Uls_u2p14[1]	2294
t_APASmoothX_Uls_u2p14[2]	2458
t_APASmoothX_Uls_u2p14[3]	2621
t_APASmoothX_Uls_u2p14[4]	2785
t_APASmoothX_Uls_u2p14[5]	2949
t_APASmoothX_Uls_u2p14[6]	3113
t_APASmoothX_Uls_u2p14[7]	3277
t_APASmoothX_Uls_u2p14[8]	3441
t_APASmoothX_Uls_u2p14[9]	3604
t_APASmoothY_Uls_u2p14[0]	14909
t_APASmoothY_Uls_u2p14[1]	15073
t_APASmoothY_Uls_u2p14[2]	15237
t_APASmoothY_Uls_u2p14[3]	15401
t_APASmoothY_Uls_u2p14[4]	15565
t_APASmoothY_Uls_u2p14[5]	15729
t_APASmoothY_Uls_u2p14[6]	15892
t_APASmoothY_Uls_u2p14[7]	16056
t_APASmoothY_Uls_u2p14[8]	16220
t_APASmoothY_Uls_u2p14[9]	16384
t_LKASlewX_Kph_u8p8[0]	6656
t_LKASlewX_Kph_u8p8[1]	7168
t_LKASlewX_Kph_u8p8[2]	7680
t_LKASlewX_Kph_u8p8[3]	8192
t_LKASlewX_Kph_u8p8[4]	8704
t_LKASlewX_Kph_u8p8[5]	9216
t_LKASlewY_NmpSec_u4p12[0]	5325
t_LKASlewY_NmpSec_u4p12[1]	5734
t_LKASlewY_NmpSec_u4p12[2]	6144
t_LKASlewY_NmpSec_u4p12[3]	6554
t_LKASlewY_NmpSec_u4p12[4]	6963
t_LKASlewY_NmpSec_u4p12[5]	7373
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.68999958
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.17999983
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	51.1932983
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	4.4000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.299000025	0.298999995 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.60502791	-1.60502994 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	4.4000001	4.4000001 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-2.09399986	-2.0940001 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-2.09399986	-2.0940001 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-2.09399986	-2.0940001 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.31560016	1.31560004 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.15 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.192000002
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0328000002
TrqArblim_PosSrvcCmd_MtrNm_M_f32	6.57999992
TrqArblim_PrelKATrqReq_HwNm_M_f32	-1.60000002
k_APAEnableRate_pSec_f32	64.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.802999973
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	5734
t_APADisableRateX_HwNm_u4p12[1]	6144
t_APADisableRateX_HwNm_u4p12[2]	6554
t_APADisableRateX_HwNm_u4p12[3]	6963
t_APADisableRateX_HwNm_u4p12[4]	7373
t_APADisableRateX_HwNm_u4p12[5]	7782
t_APADisableRateY_pSec_u7p9[0]	7168
t_APADisableRateY_pSec_u7p9[1]	7680
t_APADisableRateY_pSec_u7p9[2]	8192
t_APADisableRateY_pSec_u7p9[3]	8704
t_APADisableRateY_pSec_u7p9[4]	9216
t_APADisableRateY_pSec_u7p9[5]	9728
t_APASmoothX_Uls_u2p14[0]	2294
t_APASmoothX_Uls_u2p14[1]	2458
t_APASmoothX_Uls_u2p14[2]	2621
t_APASmoothX_Uls_u2p14[3]	2785
t_APASmoothX_Uls_u2p14[4]	2949
t_APASmoothX_Uls_u2p14[5]	3113
t_APASmoothX_Uls_u2p14[6]	3277
t_APASmoothX_Uls_u2p14[7]	3441
t_APASmoothX_Uls_u2p14[8]	3604
t_APASmoothX_Uls_u2p14[9]	3768
t_APASmoothY_Uls_u2p14[0]	6226
t_APASmoothY_Uls_u2p14[1]	6390
t_APASmoothY_Uls_u2p14[2]	6554
t_APASmoothY_Uls_u2p14[3]	6717
t_APASmoothY_Uls_u2p14[4]	6881
t_APASmoothY_Uls_u2p14[5]	7045
t_APASmoothY_Uls_u2p14[6]	7209
t_APASmoothY_Uls_u2p14[7]	7373
t_APASmoothY_Uls_u2p14[8]	7537
t_APASmoothY_Uls_u2p14[9]	7700

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[0]	7168
t_LKASlewX_Kph_u8p8[1]	7680
t_LKASlewX_Kph_u8p8[2]	8192
t_LKASlewX_Kph_u8p8[3]	8704
t_LKASlewX_Kph_u8p8[4]	9216
t_LKASlewX_Kph_u8p8[5]	9728
t_LKASlewY_NmpSec_u4p12[0]	5734
t_LKASlewY_NmpSec_u4p12[1]	6144
t_LKASlewY_NmpSec_u4p12[2]	6554
t_LKASlewY_NmpSec_u4p12[3]	6963
t_LKASlewY_NmpSec_u4p12[4]	7373
t_LKASlewY_NmpSec_u4p12[5]	7782
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCscillate_State_enum	target_TrqArblim_Per1_GMOSHOCscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOCscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.69000006
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	57.6432991
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-4.4000001
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.69999981

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.32100001	0.32100001 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.981504083	-0.981504023 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-4.4000001	-4.4000001 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-1.57972467	-1.57972002 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.57972467	-1.57972467 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-1.57972467	-1.57972467 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.03621292	8.03621006 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.16 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.204999998
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.7099999979
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0350000001
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.78000021
TrqArblim_PrelKATrqReq_HwNm_M_f32	-1.10000002

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TrqArblim_Per1

Name	Input Value
k_APAEnableRate_pSec_f32	69
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.337000012
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	6144
t_APADisableRateX_HwNm_u4p12[1]	6554
t_APADisableRateX_HwNm_u4p12[2]	6963
t_APADisableRateX_HwNm_u4p12[3]	7373
t_APADisableRateX_HwNm_u4p12[4]	7782
t_APADisableRateX_HwNm_u4p12[5]	8192
t_APADisableRateY_pSec_u7p9[0]	7680
t_APADisableRateY_pSec_u7p9[1]	8192
t_APADisableRateY_pSec_u7p9[2]	8704
t_APADisableRateY_pSec_u7p9[3]	9216
t_APADisableRateY_pSec_u7p9[4]	9728
t_APADisableRateY_pSec_u7p9[5]	10240
t_APASmoothX_Uls_u2p14[0]	2458
t_APASmoothX_Uls_u2p14[1]	2621
t_APASmoothX_Uls_u2p14[2]	2785
t_APASmoothX_Uls_u2p14[3]	2949
t_APASmoothX_Uls_u2p14[4]	3113
t_APASmoothX_Uls_u2p14[5]	3277
t_APASmoothX_Uls_u2p14[6]	3441
t_APASmoothX_Uls_u2p14[7]	3604
t_APASmoothX_Uls_u2p14[8]	3768
t_APASmoothX_Uls_u2p14[9]	3932
t_APASmoothY_Uls_u2p14[0]	6390
t_APASmoothY_Uls_u2p14[1]	6554
t_APASmoothY_Uls_u2p14[2]	6717
t_APASmoothY_Uls_u2p14[3]	6881
t_APASmoothY_Uls_u2p14[4]	7045
t_APASmoothY_Uls_u2p14[5]	7209
t_APASmoothY_Uls_u2p14[6]	7373
t_APASmoothY_Uls_u2p14[7]	7537
t_APASmoothY_Uls_u2p14[8]	7700
t_APASmoothY_Uls_u2p14[9]	7864
t_LKASlewX_Kph_u8p8[0]	7680
t_LKASlewX_Kph_u8p8[1]	8192
t_LKASlewX_Kph_u8p8[2]	8704
t_LKASlewX_Kph_u8p8[3]	9216
t_LKASlewX_Kph_u8p8[4]	9728
t_LKASlewX_Kph_u8p8[5]	10240
t_LKASlewY_NmpSec_u4p12[0]	6144
t_LKASlewY_NmpSec_u4p12[1]	6554
t_LKASlewY_NmpSec_u4p12[2]	6963
t_LKASlewY_NmpSec_u4p12[3]	7373
t_LKASlewY_NmpSec_u4p12[4]	7782
t_LKASlewY_NmpSec_u4p12[5]	8192
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.1800003
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.19999981

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TrqArblim_Per1

Name	Input Value
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	64.0932999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-4.30000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.60000038

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.174999997	0.174999997 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.03515005	-1.03515005 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.78000021	7.78000021 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.09399998	-1.09399998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.564992547	0.564993024 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.17 (Repeat Count = 1)		✔
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.217999995	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.100000001	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0372000001	
TrqArblim_PosSrvcCmd_MtrNm_M_f32	8.97999954	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.600000024	
k_APAEnableRate_pSec_f32	73.5	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	1.25	
k_LKAUseSlewCal_lgc	1	
t_APADisableRateX_HwNm_u4p12[0]	6554	
t_APADisableRateX_HwNm_u4p12[1]	6963	
t_APADisableRateX_HwNm_u4p12[2]	7373	
t_APADisableRateX_HwNm_u4p12[3]	7782	
t_APADisableRateX_HwNm_u4p12[4]	8192	
t_APADisableRateX_HwNm_u4p12[5]	8602	
t_APADisableRateY_pSec_u7p9[0]	8192	
t_APADisableRateY_pSec_u7p9[1]	8704	
t_APADisableRateY_pSec_u7p9[2]	9216	
t_APADisableRateY_pSec_u7p9[3]	9728	
t_APADisableRateY_pSec_u7p9[4]	10240	
t_APADisableRateY_pSec_u7p9[5]	10752	
t_APASmoothX_Uls_u2p14[0]	2621	
t_APASmoothX_Uls_u2p14[1]	2785	
t_APASmoothX_Uls_u2p14[2]	2949	
t_APASmoothX_Uls_u2p14[3]	3113	
t_APASmoothX_Uls_u2p14[4]	3277	
t_APASmoothX_Uls_u2p14[5]	3441	
t_APASmoothX_Uls_u2p14[6]	3604	
t_APASmoothX_Uls_u2p14[7]	3768	
t_APASmoothX_Uls_u2p14[8]	3932	
t_APASmoothX_Uls_u2p14[9]	4096	
t_APASmoothY_Uls_u2p14[0]	6554	
t_APASmoothY_Uls_u2p14[1]	6717	
t_APASmoothY_Uls_u2p14[2]	6881	
t_APASmoothY_Uls_u2p14[3]	7045	
t_APASmoothY_Uls_u2p14[4]	7209	
t_APASmoothY_Uls_u2p14[5]	7373	
t_APASmoothY_Uls_u2p14[6]	7537	

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TrqArblim_Per1

Name	Input Value
t_APASmoothY_Uls_u2p14[7]	7700
t_APASmoothY_Uls_u2p14[8]	7864
t_APASmoothY_Uls_u2p14[9]	8028
t_LKASlewX_Kph_u8p8[0]	8192
t_LKASlewX_Kph_u8p8[1]	8704
t_LKASlewX_Kph_u8p8[2]	9216
t_LKASlewX_Kph_u8p8[3]	9728
t_LKASlewX_Kph_u8p8[4]	10240
t_LKASlewX_Kph_u8p8[5]	10752
t_LKASlewY_NmpSec_u4p12[0]	6554
t_LKASlewY_NmpSec_u4p12[1]	6963
t_LKASlewY_NmpSec_u4p12[2]	7373
t_LKASlewY_NmpSec_u4p12[3]	7782
t_LKASlewY_NmpSec_u4p12[4]	8192
t_LKASlewY_NmpSec_u4p12[5]	8602
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	10
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.71000004
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	70.5432968
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-4.0999999
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.10000038

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.36500001	0.36500001 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.27572003	0.27572 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-4.0999999	-4.0999999 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-0.594000041	-0.593999982 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.821153581	0.821153998 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.821153581	0.821153998 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.25	-1.25 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.25	-1.25 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.733270347	-0.733269989 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.821153581	0.821153998 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.18 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.231000006
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.509999999

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TrqArblim_Per1

Name	Input Value
TrqArblim_HwTorqueSV_HwNm_M_Str_K_Uls_f32	0.0394000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.1800003
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.100000001
k_APAEnableRate_pSec_f32	78
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	6963
t_APADisableRateX_HwNm_u4p12[1]	7373
t_APADisableRateX_HwNm_u4p12[2]	7782
t_APADisableRateX_HwNm_u4p12[3]	8192
t_APADisableRateX_HwNm_u4p12[4]	8602
t_APADisableRateX_HwNm_u4p12[5]	9011
t_APADisableRateY_pSec_u7p9[0]	8704
t_APADisableRateY_pSec_u7p9[1]	9216
t_APADisableRateY_pSec_u7p9[2]	9728
t_APADisableRateY_pSec_u7p9[3]	10240
t_APADisableRateY_pSec_u7p9[4]	10752
t_APADisableRateY_pSec_u7p9[5]	11264
t_APASmoothX_Uls_u2p14[0]	2785
t_APASmoothX_Uls_u2p14[1]	2949
t_APASmoothX_Uls_u2p14[2]	3113
t_APASmoothX_Uls_u2p14[3]	3277
t_APASmoothX_Uls_u2p14[4]	3441
t_APASmoothX_Uls_u2p14[5]	3604
t_APASmoothX_Uls_u2p14[6]	3768
t_APASmoothX_Uls_u2p14[7]	3932
t_APASmoothX_Uls_u2p14[8]	4096
t_APASmoothX_Uls_u2p14[9]	4260
t_APASmoothY_Uls_u2p14[0]	6717
t_APASmoothY_Uls_u2p14[1]	6881
t_APASmoothY_Uls_u2p14[2]	7045
t_APASmoothY_Uls_u2p14[3]	7209
t_APASmoothY_Uls_u2p14[4]	7373
t_APASmoothY_Uls_u2p14[5]	7537
t_APASmoothY_Uls_u2p14[6]	7700
t_APASmoothY_Uls_u2p14[7]	7864
t_APASmoothY_Uls_u2p14[8]	8028
t_APASmoothY_Uls_u2p14[9]	8192
t_LKASlewX_Kph_u8p8[0]	8704
t_LKASlewX_Kph_u8p8[1]	9216
t_LKASlewX_Kph_u8p8[2]	9728
t_LKASlewX_Kph_u8p8[3]	10240
t_LKASlewX_Kph_u8p8[4]	10752
t_LKASlewX_Kph_u8p8[5]	11264
t_LKASlewY_NmpSec_u4p12[0]	6963
t_LKASlewY_NmpSec_u4p12[1]	7373
t_LKASlewY_NmpSec_u4p12[2]	7782
t_LKASlewY_NmpSec_u4p12[3]	8192
t_LKASlewY_NmpSec_u4p12[4]	8602
t_LKASlewY_NmpSec_u4p12[5]	9011
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046
target_TrqArblim_Per1_ESCState_State_enum.value	2

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	76.9933014		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.196999997	0.196999997 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.489905983	0.489906013 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.1800003	10.1800003 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.0940000042	-0.0939999968 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.24000001	-3.24000001 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.876287162	0.876286983 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.19 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.244000003
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.12
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0416000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.400000006
k_APAEnableRate_pSec_f32	82.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	7373
t_APADisableRateX_HwNm_u4p12[1]	7782
t_APADisableRateX_HwNm_u4p12[2]	8192
t_APADisableRateX_HwNm_u4p12[3]	8602
t_APADisableRateX_HwNm_u4p12[4]	9011
t_APADisableRateX_HwNm_u4p12[5]	9421
t_APADisableRateY_pSec_u7p9[0]	9216
t_APADisableRateY_pSec_u7p9[1]	9728
t_APADisableRateY_pSec_u7p9[2]	10240
t_APADisableRateY_pSec_u7p9[3]	10752
t_APADisableRateY_pSec_u7p9[4]	11264
t_APADisableRateY_pSec_u7p9[5]	11776
t_APASmoothX_Uls_u2p14[0]	2949
t_APASmoothX_Uls_u2p14[1]	3113
t_APASmoothX_Uls_u2p14[2]	3277
t_APASmoothX_Uls_u2p14[3]	3441
t_APASmoothX_Uls_u2p14[4]	3604
t_APASmoothX_Uls_u2p14[5]	3768
t_APASmoothX_Uls_u2p14[6]	3932
t_APASmoothX_Uls_u2p14[7]	4096
t_APASmoothX_Uls_u2p14[8]	4260
t_APASmoothX_Uls_u2p14[9]	4424
t_APASmoothY_Uls_u2p14[0]	0
t_APASmoothY_Uls_u2p14[1]	164
t_APASmoothY_Uls_u2p14[2]	328
t_APASmoothY_Uls_u2p14[3]	492

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TrqArblim_Per1

Name	Input Value
t_APASmoothY_Uls_u2p14[4]	655
t_APASmoothY_Uls_u2p14[5]	819
t_APASmoothY_Uls_u2p14[6]	983
t_APASmoothY_Uls_u2p14[7]	1147
t_APASmoothY_Uls_u2p14[8]	1311
t_APASmoothY_Uls_u2p14[9]	1475
t_LKASlewX_Kph_u8p8[0]	9216
t_LKASlewX_Kph_u8p8[1]	9728
t_LKASlewX_Kph_u8p8[2]	10240
t_LKASlewX_Kph_u8p8[3]	10752
t_LKASlewX_Kph_u8p8[4]	11264
t_LKASlewX_Kph_u8p8[5]	11776
t_LKASlewY_NmpSec_u4p12[0]	7373
t_LKASlewY_NmpSec_u4p12[1]	7782
t_LKASlewY_NmpSec_u4p12[2]	8192
t_LKASlewY_NmpSec_u4p12[3]	8602
t_LKASlewY_NmpSec_u4p12[4]	9011
t_LKASlewY_NmpSec_u4p12[5]	9421
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.34000015
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-2.73000002
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	83.4432983
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-3.70000005
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.409000009	0.409000009 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.29555202	1.29554999 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-3.70000005	-3.70000005 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.382734507	0.382735014 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.382734507	0.382734507 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.382734507	0.382734507 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.86376238	1.86375999 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.20 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	

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TrqArblim_Per1

Name	Input Value
TrqArblim_APADisableRate_Uls_M_f32	0.256999999
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.52000046
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.899999976
k_APAEnableRate_pSec_f32	87
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	7782
t_APADisableRateX_HwNm_u4p12[1]	8192
t_APADisableRateX_HwNm_u4p12[2]	8602
t_APADisableRateX_HwNm_u4p12[3]	9011
t_APADisableRateX_HwNm_u4p12[4]	9421
t_APADisableRateX_HwNm_u4p12[5]	9830
t_APADisableRateY_pSec_u7p9[0]	9728
t_APADisableRateY_pSec_u7p9[1]	10240
t_APADisableRateY_pSec_u7p9[2]	10752
t_APADisableRateY_pSec_u7p9[3]	11264
t_APADisableRateY_pSec_u7p9[4]	11776
t_APADisableRateY_pSec_u7p9[5]	12288
t_APASmoothX_Uls_u2p14[0]	3113
t_APASmoothX_Uls_u2p14[1]	3277
t_APASmoothX_Uls_u2p14[2]	3441
t_APASmoothX_Uls_u2p14[3]	3604
t_APASmoothX_Uls_u2p14[4]	3768
t_APASmoothX_Uls_u2p14[5]	3932
t_APASmoothX_Uls_u2p14[6]	4096
t_APASmoothX_Uls_u2p14[7]	4260
t_APASmoothX_Uls_u2p14[8]	4424
t_APASmoothX_Uls_u2p14[9]	4588
t_APASmoothY_Uls_u2p14[0]	164
t_APASmoothY_Uls_u2p14[1]	328
t_APASmoothY_Uls_u2p14[2]	492
t_APASmoothY_Uls_u2p14[3]	655
t_APASmoothY_Uls_u2p14[4]	819
t_APASmoothY_Uls_u2p14[5]	983
t_APASmoothY_Uls_u2p14[6]	1147
t_APASmoothY_Uls_u2p14[7]	1311
t_APASmoothY_Uls_u2p14[8]	1475
t_APASmoothY_Uls_u2p14[9]	1638
t_LKASlewX_Kph_u8p8[0]	9728
t_LKASlewX_Kph_u8p8[1]	10240
t_LKASlewX_Kph_u8p8[2]	10752
t_LKASlewX_Kph_u8p8[3]	11264
t_LKASlewX_Kph_u8p8[4]	11776
t_LKASlewX_Kph_u8p8[5]	12288
t_LKASlewY_NmpSec_u4p12[0]	7782
t_LKASlewY_NmpSec_u4p12[1]	8192
t_LKASlewY_NmpSec_u4p12[2]	8602
t_LKASlewY_NmpSec_u4p12[3]	9011
t_LKASlewY_NmpSec_u4p12[4]	9421
t_LKASlewY_NmpSec_u4p12[5]	9830
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32

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TrqArblim_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.86000013		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-2.24000001		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	89.8933029		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.430999994	0.430999994 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	4	4	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.39755797	1.39756 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.5	-3.5 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.893999994	0.893999994 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✔
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.150813162	-0.150812998 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.21 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.270000011
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.39999998
k_APAEnableRate_pSec_f32	91.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	8192
t_APADisableRateX_HwNm_u4p12[1]	8602
t_APADisableRateX_HwNm_u4p12[2]	9011
t_APADisableRateX_HwNm_u4p12[3]	9421
t_APADisableRateX_HwNm_u4p12[4]	9830
t_APADisableRateX_HwNm_u4p12[5]	10240
t_APADisableRateY_pSec_u7p9[0]	10240
t_APADisableRateY_pSec_u7p9[1]	10752
t_APADisableRateY_pSec_u7p9[2]	11264
t_APADisableRateY_pSec_u7p9[3]	11776
t_APADisableRateY_pSec_u7p9[4]	12288
t_APADisableRateY_pSec_u7p9[5]	12800
t_APASmoothX_Uls_u2p14[0]	3277
t_APASmoothX_Uls_u2p14[1]	3441
t_APASmoothX_Uls_u2p14[2]	3604
t_APASmoothX_Uls_u2p14[3]	3768
t_APASmoothX_Uls_u2p14[4]	3932
t_APASmoothX_Uls_u2p14[5]	4096
t_APASmoothX_Uls_u2p14[6]	4260
t_APASmoothX_Uls_u2p14[7]	4424
t_APASmoothX_Uls_u2p14[8]	4588
t_APASmoothX_Uls_u2p14[9]	4751
t_APASmoothY_Uls_u2p14[0]	328

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Name	Input Value		
t_APASmoothY_Uls_u2p14[1]	492		
t_APASmoothY_Uls_u2p14[2]	655		
t_APASmoothY_Uls_u2p14[3]	819		
t_APASmoothY_Uls_u2p14[4]	983		
t_APASmoothY_Uls_u2p14[5]	1147		
t_APASmoothY_Uls_u2p14[6]	1311		
t_APASmoothY_Uls_u2p14[7]	1475		
t_APASmoothY_Uls_u2p14[8]	1638		
t_APASmoothY_Uls_u2p14[9]	1802		
t_LKASlewX_Kph_u8p8[0]	10240		
t_LKASlewX_Kph_u8p8[1]	10752		
t_LKASlewX_Kph_u8p8[2]	11264		
t_LKASlewX_Kph_u8p8[3]	11776		
t_LKASlewX_Kph_u8p8[4]	12288		
t_LKASlewX_Kph_u8p8[5]	12800		
t_LKASlewY_NmpSec_u4p12[0]	8192		
t_LKASlewY_NmpSec_u4p12[1]	8602		
t_LKASlewY_NmpSec_u4p12[2]	9011		
t_LKASlewY_NmpSec_u4p12[3]	9421		
t_LKASlewY_NmpSec_u4p12[4]	9830		
t_LKASlewY_NmpSec_u4p12[5]	10240		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.85999996		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.5		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	96.3432999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.230000004	0.223203003 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	0	0	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.77880001	1.77880001 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-9.03999996	-9.03999996 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.39399993	1.39400005 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.39399993	1.39400005 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.39399993	1.39400005 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0.000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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Test Step 3.22 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.282999992
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.95000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0482000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.56000042
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.89999998
k_APAEnableRate_pSec_f32	96
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.1400001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	8602
t_APADisableRateX_HwNm_u4p12[1]	9011
t_APADisableRateX_HwNm_u4p12[2]	9421
t_APADisableRateX_HwNm_u4p12[3]	9830
t_APADisableRateX_HwNm_u4p12[4]	10240
t_APADisableRateX_HwNm_u4p12[5]	10650
t_APADisableRateY_pSec_u7p9[0]	10752
t_APADisableRateY_pSec_u7p9[1]	11264
t_APADisableRateY_pSec_u7p9[2]	11776
t_APADisableRateY_pSec_u7p9[3]	12288
t_APADisableRateY_pSec_u7p9[4]	12800
t_APADisableRateY_pSec_u7p9[5]	13312
t_APASmoothX_Uls_u2p14[0]	3441
t_APASmoothX_Uls_u2p14[1]	3604
t_APASmoothX_Uls_u2p14[2]	3768
t_APASmoothX_Uls_u2p14[3]	3932
t_APASmoothX_Uls_u2p14[4]	4096
t_APASmoothX_Uls_u2p14[5]	4260
t_APASmoothX_Uls_u2p14[6]	4424
t_APASmoothX_Uls_u2p14[7]	4588
t_APASmoothX_Uls_u2p14[8]	4751
t_APASmoothX_Uls_u2p14[9]	4915
t_APASmoothY_Uls_u2p14[0]	492
t_APASmoothY_Uls_u2p14[1]	655
t_APASmoothY_Uls_u2p14[2]	819
t_APASmoothY_Uls_u2p14[3]	983
t_APASmoothY_Uls_u2p14[4]	1147
t_APASmoothY_Uls_u2p14[5]	1311
t_APASmoothY_Uls_u2p14[6]	1475
t_APASmoothY_Uls_u2p14[7]	1638
t_APASmoothY_Uls_u2p14[8]	1802
t_APASmoothY_Uls_u2p14[9]	1966
t_LKASlewX_Kph_u8p8[0]	10752
t_LKASlewX_Kph_u8p8[1]	11264
t_LKASlewX_Kph_u8p8[2]	11776
t_LKASlewX_Kph_u8p8[3]	12288
t_LKASlewX_Kph_u8p8[4]	12800
t_LKASlewX_Kph_u8p8[5]	13312
t_LKASlewY_NmpSec_u4p12[0]	8602
t_LKASlewY_NmpSec_u4p12[1]	9011
t_LKASlewY_NmpSec_u4p12[2]	9421
t_LKASlewY_NmpSec_u4p12[3]	9830
t_LKASlewY_NmpSec_u4p12[4]	10240
t_LKASlewY_NmpSec_u4p12[5]	10650
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32

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Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.25
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.850000024
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	102.792999
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.0999999
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.474999994	0.474999994 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.36195993	2.36195993 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.0999999	-3.0999999 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.88499177	1.88498998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.1400001	-3.1400001 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.62330723	8.62331009 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.23 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.296000004
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.55999994
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0504000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.07999992
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.40000001
k_APAEnableRate_pSec_f32	100.5
k_APAUseADmpScale_Igc	1
k_APAUseAsstScale_Igc	1
k_APAUseRetScale_Igc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	9011
t_APADisableRateX_HwNm_u4p12[1]	9421
t_APADisableRateX_HwNm_u4p12[2]	9830
t_APADisableRateX_HwNm_u4p12[3]	10240
t_APADisableRateX_HwNm_u4p12[4]	10650
t_APADisableRateX_HwNm_u4p12[5]	11059
t_APADisableRateY_pSec_u7p9[0]	11264
t_APADisableRateY_pSec_u7p9[1]	11776
t_APADisableRateY_pSec_u7p9[2]	12288
t_APADisableRateY_pSec_u7p9[3]	12800
t_APADisableRateY_pSec_u7p9[4]	13312
t_APADisableRateY_pSec_u7p9[5]	13824
t_APASmoothX_Uls_u2p14[0]	3604
t_APASmoothX_Uls_u2p14[1]	3768
t_APASmoothX_Uls_u2p14[2]	3932
t_APASmoothX_Uls_u2p14[3]	4096
t_APASmoothX_Uls_u2p14[4]	4260
t_APASmoothX_Uls_u2p14[5]	4424
t_APASmoothX_Uls_u2p14[6]	4588
t_APASmoothX_Uls_u2p14[7]	4751

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Name	Input Value
t_APASmoothX_Uls_u2p14[8]	4915
t_APASmoothX_Uls_u2p14[9]	5079
t_APASmoothY_Uls_u2p14[0]	655
t_APASmoothY_Uls_u2p14[1]	819
t_APASmoothY_Uls_u2p14[2]	983
t_APASmoothY_Uls_u2p14[3]	1147
t_APASmoothY_Uls_u2p14[4]	1311
t_APASmoothY_Uls_u2p14[5]	1475
t_APASmoothY_Uls_u2p14[6]	1638
t_APASmoothY_Uls_u2p14[7]	1802
t_APASmoothY_Uls_u2p14[8]	1966
t_APASmoothY_Uls_u2p14[9]	2130
t_LKASlewX_Kph_u8p8[0]	11264
t_LKASlewX_Kph_u8p8[1]	11776
t_LKASlewX_Kph_u8p8[2]	12288
t_LKASlewX_Kph_u8p8[3]	12800
t_LKASlewX_Kph_u8p8[4]	13312
t_LKASlewX_Kph_u8p8[5]	13824
t_LKASlewY_NmpSec_u4p12[0]	9011
t_LKASlewY_NmpSec_u4p12[1]	9421
t_LKASlewY_NmpSec_u4p12[2]	9830
t_LKASlewY_NmpSec_u4p12[3]	10240
t_LKASlewY_NmpSec_u4p12[4]	10650
t_LKASlewY_NmpSec_u4p12[5]	11059
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.11999989
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64000034
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.20000005
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	109.242996
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.9000001
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.241999999	0.241999999 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.94511986	2.9451201 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.07999992	-8.07999992 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	2.39400005	2.39400005 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.985007942	0.985008001 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.985007942	0.985008001 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-2.57999992	-2.57999992 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-2.57999992	-2.57999992 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.12113589	-0.121136002 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.985007942	0.985008001 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 3.24 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.308999985
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.5999999
TrqArblim_PrelKATrqReq_HwNm_M_f32	2.9000001
k_APAEnableRate_pSec_f32	105
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	9421
t_APADisableRateX_HwNm_u4p12[1]	9830
t_APADisableRateX_HwNm_u4p12[2]	10240
t_APADisableRateX_HwNm_u4p12[3]	10650
t_APADisableRateX_HwNm_u4p12[4]	11059
t_APADisableRateX_HwNm_u4p12[5]	11469
t_APADisableRateY_pSec_u7p9[0]	11776
t_APADisableRateY_pSec_u7p9[1]	12288
t_APADisableRateY_pSec_u7p9[2]	12800
t_APADisableRateY_pSec_u7p9[3]	13312
t_APADisableRateY_pSec_u7p9[4]	13824
t_APADisableRateY_pSec_u7p9[5]	14336
t_APASmoothX_Uls_u2p14[0]	3768
t_APASmoothX_Uls_u2p14[1]	3932
t_APASmoothX_Uls_u2p14[2]	4096
t_APASmoothX_Uls_u2p14[3]	4260
t_APASmoothX_Uls_u2p14[4]	4424
t_APASmoothX_Uls_u2p14[5]	4588
t_APASmoothX_Uls_u2p14[6]	4751
t_APASmoothX_Uls_u2p14[7]	4915
t_APASmoothX_Uls_u2p14[8]	5079
t_APASmoothX_Uls_u2p14[9]	5243
t_APASmoothY_Uls_u2p14[0]	819
t_APASmoothY_Uls_u2p14[1]	983
t_APASmoothY_Uls_u2p14[2]	1147
t_APASmoothY_Uls_u2p14[3]	1311
t_APASmoothY_Uls_u2p14[4]	1475
t_APASmoothY_Uls_u2p14[5]	1638
t_APASmoothY_Uls_u2p14[6]	1802
t_APASmoothY_Uls_u2p14[7]	1966
t_APASmoothY_Uls_u2p14[8]	2130
t_APASmoothY_Uls_u2p14[9]	2294
t_LKASlewX_Kph_u8p8[0]	11776
t_LKASlewX_Kph_u8p8[1]	12288
t_LKASlewX_Kph_u8p8[2]	12800
t_LKASlewX_Kph_u8p8[3]	13312
t_LKASlewX_Kph_u8p8[4]	13824
t_LKASlewX_Kph_u8p8[5]	14336
t_LKASlewY_NmpSec_u4p12[0]	9421
t_LKASlewY_NmpSec_u4p12[1]	9830
t_LKASlewY_NmpSec_u4p12[2]	10240
t_LKASlewY_NmpSec_u4p12[3]	10650
t_LKASlewY_NmpSec_u4p12[4]	11059
t_LKASlewY_NmpSec_u4p12[5]	11469
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32

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TrqArblim_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.02999973		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.54999995		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	115.693001		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.4000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.252999991	0.252999991 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.52828002	3.52828002 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.5999999	-7.5999999 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.88649678	2.88649988 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.88649678	2.88649678 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.88649678	2.88649678 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.25963926	4.25964022 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.25 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.321999997
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.78000021
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.11999989
TrqArblim_PrelKATrqReq_HwNm_M_f32	3.4000001
k_APAEnableRate_pSec_f32	109.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	9830
t_APADisableRateX_HwNm_u4p12[1]	10240
t_APADisableRateX_HwNm_u4p12[2]	10650
t_APADisableRateX_HwNm_u4p12[3]	11059
t_APADisableRateX_HwNm_u4p12[4]	11469
t_APADisableRateX_HwNm_u4p12[5]	11878
t_APADisableRateY_pSec_u7p9[0]	12288
t_APADisableRateY_pSec_u7p9[1]	12800
t_APADisableRateY_pSec_u7p9[2]	13312
t_APADisableRateY_pSec_u7p9[3]	13824
t_APADisableRateY_pSec_u7p9[4]	14336
t_APADisableRateY_pSec_u7p9[5]	14848
t_APASmoothX_Uls_u2p14[0]	3932
t_APASmoothX_Uls_u2p14[1]	4096
t_APASmoothX_Uls_u2p14[2]	4260
t_APASmoothX_Uls_u2p14[3]	4424
t_APASmoothX_Uls_u2p14[4]	4588
t_APASmoothX_Uls_u2p14[5]	4751

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TrqArblim_Per1

Name	Input Value		
t_APASmoothX_Uls_u2p14[6]	4915		
t_APASmoothX_Uls_u2p14[7]	5079		
t_APASmoothX_Uls_u2p14[8]	5243		
t_APASmoothX_Uls_u2p14[9]	5407		
t_APASmoothY_Uls_u2p14[0]	983		
t_APASmoothY_Uls_u2p14[1]	1147		
t_APASmoothY_Uls_u2p14[2]	1311		
t_APASmoothY_Uls_u2p14[3]	1475		
t_APASmoothY_Uls_u2p14[4]	1638		
t_APASmoothY_Uls_u2p14[5]	1802		
t_APASmoothY_Uls_u2p14[6]	1966		
t_APASmoothY_Uls_u2p14[7]	2130		
t_APASmoothY_Uls_u2p14[8]	2294		
t_APASmoothY_Uls_u2p14[9]	2458		
t_LKASlewX_Kph_u8p8[0]	12288		
t_LKASlewX_Kph_u8p8[1]	12800		
t_LKASlewX_Kph_u8p8[2]	13312		
t_LKASlewX_Kph_u8p8[3]	13824		
t_LKASlewX_Kph_u8p8[4]	14336		
t_LKASlewX_Kph_u8p8[5]	14848		
t_LKASlewY_NmpSec_u4p12[0]	9830		
t_LKASlewY_NmpSec_u4p12[1]	10240		
t_LKASlewY_NmpSec_u4p12[2]	10650		
t_LKASlewY_NmpSec_u4p12[3]	11059		
t_LKASlewY_NmpSec_u4p12[4]	11469		
t_LKASlewY_NmpSec_u4p12[5]	11878		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.15999985		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.42000008		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.89999998		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	122.142998		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-2.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.4000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.541000009	0.541000009 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	4	4	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.11144018	4.11144018 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-2.5	-2.5 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.39400005	3.39400005 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.918836772	0.918837011 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.918836772	0.918837011 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-4.60290813	-4.60291004 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.918836772	0.918837011 ± 0000390625	✔

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TrqArblim_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.26 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.335000008
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-6.63999987
TrqArblim_PrelKATrqReq_HwNm_M_f32	3.9000001
k_APAAEnableRate_pSec_f32	114
k_APAAUseADmpScale_lgc	0
k_APAAUseAsstScale_lgc	0
k_APAAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	10240
t_APADisableRateX_HwNm_u4p12[1]	10650
t_APADisableRateX_HwNm_u4p12[2]	11059
t_APADisableRateX_HwNm_u4p12[3]	11469
t_APADisableRateX_HwNm_u4p12[4]	11878
t_APADisableRateX_HwNm_u4p12[5]	12288
t_APADisableRateY_pSec_u7p9[0]	12800
t_APADisableRateY_pSec_u7p9[1]	13312
t_APADisableRateY_pSec_u7p9[2]	13824
t_APADisableRateY_pSec_u7p9[3]	14336
t_APADisableRateY_pSec_u7p9[4]	14848
t_APADisableRateY_pSec_u7p9[5]	15360
t_APASmoothX_Uls_u2p14[0]	4096
t_APASmoothX_Uls_u2p14[1]	4260
t_APASmoothX_Uls_u2p14[2]	4424
t_APASmoothX_Uls_u2p14[3]	4588
t_APASmoothX_Uls_u2p14[4]	4751
t_APASmoothX_Uls_u2p14[5]	4915
t_APASmoothX_Uls_u2p14[6]	5079
t_APASmoothX_Uls_u2p14[7]	5243
t_APASmoothX_Uls_u2p14[8]	5407
t_APASmoothX_Uls_u2p14[9]	5571
t_APASmoothY_Uls_u2p14[0]	1147
t_APASmoothY_Uls_u2p14[1]	1311
t_APASmoothY_Uls_u2p14[2]	1475
t_APASmoothY_Uls_u2p14[3]	1638
t_APASmoothY_Uls_u2p14[4]	1802
t_APASmoothY_Uls_u2p14[5]	1966
t_APASmoothY_Uls_u2p14[6]	2130
t_APASmoothY_Uls_u2p14[7]	2294
t_APASmoothY_Uls_u2p14[8]	2458
t_APASmoothY_Uls_u2p14[9]	2621
t_LKASlewX_Kph_u8p8[0]	12800
t_LKASlewX_Kph_u8p8[1]	13312
t_LKASlewX_Kph_u8p8[2]	13824
t_LKASlewX_Kph_u8p8[3]	14336
t_LKASlewX_Kph_u8p8[4]	14848
t_LKASlewX_Kph_u8p8[5]	15360
t_LKASlewY_NmpSec_u4p12[0]	10240
t_LKASlewY_NmpSec_u4p12[1]	10650
t_LKASlewY_NmpSec_u4p12[2]	11059
t_LKASlewY_NmpSec_u4p12[3]	11469
t_LKASlewY_NmpSec_u4p12[4]	11878
t_LKASlewY_NmpSec_u4p12[5]	12288
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCcmd_HwNm_f32	target_TrqArblim_Per1_ESCcmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum

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TrqArblim_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCcmd_HwNm_f32.value	-5.67999983		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.80999994		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.25		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.593002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.209999993		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.563000023	0.563000023 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	0	0	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.69460011	4.69460011 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.29999995	-2.29999995 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.88800168	3.88800001 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.00285077095	0.00285077002 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.27 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.34799999
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.4000001
k_APAEnableRate_pSec_f32	118.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	10650
t_APADisableRateX_HwNm_u4p12[1]	11059
t_APADisableRateX_HwNm_u4p12[2]	11469
t_APADisableRateX_HwNm_u4p12[3]	11878
t_APADisableRateX_HwNm_u4p12[4]	12288
t_APADisableRateX_HwNm_u4p12[5]	12698
t_APADisableRateY_pSec_u7p9[0]	13312
t_APADisableRateY_pSec_u7p9[1]	13824
t_APADisableRateY_pSec_u7p9[2]	14336
t_APADisableRateY_pSec_u7p9[3]	14848
t_APADisableRateY_pSec_u7p9[4]	15360

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	15872		
t_APASmoothX_Uls_u2p14[0]	4260		
t_APASmoothX_Uls_u2p14[1]	4424		
t_APASmoothX_Uls_u2p14[2]	4588		
t_APASmoothX_Uls_u2p14[3]	4751		
t_APASmoothX_Uls_u2p14[4]	4915		
t_APASmoothX_Uls_u2p14[5]	5079		
t_APASmoothX_Uls_u2p14[6]	5243		
t_APASmoothX_Uls_u2p14[7]	5407		
t_APASmoothX_Uls_u2p14[8]	5571		
t_APASmoothX_Uls_u2p14[9]	5734		
t_APASmoothY_Uls_u2p14[0]	1311		
t_APASmoothY_Uls_u2p14[1]	1475		
t_APASmoothY_Uls_u2p14[2]	1638		
t_APASmoothY_Uls_u2p14[3]	1802		
t_APASmoothY_Uls_u2p14[4]	1966		
t_APASmoothY_Uls_u2p14[5]	2130		
t_APASmoothY_Uls_u2p14[6]	2294		
t_APASmoothY_Uls_u2p14[7]	2458		
t_APASmoothY_Uls_u2p14[8]	2621		
t_APASmoothY_Uls_u2p14[9]	2785		
t_LKASlewX_Kph_u8p8[0]	13312		
t_LKASlewX_Kph_u8p8[1]	13824		
t_LKASlewX_Kph_u8p8[2]	14336		
t_LKASlewX_Kph_u8p8[3]	14848		
t_LKASlewX_Kph_u8p8[4]	15360		
t_LKASlewX_Kph_u8p8[5]	15872		
t_LKASlewY_NmpSec_u4p12[0]	10650		
t_LKASlewY_NmpSec_u4p12[1]	11059		
t_LKASlewY_NmpSec_u4p12[2]	11469		
t_LKASlewY_NmpSec_u4p12[3]	11878		
t_LKASlewY_NmpSec_u4p12[4]	12288		
t_LKASlewY_NmpSec_u4p12[5]	12698		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.19999981		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.59999999		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	135.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.09999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.560000002		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.285999984	0.286000013 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.27776003	5.27776003 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985	-6.15999985 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.39379978	4.39379978 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.186563313	-0.186563 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.28 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.361000001
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.61000013
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0614
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.67999983
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.90000001
k_APAEnableRate_pSec_f32	123
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	11059
t_APADisableRateX_HwNm_u4p12[1]	11469
t_APADisableRateX_HwNm_u4p12[2]	11878
t_APADisableRateX_HwNm_u4p12[3]	12288
t_APADisableRateX_HwNm_u4p12[4]	12698
t_APADisableRateX_HwNm_u4p12[5]	13107
t_APADisableRateY_pSec_u7p9[0]	13824
t_APADisableRateY_pSec_u7p9[1]	14336
t_APADisableRateY_pSec_u7p9[2]	14848
t_APADisableRateY_pSec_u7p9[3]	15360
t_APADisableRateY_pSec_u7p9[4]	15872
t_APADisableRateY_pSec_u7p9[5]	16384
t_APASmoothX_Uls_u2p14[0]	4424
t_APASmoothX_Uls_u2p14[1]	4588
t_APASmoothX_Uls_u2p14[2]	4751
t_APASmoothX_Uls_u2p14[3]	4915
t_APASmoothX_Uls_u2p14[4]	5079
t_APASmoothX_Uls_u2p14[5]	5243
t_APASmoothX_Uls_u2p14[6]	5407
t_APASmoothX_Uls_u2p14[7]	5571
t_APASmoothX_Uls_u2p14[8]	5734
t_APASmoothX_Uls_u2p14[9]	5898
t_APASmoothY_Uls_u2p14[0]	1475
t_APASmoothY_Uls_u2p14[1]	1638
t_APASmoothY_Uls_u2p14[2]	1802
t_APASmoothY_Uls_u2p14[3]	1966
t_APASmoothY_Uls_u2p14[4]	2130
t_APASmoothY_Uls_u2p14[5]	2294
t_APASmoothY_Uls_u2p14[6]	2458
t_APASmoothY_Uls_u2p14[7]	2621
t_APASmoothY_Uls_u2p14[8]	2785
t_APASmoothY_Uls_u2p14[9]	2949
t_LKASlewX_Kph_u8p8[0]	13824
t_LKASlewX_Kph_u8p8[1]	14336
t_LKASlewX_Kph_u8p8[2]	14848
t_LKASlewX_Kph_u8p8[3]	15360
t_LKASlewX_Kph_u8p8[4]	15872
t_LKASlewX_Kph_u8p8[5]	16384
t_LKASlewY_NmpSec_u4p12[0]	11059
t_LKASlewY_NmpSec_u4p12[1]	11469
t_LKASlewY_NmpSec_u4p12[2]	11878
t_LKASlewY_NmpSec_u4p12[3]	12288
t_LKASlewY_NmpSec_u4p12[4]	12698
t_LKASlewY_NmpSec_u4p12[5]	13107
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32

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TrqArblim_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.59000015		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.95000005		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	141.492996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-1.89999998		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.910000026		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.606999993	0.606999993 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.86091995	5.86091995 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-1.89999998	-1.89999998 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.88950682	4.88951015 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.69000006	-3.69000006 ± 0000390625	✔
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.207585543	-0.207586005 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.29 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.374000013
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.21999979
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0636000037
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-5.19999981
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.4000001
k_APAEnableRate_pSec_f32	127.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	11469
t_APADisableRateX_HwNm_u4p12[1]	11878
t_APADisableRateX_HwNm_u4p12[2]	12288
t_APADisableRateX_HwNm_u4p12[3]	12698
t_APADisableRateX_HwNm_u4p12[4]	13107
t_APADisableRateX_HwNm_u4p12[5]	13517
t_APADisableRateY_pSec_u7p9[0]	14336
t_APADisableRateY_pSec_u7p9[1]	14848

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	15360		
t_APADisableRateY_pSec_u7p9[3]	15872		
t_APADisableRateY_pSec_u7p9[4]	16384		
t_APADisableRateY_pSec_u7p9[5]	16896		
t_APASmoothX_Uls_u2p14[0]	4588		
t_APASmoothX_Uls_u2p14[1]	4751		
t_APASmoothX_Uls_u2p14[2]	4915		
t_APASmoothX_Uls_u2p14[3]	5079		
t_APASmoothX_Uls_u2p14[4]	5243		
t_APASmoothX_Uls_u2p14[5]	5407		
t_APASmoothX_Uls_u2p14[6]	5571		
t_APASmoothX_Uls_u2p14[7]	5734		
t_APASmoothX_Uls_u2p14[8]	5898		
t_APASmoothX_Uls_u2p14[9]	6062		
t_APASmoothY_Uls_u2p14[0]	1638		
t_APASmoothY_Uls_u2p14[1]	1802		
t_APASmoothY_Uls_u2p14[2]	1966		
t_APASmoothY_Uls_u2p14[3]	2130		
t_APASmoothY_Uls_u2p14[4]	2294		
t_APASmoothY_Uls_u2p14[5]	2458		
t_APASmoothY_Uls_u2p14[6]	2621		
t_APASmoothY_Uls_u2p14[7]	2785		
t_APASmoothY_Uls_u2p14[8]	2949		
t_APASmoothY_Uls_u2p14[9]	3113		
t_LKASlewX_Kph_u8p8[0]	14336		
t_LKASlewX_Kph_u8p8[1]	14848		
t_LKASlewX_Kph_u8p8[2]	15360		
t_LKASlewX_Kph_u8p8[3]	15872		
t_LKASlewX_Kph_u8p8[4]	16384		
t_LKASlewX_Kph_u8p8[5]	16896		
t_LKASlewY_NmpSec_u4p12[0]	11469		
t_LKASlewY_NmpSec_u4p12[1]	11878		
t_LKASlewY_NmpSec_u4p12[2]	12288		
t_LKASlewY_NmpSec_u4p12[3]	12698		
t_LKASlewY_NmpSec_u4p12[4]	13107		
t_LKASlewY_NmpSec_u4p12[5]	13517		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHocillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-4.98000002		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	3.29999995		
target_TrqArblim_Per1_LKASState_State_enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.942993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-1.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.25999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.308000028	0.307999998 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.44407988	6.44407988 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-5.19999981	-5.19999981 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.39340019	5.39340019 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.960578859	0.960578978 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.960578859	0.960578978 ± 0.000390625	✔
target TrqArblim Per1 ESCIsLimited Cnt lgc.value	0	0	

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.20498994	-0.20499 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.960578859	0.960578978 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.30 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.386999995
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.82999992
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0658000037
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-4.71999979
TrqArblim_PrelKATrqReq_HwNm_M_f32	5.90000001
k_APAEnableRate_pSec_f32	132
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	11878
t_APADisableRateX_HwNm_u4p12[1]	12288
t_APADisableRateX_HwNm_u4p12[2]	12698
t_APADisableRateX_HwNm_u4p12[3]	13107
t_APADisableRateX_HwNm_u4p12[4]	13517
t_APADisableRateX_HwNm_u4p12[5]	13926
t_APADisableRateY_pSec_u7p9[0]	14848
t_APADisableRateY_pSec_u7p9[1]	15360
t_APADisableRateY_pSec_u7p9[2]	15872
t_APADisableRateY_pSec_u7p9[3]	16384
t_APADisableRateY_pSec_u7p9[4]	16896
t_APADisableRateY_pSec_u7p9[5]	17408
t_APASmoothX_Uls_u2p14[0]	4751
t_APASmoothX_Uls_u2p14[1]	4915
t_APASmoothX_Uls_u2p14[2]	5079
t_APASmoothX_Uls_u2p14[3]	5243
t_APASmoothX_Uls_u2p14[4]	5407
t_APASmoothX_Uls_u2p14[5]	5571
t_APASmoothX_Uls_u2p14[6]	5734
t_APASmoothX_Uls_u2p14[7]	5898
t_APASmoothX_Uls_u2p14[8]	6062
t_APASmoothX_Uls_u2p14[9]	6226
t_APASmoothY_Uls_u2p14[0]	1802
t_APASmoothY_Uls_u2p14[1]	1966
t_APASmoothY_Uls_u2p14[2]	2130
t_APASmoothY_Uls_u2p14[3]	2294
t_APASmoothY_Uls_u2p14[4]	2458
t_APASmoothY_Uls_u2p14[5]	2621
t_APASmoothY_Uls_u2p14[6]	2785
t_APASmoothY_Uls_u2p14[7]	2949
t_APASmoothY_Uls_u2p14[8]	3113
t_APASmoothY_Uls_u2p14[9]	3277
t_LKASlewX_Kph_u8p8[0]	14848
t_LKASlewX_Kph_u8p8[1]	15360
t_LKASlewX_Kph_u8p8[2]	15872
t_LKASlewX_Kph_u8p8[3]	16384
t_LKASlewX_Kph_u8p8[4]	16896
t_LKASlewX_Kph_u8p8[5]	17408
t_LKASlewY_NmpSec_u4p12[0]	11878
t_LKASlewY_NmpSec_u4p12[1]	12288
t_LKASlewY_NmpSec_u4p12[2]	12698
t_LKASlewY_NmpSec_u4p12[3]	13107
t_LKASlewY_NmpSec_u4p12[4]	13517

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[5]	13926
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-4.36999989
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	3.6500001
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	154.393005
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.61000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.651000023	0.651000023 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.0272398	7.0272398 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.5	-1.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.89101171	5.89100981 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.41468811	1.41469002 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.31 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.400000006
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.43999958
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0680000037
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.23999977
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.4000001
k_APAEnableRate_pSec_f32	136.5
k_APAUseADmpScale_Igc	1
k_APAUseAsstScale_Igc	1
k_APAUseRetScale_Igc	1
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	12288
t_APADisableRateX_HwNm_u4p12[1]	12698
t_APADisableRateX_HwNm_u4p12[2]	13107
t_APADisableRateX_HwNm_u4p12[3]	13517
t_APADisableRateX_HwNm_u4p12[4]	13926

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	14336		
t_APADisableRateY_pSec_u7p9[0]	15360		
t_APADisableRateY_pSec_u7p9[1]	15872		
t_APADisableRateY_pSec_u7p9[2]	16384		
t_APADisableRateY_pSec_u7p9[3]	16896		
t_APADisableRateY_pSec_u7p9[4]	17408		
t_APADisableRateY_pSec_u7p9[5]	17920		
t_APASmoothX_Uls_u2p14[0]	4915		
t_APASmoothX_Uls_u2p14[1]	5079		
t_APASmoothX_Uls_u2p14[2]	5243		
t_APASmoothX_Uls_u2p14[3]	5407		
t_APASmoothX_Uls_u2p14[4]	5571		
t_APASmoothX_Uls_u2p14[5]	5734		
t_APASmoothX_Uls_u2p14[6]	5898		
t_APASmoothX_Uls_u2p14[7]	6062		
t_APASmoothX_Uls_u2p14[8]	6226		
t_APASmoothX_Uls_u2p14[9]	6390		
t_APASmoothY_Uls_u2p14[0]	1966		
t_APASmoothY_Uls_u2p14[1]	2130		
t_APASmoothY_Uls_u2p14[2]	2294		
t_APASmoothY_Uls_u2p14[3]	2458		
t_APASmoothY_Uls_u2p14[4]	2621		
t_APASmoothY_Uls_u2p14[5]	2785		
t_APASmoothY_Uls_u2p14[6]	2949		
t_APASmoothY_Uls_u2p14[7]	3113		
t_APASmoothY_Uls_u2p14[8]	3277		
t_APASmoothY_Uls_u2p14[9]	3441		
t_LKASlewX_Kph_u8p8[0]	15360		
t_LKASlewX_Kph_u8p8[1]	15872		
t_LKASlewX_Kph_u8p8[2]	16384		
t_LKASlewX_Kph_u8p8[3]	16896		
t_LKASlewX_Kph_u8p8[4]	17408		
t_LKASlewX_Kph_u8p8[5]	17920		
t_LKASlewY_NmpSec_u4p12[0]	12288		
t_LKASlewY_NmpSec_u4p12[1]	12698		
t_LKASlewY_NmpSec_u4p12[2]	13107		
t_LKASlewY_NmpSec_u4p12[3]	13517		
t_LKASlewY_NmpSec_u4p12[4]	13926		
t_LKASlewY_NmpSec_u4p12[5]	14336		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCoscillate_State_enum	target_TrqArblim_Per1_GMOSHOCoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.82000017		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOCoscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-3.75999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	160.843002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-1.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.96000004		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.330000013	0.330000013 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	0	0	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.61039972	7.6104002 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-4.23999977	-4.23999977 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.39300013	6.39300013 ± 0.03125	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.950512052	0.950511992 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.950512052	0.950511992 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.209828764	-0.209829003 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.950512052	0.950511992 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1		✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3		✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1		✓

Test Step 3.32 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.412999988	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0702000037	
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-3.75999999	
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.9000001	
k_APAEnableRate_pSec_f32	141	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	1.58000004	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	12698	
t_APADisableRateX_HwNm_u4p12[1]	13107	
t_APADisableRateX_HwNm_u4p12[2]	13517	
t_APADisableRateX_HwNm_u4p12[3]	13926	
t_APADisableRateX_HwNm_u4p12[4]	14336	
t_APADisableRateX_HwNm_u4p12[5]	14746	
t_APADisableRateY_pSec_u7p9[0]	15872	
t_APADisableRateY_pSec_u7p9[1]	16384	
t_APADisableRateY_pSec_u7p9[2]	16896	
t_APADisableRateY_pSec_u7p9[3]	17408	
t_APADisableRateY_pSec_u7p9[4]	17920	
t_APADisableRateY_pSec_u7p9[5]	18432	
t_APASmoothX_Uls_u2p14[0]	5079	
t_APASmoothX_Uls_u2p14[1]	5243	
t_APASmoothX_Uls_u2p14[2]	5407	
t_APASmoothX_Uls_u2p14[3]	5571	
t_APASmoothX_Uls_u2p14[4]	5734	
t_APASmoothX_Uls_u2p14[5]	5898	
t_APASmoothX_Uls_u2p14[6]	6062	
t_APASmoothX_Uls_u2p14[7]	6226	
t_APASmoothX_Uls_u2p14[8]	6390	
t_APASmoothX_Uls_u2p14[9]	6554	
t_APASmoothY_Uls_u2p14[0]	2130	
t_APASmoothY_Uls_u2p14[1]	2294	
t_APASmoothY_Uls_u2p14[2]	2458	
t_APASmoothY_Uls_u2p14[3]	2621	
t_APASmoothY_Uls_u2p14[4]	2785	
t_APASmoothY_Uls_u2p14[5]	2949	
t_APASmoothY_Uls_u2p14[6]	3113	
t_APASmoothY_Uls_u2p14[7]	3277	
t_APASmoothY_Uls_u2p14[8]	3441	
t_APASmoothY_Uls_u2p14[9]	3604	
t_LKASlewX_Kph_u8p8[0]	15872	
t_LKASlewX_Kph_u8p8[1]	16384	
t_LKASlewX_Kph_u8p8[2]	16896	
t_LKASlewX_Kph_u8p8[3]	17408	
t_LKASlewX_Kph_u8p8[4]	17920	
t_LKASlewX_Kph_u8p8[5]	18432	
t_LKASlewY_NmpSec_u4p12[0]	12698	
t_LKASlewY_NmpSec_u4p12[1]	13107	

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[2]	13517
t_LKASlewY_NmpSec_u4p12[3]	13926
t_LKASlewY_NmpSec_u4p12[4]	14336
t_LKASlewY_NmpSec_u4p12[5]	14746
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-3.15000001
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.34999999
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.292999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.10000002
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.30999994

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.694999993	0.694999993 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.19355965	8.19355965 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.10000002	-1.10000002 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	6.89251661	6.89251995 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.58000004	-1.58000004 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-1.58000004	-1.58000004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.14183235	2.14182997 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.33 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.425999999
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.65999985
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0724000037
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.27999997
TrqArblim_PrelKATrqReq_HwNm_M_f32	7.4000001
k_APAEnableRate_pSec_f32	145.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	13107
t_APADisableRateX_HwNm_u4p12[1]	13517

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	13926		
t_APADisableRateX_HwNm_u4p12[3]	14336		
t_APADisableRateX_HwNm_u4p12[4]	14746		
t_APADisableRateX_HwNm_u4p12[5]	15155		
t_APADisableRateY_pSec_u7p9[0]	16384		
t_APADisableRateY_pSec_u7p9[1]	16896		
t_APADisableRateY_pSec_u7p9[2]	17408		
t_APADisableRateY_pSec_u7p9[3]	17920		
t_APADisableRateY_pSec_u7p9[4]	18432		
t_APADisableRateY_pSec_u7p9[5]	18944		
t_APASmoothX_Uls_u2p14[0]	5243		
t_APASmoothX_Uls_u2p14[1]	5407		
t_APASmoothX_Uls_u2p14[2]	5571		
t_APASmoothX_Uls_u2p14[3]	5734		
t_APASmoothX_Uls_u2p14[4]	5898		
t_APASmoothX_Uls_u2p14[5]	6062		
t_APASmoothX_Uls_u2p14[6]	6226		
t_APASmoothX_Uls_u2p14[7]	6390		
t_APASmoothX_Uls_u2p14[8]	6554		
t_APASmoothX_Uls_u2p14[9]	6717		
t_APASmoothY_Uls_u2p14[0]	2294		
t_APASmoothY_Uls_u2p14[1]	2458		
t_APASmoothY_Uls_u2p14[2]	2621		
t_APASmoothY_Uls_u2p14[3]	2785		
t_APASmoothY_Uls_u2p14[4]	2949		
t_APASmoothY_Uls_u2p14[5]	3113		
t_APASmoothY_Uls_u2p14[6]	3277		
t_APASmoothY_Uls_u2p14[7]	3441		
t_APASmoothY_Uls_u2p14[8]	3604		
t_APASmoothY_Uls_u2p14[9]	3768		
t_LKASlewX_Kph_u8p8[0]	16384		
t_LKASlewX_Kph_u8p8[1]	16896		
t_LKASlewX_Kph_u8p8[2]	17408		
t_LKASlewX_Kph_u8p8[3]	17920		
t_LKASlewX_Kph_u8p8[4]	18432		
t_LKASlewX_Kph_u8p8[5]	18944		
t_LKASlewY_NmpSec_u4p12[0]	13107		
t_LKASlewY_NmpSec_u4p12[1]	13517		
t_LKASlewY_NmpSec_u4p12[2]	13926		
t_LKASlewY_NmpSec_u4p12[3]	14336		
t_LKASlewY_NmpSec_u4p12[4]	14746		
t_LKASlewY_NmpSec_u4p12[5]	15155		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.42000008		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-2.53999996		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.69999981		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	173.742996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-0.899999976		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.66000009		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.351999998	0.351999998 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.77672005	8.77672005 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.27999997	-3.27999997 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.39260006	7.39260006 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.939457059	0.939456999 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.939457059	0.939456999 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4.28000021	-4.28000021 ± 0.000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.198580936	-0.198580995 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.939457059	0.939456999 ± 0.000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.34 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.43900001
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0746000037
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.79999995
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.90000001
k_APAEnableRate_pSec_f32	150
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	13517
t_APADisableRateX_HwNm_u4p12[1]	13926
t_APADisableRateX_HwNm_u4p12[2]	14336
t_APADisableRateX_HwNm_u4p12[3]	14746
t_APADisableRateX_HwNm_u4p12[4]	15155
t_APADisableRateX_HwNm_u4p12[5]	15565
t_APADisableRateY_pSec_u7p9[0]	16896
t_APADisableRateY_pSec_u7p9[1]	17408
t_APADisableRateY_pSec_u7p9[2]	17920
t_APADisableRateY_pSec_u7p9[3]	18432
t_APADisableRateY_pSec_u7p9[4]	18944
t_APADisableRateY_pSec_u7p9[5]	19456
t_APASmoothX_Uls_u2p14[0]	5407
t_APASmoothX_Uls_u2p14[1]	5571
t_APASmoothX_Uls_u2p14[2]	5734
t_APASmoothX_Uls_u2p14[3]	5898
t_APASmoothX_Uls_u2p14[4]	6062
t_APASmoothX_Uls_u2p14[5]	6226
t_APASmoothX_Uls_u2p14[6]	6390
t_APASmoothX_Uls_u2p14[7]	6554
t_APASmoothX_Uls_u2p14[8]	6717
t_APASmoothX_Uls_u2p14[9]	6881
t_APASmoothY_Uls_u2p14[0]	2458
t_APASmoothY_Uls_u2p14[1]	2621
t_APASmoothY_Uls_u2p14[2]	2785
t_APASmoothY_Uls_u2p14[3]	2949
t_APASmoothY_Uls_u2p14[4]	3113
t_APASmoothY_Uls_u2p14[5]	3277
t_APASmoothY_Uls_u2p14[6]	3441
t_APASmoothY_Uls_u2p14[7]	3604
t_APASmoothY_Uls_u2p14[8]	3768
t_APASmoothY_Uls_u2p14[9]	3932
t_LKASlewX_Kph_u8p8[0]	16896
t_LKASlewX_Kph_u8p8[1]	17408
t_LKASlewX_Kph_u8p8[2]	17920
t_LKASlewX_Kph_u8p8[3]	18432
t_LKASlewX_Kph_u8p8[4]	18944

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[5]	19456
t_LKASlewY_NmpSec_u4p12[0]	13517
t_LKASlewY_NmpSec_u4p12[1]	13926
t_LKASlewY_NmpSec_u4p12[2]	14336
t_LKASlewY_NmpSec_u4p12[3]	14746
t_LKASlewY_NmpSec_u4p12[4]	15155
t_LKASlewY_NmpSec_u4p12[5]	15565
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.21999979
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-1.92999995
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.05000019
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	180.192993
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.699999988
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.00999999

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.363000005	0.363000005 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.39797783	-9.39797974 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.79999995	-2.79999995 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	7.89400005	7.89400005 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.185984328	-0.185984001 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.35 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.451999992	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0768000036	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.31999993	
TrqArblim_PrelKATrqReq_HwNm_M_f32	8.39999962	
k_APAEnableRate_pSec_f32	154.5	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	3.1400001	

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TrqArblim_Per1

Name	Input Value
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	13926
t_APADisableRateX_HwNm_u4p12[1]	14336
t_APADisableRateX_HwNm_u4p12[2]	14746
t_APADisableRateX_HwNm_u4p12[3]	15155
t_APADisableRateX_HwNm_u4p12[4]	15565
t_APADisableRateX_HwNm_u4p12[5]	15974
t_APADisableRateY_pSec_u7p9[0]	17408
t_APADisableRateY_pSec_u7p9[1]	17920
t_APADisableRateY_pSec_u7p9[2]	18432
t_APADisableRateY_pSec_u7p9[3]	18944
t_APADisableRateY_pSec_u7p9[4]	19456
t_APADisableRateY_pSec_u7p9[5]	19968
t_APASmoothX_Uls_u2p14[0]	5571
t_APASmoothX_Uls_u2p14[1]	5734
t_APASmoothX_Uls_u2p14[2]	5898
t_APASmoothX_Uls_u2p14[3]	6062
t_APASmoothX_Uls_u2p14[4]	6226
t_APASmoothX_Uls_u2p14[5]	6390
t_APASmoothX_Uls_u2p14[6]	6554
t_APASmoothX_Uls_u2p14[7]	6717
t_APASmoothX_Uls_u2p14[8]	6881
t_APASmoothX_Uls_u2p14[9]	7045
t_APASmoothY_Uls_u2p14[0]	2621
t_APASmoothY_Uls_u2p14[1]	2785
t_APASmoothY_Uls_u2p14[2]	2949
t_APASmoothY_Uls_u2p14[3]	3113
t_APASmoothY_Uls_u2p14[4]	3277
t_APASmoothY_Uls_u2p14[5]	3441
t_APASmoothY_Uls_u2p14[6]	3604
t_APASmoothY_Uls_u2p14[7]	3768
t_APASmoothY_Uls_u2p14[8]	3932
t_APASmoothY_Uls_u2p14[9]	4096
t_LKASlewX_Kph_u8p8[0]	17408
t_LKASlewX_Kph_u8p8[1]	17920
t_LKASlewX_Kph_u8p8[2]	18432
t_LKASlewX_Kph_u8p8[3]	18944
t_LKASlewX_Kph_u8p8[4]	19456
t_LKASlewX_Kph_u8p8[5]	19968
t_LKASlewY_NmpSec_u4p12[0]	13926
t_LKASlewY_NmpSec_u4p12[1]	14336
t_LKASlewY_NmpSec_u4p12[2]	14746
t_LKASlewY_NmpSec_u4p12[3]	15155
t_LKASlewY_NmpSec_u4p12[4]	15565
t_LKASlewY_NmpSec_u4p12[5]	15974
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.01999998
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-1.32000005
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.4000001
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	186.643005
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.3599999

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.761000037	0.760999978 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.13062382	9.13062 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.5	-0.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.39219952	8.39220047 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0951250046	-0.0951249972 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.36 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.465000004	
TrqArblim_ESC_State_Cnt_M_enum	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0790000036	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.84000003	
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.69999981	
k_APAEnableRate_pSec_f32	159	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	2.57999992	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	14336	
t_APADisableRateX_HwNm_u4p12[1]	14746	
t_APADisableRateX_HwNm_u4p12[2]	15155	
t_APADisableRateX_HwNm_u4p12[3]	15565	
t_APADisableRateX_HwNm_u4p12[4]	15974	
t_APADisableRateX_HwNm_u4p12[5]	16384	
t_APADisableRateY_pSec_u7p9[0]	17920	
t_APADisableRateY_pSec_u7p9[1]	18432	
t_APADisableRateY_pSec_u7p9[2]	18944	
t_APADisableRateY_pSec_u7p9[3]	19456	
t_APADisableRateY_pSec_u7p9[4]	19968	
t_APADisableRateY_pSec_u7p9[5]	20480	
t_APASmoothX_Uls_u2p14[0]	5734	
t_APASmoothX_Uls_u2p14[1]	5898	
t_APASmoothX_Uls_u2p14[2]	6062	
t_APASmoothX_Uls_u2p14[3]	6226	
t_APASmoothX_Uls_u2p14[4]	6390	
t_APASmoothX_Uls_u2p14[5]	6554	
t_APASmoothX_Uls_u2p14[6]	6717	
t_APASmoothX_Uls_u2p14[7]	6881	
t_APASmoothX_Uls_u2p14[8]	7045	
t_APASmoothX_Uls_u2p14[9]	7209	
t_APASmoothY_Uls_u2p14[0]	2785	
t_APASmoothY_Uls_u2p14[1]	2949	
t_APASmoothY_Uls_u2p14[2]	3113	
t_APASmoothY_Uls_u2p14[3]	3277	
t_APASmoothY_Uls_u2p14[4]	3441	
t_APASmoothY_Uls_u2p14[5]	3604	
t_APASmoothY_Uls_u2p14[6]	3768	
t_APASmoothY_Uls_u2p14[7]	3932	
t_APASmoothY_Uls_u2p14[8]	4096	
t_APASmoothY_Uls_u2p14[9]	4260	
t_LKASlewX_Kph_u8p8[0]	17920	
t_LKASlewX_Kph_u8p8[1]	18432	
t_LKASlewX_Kph_u8p8[2]	18944	

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[3]	19456
t_LKASlewX_Kph_u8p8[4]	19968
t_LKASlewX_Kph_u8p8[5]	20480
t_LKASlewY_NmpSec_u4p12[0]	14336
t_LKASlewY_NmpSec_u4p12[1]	14746
t_LKASlewY_NmpSec_u4p12[2]	15155
t_LKASlewY_NmpSec_u4p12[3]	15565
t_LKASlewY_NmpSec_u4p12[4]	15974
t_LKASlewY_NmpSec_u4p12[5]	16384
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-0.709999979
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.75
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	193.093002
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.300000012
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.4000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.782999992	0.782999992 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.056090001	-0.056090001 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.300000012	-0.300000012 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	8.69400024	8.69400024 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-4.46107626	-4.46108007 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.37 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.477999985
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0812000036
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.36000001
TrqArblim_PrelKATrqReq_HwNm_M_f32	-8
k_APAEnableRate_pSec_f32	163.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1

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TrqArblim_Per1

Name	Input Value
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	14746
t_APADisableRateX_HwNm_u4p12[1]	15155
t_APADisableRateX_HwNm_u4p12[2]	15565
t_APADisableRateX_HwNm_u4p12[3]	15974
t_APADisableRateX_HwNm_u4p12[4]	16384
t_APADisableRateX_HwNm_u4p12[5]	16794
t_APADisableRateY_pSec_u7p9[0]	18432
t_APADisableRateY_pSec_u7p9[1]	18944
t_APADisableRateY_pSec_u7p9[2]	19456
t_APADisableRateY_pSec_u7p9[3]	19968
t_APADisableRateY_pSec_u7p9[4]	20480
t_APADisableRateY_pSec_u7p9[5]	20992
t_APASmoothX_Uls_u2p14[0]	5898
t_APASmoothX_Uls_u2p14[1]	6062
t_APASmoothX_Uls_u2p14[2]	6226
t_APASmoothX_Uls_u2p14[3]	6390
t_APASmoothX_Uls_u2p14[4]	6554
t_APASmoothX_Uls_u2p14[5]	6717
t_APASmoothX_Uls_u2p14[6]	6881
t_APASmoothX_Uls_u2p14[7]	7045
t_APASmoothX_Uls_u2p14[8]	7209
t_APASmoothX_Uls_u2p14[9]	7373
t_APASmoothY_Uls_u2p14[0]	2949
t_APASmoothY_Uls_u2p14[1]	3113
t_APASmoothY_Uls_u2p14[2]	3277
t_APASmoothY_Uls_u2p14[3]	3441
t_APASmoothY_Uls_u2p14[4]	3604
t_APASmoothY_Uls_u2p14[5]	3768
t_APASmoothY_Uls_u2p14[6]	3932
t_APASmoothY_Uls_u2p14[7]	4096
t_APASmoothY_Uls_u2p14[8]	4260
t_APASmoothY_Uls_u2p14[9]	4424
t_LKASlewX_Kph_u8p8[0]	18432
t_LKASlewX_Kph_u8p8[1]	18944
t_LKASlewX_Kph_u8p8[2]	19456
t_LKASlewX_Kph_u8p8[3]	19968
t_LKASlewX_Kph_u8p8[4]	20480
t_LKASlewX_Kph_u8p8[5]	20992
t_LKASlewY_NmpSec_u4p12[0]	14746
t_LKASlewY_NmpSec_u4p12[1]	15155
t_LKASlewY_NmpSec_u4p12[2]	15565
t_LKASlewY_NmpSec_u4p12[3]	15974
t_LKASlewY_NmpSec_u4p12[4]	16384
t_LKASlewY_NmpSec_u4p12[5]	16794
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-0.620000005
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-0.100000001
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.0999999
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	199.542999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-0.100000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.395999968	0.395999998 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.81707191	5.81707001 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-1.36000001	-1.36000001 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.99179983	-7.99179983 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.239999995	-0.239999995 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-0.239999995	-0.239999995 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.116297916	-0.116297998 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.38 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.490999997
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.86000013
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0834000036
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-0.879999995
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7
k_APAEnableRate_pSec_f32	168
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	15155
t_APADisableRateX_HwNm_u4p12[1]	15565
t_APADisableRateX_HwNm_u4p12[2]	15974
t_APADisableRateX_HwNm_u4p12[3]	16384
t_APADisableRateX_HwNm_u4p12[4]	16794
t_APADisableRateX_HwNm_u4p12[5]	17203
t_APADisableRateY_pSec_u7p9[0]	18944
t_APADisableRateY_pSec_u7p9[1]	19456
t_APADisableRateY_pSec_u7p9[2]	19968
t_APADisableRateY_pSec_u7p9[3]	20480
t_APADisableRateY_pSec_u7p9[4]	20992
t_APADisableRateY_pSec_u7p9[5]	21504
t_APASmoothX_Uls_u2p14[0]	6062
t_APASmoothX_Uls_u2p14[1]	6226
t_APASmoothX_Uls_u2p14[2]	6390
t_APASmoothX_Uls_u2p14[3]	6554
t_APASmoothX_Uls_u2p14[4]	6717
t_APASmoothX_Uls_u2p14[5]	6881
t_APASmoothX_Uls_u2p14[6]	7045
t_APASmoothX_Uls_u2p14[7]	7209
t_APASmoothX_Uls_u2p14[8]	7373
t_APASmoothX_Uls_u2p14[9]	7537
t_APASmoothY_Uls_u2p14[0]	3113
t_APASmoothY_Uls_u2p14[1]	3277
t_APASmoothY_Uls_u2p14[2]	3441
t_APASmoothY_Uls_u2p14[3]	3604
t_APASmoothY_Uls_u2p14[4]	3768
t_APASmoothY_Uls_u2p14[5]	3932
t_APASmoothY_Uls_u2p14[6]	4096
t_APASmoothY_Uls_u2p14[7]	4260
t_APASmoothY_Uls_u2p14[8]	4424
t_APASmoothY_Uls_u2p14[9]	4588

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TrqArblim_Per1

Name	Input Value		
t_LKASlewX_Kph_u8p8[0]	18944		
t_LKASlewX_Kph_u8p8[1]	19456		
t_LKASlewX_Kph_u8p8[2]	19968		
t_LKASlewX_Kph_u8p8[3]	20480		
t_LKASlewX_Kph_u8p8[4]	20992		
t_LKASlewX_Kph_u8p8[5]	21504		
t_LKASlewY_NmpSec_u4p12[0]	15155		
t_LKASlewY_NmpSec_u4p12[1]	15565		
t_LKASlewY_NmpSec_u4p12[2]	15974		
t_LKASlewY_NmpSec_u4p12[3]	16384		
t_LKASlewY_NmpSec_u4p12[4]	16794		
t_LKASlewY_NmpSec_u4p12[5]	17203		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.509999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.449999981		
target_TrqArblim_Per1_LKASState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	205.992996		
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.100000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.09999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.407000005	0.407000005 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.32874203	-5.32874012 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.879999995	-0.879999995 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.99399996	-6.99399996 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.140000001	0.140000001 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.140000001	0.140000001 ± 0.000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-4.1812768	-4.18128014 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.39 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.504000008
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.6499999976
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.400000006
TrqArblim_PrelKATrqReq_HwNm_M_f32	-6

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TrqArblim_Per1

Name	Input Value
k_APAEnableRate_pSec_f32	172.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	15565
t_APADisableRateX_HwNm_u4p12[1]	15974
t_APADisableRateX_HwNm_u4p12[2]	16384
t_APADisableRateX_HwNm_u4p12[3]	16794
t_APADisableRateX_HwNm_u4p12[4]	17203
t_APADisableRateX_HwNm_u4p12[5]	17613
t_APADisableRateY_pSec_u7p9[0]	19456
t_APADisableRateY_pSec_u7p9[1]	19968
t_APADisableRateY_pSec_u7p9[2]	20480
t_APADisableRateY_pSec_u7p9[3]	20992
t_APADisableRateY_pSec_u7p9[4]	21504
t_APADisableRateY_pSec_u7p9[5]	22016
t_APASmoothX_Uls_u2p14[0]	6226
t_APASmoothX_Uls_u2p14[1]	6390
t_APASmoothX_Uls_u2p14[2]	6554
t_APASmoothX_Uls_u2p14[3]	6717
t_APASmoothX_Uls_u2p14[4]	6881
t_APASmoothX_Uls_u2p14[5]	7045
t_APASmoothX_Uls_u2p14[6]	7209
t_APASmoothX_Uls_u2p14[7]	7373
t_APASmoothX_Uls_u2p14[8]	7537
t_APASmoothX_Uls_u2p14[9]	7700
t_APASmoothY_Uls_u2p14[0]	3277
t_APASmoothY_Uls_u2p14[1]	3441
t_APASmoothY_Uls_u2p14[2]	3604
t_APASmoothY_Uls_u2p14[3]	3768
t_APASmoothY_Uls_u2p14[4]	3932
t_APASmoothY_Uls_u2p14[5]	4096
t_APASmoothY_Uls_u2p14[6]	4260
t_APASmoothY_Uls_u2p14[7]	4424
t_APASmoothY_Uls_u2p14[8]	4588
t_APASmoothY_Uls_u2p14[9]	4751
t_LKASlewX_Kph_u8p8[0]	19456
t_LKASlewX_Kph_u8p8[1]	19968
t_LKASlewX_Kph_u8p8[2]	20480
t_LKASlewX_Kph_u8p8[3]	20992
t_LKASlewX_Kph_u8p8[4]	21504
t_LKASlewX_Kph_u8p8[5]	22016
t_LKASlewY_NmpSec_u4p12[0]	15565
t_LKASlewY_NmpSec_u4p12[1]	15974
t_LKASlewY_NmpSec_u4p12[2]	16384
t_LKASlewY_NmpSec_u4p12[3]	16794
t_LKASlewY_NmpSec_u4p12[4]	17203
t_LKASlewY_NmpSec_u4p12[5]	17613
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	1.77999997
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.12
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.80000019

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TrqArblim_Per1

Name	Input Value
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	212.442993
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.300000012
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.9000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.849000037	0.848999977 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.650590241	0.650590003 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.300000012	0.300000012 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.99139977	-5.99139977 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.0738574117	0.0738573968 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1		✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3		✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1		✓

Test Step 3.40 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.51700002	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390027	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.0799999982	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5	
k_APAEnableRate_pSec_f32	177	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	1.27999997	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	15974	
t_APADisableRateX_HwNm_u4p12[1]	16384	
t_APADisableRateX_HwNm_u4p12[2]	16794	
t_APADisableRateX_HwNm_u4p12[3]	17203	
t_APADisableRateX_HwNm_u4p12[4]	17613	
t_APADisableRateX_HwNm_u4p12[5]	18022	
t_APADisableRateY_pSec_u7p9[0]	19968	
t_APADisableRateY_pSec_u7p9[1]	20480	
t_APADisableRateY_pSec_u7p9[2]	20992	
t_APADisableRateY_pSec_u7p9[3]	21504	
t_APADisableRateY_pSec_u7p9[4]	22016	
t_APADisableRateY_pSec_u7p9[5]	22528	
t_APASmoothX_Uls_u2p14[0]	6390	
t_APASmoothX_Uls_u2p14[1]	6554	
t_APASmoothX_Uls_u2p14[2]	6717	
t_APASmoothX_Uls_u2p14[3]	6881	
t_APASmoothX_Uls_u2p14[4]	7045	
t_APASmoothX_Uls_u2p14[5]	7209	
t_APASmoothX_Uls_u2p14[6]	7373	
t_APASmoothX_Uls_u2p14[7]	7537	
t_APASmoothX_Uls_u2p14[8]	7700	
t_APASmoothX_Uls_u2p14[9]	7864	
t_APASmoothY_Uls_u2p14[0]	3441	
t_APASmoothY_Uls_u2p14[1]	3604	
t_APASmoothY_Uls_u2p14[2]	3768	
t_APASmoothY_Uls_u2p14[3]	3932	
t_APASmoothY_Uls_u2p14[4]	4096	
t_APASmoothY_Uls_u2p14[5]	4260	
t_APASmoothY_Uls_u2p14[6]	4424	

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TrqArblim_Per1

Name	Input Value
t_APASmoothY_Uls_u2p14[7]	4588
t_APASmoothY_Uls_u2p14[8]	4751
t_APASmoothY_Uls_u2p14[9]	4915
t_LKASlewX_Kph_u8p8[0]	19968
t_LKASlewX_Kph_u8p8[1]	20480
t_LKASlewX_Kph_u8p8[2]	20992
t_LKASlewX_Kph_u8p8[3]	21504
t_LKASlewX_Kph_u8p8[4]	22016
t_LKASlewX_Kph_u8p8[5]	22528
t_LKASlewY_NmpSec_u4p12[0]	15974
t_LKASlewY_NmpSec_u4p12[1]	16384
t_LKASlewY_NmpSec_u4p12[2]	16794
t_LKASlewY_NmpSec_u4p12[3]	17203
t_LKASlewY_NmpSec_u4p12[4]	17613
t_LKASlewY_NmpSec_u4p12[5]	18022
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	2.98000002
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.73000002
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.1500001
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	218.893005
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.70000005

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.43900001	0.43900001 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.58200288	1.58200002 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.0799999982	0.0799999982 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-4.99399996	-4.99399996 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-3.69090486	-3.69090009 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.41 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.529999971
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.76999998

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TrqArblim_Per1

Name	Input Value
TrqArblim_HwTorqueSV_HwNm_M_Str_K_Uls_f32	0.0500000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.22999995
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4
k_APAEnableRate_pSec_f32	181.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	16384
t_APADisableRateX_HwNm_u4p12[1]	16794
t_APADisableRateX_HwNm_u4p12[2]	17203
t_APADisableRateX_HwNm_u4p12[3]	17613
t_APADisableRateX_HwNm_u4p12[4]	18022
t_APADisableRateX_HwNm_u4p12[5]	18432
t_APADisableRateY_pSec_u7p9[0]	20480
t_APADisableRateY_pSec_u7p9[1]	20992
t_APADisableRateY_pSec_u7p9[2]	21504
t_APADisableRateY_pSec_u7p9[3]	22016
t_APADisableRateY_pSec_u7p9[4]	22528
t_APADisableRateY_pSec_u7p9[5]	23040
t_APASmoothX_Uls_u2p14[0]	6554
t_APASmoothX_Uls_u2p14[1]	6717
t_APASmoothX_Uls_u2p14[2]	6881
t_APASmoothX_Uls_u2p14[3]	7045
t_APASmoothX_Uls_u2p14[4]	7209
t_APASmoothX_Uls_u2p14[5]	7373
t_APASmoothX_Uls_u2p14[6]	7537
t_APASmoothX_Uls_u2p14[7]	7700
t_APASmoothX_Uls_u2p14[8]	7864
t_APASmoothX_Uls_u2p14[9]	8028
t_APASmoothY_Uls_u2p14[0]	3604
t_APASmoothY_Uls_u2p14[1]	3768
t_APASmoothY_Uls_u2p14[2]	3932
t_APASmoothY_Uls_u2p14[3]	4096
t_APASmoothY_Uls_u2p14[4]	4260
t_APASmoothY_Uls_u2p14[5]	4424
t_APASmoothY_Uls_u2p14[6]	4588
t_APASmoothY_Uls_u2p14[7]	4751
t_APASmoothY_Uls_u2p14[8]	4915
t_APASmoothY_Uls_u2p14[9]	5079
t_LKASlewX_Kph_u8p8[0]	20480
t_LKASlewX_Kph_u8p8[1]	20992
t_LKASlewX_Kph_u8p8[2]	21504
t_LKASlewX_Kph_u8p8[3]	22016
t_LKASlewX_Kph_u8p8[4]	22528
t_LKASlewX_Kph_u8p8[5]	23040
t_LKASlewY_NmpSec_u4p12[0]	16384
t_LKASlewY_NmpSec_u4p12[1]	16794
t_LKASlewY_NmpSec_u4p12[2]	17203
t_LKASlewY_NmpSec_u4p12[3]	17613
t_LKASlewY_NmpSec_u4p12[4]	18022
t_LKASlewY_NmpSec_u4p12[5]	18432
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	4.17999983
target_TrqArblim_Per1_ESCState_State_enum.value	0

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TrqArblim_Per1

Name	Input Value
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.33999991
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.5
target_TrqArblim_Per1_LKASate_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	225.343002
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.699999988
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.893000007	0.893000007 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79849994	1.79849994 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.699999988	0.699999988 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.99099994	-3.99099994 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.193779469	0.193779007 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0.000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.42 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.32999992
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.023
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3
k_APAEnableRate_pSec_f32	186
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	16794
t_APADisableRateX_HwNm_u4p12[1]	17203
t_APADisableRateX_HwNm_u4p12[2]	17613
t_APADisableRateX_HwNm_u4p12[3]	18022
t_APADisableRateX_HwNm_u4p12[4]	18432
t_APADisableRateX_HwNm_u4p12[5]	18842
t_APADisableRateY_pSec_u7p9[0]	20992
t_APADisableRateY_pSec_u7p9[1]	21504
t_APADisableRateY_pSec_u7p9[2]	22016
t_APADisableRateY_pSec_u7p9[3]	22528
t_APADisableRateY_pSec_u7p9[4]	23040
t_APADisableRateY_pSec_u7p9[5]	23552
t_APASmoothX_Uls_u2p14[0]	6717
t_APASmoothX_Uls_u2p14[1]	6881
t_APASmoothX_Uls_u2p14[2]	7045
t_APASmoothX_Uls_u2p14[3]	7209
t_APASmoothX_Uls_u2p14[4]	7373
t_APASmoothX_Uls_u2p14[5]	7537
t_APASmoothX_Uls_u2p14[6]	7700
t_APASmoothX_Uls_u2p14[7]	7864
t_APASmoothX_Uls_u2p14[8]	8028
t_APASmoothX_Uls_u2p14[9]	8192
t_APASmoothY_Uls_u2p14[0]	3768
t_APASmoothY_Uls_u2p14[1]	3932
t_APASmoothY_Uls_u2p14[2]	4096
t_APASmoothY_Uls_u2p14[3]	4260

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	4424		
t_APASmoothY_Uls_u2p14[5]	4588		
t_APASmoothY_Uls_u2p14[6]	4751		
t_APASmoothY_Uls_u2p14[7]	4915		
t_APASmoothY_Uls_u2p14[8]	5079		
t_APASmoothY_Uls_u2p14[9]	5243		
t_LKASlewX_Kph_u8p8[0]	20992		
t_LKASlewX_Kph_u8p8[1]	21504		
t_LKASlewX_Kph_u8p8[2]	22016		
t_LKASlewX_Kph_u8p8[3]	22528		
t_LKASlewX_Kph_u8p8[4]	23040		
t_LKASlewX_Kph_u8p8[5]	23552		
t_LKASlewY_NmpSec_u4p12[0]	16794		
t_LKASlewY_NmpSec_u4p12[1]	17203		
t_LKASlewY_NmpSec_u4p12[2]	17613		
t_LKASlewY_NmpSec_u4p12[3]	18022		
t_LKASlewY_NmpSec_u4p12[4]	18432		
t_LKASlewY_NmpSec_u4p12[5]	18842		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.95000005		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.23000002		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	0.899999976		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.372000009	0.372000009 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.34425998	2.34425998 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	0.899999976	0.899999976 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.99399996	-2.99399996 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	2.75	2.75 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.75	2.75 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-3.22300243	-3.22300005 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✔

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.43 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	

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TrqArblim_Per1

Name	Input Value
TrqArblim_APADisableRate_Uls_M_f32	1
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.8900001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0340000018
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.51999998
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2
k_APAEnableRate_pSec_f32	190.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	17203
t_APADisableRateX_HwNm_u4p12[1]	17613
t_APADisableRateX_HwNm_u4p12[2]	18022
t_APADisableRateX_HwNm_u4p12[3]	18432
t_APADisableRateX_HwNm_u4p12[4]	18842
t_APADisableRateX_HwNm_u4p12[5]	19251
t_APADisableRateY_pSec_u7p9[0]	21504
t_APADisableRateY_pSec_u7p9[1]	22016
t_APADisableRateY_pSec_u7p9[2]	22528
t_APADisableRateY_pSec_u7p9[3]	23040
t_APADisableRateY_pSec_u7p9[4]	23552
t_APADisableRateY_pSec_u7p9[5]	24064
t_APASmoothX_Uls_u2p14[0]	6881
t_APASmoothX_Uls_u2p14[1]	7045
t_APASmoothX_Uls_u2p14[2]	7209
t_APASmoothX_Uls_u2p14[3]	7373
t_APASmoothX_Uls_u2p14[4]	7537
t_APASmoothX_Uls_u2p14[5]	7700
t_APASmoothX_Uls_u2p14[6]	7864
t_APASmoothX_Uls_u2p14[7]	8028
t_APASmoothX_Uls_u2p14[8]	8192
t_APASmoothX_Uls_u2p14[9]	8356
t_APASmoothY_Uls_u2p14[0]	3932
t_APASmoothY_Uls_u2p14[1]	4096
t_APASmoothY_Uls_u2p14[2]	4260
t_APASmoothY_Uls_u2p14[3]	4424
t_APASmoothY_Uls_u2p14[4]	4588
t_APASmoothY_Uls_u2p14[5]	4751
t_APASmoothY_Uls_u2p14[6]	4915
t_APASmoothY_Uls_u2p14[7]	5079
t_APASmoothY_Uls_u2p14[8]	5243
t_APASmoothY_Uls_u2p14[9]	5407
t_LKASlewX_Kph_u8p8[0]	21504
t_LKASlewX_Kph_u8p8[1]	22016
t_LKASlewX_Kph_u8p8[2]	22528
t_LKASlewX_Kph_u8p8[3]	23040
t_LKASlewX_Kph_u8p8[4]	23552
t_LKASlewX_Kph_u8p8[5]	24064
t_LKASlewY_NmpSec_u4p12[0]	17203
t_LKASlewY_NmpSec_u4p12[1]	17613
t_LKASlewY_NmpSec_u4p12[2]	18022
t_LKASlewY_NmpSec_u4p12[3]	18432
t_LKASlewY_NmpSec_u4p12[4]	18842
t_LKASlewY_NmpSec_u4p12[5]	19251
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32

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TrqArblim_Per1

Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.55999994		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.19999981		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	238.242996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.10000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.91278005	2.91278005 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.10000002	1.10000002 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.99060011	-1.99059999 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.66998291	0.669983029 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.66998291	0.669983029 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.23000002	1.23000002 ± 0000390625	✔
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	1.23000002	1.23000002 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.363018811	0.36301899 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.66998291	0.669983029 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.44 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.300000012
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.45000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0450000018
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1
k_APAEnableRate_pSec_f32	195
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	17613
t_APADisableRateX_HwNm_u4p12[1]	18022
t_APADisableRateX_HwNm_u4p12[2]	18432
t_APADisableRateX_HwNm_u4p12[3]	18842
t_APADisableRateX_HwNm_u4p12[4]	19251
t_APADisableRateX_HwNm_u4p12[5]	19661
t_APADisableRateY_pSec_u7p9[0]	22016
t_APADisableRateY_pSec_u7p9[1]	22528
t_APADisableRateY_pSec_u7p9[2]	23040
t_APADisableRateY_pSec_u7p9[3]	23552
t_APADisableRateY_pSec_u7p9[4]	24064
t_APADisableRateY_pSec_u7p9[5]	24576
t_APASmoothX_Uls_u2p14[0]	7045
t_APASmoothX_Uls_u2p14[1]	7209
t_APASmoothX_Uls_u2p14[2]	7373
t_APASmoothX_Uls_u2p14[3]	7537
t_APASmoothX_Uls_u2p14[4]	7700
t_APASmoothX_Uls_u2p14[5]	7864
t_APASmoothX_Uls_u2p14[6]	8028
t_APASmoothX_Uls_u2p14[7]	8192
t_APASmoothX_Uls_u2p14[8]	8356
t_APASmoothX_Uls_u2p14[9]	8520
t_APASmoothY_Uls_u2p14[0]	4096

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[1]	4260		
t_APASmoothY_Uls_u2p14[2]	4424		
t_APASmoothY_Uls_u2p14[3]	4588		
t_APASmoothY_Uls_u2p14[4]	4751		
t_APASmoothY_Uls_u2p14[5]	4915		
t_APASmoothY_Uls_u2p14[6]	5079		
t_APASmoothY_Uls_u2p14[7]	5243		
t_APASmoothY_Uls_u2p14[8]	5407		
t_APASmoothY_Uls_u2p14[9]	5571		
t_LKASlewX_Kph_u8p8[0]	22016		
t_LKASlewX_Kph_u8p8[1]	22528		
t_LKASlewX_Kph_u8p8[2]	23040		
t_LKASlewX_Kph_u8p8[3]	23552		
t_LKASlewX_Kph_u8p8[4]	24064		
t_LKASlewX_Kph_u8p8[5]	24576		
t_LKASlewY_NmpSec_u4p12[0]	17613		
t_LKASlewY_NmpSec_u4p12[1]	18022		
t_LKASlewY_NmpSec_u4p12[2]	18432		
t_LKASlewY_NmpSec_u4p12[3]	18842		
t_LKASlewY_NmpSec_u4p12[4]	19251		
t_LKASlewY_NmpSec_u4p12[5]	19661		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCoscillate_State_enum	target_TrqArblim_Per1_GMOSHOCoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOCoscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.17000008		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.55000019		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	244.692993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	1.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.90000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.690000057	0.689999998 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.48239994	3.48239994 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	1.29999995	1.29999995 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.994000018	-0.994000018 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.994000018	-0.994000018 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-0.994000018	-0.994000018 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.30500409	0.305004001 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 3.45 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0250000004
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.01000023
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0560000017
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998
TrqArblim_PreLKATrqReq_HwNm_M_f32	0
k_APAEnableRate_pSec_f32	199.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	18022
t_APADisableRateX_HwNm_u4p12[1]	18432
t_APADisableRateX_HwNm_u4p12[2]	18842
t_APADisableRateX_HwNm_u4p12[3]	19251
t_APADisableRateX_HwNm_u4p12[4]	19661
t_APADisableRateX_HwNm_u4p12[5]	20070
t_APADisableRateY_pSec_u7p9[0]	22528
t_APADisableRateY_pSec_u7p9[1]	23040
t_APADisableRateY_pSec_u7p9[2]	23552
t_APADisableRateY_pSec_u7p9[3]	24064
t_APADisableRateY_pSec_u7p9[4]	24576
t_APADisableRateY_pSec_u7p9[5]	25088
t_APASmoothX_Uls_u2p14[0]	7209
t_APASmoothX_Uls_u2p14[1]	7373
t_APASmoothX_Uls_u2p14[2]	7537
t_APASmoothX_Uls_u2p14[3]	7700
t_APASmoothX_Uls_u2p14[4]	7864
t_APASmoothX_Uls_u2p14[5]	8028
t_APASmoothX_Uls_u2p14[6]	8192
t_APASmoothX_Uls_u2p14[7]	8356
t_APASmoothX_Uls_u2p14[8]	8520
t_APASmoothX_Uls_u2p14[9]	8684
t_APASmoothY_Uls_u2p14[0]	4260
t_APASmoothY_Uls_u2p14[1]	4424
t_APASmoothY_Uls_u2p14[2]	4588
t_APASmoothY_Uls_u2p14[3]	4751
t_APASmoothY_Uls_u2p14[4]	4915
t_APASmoothY_Uls_u2p14[5]	5079
t_APASmoothY_Uls_u2p14[6]	5243
t_APASmoothY_Uls_u2p14[7]	5407
t_APASmoothY_Uls_u2p14[8]	5571
t_APASmoothY_Uls_u2p14[9]	5734
t_LKASlewX_Kph_u8p8[0]	22528
t_LKASlewX_Kph_u8p8[1]	23040
t_LKASlewX_Kph_u8p8[2]	23552
t_LKASlewX_Kph_u8p8[3]	24064
t_LKASlewX_Kph_u8p8[4]	24576
t_LKASlewX_Kph_u8p8[5]	25088
t_LKASlewY_NmpSec_u4p12[0]	18022
t_LKASlewY_NmpSec_u4p12[1]	18432
t_LKASlewY_NmpSec_u4p12[2]	18842
t_LKASlewY_NmpSec_u4p12[3]	19251
t_LKASlewY_NmpSec_u4p12[4]	19661
t_LKASlewY_NmpSec_u4p12[5]	20070
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.78000021
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.89999962
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	251.143005
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	1.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.70000005

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.05312014	4.05312014 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-10.2399998	-10.2399998 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	0	0 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-2.70000005	-2.70000005 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.46 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.529999971
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.76999998
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0500000007
TrqArblim_PosSrvcCmd_MtrNm_M_f32	0.560000002
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4
k_APAEnableRate_pSec_f32	181.5
k_APAUseADmpScale_Igc	1
k_APAUseAsstScale_Igc	1
k_APAUseRetScale_Igc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	16384
t_APADisableRateX_HwNm_u4p12[1]	16794
t_APADisableRateX_HwNm_u4p12[2]	17203
t_APADisableRateX_HwNm_u4p12[3]	17613
t_APADisableRateX_HwNm_u4p12[4]	18022
t_APADisableRateX_HwNm_u4p12[5]	18432
t_APADisableRateY_pSec_u7p9[0]	20480
t_APADisableRateY_pSec_u7p9[1]	20992
t_APADisableRateY_pSec_u7p9[2]	21504
t_APADisableRateY_pSec_u7p9[3]	22016
t_APADisableRateY_pSec_u7p9[4]	22528
t_APADisableRateY_pSec_u7p9[5]	23040
t_APASmoothX_Uls_u2p14[0]	6554
t_APASmoothX_Uls_u2p14[1]	6717
t_APASmoothX_Uls_u2p14[2]	6881
t_APASmoothX_Uls_u2p14[3]	7045
t_APASmoothX_Uls_u2p14[4]	7209
t_APASmoothX_Uls_u2p14[5]	7373
t_APASmoothX_Uls_u2p14[6]	7537
t_APASmoothX_Uls_u2p14[7]	7700

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TrqArblim_Per1

Name	Input Value
t_APASmoothX_Uls_u2p14[8]	7864
t_APASmoothX_Uls_u2p14[9]	8028
t_APASmoothY_Uls_u2p14[0]	3604
t_APASmoothY_Uls_u2p14[1]	3768
t_APASmoothY_Uls_u2p14[2]	3932
t_APASmoothY_Uls_u2p14[3]	4096
t_APASmoothY_Uls_u2p14[4]	4260
t_APASmoothY_Uls_u2p14[5]	4424
t_APASmoothY_Uls_u2p14[6]	4588
t_APASmoothY_Uls_u2p14[7]	4751
t_APASmoothY_Uls_u2p14[8]	4915
t_APASmoothY_Uls_u2p14[9]	5079
t_LKASlewX_Kph_u8p8[0]	20480
t_LKASlewX_Kph_u8p8[1]	20992
t_LKASlewX_Kph_u8p8[2]	21504
t_LKASlewX_Kph_u8p8[3]	22016
t_LKASlewX_Kph_u8p8[4]	22528
t_LKASlewX_Kph_u8p8[5]	23040
t_LKASlewY_NmpSec_u4p12[0]	16384
t_LKASlewY_NmpSec_u4p12[1]	16794
t_LKASlewY_NmpSec_u4p12[2]	17203
t_LKASlewY_NmpSec_u4p12[3]	17613
t_LKASlewY_NmpSec_u4p12[4]	18022
t_LKASlewY_NmpSec_u4p12[5]	18432
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	4.17999983
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.33999991
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.5
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	225.343002
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	0.699999988
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.893000007	0.893000007 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79849994	1.79849994 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	0.699999988	0.699999988 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-3.99099994	-3.99099994 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.193779469	0.193779007 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 3.47 (Repeat Count = 1)



Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.075000003
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.13000011
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0780000016
TrqArblim_PosSrvcCmd_MtrNm_M_f32	0
TrqArblim_PrelKATrqReq_HwNm_M_f32	2
k_APAEnableRate_pSec_f32	208.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	18842
t_APADisableRateX_HwNm_u4p12[1]	19251
t_APADisableRateX_HwNm_u4p12[2]	19661
t_APADisableRateX_HwNm_u4p12[3]	20070
t_APADisableRateX_HwNm_u4p12[4]	20480
t_APADisableRateX_HwNm_u4p12[5]	20890
t_APADisableRateY_pSec_u7p9[0]	23552
t_APADisableRateY_pSec_u7p9[1]	24064
t_APADisableRateY_pSec_u7p9[2]	24576
t_APADisableRateY_pSec_u7p9[3]	25088
t_APADisableRateY_pSec_u7p9[4]	25600
t_APADisableRateY_pSec_u7p9[5]	26112
t_APASmoothX_Uls_u2p14[0]	7537
t_APASmoothX_Uls_u2p14[1]	7700
t_APASmoothX_Uls_u2p14[2]	7864
t_APASmoothX_Uls_u2p14[3]	8028
t_APASmoothX_Uls_u2p14[4]	8192
t_APASmoothX_Uls_u2p14[5]	8356
t_APASmoothX_Uls_u2p14[6]	8520
t_APASmoothX_Uls_u2p14[7]	8684
t_APASmoothX_Uls_u2p14[8]	8847
t_APASmoothX_Uls_u2p14[9]	9011
t_APASmoothY_Uls_u2p14[0]	4588
t_APASmoothY_Uls_u2p14[1]	4751
t_APASmoothY_Uls_u2p14[2]	4915
t_APASmoothY_Uls_u2p14[3]	5079
t_APASmoothY_Uls_u2p14[4]	5243
t_APASmoothY_Uls_u2p14[5]	5407
t_APASmoothY_Uls_u2p14[6]	5571
t_APASmoothY_Uls_u2p14[7]	5734
t_APASmoothY_Uls_u2p14[8]	5898
t_APASmoothY_Uls_u2p14[9]	6062
t_LKASlewX_Kph_u8p8[0]	23552
t_LKASlewX_Kph_u8p8[1]	24064
t_LKASlewX_Kph_u8p8[2]	24576
t_LKASlewX_Kph_u8p8[3]	25088
t_LKASlewX_Kph_u8p8[4]	25600
t_LKASlewX_Kph_u8p8[5]	26112
t_LKASlewY_NmpSec_u4p12[0]	18842
t_LKASlewY_NmpSec_u4p12[1]	19251
t_LKASlewY_NmpSec_u4p12[2]	19661
t_LKASlewY_NmpSec_u4p12[3]	20070
t_LKASlewY_NmpSec_u4p12[4]	20480
t_LKASlewY_NmpSec_u4p12[5]	20890
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	9.60000038
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	264.042999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.89999998
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.29999995

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.492000043	0.492000014 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.19786024	5.19785976 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.89999998	1.89999998 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.99079978	1.98979998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.846520245	0.846520007 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.846520245	0.846520007 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.25	-3.25 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.291611522	0.291610986 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.846520245	0.846520007 ± 0.000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.48 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.100000001
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.3499999
TrqArblim_PreLKATrqReq_HwNm_M_f32	3
k_APAEnableRate_pSec_f32	213
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.1400001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	19251
t_APADisableRateX_HwNm_u4p12[1]	19661
t_APADisableRateX_HwNm_u4p12[2]	20070
t_APADisableRateX_HwNm_u4p12[3]	20480
t_APADisableRateX_HwNm_u4p12[4]	20890
t_APADisableRateX_HwNm_u4p12[5]	21299
t_APADisableRateY_pSec_u7p9[0]	24064
t_APADisableRateY_pSec_u7p9[1]	24576
t_APADisableRateY_pSec_u7p9[2]	25088
t_APADisableRateY_pSec_u7p9[3]	25600
t_APADisableRateY_pSec_u7p9[4]	26112
t_APADisableRateY_pSec_u7p9[5]	26624
t_APASmoothX_Uls_u2p14[0]	7700
t_APASmoothX_Uls_u2p14[1]	7864
t_APASmoothX_Uls_u2p14[2]	8028
t_APASmoothX_Uls_u2p14[3]	8192
t_APASmoothX_Uls_u2p14[4]	8356
t_APASmoothX_Uls_u2p14[5]	8520

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TrqArblim_Per1

Name	Input Value		
t_APASmoothX_Uls_u2p14[6]	8684		
t_APASmoothX_Uls_u2p14[7]	8847		
t_APASmoothX_Uls_u2p14[8]	9011		
t_APASmoothX_Uls_u2p14[9]	9175		
t_APASmoothY_Uls_u2p14[0]	4751		
t_APASmoothY_Uls_u2p14[1]	4915		
t_APASmoothY_Uls_u2p14[2]	5079		
t_APASmoothY_Uls_u2p14[3]	5243		
t_APASmoothY_Uls_u2p14[4]	5407		
t_APASmoothY_Uls_u2p14[5]	5571		
t_APASmoothY_Uls_u2p14[6]	5734		
t_APASmoothY_Uls_u2p14[7]	5898		
t_APASmoothY_Uls_u2p14[8]	6062		
t_APASmoothY_Uls_u2p14[9]	6226		
t_LKASlewX_Kph_u8p8[0]	24064		
t_LKASlewX_Kph_u8p8[1]	24576		
t_LKASlewX_Kph_u8p8[2]	25088		
t_LKASlewX_Kph_u8p8[3]	25600		
t_LKASlewX_Kph_u8p8[4]	26112		
t_LKASlewX_Kph_u8p8[5]	26624		
t_LKASlewY_NmpSec_u4p12[0]	19251		
t_LKASlewY_NmpSec_u4p12[1]	19661		
t_LKASlewY_NmpSec_u4p12[2]	20070		
t_LKASlewY_NmpSec_u4p12[3]	20480		
t_LKASlewY_NmpSec_u4p12[4]	20890		
t_LKASlewY_NmpSec_u4p12[5]	21299		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6.61000013		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	9.94999981		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	270.493011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	2.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.526000023	0.526000023 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.77188015	5.77188015 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	2.0999999	2.0999999 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.99060011	2.98959994 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.818063855	0.818063974 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.818063855	0.818063974 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.1400001	-3.1400001 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-1.71793401	-1.71792996 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.818063855	0.818063974 ± 0000390625	✔

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TrqArblim_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.49 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.125
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.10000001
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-6.53999996
TrqArblim_PrelKATrqReq_HwNm_M_f32	4
k_APAEnableRate_pSec_f32	217.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	19661
t_APADisableRateX_HwNm_u4p12[1]	20070
t_APADisableRateX_HwNm_u4p12[2]	20480
t_APADisableRateX_HwNm_u4p12[3]	20890
t_APADisableRateX_HwNm_u4p12[4]	21299
t_APADisableRateX_HwNm_u4p12[5]	21709
t_APADisableRateY_pSec_u7p9[0]	24576
t_APADisableRateY_pSec_u7p9[1]	25088
t_APADisableRateY_pSec_u7p9[2]	25600
t_APADisableRateY_pSec_u7p9[3]	26112
t_APADisableRateY_pSec_u7p9[4]	26624
t_APADisableRateY_pSec_u7p9[5]	27136
t_APASmoothX_Uls_u2p14[0]	7864
t_APASmoothX_Uls_u2p14[1]	8028
t_APASmoothX_Uls_u2p14[2]	8192
t_APASmoothX_Uls_u2p14[3]	8356
t_APASmoothX_Uls_u2p14[4]	8520
t_APASmoothX_Uls_u2p14[5]	8684
t_APASmoothX_Uls_u2p14[6]	8847
t_APASmoothX_Uls_u2p14[7]	9011
t_APASmoothX_Uls_u2p14[8]	9175
t_APASmoothX_Uls_u2p14[9]	9339
t_APASmoothY_Uls_u2p14[0]	4915
t_APASmoothY_Uls_u2p14[1]	5079
t_APASmoothY_Uls_u2p14[2]	5243
t_APASmoothY_Uls_u2p14[3]	5407
t_APASmoothY_Uls_u2p14[4]	5571
t_APASmoothY_Uls_u2p14[5]	5734
t_APASmoothY_Uls_u2p14[6]	5898
t_APASmoothY_Uls_u2p14[7]	6062
t_APASmoothY_Uls_u2p14[8]	6226
t_APASmoothY_Uls_u2p14[9]	6390
t_LKASlewX_Kph_u8p8[0]	24576
t_LKASlewX_Kph_u8p8[1]	25088
t_LKASlewX_Kph_u8p8[2]	25600
t_LKASlewX_Kph_u8p8[3]	26112
t_LKASlewX_Kph_u8p8[4]	26624
t_LKASlewX_Kph_u8p8[5]	27136
t_LKASlewY_NmpSec_u4p12[0]	19661
t_LKASlewY_NmpSec_u4p12[1]	20070
t_LKASlewY_NmpSec_u4p12[2]	20480
t_LKASlewY_NmpSec_u4p12[3]	20890
t_LKASlewY_NmpSec_u4p12[4]	21299
t_LKASlewY_NmpSec_u4p12[5]	21709
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCcmd_HwNm_f32.value	-9.03999996
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.21999979
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.0500002
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	276.942993
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.29999995
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.89999998

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0189999938	0.0189999994 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34700012	6.34700012 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.53999996	-6.53999996 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.99039984	3.98939991 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.994300246	0.994300008 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.994300246	0.994300008 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0372764729	-0.037276499 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.994300246	0.994300008 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.50 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.150000006
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.80999994
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.111000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.46000004
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.80000019
k_APAEnableRate_pSec_f32	222
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	20070
t_APADisableRateX_HwNm_u4p12[1]	20480
t_APADisableRateX_HwNm_u4p12[2]	20890
t_APADisableRateX_HwNm_u4p12[3]	21299
t_APADisableRateX_HwNm_u4p12[4]	21709
t_APADisableRateX_HwNm_u4p12[5]	22118
t_APADisableRateY_pSec_u7p9[0]	25088
t_APADisableRateY_pSec_u7p9[1]	25600
t_APADisableRateY_pSec_u7p9[2]	26112
t_APADisableRateY_pSec_u7p9[3]	26624
t_APADisableRateY_pSec_u7p9[4]	27136

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	27648		
t_APASmoothX_Uls_u2p14[0]	8028		
t_APASmoothX_Uls_u2p14[1]	8192		
t_APASmoothX_Uls_u2p14[2]	8356		
t_APASmoothX_Uls_u2p14[3]	8520		
t_APASmoothX_Uls_u2p14[4]	8684		
t_APASmoothX_Uls_u2p14[5]	8847		
t_APASmoothX_Uls_u2p14[6]	9011		
t_APASmoothX_Uls_u2p14[7]	9175		
t_APASmoothX_Uls_u2p14[8]	9339		
t_APASmoothX_Uls_u2p14[9]	9503		
t_APASmoothY_Uls_u2p14[0]	5079		
t_APASmoothY_Uls_u2p14[1]	5243		
t_APASmoothY_Uls_u2p14[2]	5407		
t_APASmoothY_Uls_u2p14[3]	5571		
t_APASmoothY_Uls_u2p14[4]	5734		
t_APASmoothY_Uls_u2p14[5]	5898		
t_APASmoothY_Uls_u2p14[6]	6062		
t_APASmoothY_Uls_u2p14[7]	6226		
t_APASmoothY_Uls_u2p14[8]	6390		
t_APASmoothY_Uls_u2p14[9]	6554		
t_LKASlewX_Kph_u8p8[0]	25088		
t_LKASlewX_Kph_u8p8[1]	25600		
t_LKASlewX_Kph_u8p8[2]	26112		
t_LKASlewX_Kph_u8p8[3]	26624		
t_LKASlewX_Kph_u8p8[4]	27136		
t_LKASlewX_Kph_u8p8[5]	27648		
t_LKASlewY_NmpSec_u4p12[0]	20070		
t_LKASlewY_NmpSec_u4p12[1]	20480		
t_LKASlewY_NmpSec_u4p12[2]	20890		
t_LKASlewY_NmpSec_u4p12[3]	21299		
t_LKASlewY_NmpSec_u4p12[4]	21709		
t_LKASlewY_NmpSec_u4p12[5]	22118		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.82999992		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.21		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	283.393005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.594000041	0.593999982 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	4	4	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.92322016	6.92322016 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.5	2.5 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.79400063	-8.79399967 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.000390625	✔

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Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.594036281	0.594035983 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.51 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.174999997
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.369999989
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.122000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019
k_APAEnableRate_pSec_f32	226.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	20480
t_APADisableRateX_HwNm_u4p12[1]	20890
t_APADisableRateX_HwNm_u4p12[2]	21299
t_APADisableRateX_HwNm_u4p12[3]	21709
t_APADisableRateX_HwNm_u4p12[4]	22118
t_APADisableRateX_HwNm_u4p12[5]	22528
t_APADisableRateY_pSec_u7p9[0]	25600
t_APADisableRateY_pSec_u7p9[1]	26112
t_APADisableRateY_pSec_u7p9[2]	26624
t_APADisableRateY_pSec_u7p9[3]	27136
t_APADisableRateY_pSec_u7p9[4]	27648
t_APADisableRateY_pSec_u7p9[5]	28160
t_APASmoothX_Uls_u2p14[0]	8192
t_APASmoothX_Uls_u2p14[1]	8356
t_APASmoothX_Uls_u2p14[2]	8520
t_APASmoothX_Uls_u2p14[3]	8684
t_APASmoothX_Uls_u2p14[4]	8847
t_APASmoothX_Uls_u2p14[5]	9011
t_APASmoothX_Uls_u2p14[6]	9175
t_APASmoothX_Uls_u2p14[7]	9339
t_APASmoothX_Uls_u2p14[8]	9503
t_APASmoothX_Uls_u2p14[9]	9667
t_APASmoothY_Uls_u2p14[0]	5243
t_APASmoothY_Uls_u2p14[1]	5407
t_APASmoothY_Uls_u2p14[2]	5571
t_APASmoothY_Uls_u2p14[3]	5734
t_APASmoothY_Uls_u2p14[4]	5898
t_APASmoothY_Uls_u2p14[5]	6062
t_APASmoothY_Uls_u2p14[6]	6226
t_APASmoothY_Uls_u2p14[7]	6390
t_APASmoothY_Uls_u2p14[8]	6554
t_APASmoothY_Uls_u2p14[9]	6717
t_LKASlewX_Kph_u8p8[0]	25600
t_LKASlewX_Kph_u8p8[1]	26112
t_LKASlewX_Kph_u8p8[2]	26624
t_LKASlewX_Kph_u8p8[3]	27136
t_LKASlewX_Kph_u8p8[4]	27648
t_LKASlewX_Kph_u8p8[5]	28160
t_LKASlewY_NmpSec_u4p12[0]	20480
t_LKASlewY_NmpSec_u4p12[1]	20890
t_LKASlewY_NmpSec_u4p12[2]	21299
t_LKASlewY_NmpSec_u4p12[3]	21709
t_LKASlewY_NmpSec_u4p12[4]	22118
t_LKASlewY_NmpSec_u4p12[5]	22528
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32

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Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.43999958
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.230000004
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	289.842987
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.70000005
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0649999902	0.0649999976 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.50053978	7.50053978 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.75	2.75 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.78999996	8.78899956 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.979199529	0.979200006 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.979199529	0.979200006 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.0572012998	0.0572012998 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.979199529	0.979200006 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.52 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.200000003
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.92999983
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	0
k_APAEnableRate_pSec_f32	231
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	20890
t_APADisableRateX_HwNm_u4p12[1]	21299
t_APADisableRateX_HwNm_u4p12[2]	21709
t_APADisableRateX_HwNm_u4p12[3]	22118
t_APADisableRateX_HwNm_u4p12[4]	22528
t_APADisableRateX_HwNm_u4p12[5]	22938
t_APADisableRateY_pSec_u7p9[0]	26112
t_APADisableRateY_pSec_u7p9[1]	26624

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	27136		
t_APADisableRateY_pSec_u7p9[3]	27648		
t_APADisableRateY_pSec_u7p9[4]	28160		
t_APADisableRateY_pSec_u7p9[5]	28672		
t_APASmoothX_Uls_u2p14[0]	8356		
t_APASmoothX_Uls_u2p14[1]	8520		
t_APASmoothX_Uls_u2p14[2]	8684		
t_APASmoothX_Uls_u2p14[3]	8847		
t_APASmoothX_Uls_u2p14[4]	9011		
t_APASmoothX_Uls_u2p14[5]	9175		
t_APASmoothX_Uls_u2p14[6]	9339		
t_APASmoothX_Uls_u2p14[7]	9503		
t_APASmoothX_Uls_u2p14[8]	9667		
t_APASmoothX_Uls_u2p14[9]	9830		
t_APASmoothY_Uls_u2p14[0]	5407		
t_APASmoothY_Uls_u2p14[1]	5571		
t_APASmoothY_Uls_u2p14[2]	5734		
t_APASmoothY_Uls_u2p14[3]	5898		
t_APASmoothY_Uls_u2p14[4]	6062		
t_APASmoothY_Uls_u2p14[5]	6226		
t_APASmoothY_Uls_u2p14[6]	6390		
t_APASmoothY_Uls_u2p14[7]	6554		
t_APASmoothY_Uls_u2p14[8]	6717		
t_APASmoothY_Uls_u2p14[9]	6881		
t_LKASlewX_Kph_u8p8[0]	26112		
t_LKASlewX_Kph_u8p8[1]	26624		
t_LKASlewX_Kph_u8p8[2]	27136		
t_LKASlewX_Kph_u8p8[3]	27648		
t_LKASlewX_Kph_u8p8[4]	28160		
t_LKASlewX_Kph_u8p8[5]	28672		
t_LKASlewY_NmpSec_u4p12[0]	20890		
t_LKASlewY_NmpSec_u4p12[1]	21299		
t_LKASlewY_NmpSec_u4p12[2]	21709		
t_LKASlewY_NmpSec_u4p12[3]	22118		
t_LKASlewY_NmpSec_u4p12[4]	22528		
t_LKASlewY_NmpSec_u4p12[5]	22938		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.05000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.12		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	296.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	2.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.662	0.662 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.07895947	8.07896042 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	2.9000001	2.9000001 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	0	0 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.75	-0.75 ± 0.000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-0.75	-0.75 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.493716717	-0.493717015 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.53 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.224999994
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.48999977
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.143999994
TrqArblim_PosSrvcCmd_MtrNm_M_f32	3.32999992
TrqArblim_PrelKATrqReq_HwNm_M_f32	4.59999999
k_APAEnableRate_pSec_f32	235.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	21299
t_APADisableRateX_HwNm_u4p12[1]	21709
t_APADisableRateX_HwNm_u4p12[2]	22118
t_APADisableRateX_HwNm_u4p12[3]	22528
t_APADisableRateX_HwNm_u4p12[4]	22938
t_APADisableRateX_HwNm_u4p12[5]	23347
t_APADisableRateY_pSec_u7p9[0]	26624
t_APADisableRateY_pSec_u7p9[1]	27136
t_APADisableRateY_pSec_u7p9[2]	27648
t_APADisableRateY_pSec_u7p9[3]	28160
t_APADisableRateY_pSec_u7p9[4]	28672
t_APADisableRateY_pSec_u7p9[5]	29184
t_APASmoothX_Uls_u2p14[0]	8520
t_APASmoothX_Uls_u2p14[1]	8684
t_APASmoothX_Uls_u2p14[2]	8847
t_APASmoothX_Uls_u2p14[3]	9011
t_APASmoothX_Uls_u2p14[4]	9175
t_APASmoothX_Uls_u2p14[5]	9339
t_APASmoothX_Uls_u2p14[6]	9503
t_APASmoothX_Uls_u2p14[7]	9667
t_APASmoothX_Uls_u2p14[8]	9830
t_APASmoothX_Uls_u2p14[9]	9994
t_APASmoothY_Uls_u2p14[0]	5571
t_APASmoothY_Uls_u2p14[1]	5734
t_APASmoothY_Uls_u2p14[2]	5898
t_APASmoothY_Uls_u2p14[3]	6062
t_APASmoothY_Uls_u2p14[4]	6226
t_APASmoothY_Uls_u2p14[5]	6390
t_APASmoothY_Uls_u2p14[6]	6554
t_APASmoothY_Uls_u2p14[7]	6717
t_APASmoothY_Uls_u2p14[8]	6881
t_APASmoothY_Uls_u2p14[9]	7045
t_LKASlewX_Kph_u8p8[0]	26624
t_LKASlewX_Kph_u8p8[1]	27136
t_LKASlewX_Kph_u8p8[2]	27648
t_LKASlewX_Kph_u8p8[3]	28160
t_LKASlewX_Kph_u8p8[4]	28672
t_LKASlewX_Kph_u8p8[5]	29184
t_LKASlewY_NmpSec_u4p12[0]	21299
t_LKASlewY_NmpSec_u4p12[1]	21709
t_LKASlewY_NmpSec_u4p12[2]	22118
t_LKASlewY_NmpSec_u4p12[3]	22528
t_LKASlewY_NmpSec_u4p12[4]	22938

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[5]	23347
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.11999989
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.65999985
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.00999999
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	302.743011
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.09999999
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.10000002

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.110999987	0.111000001 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.65847969	8.65847969 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.32999992	3.32999992 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.59399986	4.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.125684112	0.125683993 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.54 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.25
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.61999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.59999999
k_APAEnableRate_pSec_f32	240
k_APAUseADmpScale_Igc	1
k_APAUseAsstScale_Igc	1
k_APAUseRetScale_Igc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	21709
t_APADisableRateX_HwNm_u4p12[1]	22118
t_APADisableRateX_HwNm_u4p12[2]	22528
t_APADisableRateX_HwNm_u4p12[3]	22938
t_APADisableRateX_HwNm_u4p12[4]	23347

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	23757		
t_APADisableRateY_pSec_u7p9[0]	27136		
t_APADisableRateY_pSec_u7p9[1]	27648		
t_APADisableRateY_pSec_u7p9[2]	28160		
t_APADisableRateY_pSec_u7p9[3]	28672		
t_APADisableRateY_pSec_u7p9[4]	29184		
t_APADisableRateY_pSec_u7p9[5]	29696		
t_APASmoothX_Uls_u2p14[0]	8684		
t_APASmoothX_Uls_u2p14[1]	8847		
t_APASmoothX_Uls_u2p14[2]	9011		
t_APASmoothX_Uls_u2p14[3]	9175		
t_APASmoothX_Uls_u2p14[4]	9339		
t_APASmoothX_Uls_u2p14[5]	9503		
t_APASmoothX_Uls_u2p14[6]	9667		
t_APASmoothX_Uls_u2p14[7]	9830		
t_APASmoothX_Uls_u2p14[8]	9994		
t_APASmoothX_Uls_u2p14[9]	10158		
t_APASmoothY_Uls_u2p14[0]	5734		
t_APASmoothY_Uls_u2p14[1]	5898		
t_APASmoothY_Uls_u2p14[2]	6062		
t_APASmoothY_Uls_u2p14[3]	6226		
t_APASmoothY_Uls_u2p14[4]	6390		
t_APASmoothY_Uls_u2p14[5]	6554		
t_APASmoothY_Uls_u2p14[6]	6717		
t_APASmoothY_Uls_u2p14[7]	6881		
t_APASmoothY_Uls_u2p14[8]	7045		
t_APASmoothY_Uls_u2p14[9]	7209		
t_LKASlewX_Kph_u8p8[0]	27136		
t_LKASlewX_Kph_u8p8[1]	27648		
t_LKASlewX_Kph_u8p8[2]	28160		
t_LKASlewX_Kph_u8p8[3]	28672		
t_LKASlewX_Kph_u8p8[4]	29184		
t_LKASlewX_Kph_u8p8[5]	29696		
t_LKASlewY_NmpSec_u4p12[0]	21709		
t_LKASlewY_NmpSec_u4p12[1]	22118		
t_LKASlewY_NmpSec_u4p12[2]	22528		
t_LKASlewY_NmpSec_u4p12[3]	22938		
t_LKASlewY_NmpSec_u4p12[4]	23347		
t_LKASlewY_NmpSec_u4p12[5]	23757		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.90000001		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.730000019	0.730000019 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.74800014	7.74800014 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	3.29999995	3.29999995 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.58939981	-4.58839989 ± 0.03125	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.159965992	0.159966007 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.55 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.275000006	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.609999966	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.165999994	
TrqArblim_PosSrvcCmd_MtrNm_M_f32	3.91000009	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038	
k_APAEnableRate_pSec_f32	0	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	2.75	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	22118	
t_APADisableRateX_HwNm_u4p12[1]	22528	
t_APADisableRateX_HwNm_u4p12[2]	22938	
t_APADisableRateX_HwNm_u4p12[3]	23347	
t_APADisableRateX_HwNm_u4p12[4]	23757	
t_APADisableRateX_HwNm_u4p12[5]	24166	
t_APADisableRateY_pSec_u7p9[0]	27648	
t_APADisableRateY_pSec_u7p9[1]	28160	
t_APADisableRateY_pSec_u7p9[2]	28672	
t_APADisableRateY_pSec_u7p9[3]	29184	
t_APADisableRateY_pSec_u7p9[4]	29696	
t_APADisableRateY_pSec_u7p9[5]	30208	
t_APASmoothX_Uls_u2p14[0]	8847	
t_APASmoothX_Uls_u2p14[1]	9011	
t_APASmoothX_Uls_u2p14[2]	9175	
t_APASmoothX_Uls_u2p14[3]	9339	
t_APASmoothX_Uls_u2p14[4]	9503	
t_APASmoothX_Uls_u2p14[5]	9667	
t_APASmoothX_Uls_u2p14[6]	9830	
t_APASmoothX_Uls_u2p14[7]	9994	
t_APASmoothX_Uls_u2p14[8]	10158	
t_APASmoothX_Uls_u2p14[9]	10322	
t_APASmoothY_Uls_u2p14[0]	5898	
t_APASmoothY_Uls_u2p14[1]	6062	
t_APASmoothY_Uls_u2p14[2]	6226	
t_APASmoothY_Uls_u2p14[3]	6390	
t_APASmoothY_Uls_u2p14[4]	6554	
t_APASmoothY_Uls_u2p14[5]	6717	
t_APASmoothY_Uls_u2p14[6]	6881	
t_APASmoothY_Uls_u2p14[7]	7045	
t_APASmoothY_Uls_u2p14[8]	7209	
t_APASmoothY_Uls_u2p14[9]	7373	
t_LKASlewX_Kph_u8p8[0]	27648	
t_LKASlewX_Kph_u8p8[1]	28160	
t_LKASlewX_Kph_u8p8[2]	28672	
t_LKASlewX_Kph_u8p8[3]	29184	
t_LKASlewX_Kph_u8p8[4]	29696	
t_LKASlewX_Kph_u8p8[5]	30208	
t_LKASlewY_NmpSec_u4p12[0]	22118	
t_LKASlewY_NmpSec_u4p12[1]	22528	

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[2]	22938
t_LKASlewY_NmpSec_u4p12[3]	23347
t_LKASlewY_NmpSec_u4p12[4]	23757
t_LKASlewY_NmpSec_u4p12[5]	24166
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.15999985
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.21000004
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	3.78999996
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	315.643005
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.699999988

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.157000005	0.157000005 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.21560001	8.21560001 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.91000009	3.91000009 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-8.09400082	-8.09399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.479015768	-0.479016006 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.56 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.300000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.35000038
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.177000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.19999981
TrqArblim_PrelKATrqReq_HwNm_M_f32	-7.0999999
k_APAEnableRate_pSec_f32	500
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	23347		
t_APADisableRateX_HwNm_u4p12[3]	23757		
t_APADisableRateX_HwNm_u4p12[4]	24166		
t_APADisableRateX_HwNm_u4p12[5]	24576		
t_APADisableRateY_pSec_u7p9[0]	28160		
t_APADisableRateY_pSec_u7p9[1]	28672		
t_APADisableRateY_pSec_u7p9[2]	29184		
t_APADisableRateY_pSec_u7p9[3]	29696		
t_APADisableRateY_pSec_u7p9[4]	30208		
t_APADisableRateY_pSec_u7p9[5]	30720		
t_APASmoothX_Uls_u2p14[0]	9011		
t_APASmoothX_Uls_u2p14[1]	9175		
t_APASmoothX_Uls_u2p14[2]	9339		
t_APASmoothX_Uls_u2p14[3]	9503		
t_APASmoothX_Uls_u2p14[4]	9667		
t_APASmoothX_Uls_u2p14[5]	9830		
t_APASmoothX_Uls_u2p14[6]	9994		
t_APASmoothX_Uls_u2p14[7]	10158		
t_APASmoothX_Uls_u2p14[8]	10322		
t_APASmoothX_Uls_u2p14[9]	10486		
t_APASmoothY_Uls_u2p14[0]	6062		
t_APASmoothY_Uls_u2p14[1]	6226		
t_APASmoothY_Uls_u2p14[2]	6390		
t_APASmoothY_Uls_u2p14[3]	6554		
t_APASmoothY_Uls_u2p14[4]	6717		
t_APASmoothY_Uls_u2p14[5]	6881		
t_APASmoothY_Uls_u2p14[6]	7045		
t_APASmoothY_Uls_u2p14[7]	7209		
t_APASmoothY_Uls_u2p14[8]	7373		
t_APASmoothY_Uls_u2p14[9]	7537		
t_LKASlewX_Kph_u8p8[0]	28160		
t_LKASlewX_Kph_u8p8[1]	28672		
t_LKASlewX_Kph_u8p8[2]	29184		
t_LKASlewX_Kph_u8p8[3]	29696		
t_LKASlewX_Kph_u8p8[4]	30208		
t_LKASlewX_Kph_u8p8[5]	30720		
t_LKASlewY_NmpSec_u4p12[0]	22528		
t_LKASlewY_NmpSec_u4p12[1]	22938		
t_LKASlewY_NmpSec_u4p12[2]	23347		
t_LKASlewY_NmpSec_u4p12[3]	23757		
t_LKASlewY_NmpSec_u4p12[4]	24166		
t_LKASlewY_NmpSec_u4p12[5]	24576		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.67999983		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.76999998		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.67999983		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	322.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	3.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.180000007	0.180000007 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.38176012	-7.38176012 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.19999981	4.19999981 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.08899975	-7.08799982 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.933400869	0.933400989 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.933400869	0.933400989 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.279716313	0.279716015 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.933400869	0.933400989 ± 0.000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.57 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.324999988
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.187999994
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.48999977
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.0999999
k_APAEnableRate_pSec_f32	250.339996
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	22938
t_APADisableRateX_HwNm_u4p12[1]	23347
t_APADisableRateX_HwNm_u4p12[2]	23757
t_APADisableRateX_HwNm_u4p12[3]	24166
t_APADisableRateX_HwNm_u4p12[4]	24576
t_APADisableRateX_HwNm_u4p12[5]	24986
t_APADisableRateY_pSec_u7p9[0]	28672
t_APADisableRateY_pSec_u7p9[1]	29184
t_APADisableRateY_pSec_u7p9[2]	29696
t_APADisableRateY_pSec_u7p9[3]	30208
t_APADisableRateY_pSec_u7p9[4]	30720
t_APADisableRateY_pSec_u7p9[5]	31232
t_APASmoothX_Uls_u2p14[0]	9175
t_APASmoothX_Uls_u2p14[1]	9339
t_APASmoothX_Uls_u2p14[2]	9503
t_APASmoothX_Uls_u2p14[3]	9667
t_APASmoothX_Uls_u2p14[4]	9830
t_APASmoothX_Uls_u2p14[5]	9994
t_APASmoothX_Uls_u2p14[6]	10158
t_APASmoothX_Uls_u2p14[7]	10322
t_APASmoothX_Uls_u2p14[8]	10486
t_APASmoothX_Uls_u2p14[9]	10650
t_APASmoothY_Uls_u2p14[0]	6226
t_APASmoothY_Uls_u2p14[1]	6390
t_APASmoothY_Uls_u2p14[2]	6554
t_APASmoothY_Uls_u2p14[3]	6717
t_APASmoothY_Uls_u2p14[4]	6881
t_APASmoothY_Uls_u2p14[5]	7045
t_APASmoothY_Uls_u2p14[6]	7209
t_APASmoothY_Uls_u2p14[7]	7373
t_APASmoothY_Uls_u2p14[8]	7537
t_APASmoothY_Uls_u2p14[9]	7700
t_LKASlewX_Kph_u8p8[0]	28672
t_LKASlewX_Kph_u8p8[1]	29184
t_LKASlewX_Kph_u8p8[2]	29696
t_LKASlewX_Kph_u8p8[3]	30208
t_LKASlewX_Kph_u8p8[4]	30720

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[5]	31232
t_LKASlewY_NmpSec_u4p12[0]	22938
t_LKASlewY_NmpSec_u4p12[1]	23347
t_LKASlewY_NmpSec_u4p12[2]	23757
t_LKASlewY_NmpSec_u4p12[3]	24166
t_LKASlewY_NmpSec_u4p12[4]	24576
t_LKASlewY_NmpSec_u4p12[5]	24986
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.19999981
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.32999992
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.57000017
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	328.542999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.90000001
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.300000012

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.825680017	0.825680017 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.86995983	-6.86995983 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.90000001	3.90000001 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-6.09399986	-6.09399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.24000001	-3.24000001 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.51337719	1.51338005 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.58 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.349999994
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.64999962
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.199000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.78000021
TrqArblim_PrelKATrqReq_HwNm_M_f32	-5.0999999
k_APAEnableRate_pSec_f32	246
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004

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TrqArblim_Per1

Name	Input Value
k_LKAUseSlewCal_Igc	0
t_APADisableRateX_HwNm_u4p12[0]	23347
t_APADisableRateX_HwNm_u4p12[1]	23757
t_APADisableRateX_HwNm_u4p12[2]	24166
t_APADisableRateX_HwNm_u4p12[3]	24576
t_APADisableRateX_HwNm_u4p12[4]	24986
t_APADisableRateX_HwNm_u4p12[5]	25395
t_APADisableRateY_pSec_u7p9[0]	29184
t_APADisableRateY_pSec_u7p9[1]	29696
t_APADisableRateY_pSec_u7p9[2]	30208
t_APADisableRateY_pSec_u7p9[3]	30720
t_APADisableRateY_pSec_u7p9[4]	31232
t_APADisableRateY_pSec_u7p9[5]	31744
t_APASmoothX_Uls_u2p14[0]	9339
t_APASmoothX_Uls_u2p14[1]	9503
t_APASmoothX_Uls_u2p14[2]	9667
t_APASmoothX_Uls_u2p14[3]	9830
t_APASmoothX_Uls_u2p14[4]	9994
t_APASmoothX_Uls_u2p14[5]	10158
t_APASmoothX_Uls_u2p14[6]	10322
t_APASmoothX_Uls_u2p14[7]	10486
t_APASmoothX_Uls_u2p14[8]	10650
t_APASmoothX_Uls_u2p14[9]	10813
t_APASmoothY_Uls_u2p14[0]	6390
t_APASmoothY_Uls_u2p14[1]	6554
t_APASmoothY_Uls_u2p14[2]	6717
t_APASmoothY_Uls_u2p14[3]	6881
t_APASmoothY_Uls_u2p14[4]	7045
t_APASmoothY_Uls_u2p14[5]	7209
t_APASmoothY_Uls_u2p14[6]	7373
t_APASmoothY_Uls_u2p14[7]	7537
t_APASmoothY_Uls_u2p14[8]	7700
t_APASmoothY_Uls_u2p14[9]	7864
t_LKASlewX_Kph_u8p8[0]	29184
t_LKASlewX_Kph_u8p8[1]	29696
t_LKASlewX_Kph_u8p8[2]	30208
t_LKASlewX_Kph_u8p8[3]	30720
t_LKASlewX_Kph_u8p8[4]	31232
t_LKASlewX_Kph_u8p8[5]	31744
t_LKASlewY_NmpSec_u4p12[0]	23347
t_LKASlewY_NmpSec_u4p12[1]	23757
t_LKASlewY_NmpSec_u4p12[2]	24166
t_LKASlewY_NmpSec_u4p12[3]	24576
t_LKASlewY_NmpSec_u4p12[4]	24986
t_LKASlewY_NmpSec_u4p12[5]	25395
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.8900001
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.46000004
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	334.993011
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.0999999
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.100000001

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.842000008	0.842000008 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.35353947	-6.35353994 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.0999999	4.0999999 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.09399986	-5.09399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.58000004	-1.58000004 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-1.58000004	-1.58000004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.5569886	1.55699003 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.59 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.375	
TrqArblim_ESC_State_Cnt_M_enum	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.30000019	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.209999993	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.0999999	
k_APAEnableRate_pSec_f32	255.75	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	4.28000021	
k_LKAUseSlewCal_lgc	1	
t_APADisableRateX_HwNm_u4p12[0]	23757	
t_APADisableRateX_HwNm_u4p12[1]	24166	
t_APADisableRateX_HwNm_u4p12[2]	24576	
t_APADisableRateX_HwNm_u4p12[3]	24986	
t_APADisableRateX_HwNm_u4p12[4]	25395	
t_APADisableRateX_HwNm_u4p12[5]	25805	
t_APADisableRateY_pSec_u7p9[0]	29696	
t_APADisableRateY_pSec_u7p9[1]	30208	
t_APADisableRateY_pSec_u7p9[2]	30720	
t_APADisableRateY_pSec_u7p9[3]	31232	
t_APADisableRateY_pSec_u7p9[4]	31744	
t_APADisableRateY_pSec_u7p9[5]	32256	
t_APASmoothX_Uls_u2p14[0]	9503	
t_APASmoothX_Uls_u2p14[1]	9667	
t_APASmoothX_Uls_u2p14[2]	9830	
t_APASmoothX_Uls_u2p14[3]	9994	
t_APASmoothX_Uls_u2p14[4]	10158	
t_APASmoothX_Uls_u2p14[5]	10322	
t_APASmoothX_Uls_u2p14[6]	10486	
t_APASmoothX_Uls_u2p14[7]	10650	
t_APASmoothX_Uls_u2p14[8]	10813	
t_APASmoothX_Uls_u2p14[9]	10977	
t_APASmoothY_Uls_u2p14[0]	6554	
t_APASmoothY_Uls_u2p14[1]	6717	
t_APASmoothY_Uls_u2p14[2]	6881	
t_APASmoothY_Uls_u2p14[3]	7045	
t_APASmoothY_Uls_u2p14[4]	7209	
t_APASmoothY_Uls_u2p14[5]	7373	
t_APASmoothY_Uls_u2p14[6]	7537	
t_APASmoothY_Uls_u2p14[7]	7700	
t_APASmoothY_Uls_u2p14[8]	7864	
t_APASmoothY_Uls_u2p14[9]	8028	
t_LKASlewX_Kph_u8p8[0]	29696	
t_LKASlewX_Kph_u8p8[1]	30208	
t_LKASlewX_Kph_u8p8[2]	30720	

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[3]	31232
t_LKASlewX_Kph_u8p8[4]	31744
t_LKASlewX_Kph_u8p8[5]	32256
t_LKASlewY_NmpSec_u4p12[0]	23757
t_LKASlewY_NmpSec_u4p12[1]	24166
t_LKASlewY_NmpSec_u4p12[2]	24576
t_LKASlewY_NmpSec_u4p12[3]	24986
t_LKASlewY_NmpSec_u4p12[4]	25395
t_LKASlewY_NmpSec_u4p12[5]	25805
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHocillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.45000005
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.34999999
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	341.442993
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.30000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.100000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.258355469	0.248999998 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.83250046	-5.83249998 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017	5.07000017 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-4.08839989	-4.08739996 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.523976862	0.505002975 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.60 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.400000006
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.94999981
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.221000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.36000013
TrqArblim_PrelKATrqReq_HwNm_M_f32	-3.09999999
k_APAEnableRate_pSec_f32	265.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	1

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TrqArblim_Per1

Name	Input Value
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	24166
t_APADisableRateX_HwNm_u4p12[1]	24576
t_APADisableRateX_HwNm_u4p12[2]	24986
t_APADisableRateX_HwNm_u4p12[3]	25395
t_APADisableRateX_HwNm_u4p12[4]	25805
t_APADisableRateX_HwNm_u4p12[5]	26214
t_APADisableRateY_pSec_u7p9[0]	30208
t_APADisableRateY_pSec_u7p9[1]	30720
t_APADisableRateY_pSec_u7p9[2]	31232
t_APADisableRateY_pSec_u7p9[3]	31744
t_APADisableRateY_pSec_u7p9[4]	32256
t_APADisableRateY_pSec_u7p9[5]	32768
t_APASmoothX_Uls_u2p14[0]	9667
t_APASmoothX_Uls_u2p14[1]	9830
t_APASmoothX_Uls_u2p14[2]	9994
t_APASmoothX_Uls_u2p14[3]	10158
t_APASmoothX_Uls_u2p14[4]	10322
t_APASmoothX_Uls_u2p14[5]	10486
t_APASmoothX_Uls_u2p14[6]	10650
t_APASmoothX_Uls_u2p14[7]	10813
t_APASmoothX_Uls_u2p14[8]	10977
t_APASmoothX_Uls_u2p14[9]	11141
t_APASmoothY_Uls_u2p14[0]	6717
t_APASmoothY_Uls_u2p14[1]	6881
t_APASmoothY_Uls_u2p14[2]	7045
t_APASmoothY_Uls_u2p14[3]	7209
t_APASmoothY_Uls_u2p14[4]	7373
t_APASmoothY_Uls_u2p14[5]	7537
t_APASmoothY_Uls_u2p14[6]	7700
t_APASmoothY_Uls_u2p14[7]	7864
t_APASmoothY_Uls_u2p14[8]	8028
t_APASmoothY_Uls_u2p14[9]	8192
t_LKASlewX_Kph_u8p8[0]	30208
t_LKASlewX_Kph_u8p8[1]	30720
t_LKASlewX_Kph_u8p8[2]	31232
t_LKASlewX_Kph_u8p8[3]	31744
t_LKASlewX_Kph_u8p8[4]	32256
t_LKASlewX_Kph_u8p8[5]	32768
t_LKASlewY_NmpSec_u4p12[0]	24166
t_LKASlewY_NmpSec_u4p12[1]	24576
t_LKASlewY_NmpSec_u4p12[2]	24986
t_LKASlewY_NmpSec_u4p12[3]	25395
t_LKASlewY_NmpSec_u4p12[4]	25805
t_LKASlewY_NmpSec_u4p12[5]	26214
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHocillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.01000023
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.23999977
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	347.893005
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.300000012		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.282000005	0.272000015 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.30683994	-5.30683994 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.36000013	5.36000013 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.884387553	0.888486981 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.61968267	0.597707987 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.61 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.25
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.61999989
TrqArblim_PrelKATrqReq_HwNm_M_f32	-4.5999999
k_APAEnableRate_pSec_f32	240
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	21709
t_APADisableRateX_HwNm_u4p12[1]	22118
t_APADisableRateX_HwNm_u4p12[2]	22528
t_APADisableRateX_HwNm_u4p12[3]	22938
t_APADisableRateX_HwNm_u4p12[4]	23347
t_APADisableRateX_HwNm_u4p12[5]	23757
t_APADisableRateY_pSec_u7p9[0]	27136
t_APADisableRateY_pSec_u7p9[1]	27648
t_APADisableRateY_pSec_u7p9[2]	28160
t_APADisableRateY_pSec_u7p9[3]	28672
t_APADisableRateY_pSec_u7p9[4]	29184
t_APADisableRateY_pSec_u7p9[5]	29696
t_APASmoothX_Uls_u2p14[0]	8684
t_APASmoothX_Uls_u2p14[1]	8847
t_APASmoothX_Uls_u2p14[2]	9011
t_APASmoothX_Uls_u2p14[3]	9175
t_APASmoothX_Uls_u2p14[4]	9339
t_APASmoothX_Uls_u2p14[5]	9503
t_APASmoothX_Uls_u2p14[6]	9667
t_APASmoothX_Uls_u2p14[7]	9830
t_APASmoothX_Uls_u2p14[8]	9994
t_APASmoothX_Uls_u2p14[9]	10158
t_APASmoothY_Uls_u2p14[0]	5734
t_APASmoothY_Uls_u2p14[1]	5898
t_APASmoothY_Uls_u2p14[2]	6062
t_APASmoothY_Uls_u2p14[3]	6226
t_APASmoothY_Uls_u2p14[4]	6390
t_APASmoothY_Uls_u2p14[5]	6554
t_APASmoothY_Uls_u2p14[6]	6717
t_APASmoothY_Uls_u2p14[7]	6881
t_APASmoothY_Uls_u2p14[8]	7045
t_APASmoothY_Uls_u2p14[9]	7209

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TrqArblim_Per1

Name	Input Value		
t_LKASlewX_Kph_u8p8[0]	27136		
t_LKASlewX_Kph_u8p8[1]	27648		
t_LKASlewX_Kph_u8p8[2]	28160		
t_LKASlewX_Kph_u8p8[3]	28672		
t_LKASlewX_Kph_u8p8[4]	29184		
t_LKASlewX_Kph_u8p8[5]	29696		
t_LKASlewY_NmpSec_u4p12[0]	21709		
t_LKASlewY_NmpSec_u4p12[1]	22118		
t_LKASlewY_NmpSec_u4p12[2]	22528		
t_LKASlewY_NmpSec_u4p12[3]	22938		
t_LKASlewY_NmpSec_u4p12[4]	23347		
t_LKASlewY_NmpSec_u4p12[5]	23757		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.9000001		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.730000019	0.730000019 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.74800014	7.74800014 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.29999995	3.29999995 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.58939981	-4.58839989 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.159965992	0.159966007 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0.000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.62 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.449999988
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002

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TrqArblim_Per1

Name	Input Value
k_APAEnableRate_pSec_f32	285
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	24986
t_APADisableRateX_HwNm_u4p12[1]	25395
t_APADisableRateX_HwNm_u4p12[2]	25805
t_APADisableRateX_HwNm_u4p12[3]	26214
t_APADisableRateX_HwNm_u4p12[4]	26624
t_APADisableRateX_HwNm_u4p12[5]	27034
t_APADisableRateY_pSec_u7p9[0]	31232
t_APADisableRateY_pSec_u7p9[1]	31744
t_APADisableRateY_pSec_u7p9[2]	32256
t_APADisableRateY_pSec_u7p9[3]	32768
t_APADisableRateY_pSec_u7p9[4]	33280
t_APADisableRateY_pSec_u7p9[5]	33792
t_APASmoothX_Uls_u2p14[0]	9994
t_APASmoothX_Uls_u2p14[1]	10158
t_APASmoothX_Uls_u2p14[2]	10322
t_APASmoothX_Uls_u2p14[3]	10486
t_APASmoothX_Uls_u2p14[4]	10650
t_APASmoothX_Uls_u2p14[5]	10813
t_APASmoothX_Uls_u2p14[6]	10977
t_APASmoothX_Uls_u2p14[7]	11141
t_APASmoothX_Uls_u2p14[8]	11305
t_APASmoothX_Uls_u2p14[9]	11469
t_APASmoothY_Uls_u2p14[0]	7045
t_APASmoothY_Uls_u2p14[1]	7209
t_APASmoothY_Uls_u2p14[2]	7373
t_APASmoothY_Uls_u2p14[3]	7537
t_APASmoothY_Uls_u2p14[4]	7700
t_APASmoothY_Uls_u2p14[5]	7864
t_APASmoothY_Uls_u2p14[6]	8028
t_APASmoothY_Uls_u2p14[7]	8192
t_APASmoothY_Uls_u2p14[8]	8356
t_APASmoothY_Uls_u2p14[9]	8520
t_LKASlewX_Kph_u8p8[0]	31232
t_LKASlewX_Kph_u8p8[1]	31744
t_LKASlewX_Kph_u8p8[2]	32256
t_LKASlewX_Kph_u8p8[3]	32768
t_LKASlewX_Kph_u8p8[4]	33280
t_LKASlewX_Kph_u8p8[5]	33792
t_LKASlewY_NmpSec_u4p12[0]	24986
t_LKASlewY_NmpSec_u4p12[1]	25395
t_LKASlewY_NmpSec_u4p12[2]	25805
t_LKASlewY_NmpSec_u4p12[3]	26214
t_LKASlewY_NmpSec_u4p12[4]	26624
t_LKASlewY_NmpSec_u4p12[5]	27034
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.13000011
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.0200005

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	360.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.699999988		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.327999979	0.317999989 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.24166012	-4.24166012 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006	5.94000006 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.08779979	-1.08679998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.858962417	0.863261998 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-2.57999992	-2.57999992 ± 0.000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-2.57999992	-2.57999992 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.83776325	0.812222004 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.858962417	0.863261998 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.63 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.474999994
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.9000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.254000008
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.23000002
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.100000001
k_APAEnableRate_pSec_f32	294.75
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	25395
t_APADisableRateX_HwNm_u4p12[1]	25805
t_APADisableRateX_HwNm_u4p12[2]	26214
t_APADisableRateX_HwNm_u4p12[3]	26624
t_APADisableRateX_HwNm_u4p12[4]	27034
t_APADisableRateX_HwNm_u4p12[5]	27443
t_APADisableRateY_pSec_u7p9[0]	31744
t_APADisableRateY_pSec_u7p9[1]	32256
t_APADisableRateY_pSec_u7p9[2]	32768
t_APADisableRateY_pSec_u7p9[3]	33280
t_APADisableRateY_pSec_u7p9[4]	33792
t_APADisableRateY_pSec_u7p9[5]	34304
t_APASmoothX_Uls_u2p14[0]	10158
t_APASmoothX_Uls_u2p14[1]	10322
t_APASmoothX_Uls_u2p14[2]	10486
t_APASmoothX_Uls_u2p14[3]	10650
t_APASmoothX_Uls_u2p14[4]	10813
t_APASmoothX_Uls_u2p14[5]	10977
t_APASmoothX_Uls_u2p14[6]	11141
t_APASmoothX_Uls_u2p14[7]	11305
t_APASmoothX_Uls_u2p14[8]	11469
t_APASmoothX_Uls_u2p14[9]	11633
t_APASmoothY_Uls_u2p14[0]	7209
t_APASmoothY_Uls_u2p14[1]	7373
t_APASmoothY_Uls_u2p14[2]	7537
t_APASmoothY_Uls_u2p14[3]	7700
t_APASmoothY_Uls_u2p14[4]	7864
t_APASmoothY_Uls_u2p14[5]	8028
t_APASmoothY_Uls_u2p14[6]	8192

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[7]	8356		
t_APASmoothY_Uls_u2p14[8]	8520		
t_APASmoothY_Uls_u2p14[9]	8684		
t_LKASlewX_Kph_u8p8[0]	31744		
t_LKASlewX_Kph_u8p8[1]	32256		
t_LKASlewX_Kph_u8p8[2]	32768		
t_LKASlewX_Kph_u8p8[3]	33280		
t_LKASlewX_Kph_u8p8[4]	33792		
t_LKASlewX_Kph_u8p8[5]	34304		
t_LKASlewY_NmpSec_u4p12[0]	25395		
t_LKASlewY_NmpSec_u4p12[1]	25805		
t_LKASlewY_NmpSec_u4p12[2]	26214		
t_LKASlewY_NmpSec_u4p12[3]	26624		
t_LKASlewY_NmpSec_u4p12[4]	27034		
t_LKASlewY_NmpSec_u4p12[5]	27443		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCoscillate_State_enum	target_TrqArblim_Per1_GMOSHOCoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.42000008		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOCoscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.69000006		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.22000003		
target_TrqArblim_Per1_LKASState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	367.243011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	5.09999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.899999976		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.70213985	-3.70214009 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	5.09999999	5.09999999 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.0940000042	-0.0939999968 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.469970703	0.469971001 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.239999995	-0.239999995 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.239999995	-0.239999995 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.60314941	3.60314989 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.64 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.5
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.55000019

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TrqArblim_Per1

Name	Input Value
TrqArblim_HwTorqueSV_HwNm_M_Str_K_Uls_f32	0.264999986
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.51999998
TrqArblim_PrelKATrqReq_HwNm_M_f32	0.899999976
k_APAEnableRate_pSec_f32	304.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t_APADisableRateY_pSec_u7p9[0]	32256
t_APADisableRateY_pSec_u7p9[1]	32768
t_APADisableRateY_pSec_u7p9[2]	33280
t_APADisableRateY_pSec_u7p9[3]	33792
t_APADisableRateY_pSec_u7p9[4]	34304
t_APADisableRateY_pSec_u7p9[5]	34816
t_APASmoothX_Uls_u2p14[0]	10322
t_APASmoothX_Uls_u2p14[1]	10486
t_APASmoothX_Uls_u2p14[2]	10650
t_APASmoothX_Uls_u2p14[3]	10813
t_APASmoothX_Uls_u2p14[4]	10977
t_APASmoothX_Uls_u2p14[5]	11141
t_APASmoothX_Uls_u2p14[6]	11305
t_APASmoothX_Uls_u2p14[7]	11469
t_APASmoothX_Uls_u2p14[8]	11633
t_APASmoothX_Uls_u2p14[9]	11796
t_APASmoothY_Uls_u2p14[0]	7373
t_APASmoothY_Uls_u2p14[1]	7537
t_APASmoothY_Uls_u2p14[2]	7700
t_APASmoothY_Uls_u2p14[3]	7864
t_APASmoothY_Uls_u2p14[4]	8028
t_APASmoothY_Uls_u2p14[5]	8192
t_APASmoothY_Uls_u2p14[6]	8356
t_APASmoothY_Uls_u2p14[7]	8520
t_APASmoothY_Uls_u2p14[8]	8684
t_APASmoothY_Uls_u2p14[9]	8847
t_LKASlewX_Kph_u8p8[0]	32256
t_LKASlewX_Kph_u8p8[1]	32768
t_LKASlewX_Kph_u8p8[2]	33280
t_LKASlewX_Kph_u8p8[3]	33792
t_LKASlewX_Kph_u8p8[4]	34304
t_LKASlewX_Kph_u8p8[5]	34816
t_LKASlewY_NmpSec_u4p12[0]	25805
t_LKASlewY_NmpSec_u4p12[1]	26214
t_LKASlewY_NmpSec_u4p12[2]	26624
t_LKASlewY_NmpSec_u4p12[3]	27034
t_LKASlewY_NmpSec_u4p12[4]	27443
t_LKASlewY_NmpSec_u4p12[5]	27853
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.21999979
target_TrqArblim_Per1_ESCState_State_enum.value	3

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TrqArblim_Per1

Name	Input Value
target_TrqArblim_Per1_GMOSHocillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6.25
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-9.86999989
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	373.692993
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	5.30000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.10000002

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.15800023	-3.15799999 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.30000019	5.30000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.893999994	0.893999994 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.460021973	0.460022002 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.460021973	0.460022002 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.86188364	2.86188006 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.460021973	0.460022002 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.65 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.25
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.61999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.59999999
k_APAEnableRate_pSec_f32	240
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	21709
t_APADisableRateX_HwNm_u4p12[1]	22118
t_APADisableRateX_HwNm_u4p12[2]	22528
t_APADisableRateX_HwNm_u4p12[3]	22938
t_APADisableRateX_HwNm_u4p12[4]	23347
t_APADisableRateX_HwNm_u4p12[5]	23757
t_APADisableRateY_pSec_u7p9[0]	27136
t_APADisableRateY_pSec_u7p9[1]	27648
t_APADisableRateY_pSec_u7p9[2]	28160
t_APADisableRateY_pSec_u7p9[3]	28672
t_APADisableRateY_pSec_u7p9[4]	29184
t_APADisableRateY_pSec_u7p9[5]	29696
t_APASmoothX_Uls_u2p14[0]	8684
t_APASmoothX_Uls_u2p14[1]	8847
t_APASmoothX_Uls_u2p14[2]	9011
t_APASmoothX_Uls_u2p14[3]	9175
t_APASmoothX_Uls_u2p14[4]	9339
t_APASmoothX_Uls_u2p14[5]	9503
t_APASmoothX_Uls_u2p14[6]	9667
t_APASmoothX_Uls_u2p14[7]	9830
t_APASmoothX_Uls_u2p14[8]	9994
t_APASmoothX_Uls_u2p14[9]	10158
t_APASmoothY_Uls_u2p14[0]	5734
t_APASmoothY_Uls_u2p14[1]	5898
t_APASmoothY_Uls_u2p14[2]	6062
t_APASmoothY_Uls_u2p14[3]	6226

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	6390		
t_APASmoothY_Uls_u2p14[5]	6554		
t_APASmoothY_Uls_u2p14[6]	6717		
t_APASmoothY_Uls_u2p14[7]	6881		
t_APASmoothY_Uls_u2p14[8]	7045		
t_APASmoothY_Uls_u2p14[9]	7209		
t_LKASlewX_Kph_u8p8[0]	27136		
t_LKASlewX_Kph_u8p8[1]	27648		
t_LKASlewX_Kph_u8p8[2]	28160		
t_LKASlewX_Kph_u8p8[3]	28672		
t_LKASlewX_Kph_u8p8[4]	29184		
t_LKASlewX_Kph_u8p8[5]	29696		
t_LKASlewY_NmpSec_u4p12[0]	21709		
t_LKASlewY_NmpSec_u4p12[1]	22118		
t_LKASlewY_NmpSec_u4p12[2]	22528		
t_LKASlewY_NmpSec_u4p12[3]	22938		
t_LKASlewY_NmpSec_u4p12[4]	23347		
t_LKASlewY_NmpSec_u4p12[5]	23757		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.90000001		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.730000019	0.730000019 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.74800014	7.74800014 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	3.29999995	3.29999995 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.58939981	-4.58839989 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.159965992	0.159966007 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	✔

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.66 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	

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TrqArblim_Per1

Name	Input Value
TrqArblim_APADisableRate_Uls_M_f32	0.550000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.8499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.287
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.0999999
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.9000001
k_APAEnableRate_pSec_f32	324
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	26624
t_APADisableRateX_HwNm_u4p12[1]	27034
t_APADisableRateX_HwNm_u4p12[2]	27443
t_APADisableRateX_HwNm_u4p12[3]	27853
t_APADisableRateX_HwNm_u4p12[4]	28262
t_APADisableRateX_HwNm_u4p12[5]	28672
t_APADisableRateY_pSec_u7p9[0]	33280
t_APADisableRateY_pSec_u7p9[1]	33792
t_APADisableRateY_pSec_u7p9[2]	34304
t_APADisableRateY_pSec_u7p9[3]	34816
t_APADisableRateY_pSec_u7p9[4]	35328
t_APADisableRateY_pSec_u7p9[5]	35840
t_APASmoothX_Uls_u2p14[0]	10650
t_APASmoothX_Uls_u2p14[1]	10813
t_APASmoothX_Uls_u2p14[2]	10977
t_APASmoothX_Uls_u2p14[3]	11141
t_APASmoothX_Uls_u2p14[4]	11305
t_APASmoothX_Uls_u2p14[5]	11469
t_APASmoothX_Uls_u2p14[6]	11633
t_APASmoothX_Uls_u2p14[7]	11796
t_APASmoothX_Uls_u2p14[8]	11960
t_APASmoothX_Uls_u2p14[9]	12124
t_APASmoothY_Uls_u2p14[0]	7700
t_APASmoothY_Uls_u2p14[1]	7864
t_APASmoothY_Uls_u2p14[2]	8028
t_APASmoothY_Uls_u2p14[3]	8192
t_APASmoothY_Uls_u2p14[4]	8356
t_APASmoothY_Uls_u2p14[5]	8520
t_APASmoothY_Uls_u2p14[6]	8684
t_APASmoothY_Uls_u2p14[7]	8847
t_APASmoothY_Uls_u2p14[8]	9011
t_APASmoothY_Uls_u2p14[9]	9175
t_LKASlewX_Kph_u8p8[0]	0
t_LKASlewX_Kph_u8p8[1]	0
t_LKASlewX_Kph_u8p8[2]	0
t_LKASlewX_Kph_u8p8[3]	0
t_LKASlewX_Kph_u8p8[4]	0
t_LKASlewX_Kph_u8p8[5]	0
t_LKASlewY_NmpSec_u4p12[0]	26624
t_LKASlewY_NmpSec_u4p12[1]	27034
t_LKASlewY_NmpSec_u4p12[2]	27443
t_LKASlewY_NmpSec_u4p12[3]	27853
t_LKASlewY_NmpSec_u4p12[4]	28262
t_LKASlewY_NmpSec_u4p12[5]	28672
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.36999989
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-9.17000008
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	386.592987
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	5.69999981
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.05586004	-2.05586004 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	5.69999981	5.69999981 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.88600016	2.88599992 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.440002441	0.440001994 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.440002441	0.440001994 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	2.88600016	2.88599992 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.88600016	2.88599992 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.19198608	3.1919899 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.440002441	0.440001994 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.67 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.574999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.298000008
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.38999987
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.90000001
k_APAEnableRate_pSec_f32	333.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	27034
t_APADisableRateX_HwNm_u4p12[1]	27443
t_APADisableRateX_HwNm_u4p12[2]	27853
t_APADisableRateX_HwNm_u4p12[3]	28262
t_APADisableRateX_HwNm_u4p12[4]	28672
t_APADisableRateX_HwNm_u4p12[5]	29082
t_APADisableRateY_pSec_u7p9[0]	33792
t_APADisableRateY_pSec_u7p9[1]	34304
t_APADisableRateY_pSec_u7p9[2]	34816
t_APADisableRateY_pSec_u7p9[3]	35328
t_APADisableRateY_pSec_u7p9[4]	35840
t_APADisableRateY_pSec_u7p9[5]	36352
t_APASmoothX_Uls_u2p14[0]	10813
t_APASmoothX_Uls_u2p14[1]	10977
t_APASmoothX_Uls_u2p14[2]	11141
t_APASmoothX_Uls_u2p14[3]	11305
t_APASmoothX_Uls_u2p14[4]	11469
t_APASmoothX_Uls_u2p14[5]	11633
t_APASmoothX_Uls_u2p14[6]	11796
t_APASmoothX_Uls_u2p14[7]	11960
t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_Uls_u2p14[9]	12288
t_APASmoothY_Uls_u2p14[0]	7864

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[1]	8028		
t_APASmoothY_Uls_u2p14[2]	8192		
t_APASmoothY_Uls_u2p14[3]	8356		
t_APASmoothY_Uls_u2p14[4]	8520		
t_APASmoothY_Uls_u2p14[5]	8684		
t_APASmoothY_Uls_u2p14[6]	8847		
t_APASmoothY_Uls_u2p14[7]	9011		
t_APASmoothY_Uls_u2p14[8]	9175		
t_APASmoothY_Uls_u2p14[9]	9339		
t_LKASlewX_Kph_u8p8[0]	56320		
t_LKASlewX_Kph_u8p8[1]	56320		
t_LKASlewX_Kph_u8p8[2]	56320		
t_LKASlewX_Kph_u8p8[3]	56320		
t_LKASlewX_Kph_u8p8[4]	56320		
t_LKASlewX_Kph_u8p8[5]	56320		
t_LKASlewY_NmpSec_u4p12[0]	27034		
t_LKASlewY_NmpSec_u4p12[1]	27443		
t_LKASlewY_NmpSec_u4p12[2]	27853		
t_LKASlewY_NmpSec_u4p12[3]	28262		
t_LKASlewY_NmpSec_u4p12[4]	28672		
t_LKASlewY_NmpSec_u4p12[5]	29082		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-0.620000005		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.92999983		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.81999969		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	393.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	5.90000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.442999989	0.442999989 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.49785995	-1.49785995 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.38999987	7.38999987 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.88679981	3.88579988 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.620000005	-0.620000005 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.620000005	-0.620000005 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.27134562	3.27134991 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 3.68 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.600000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.308999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.67999983
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.9000001
k_APAEnableRate_pSec_f32	343.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	27443
t_APADisableRateX_HwNm_u4p12[1]	27853
t_APADisableRateX_HwNm_u4p12[2]	28262
t_APADisableRateX_HwNm_u4p12[3]	28672
t_APADisableRateX_HwNm_u4p12[4]	29082
t_APADisableRateX_HwNm_u4p12[5]	29491
t_APADisableRateY_pSec_u7p9[0]	34304
t_APADisableRateY_pSec_u7p9[1]	34816
t_APADisableRateY_pSec_u7p9[2]	35328
t_APADisableRateY_pSec_u7p9[3]	35840
t_APADisableRateY_pSec_u7p9[4]	36352
t_APADisableRateY_pSec_u7p9[5]	36864
t_APASmoothX_Uls_u2p14[0]	10977
t_APASmoothX_Uls_u2p14[1]	11141
t_APASmoothX_Uls_u2p14[2]	11305
t_APASmoothX_Uls_u2p14[3]	11469
t_APASmoothX_Uls_u2p14[4]	11633
t_APASmoothX_Uls_u2p14[5]	11796
t_APASmoothX_Uls_u2p14[6]	11960
t_APASmoothX_Uls_u2p14[7]	12124
t_APASmoothX_Uls_u2p14[8]	12288
t_APASmoothX_Uls_u2p14[9]	12452
t_APASmoothY_Uls_u2p14[0]	8028
t_APASmoothY_Uls_u2p14[1]	8192
t_APASmoothY_Uls_u2p14[2]	8356
t_APASmoothY_Uls_u2p14[3]	8520
t_APASmoothY_Uls_u2p14[4]	8684
t_APASmoothY_Uls_u2p14[5]	8847
t_APASmoothY_Uls_u2p14[6]	9011
t_APASmoothY_Uls_u2p14[7]	9175
t_APASmoothY_Uls_u2p14[8]	9339
t_APASmoothY_Uls_u2p14[9]	9503
t_LKASlewX_Kph_u8p8[0]	34304
t_LKASlewX_Kph_u8p8[1]	34816
t_LKASlewX_Kph_u8p8[2]	35328
t_LKASlewX_Kph_u8p8[3]	35840
t_LKASlewX_Kph_u8p8[4]	36352
t_LKASlewX_Kph_u8p8[5]	36864
t_LKASlewY_NmpSec_u4p12[0]	27443
t_LKASlewY_NmpSec_u4p12[1]	27853
t_LKASlewY_NmpSec_u4p12[2]	28262
t_LKASlewY_NmpSec_u4p12[3]	28672
t_LKASlewY_NmpSec_u4p12[4]	29082
t_LKASlewY_NmpSec_u4p12[5]	29491
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.48999977
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.47000027
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	399.493011
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	6.0999999
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.89999998

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.466000021	0.465999991 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.935240269	-0.935239971 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.67999983	7.67999983 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.88565111	4.88560009 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.77166456	0.771664977 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.77166456	0.771664977 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.579999983	0.579999983 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.579999983	0.579999983 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.75361621	1.75362003 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.77166456	0.771664977 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.69 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.625
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.319999993
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.96999979
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.9000001
k_APAEnableRate_pSec_f32	353.25
k_APAUseADmpScale_Igc	0
k_APAUseAsstScale_Igc	0
k_APAUseRetScale_Igc	0
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	27853
t_APADisableRateX_HwNm_u4p12[1]	28262
t_APADisableRateX_HwNm_u4p12[2]	28672
t_APADisableRateX_HwNm_u4p12[3]	29082
t_APADisableRateX_HwNm_u4p12[4]	29491
t_APADisableRateX_HwNm_u4p12[5]	29901
t_APADisableRateY_pSec_u7p9[0]	34816
t_APADisableRateY_pSec_u7p9[1]	35328
t_APADisableRateY_pSec_u7p9[2]	35840
t_APADisableRateY_pSec_u7p9[3]	36352
t_APADisableRateY_pSec_u7p9[4]	36864
t_APADisableRateY_pSec_u7p9[5]	37376
t_APASmoothX_Uls_u2p14[0]	11141
t_APASmoothX_Uls_u2p14[1]	11305
t_APASmoothX_Uls_u2p14[2]	11469
t_APASmoothX_Uls_u2p14[3]	11633
t_APASmoothX_Uls_u2p14[4]	11796
t_APASmoothX_Uls_u2p14[5]	11960
t_APASmoothX_Uls_u2p14[6]	12124
t_APASmoothX_Uls_u2p14[7]	12288

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TrqArblim_Per1

Name	Input Value
t_APASmoothX_Uls_u2p14[8]	12452
t_APASmoothX_Uls_u2p14[9]	12616
t_APASmoothY_Uls_u2p14[0]	8192
t_APASmoothY_Uls_u2p14[1]	8356
t_APASmoothY_Uls_u2p14[2]	8520
t_APASmoothY_Uls_u2p14[3]	8684
t_APASmoothY_Uls_u2p14[4]	8847
t_APASmoothY_Uls_u2p14[5]	9011
t_APASmoothY_Uls_u2p14[6]	9175
t_APASmoothY_Uls_u2p14[7]	9339
t_APASmoothY_Uls_u2p14[8]	9503
t_APASmoothY_Uls_u2p14[9]	9667
t_LKASlewX_Kph_u8p8[0]	33280
t_LKASlewX_Kph_u8p8[1]	33792
t_LKASlewX_Kph_u8p8[2]	34304
t_LKASlewX_Kph_u8p8[3]	34816
t_LKASlewX_Kph_u8p8[4]	35328
t_LKASlewX_Kph_u8p8[5]	35840
t_LKASlewY_NmpSec_u4p12[0]	0
t_LKASlewY_NmpSec_u4p12[1]	0
t_LKASlewY_NmpSec_u4p12[2]	0
t_LKASlewY_NmpSec_u4p12[3]	0
t_LKASlewY_NmpSec_u4p12[4]	0
t_LKASlewY_NmpSec_u4p12[5]	0
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	1.77999997
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.05000019
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.11999989
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	405.942993
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	6.30000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.09999999

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.368000031	-0.368000001 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	6.30000019	6.30000019 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	5.89400005	5.89400005 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.71716928	3.71717 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 3.70 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.649999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.331
TrqArblim_PosSrvcCmd_MtrNm_M_f32	8.26000023
TrqArblim_PrelKATrqReq_HwNm_M_f32	6.9000001
k_APAEnableRate_pSec_f32	363
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	28262
t_APADisableRateX_HwNm_u4p12[1]	28672
t_APADisableRateX_HwNm_u4p12[2]	29082
t_APADisableRateX_HwNm_u4p12[3]	29491
t_APADisableRateX_HwNm_u4p12[4]	29901
t_APADisableRateX_HwNm_u4p12[5]	30310
t_APADisableRateY_pSec_u7p9[0]	35328
t_APADisableRateY_pSec_u7p9[1]	35840
t_APADisableRateY_pSec_u7p9[2]	36352
t_APADisableRateY_pSec_u7p9[3]	36864
t_APADisableRateY_pSec_u7p9[4]	37376
t_APADisableRateY_pSec_u7p9[5]	37888
t_APASmoothX_Uls_u2p14[0]	11305
t_APASmoothX_Uls_u2p14[1]	11469
t_APASmoothX_Uls_u2p14[2]	11633
t_APASmoothX_Uls_u2p14[3]	11796
t_APASmoothX_Uls_u2p14[4]	11960
t_APASmoothX_Uls_u2p14[5]	12124
t_APASmoothX_Uls_u2p14[6]	12288
t_APASmoothX_Uls_u2p14[7]	12452
t_APASmoothX_Uls_u2p14[8]	12616
t_APASmoothX_Uls_u2p14[9]	12780
t_APASmoothY_Uls_u2p14[0]	8356
t_APASmoothY_Uls_u2p14[1]	8520
t_APASmoothY_Uls_u2p14[2]	8684
t_APASmoothY_Uls_u2p14[3]	8847
t_APASmoothY_Uls_u2p14[4]	9011
t_APASmoothY_Uls_u2p14[5]	9175
t_APASmoothY_Uls_u2p14[6]	9339
t_APASmoothY_Uls_u2p14[7]	9503
t_APASmoothY_Uls_u2p14[8]	9667
t_APASmoothY_Uls_u2p14[9]	9830
t_LKASlewX_Kph_u8p8[0]	33792
t_LKASlewX_Kph_u8p8[1]	34304
t_LKASlewX_Kph_u8p8[2]	34816
t_LKASlewX_Kph_u8p8[3]	35328
t_LKASlewX_Kph_u8p8[4]	35840
t_LKASlewX_Kph_u8p8[5]	36352
t_LKASlewY_NmpSec_u4p12[0]	55296
t_LKASlewY_NmpSec_u4p12[1]	55296
t_LKASlewY_NmpSec_u4p12[2]	55296
t_LKASlewY_NmpSec_u4p12[3]	55296
t_LKASlewY_NmpSec_u4p12[4]	55296
t_LKASlewY_NmpSec_u4p12[5]	55296
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	2.98000002
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.60999966
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.76999998
target_TrqArblim_Per1_LKASState_State_enum.value	0
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	412.393005
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.29999995

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.203859806	0.20386 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.5	6.5 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	6.87300014	6.87300014 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.400024414	0.400023997 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.400024414	0.400023997 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	6.1998415	6.19984007 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.400024414	0.400023997 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.71 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.675000012
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342000008
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.55000019
TrqArblim_PrelKATrqReq_HwNm_M_f32	7.90000001
k_APAEnableRate_pSec_f32	372.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	28672
t_APADisableRateX_HwNm_u4p12[1]	29082
t_APADisableRateX_HwNm_u4p12[2]	29491
t_APADisableRateX_HwNm_u4p12[3]	29901
t_APADisableRateX_HwNm_u4p12[4]	30310
t_APADisableRateX_HwNm_u4p12[5]	30720
t_APADisableRateY_pSec_u7p9[0]	35840
t_APADisableRateY_pSec_u7p9[1]	36352
t_APADisableRateY_pSec_u7p9[2]	36864
t_APADisableRateY_pSec_u7p9[3]	37376
t_APADisableRateY_pSec_u7p9[4]	37888
t_APADisableRateY_pSec_u7p9[5]	38400
t_APASmoothX_Uls_u2p14[0]	11469
t_APASmoothX_Uls_u2p14[1]	11633
t_APASmoothX_Uls_u2p14[2]	11796
t_APASmoothX_Uls_u2p14[3]	11960
t_APASmoothX_Uls_u2p14[4]	12124
t_APASmoothX_Uls_u2p14[5]	12288

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TrqArblim_Per1

Name	Input Value		
t_APASmoothX_Uls_u2p14[6]	12452		
t_APASmoothX_Uls_u2p14[7]	12616		
t_APASmoothX_Uls_u2p14[8]	12780		
t_APASmoothX_Uls_u2p14[9]	12943		
t_APASmoothY_Uls_u2p14[0]	8520		
t_APASmoothY_Uls_u2p14[1]	8684		
t_APASmoothY_Uls_u2p14[2]	8847		
t_APASmoothY_Uls_u2p14[3]	9011		
t_APASmoothY_Uls_u2p14[4]	9175		
t_APASmoothY_Uls_u2p14[5]	9339		
t_APASmoothY_Uls_u2p14[6]	9503		
t_APASmoothY_Uls_u2p14[7]	9667		
t_APASmoothY_Uls_u2p14[8]	9830		
t_APASmoothY_Uls_u2p14[9]	9994		
t_LKASlewX_Kph_u8p8[0]	34304		
t_LKASlewX_Kph_u8p8[1]	34816		
t_LKASlewX_Kph_u8p8[2]	35328		
t_LKASlewX_Kph_u8p8[3]	35840		
t_LKASlewX_Kph_u8p8[4]	36352		
t_LKASlewX_Kph_u8p8[5]	36864		
t_LKASlewY_NmpSec_u4p12[0]	6144		
t_LKASlewY_NmpSec_u4p12[1]	6554		
t_LKASlewY_NmpSec_u4p12[2]	6636		
t_LKASlewY_NmpSec_u4p12[3]	8929		
t_LKASlewY_NmpSec_u4p12[4]	12370		
t_LKASlewY_NmpSec_u4p12[5]	21627		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	4.17999983		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.35000038		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.42000008		
target_TrqArblim_Per1_LKASState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	418.842987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	6.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.535000026	0.535000026 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	0	0	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.89550018	-5.89550018 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	8.55000019	8.55000019 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.88944006	7.88944006 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.8786993	4.87869978 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔

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TrqArblim_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntrpVarXY_u16_u16Xu16Y_Cnt	3	IntrpVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.72 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.699999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.352999985
TrqArblim_PosSrvcCmd_MtrNm_M_f32	8.84000015
TrqArblim_PrelKATrqReq_HwNm_M_f32	1.10000002
k_APAEnableRate_pSec_f32	382.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	0
t_APADisableRateX_HwNm_u4p12[1]	0
t_APADisableRateX_HwNm_u4p12[2]	0
t_APADisableRateX_HwNm_u4p12[3]	0
t_APADisableRateX_HwNm_u4p12[4]	0
t_APADisableRateX_HwNm_u4p12[5]	0
t_APADisableRateY_pSec_u7p9[0]	36352
t_APADisableRateY_pSec_u7p9[1]	36864
t_APADisableRateY_pSec_u7p9[2]	37376
t_APADisableRateY_pSec_u7p9[3]	37888
t_APADisableRateY_pSec_u7p9[4]	38400
t_APADisableRateY_pSec_u7p9[5]	38912
t_APASmoothX_Uls_u2p14[0]	11633
t_APASmoothX_Uls_u2p14[1]	11796
t_APASmoothX_Uls_u2p14[2]	11960
t_APASmoothX_Uls_u2p14[3]	12124
t_APASmoothX_Uls_u2p14[4]	12288
t_APASmoothX_Uls_u2p14[5]	12452
t_APASmoothX_Uls_u2p14[6]	12616
t_APASmoothX_Uls_u2p14[7]	12780
t_APASmoothX_Uls_u2p14[8]	12943
t_APASmoothX_Uls_u2p14[9]	13107
t_APASmoothY_Uls_u2p14[0]	8684
t_APASmoothY_Uls_u2p14[1]	8847
t_APASmoothY_Uls_u2p14[2]	9011
t_APASmoothY_Uls_u2p14[3]	9175
t_APASmoothY_Uls_u2p14[4]	9339
t_APASmoothY_Uls_u2p14[5]	9503
t_APASmoothY_Uls_u2p14[6]	9667
t_APASmoothY_Uls_u2p14[7]	9830
t_APASmoothY_Uls_u2p14[8]	9994
t_APASmoothY_Uls_u2p14[9]	10158
t_LKASlewX_Kph_u8p8[0]	34816
t_LKASlewX_Kph_u8p8[1]	35328
t_LKASlewX_Kph_u8p8[2]	35840
t_LKASlewX_Kph_u8p8[3]	36352
t_LKASlewX_Kph_u8p8[4]	36864
t_LKASlewX_Kph_u8p8[5]	37376
t_LKASlewY_NmpSec_u4p12[0]	27853
t_LKASlewY_NmpSec_u4p12[1]	28262
t_LKASlewY_NmpSec_u4p12[2]	28672
t_LKASlewY_NmpSec_u4p12[3]	29082
t_LKASlewY_NmpSec_u4p12[4]	29491
t_LKASlewY_NmpSec_u4p12[5]	29901
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCcmd_HwNm_f32	target_TrqArblim_Per1_ESCcmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCcmd_HwNm_f32.value	5.38000011
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.07000017
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	425.292999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	6.9000001
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.70000005

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.60325003	-5.60325003 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	6.9000001	6.9000001 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.08539987	1.08539987 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	4.28000021	4.28000021 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.2779665	4.27796984 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.73 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.725000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.363999993
TrqArblim_PosSrvcCmd_MtrNm_M_f32	9.13000011
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.60000002
k_APAEnableRate_pSec_f32	392.25
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	40960
t_APADisableRateX_HwNm_u4p12[1]	40960
t_APADisableRateX_HwNm_u4p12[2]	40960
t_APADisableRateX_HwNm_u4p12[3]	40960
t_APADisableRateX_HwNm_u4p12[4]	40960
t_APADisableRateX_HwNm_u4p12[5]	40960
t_APADisableRateY_pSec_u7p9[0]	36864
t_APADisableRateY_pSec_u7p9[1]	37376
t_APADisableRateY_pSec_u7p9[2]	37888
t_APADisableRateY_pSec_u7p9[3]	38400
t_APADisableRateY_pSec_u7p9[4]	38912

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	39424		
t_APASmoothX_Uls_u2p14[0]	11796		
t_APASmoothX_Uls_u2p14[1]	11960		
t_APASmoothX_Uls_u2p14[2]	12124		
t_APASmoothX_Uls_u2p14[3]	12288		
t_APASmoothX_Uls_u2p14[4]	12452		
t_APASmoothX_Uls_u2p14[5]	12616		
t_APASmoothX_Uls_u2p14[6]	12780		
t_APASmoothX_Uls_u2p14[7]	12943		
t_APASmoothX_Uls_u2p14[8]	13107		
t_APASmoothX_Uls_u2p14[9]	13271		
t_APASmoothY_Uls_u2p14[0]	8847		
t_APASmoothY_Uls_u2p14[1]	9011		
t_APASmoothY_Uls_u2p14[2]	9175		
t_APASmoothY_Uls_u2p14[3]	9339		
t_APASmoothY_Uls_u2p14[4]	9503		
t_APASmoothY_Uls_u2p14[5]	9667		
t_APASmoothY_Uls_u2p14[6]	9830		
t_APASmoothY_Uls_u2p14[7]	9994		
t_APASmoothY_Uls_u2p14[8]	10158		
t_APASmoothY_Uls_u2p14[9]	10322		
t_LKASlewX_Kph_u8p8[0]	35328		
t_LKASlewX_Kph_u8p8[1]	35840		
t_LKASlewX_Kph_u8p8[2]	36352		
t_LKASlewX_Kph_u8p8[3]	36864		
t_LKASlewX_Kph_u8p8[4]	37376		
t_LKASlewX_Kph_u8p8[5]	37888		
t_LKASlewY_NmpSec_u4p12[0]	28262		
t_LKASlewY_NmpSec_u4p12[1]	28672		
t_LKASlewY_NmpSec_u4p12[2]	29082		
t_LKASlewY_NmpSec_u4p12[3]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901		
t_LKASlewY_NmpSec_u4p12[5]	30310		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64999962		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.71999979		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	431.743011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.09999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.90000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.58100003	0.58099997 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.31099987	-5.31099987 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011	9.13000011 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.59399998	1.59399998 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	3.25	3.25 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	5.76432991	5.76432991 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.74 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.75
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.375
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.42000008
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.09999999
k_APAEnableRate_pSec_f32	402
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.1400001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	4096
t_APADisableRateX_HwNm_u4p12[1]	8192
t_APADisableRateX_HwNm_u4p12[2]	12288
t_APADisableRateX_HwNm_u4p12[3]	16384
t_APADisableRateX_HwNm_u4p12[4]	20480
t_APADisableRateX_HwNm_u4p12[5]	24576
t_APADisableRateY_pSec_u7p9[0]	37376
t_APADisableRateY_pSec_u7p9[1]	37888
t_APADisableRateY_pSec_u7p9[2]	38400
t_APADisableRateY_pSec_u7p9[3]	38912
t_APADisableRateY_pSec_u7p9[4]	39424
t_APADisableRateY_pSec_u7p9[5]	39936
t_APASmoothX_Uls_u2p14[0]	11960
t_APASmoothX_Uls_u2p14[1]	12124
t_APASmoothX_Uls_u2p14[2]	12288
t_APASmoothX_Uls_u2p14[3]	12452
t_APASmoothX_Uls_u2p14[4]	12616
t_APASmoothX_Uls_u2p14[5]	12780
t_APASmoothX_Uls_u2p14[6]	12943
t_APASmoothX_Uls_u2p14[7]	13107
t_APASmoothX_Uls_u2p14[8]	13271
t_APASmoothX_Uls_u2p14[9]	13435
t_APASmoothY_Uls_u2p14[0]	9011
t_APASmoothY_Uls_u2p14[1]	9175
t_APASmoothY_Uls_u2p14[2]	9339
t_APASmoothY_Uls_u2p14[3]	9503
t_APASmoothY_Uls_u2p14[4]	9667
t_APASmoothY_Uls_u2p14[5]	9830
t_APASmoothY_Uls_u2p14[6]	9994
t_APASmoothY_Uls_u2p14[7]	10158
t_APASmoothY_Uls_u2p14[8]	10322
t_APASmoothY_Uls_u2p14[9]	10486
t_LKASlewX_Kph_u8p8[0]	35840
t_LKASlewX_Kph_u8p8[1]	36352
t_LKASlewX_Kph_u8p8[2]	36864
t_LKASlewX_Kph_u8p8[3]	37376
t_LKASlewX_Kph_u8p8[4]	37888
t_LKASlewX_Kph_u8p8[5]	38400
t_LKASlewY_NmpSec_u4p12[0]	28672
t_LKASlewY_NmpSec_u4p12[1]	29082
t_LKASlewY_NmpSec_u4p12[2]	29491
t_LKASlewY_NmpSec_u4p12[3]	29901
t_LKASlewY_NmpSec_u4p12[4]	30310
t_LKASlewY_NmpSec_u4p12[5]	30720
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	7.30000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.09999999

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.01875019	-5.01875019 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.30000019	7.30000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0849998	2.08500004 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.359985352	0.359984994 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.359985352	0.359984994 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.67210722	4.67211008 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.359985352	0.359984994 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.75 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.774999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.386000007
TrqArblim_PosSrvcCmd_MtrNm_M_f32	9.71000004
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59999999
k_APAEnableRate_pSec_f32	411.75
k_APAUseADmpScale_Igc	0
k_APAUseAsstScale_Igc	0
k_APAUseRetScale_Igc	0
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_Igc	0
t_APADisableRateX_HwNm_u4p12[0]	29491
t_APADisableRateX_HwNm_u4p12[1]	29901
t_APADisableRateX_HwNm_u4p12[2]	30310
t_APADisableRateX_HwNm_u4p12[3]	30720
t_APADisableRateX_HwNm_u4p12[4]	31130
t_APADisableRateX_HwNm_u4p12[5]	31539
t_APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	0

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	0		
t_APADisableRateY_pSec_u7p9[3]	0		
t_APADisableRateY_pSec_u7p9[4]	0		
t_APADisableRateY_pSec_u7p9[5]	0		
t_APASmoothX_Uls_u2p14[0]	12124		
t_APASmoothX_Uls_u2p14[1]	12288		
t_APASmoothX_Uls_u2p14[2]	12452		
t_APASmoothX_Uls_u2p14[3]	12616		
t_APASmoothX_Uls_u2p14[4]	12780		
t_APASmoothX_Uls_u2p14[5]	12943		
t_APASmoothX_Uls_u2p14[6]	13107		
t_APASmoothX_Uls_u2p14[7]	13271		
t_APASmoothX_Uls_u2p14[8]	13435		
t_APASmoothX_Uls_u2p14[9]	13599		
t_APASmoothY_Uls_u2p14[0]	9175		
t_APASmoothY_Uls_u2p14[1]	9339		
t_APASmoothY_Uls_u2p14[2]	9503		
t_APASmoothY_Uls_u2p14[3]	9667		
t_APASmoothY_Uls_u2p14[4]	9830		
t_APASmoothY_Uls_u2p14[5]	9994		
t_APASmoothY_Uls_u2p14[6]	10158		
t_APASmoothY_Uls_u2p14[7]	10322		
t_APASmoothY_Uls_u2p14[8]	10486		
t_APASmoothY_Uls_u2p14[9]	10650		
t_LKASlewX_Kph_u8p8[0]	36352		
t_LKASlewX_Kph_u8p8[1]	36864		
t_LKASlewX_Kph_u8p8[2]	37376		
t_LKASlewX_Kph_u8p8[3]	37888		
t_LKASlewX_Kph_u8p8[4]	38400		
t_LKASlewX_Kph_u8p8[5]	38912		
t_LKASlewY_NmpSec_u4p12[0]	29082		
t_LKASlewY_NmpSec_u4p12[1]	29491		
t_LKASlewY_NmpSec_u4p12[2]	29901		
t_LKASlewY_NmpSec_u4p12[3]	30310		
t_LKASlewY_NmpSec_u4p12[4]	30720		
t_LKASlewY_NmpSec_u4p12[5]	31130		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.94999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	444.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.774599969	0.774600029 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	4	4	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.72650003	-4.72650003 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59399986	2.5940001 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.47177887	4.47177982 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.76 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvcCmd_MtrNm_M_f32	10
TrqArblim_PrelKATrqReq_HwNm_M_f32	3.0999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	29901
t_APADisableRateX_HwNm_u4p12[1]	30310
t_APADisableRateX_HwNm_u4p12[2]	30720
t_APADisableRateX_HwNm_u4p12[3]	31130
t_APADisableRateX_HwNm_u4p12[4]	31539
t_APADisableRateX_HwNm_u4p12[5]	31949
t_APADisableRateY_pSec_u7p9[0]	65531
t_APADisableRateY_pSec_u7p9[1]	65531
t_APADisableRateY_pSec_u7p9[2]	65531
t_APADisableRateY_pSec_u7p9[3]	65531
t_APADisableRateY_pSec_u7p9[4]	65531
t_APADisableRateY_pSec_u7p9[5]	65531
t_APASmoothX_Uls_u2p14[0]	12288
t_APASmoothX_Uls_u2p14[1]	12452
t_APASmoothX_Uls_u2p14[2]	12616
t_APASmoothX_Uls_u2p14[3]	12780
t_APASmoothX_Uls_u2p14[4]	12943
t_APASmoothX_Uls_u2p14[5]	13107
t_APASmoothX_Uls_u2p14[6]	13271
t_APASmoothX_Uls_u2p14[7]	13435
t_APASmoothX_Uls_u2p14[8]	13599
t_APASmoothX_Uls_u2p14[9]	13763
t_APASmoothY_Uls_u2p14[0]	6226
t_APASmoothY_Uls_u2p14[1]	6390
t_APASmoothY_Uls_u2p14[2]	6554
t_APASmoothY_Uls_u2p14[3]	6717
t_APASmoothY_Uls_u2p14[4]	6881
t_APASmoothY_Uls_u2p14[5]	7045
t_APASmoothY_Uls_u2p14[6]	7209
t_APASmoothY_Uls_u2p14[7]	7373
t_APASmoothY_Uls_u2p14[8]	7537
t_APASmoothY_Uls_u2p14[9]	7700
t_LKASlewX_Kph_u8p8[0]	36864
t_LKASlewX_Kph_u8p8[1]	37376
t_LKASlewX_Kph_u8p8[2]	37888
t_LKASlewX_Kph_u8p8[3]	38400
t_LKASlewX_Kph_u8p8[4]	38912
t_LKASlewX_Kph_u8p8[5]	39424
t_LKASlewY_NmpSec_u4p12[0]	29491
t_LKASlewY_NmpSec_u4p12[1]	29901
t_LKASlewY_NmpSec_u4p12[2]	30310
t_LKASlewY_NmpSec_u4p12[3]	30720
t_LKASlewY_NmpSec_u4p12[4]	31130

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[5]	31539
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.1800003
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.43424988	-4.43424988 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.530029297	0.530028999 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.530029297	0.530028999 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.61877441	3.61876988 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.530029297	0.530028999 ± 0000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.77 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.824999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.5999999
k_APAEnableRate_pSec_f32	431.25
k_APAUseADmpScale_Igc	0
k_APAUseAsstScale_Igc	0
k_APAUseRetScale_Igc	0
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_Igc	0
t_APADisableRateX_HwNm_u4p12[0]	30310
t_APADisableRateX_HwNm_u4p12[1]	30720
t_APADisableRateX_HwNm_u4p12[2]	31130
t_APADisableRateX_HwNm_u4p12[3]	31539
t_APADisableRateX_HwNm_u4p12[4]	31949

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	32358		
t_APADisableRateY_pSec_u7p9[0]	37888		
t_APADisableRateY_pSec_u7p9[1]	38400		
t_APADisableRateY_pSec_u7p9[2]	38912		
t_APADisableRateY_pSec_u7p9[3]	39424		
t_APADisableRateY_pSec_u7p9[4]	39936		
t_APADisableRateY_pSec_u7p9[5]	40448		
t_APASmoothX_Uls_u2p14[0]	12452		
t_APASmoothX_Uls_u2p14[1]	12616		
t_APASmoothX_Uls_u2p14[2]	12780		
t_APASmoothX_Uls_u2p14[3]	12943		
t_APASmoothX_Uls_u2p14[4]	13107		
t_APASmoothX_Uls_u2p14[5]	13271		
t_APASmoothX_Uls_u2p14[6]	13435		
t_APASmoothX_Uls_u2p14[7]	13599		
t_APASmoothX_Uls_u2p14[8]	13763		
t_APASmoothX_Uls_u2p14[9]	13926		
t_APASmoothY_Uls_u2p14[0]	6390		
t_APASmoothY_Uls_u2p14[1]	6554		
t_APASmoothY_Uls_u2p14[2]	6717		
t_APASmoothY_Uls_u2p14[3]	6881		
t_APASmoothY_Uls_u2p14[4]	7045		
t_APASmoothY_Uls_u2p14[5]	7209		
t_APASmoothY_Uls_u2p14[6]	7373		
t_APASmoothY_Uls_u2p14[7]	7537		
t_APASmoothY_Uls_u2p14[8]	7700		
t_APASmoothY_Uls_u2p14[9]	7864		
t_LKASlewX_Kph_u8p8[0]	37376		
t_LKASlewX_Kph_u8p8[1]	37888		
t_LKASlewX_Kph_u8p8[2]	38400		
t_LKASlewX_Kph_u8p8[3]	38912		
t_LKASlewX_Kph_u8p8[4]	39424		
t_LKASlewX_Kph_u8p8[5]	39936		
t_LKASlewY_NmpSec_u4p12[0]	29901		
t_LKASlewY_NmpSec_u4p12[1]	30310		
t_LKASlewY_NmpSec_u4p12[2]	30720		
t_LKASlewY_NmpSec_u4p12[3]	31130		
t_LKASlewY_NmpSec_u4p12[4]	31539		
t_LKASlewY_NmpSec_u4p12[5]	31949		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCoscillate_State_enum	target_TrqArblim_Per1_GMOSHOCoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOCoscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.25		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.32000017		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	457.542999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	7.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.676999986	0.676999986 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	0	0	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.1420002	-4.1420002 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	0.5	0.5 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.59399986	3.5940001 ± 0.03125	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.83202004	3.83202004 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.78 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.850000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqArblim_PosSrvcCmd_MtrNm_M_f32	0.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.0999999
k_APAEnableRate_pSec_f32	441
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	30720
t_APADisableRateX_HwNm_u4p12[1]	31130
t_APADisableRateX_HwNm_u4p12[2]	31539
t_APADisableRateX_HwNm_u4p12[3]	31949
t_APADisableRateX_HwNm_u4p12[4]	32358
t_APADisableRateX_HwNm_u4p12[5]	32768
t_APADisableRateY_pSec_u7p9[0]	38400
t_APADisableRateY_pSec_u7p9[1]	38912
t_APADisableRateY_pSec_u7p9[2]	39424
t_APADisableRateY_pSec_u7p9[3]	39936
t_APADisableRateY_pSec_u7p9[4]	40448
t_APADisableRateY_pSec_u7p9[5]	40960
t_APASmoothX_Uls_u2p14[0]	0
t_APASmoothX_Uls_u2p14[1]	0
t_APASmoothX_Uls_u2p14[2]	0
t_APASmoothX_Uls_u2p14[3]	0
t_APASmoothX_Uls_u2p14[4]	0
t_APASmoothX_Uls_u2p14[5]	0
t_APASmoothX_Uls_u2p14[6]	0
t_APASmoothX_Uls_u2p14[7]	0
t_APASmoothX_Uls_u2p14[8]	0
t_APASmoothX_Uls_u2p14[9]	0
t_APASmoothY_Uls_u2p14[0]	6554
t_APASmoothY_Uls_u2p14[1]	6717
t_APASmoothY_Uls_u2p14[2]	6881
t_APASmoothY_Uls_u2p14[3]	7045
t_APASmoothY_Uls_u2p14[4]	7209
t_APASmoothY_Uls_u2p14[5]	7373
t_APASmoothY_Uls_u2p14[6]	7537
t_APASmoothY_Uls_u2p14[7]	7700
t_APASmoothY_Uls_u2p14[8]	7864
t_APASmoothY_Uls_u2p14[9]	8028
t_LKASlewX_Kph_u8p8[0]	37888
t_LKASlewX_Kph_u8p8[1]	38400
t_LKASlewX_Kph_u8p8[2]	38912
t_LKASlewX_Kph_u8p8[3]	39424
t_LKASlewX_Kph_u8p8[4]	39936
t_LKASlewX_Kph_u8p8[5]	40448
t_LKASlewY_NmpSec_u4p12[0]	30310
t_LKASlewY_NmpSec_u4p12[1]	30720

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[2]	31130
t_LKASlewY_NmpSec_u4p12[3]	31539
t_LKASlewY_NmpSec_u4p12[4]	31949
t_LKASlewY_NmpSec_u4p12[5]	32358
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.9000001
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.96999979
target_TrqArblim_Per1_LKASState_State_enum.value	3
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	463.993011
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.10000038
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.9000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.700000048	0.699999988 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.84975004	-3.84975004 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75	0.75 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	4.08419991	4.08419991 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.6570068	0.657006979 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.6570068	0.657006979 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.75	-0.75 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-0.75	-0.75 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.257244885	0.257245004 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.6570068	0.657006979 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.79 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.875
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.29999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1
TrqArblim_PrelKATrqReq_HwNm_M_f32	4.5999999
k_APAEnableRate_pSec_f32	450.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	31130
t_APADisableRateX_HwNm_u4p12[1]	31539

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	31949		
t_APADisableRateX_HwNm_u4p12[3]	32358		
t_APADisableRateX_HwNm_u4p12[4]	32768		
t_APADisableRateX_HwNm_u4p12[5]	33178		
t_APADisableRateY_pSec_u7p9[0]	38912		
t_APADisableRateY_pSec_u7p9[1]	39424		
t_APADisableRateY_pSec_u7p9[2]	39936		
t_APADisableRateY_pSec_u7p9[3]	40448		
t_APADisableRateY_pSec_u7p9[4]	40960		
t_APADisableRateY_pSec_u7p9[5]	41472		
t_APASmoothX_Uls_u2p14[0]	16384		
t_APASmoothX_Uls_u2p14[1]	16384		
t_APASmoothX_Uls_u2p14[2]	16384		
t_APASmoothX_Uls_u2p14[3]	16384		
t_APASmoothX_Uls_u2p14[4]	16384		
t_APASmoothX_Uls_u2p14[5]	16384		
t_APASmoothX_Uls_u2p14[6]	16384		
t_APASmoothX_Uls_u2p14[7]	16384		
t_APASmoothX_Uls_u2p14[8]	16384		
t_APASmoothX_Uls_u2p14[9]	16384		
t_APASmoothY_Uls_u2p14[0]	6717		
t_APASmoothY_Uls_u2p14[1]	6881		
t_APASmoothY_Uls_u2p14[2]	7045		
t_APASmoothY_Uls_u2p14[3]	7209		
t_APASmoothY_Uls_u2p14[4]	7373		
t_APASmoothY_Uls_u2p14[5]	7537		
t_APASmoothY_Uls_u2p14[6]	7700		
t_APASmoothY_Uls_u2p14[7]	7864		
t_APASmoothY_Uls_u2p14[8]	8028		
t_APASmoothY_Uls_u2p14[9]	8192		
t_LKASlewX_Kph_u8p8[0]	38400		
t_LKASlewX_Kph_u8p8[1]	38912		
t_LKASlewX_Kph_u8p8[2]	39424		
t_LKASlewX_Kph_u8p8[3]	39936		
t_LKASlewX_Kph_u8p8[4]	40448		
t_LKASlewX_Kph_u8p8[5]	40960		
t_LKASlewY_NmpSec_u4p12[0]	30720		
t_LKASlewY_NmpSec_u4p12[1]	31130		
t_LKASlewY_NmpSec_u4p12[2]	31539		
t_LKASlewY_NmpSec_u4p12[3]	31949		
t_LKASlewY_NmpSec_u4p12[4]	32358		
t_LKASlewY_NmpSec_u4p12[5]	32768		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCssillate_State_enum	target_TrqArblim_Per1_GMOSHOCssillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOCssillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.55000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.61999989		
target_TrqArblim_Per1_LKASState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	470.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	8.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.09999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.55749989	-3.55749989 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.30000019	8.30000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.59399986	4.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	7.5027771	7.50277996 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.80 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.899999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.949999988
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.25
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.0999999
k_APAEnableRate_pSec_f32	460.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	31539
t_APADisableRateX_HwNm_u4p12[1]	31949
t_APADisableRateX_HwNm_u4p12[2]	32358
t_APADisableRateX_HwNm_u4p12[3]	32768
t_APADisableRateX_HwNm_u4p12[4]	33178
t_APADisableRateX_HwNm_u4p12[5]	33587
t_APADisableRateY_pSec_u7p9[0]	39424
t_APADisableRateY_pSec_u7p9[1]	39936
t_APADisableRateY_pSec_u7p9[2]	40448
t_APADisableRateY_pSec_u7p9[3]	40960
t_APADisableRateY_pSec_u7p9[4]	41472
t_APADisableRateY_pSec_u7p9[5]	41984
t_APASmoothX_Uls_u2p14[0]	12616
t_APASmoothX_Uls_u2p14[1]	12780
t_APASmoothX_Uls_u2p14[2]	12943
t_APASmoothX_Uls_u2p14[3]	13107
t_APASmoothX_Uls_u2p14[4]	13271
t_APASmoothX_Uls_u2p14[5]	13435
t_APASmoothX_Uls_u2p14[6]	13599
t_APASmoothX_Uls_u2p14[7]	13763
t_APASmoothX_Uls_u2p14[8]	13926
t_APASmoothX_Uls_u2p14[9]	14090
t_APASmoothY_Uls_u2p14[0]	9175
t_APASmoothY_Uls_u2p14[1]	9339
t_APASmoothY_Uls_u2p14[2]	9503
t_APASmoothY_Uls_u2p14[3]	9667
t_APASmoothY_Uls_u2p14[4]	9830
t_APASmoothY_Uls_u2p14[5]	9994
t_APASmoothY_Uls_u2p14[6]	10158
t_APASmoothY_Uls_u2p14[7]	10322
t_APASmoothY_Uls_u2p14[8]	10486
t_APASmoothY_Uls_u2p14[9]	10650
t_LKASlewX_Kph_u8p8[0]	38912
t_LKASlewX_Kph_u8p8[1]	39424
t_LKASlewX_Kph_u8p8[2]	39936
t_LKASlewX_Kph_u8p8[3]	40448
t_LKASlewX_Kph_u8p8[4]	40960

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[5]	41472
t_LKASlewY_NmpSec_u4p12[0]	31130
t_LKASlewY_NmpSec_u4p12[1]	31539
t_LKASlewY_NmpSec_u4p12[2]	31949
t_LKASlewY_NmpSec_u4p12[3]	32358
t_LKASlewY_NmpSec_u4p12[4]	32768
t_LKASlewY_NmpSec_u4p12[5]	33178
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.26999998
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	476.893005
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.30000019

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.26525021	-3.26524997 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.5	8.5 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	5.08379984	5.08379984 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.349975586	0.349976003 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.349975586	0.349976003 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.349975586	0.349976003 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.81 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrqArblim_PrelKATrqReq_HwNm_M_f32	5.59999999
k_APAEnableRate_pSec_f32	470.25
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.75

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TrqArblim_Per1

Name	Input Value
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	31949
t_APADisableRateX_HwNm_u4p12[1]	32358
t_APADisableRateX_HwNm_u4p12[2]	32768
t_APADisableRateX_HwNm_u4p12[3]	33178
t_APADisableRateX_HwNm_u4p12[4]	33587
t_APADisableRateX_HwNm_u4p12[5]	33997
t_APADisableRateY_pSec_u7p9[0]	39936
t_APADisableRateY_pSec_u7p9[1]	40448
t_APADisableRateY_pSec_u7p9[2]	40960
t_APADisableRateY_pSec_u7p9[3]	41472
t_APADisableRateY_pSec_u7p9[4]	41984
t_APADisableRateY_pSec_u7p9[5]	42496
t_APASmoothX_Uls_u2p14[0]	12780
t_APASmoothX_Uls_u2p14[1]	12943
t_APASmoothX_Uls_u2p14[2]	13107
t_APASmoothX_Uls_u2p14[3]	13271
t_APASmoothX_Uls_u2p14[4]	13435
t_APASmoothX_Uls_u2p14[5]	13599
t_APASmoothX_Uls_u2p14[6]	13763
t_APASmoothX_Uls_u2p14[7]	13926
t_APASmoothX_Uls_u2p14[8]	14090
t_APASmoothX_Uls_u2p14[9]	14254
t_APASmoothY_Uls_u2p14[0]	0
t_APASmoothY_Uls_u2p14[1]	0
t_APASmoothY_Uls_u2p14[2]	0
t_APASmoothY_Uls_u2p14[3]	0
t_APASmoothY_Uls_u2p14[4]	0
t_APASmoothY_Uls_u2p14[5]	0
t_APASmoothY_Uls_u2p14[6]	0
t_APASmoothY_Uls_u2p14[7]	0
t_APASmoothY_Uls_u2p14[8]	0
t_APASmoothY_Uls_u2p14[9]	0
t_LKASlewX_Kph_u8p8[0]	39424
t_LKASlewX_Kph_u8p8[1]	39936
t_LKASlewX_Kph_u8p8[2]	40448
t_LKASlewX_Kph_u8p8[3]	40960
t_LKASlewX_Kph_u8p8[4]	41472
t_LKASlewX_Kph_u8p8[5]	41984
t_LKASlewY_NmpSec_u4p12[0]	31539
t_LKASlewY_NmpSec_u4p12[1]	31949
t_LKASlewY_NmpSec_u4p12[2]	32358
t_LKASlewY_NmpSec_u4p12[3]	32768
t_LKASlewY_NmpSec_u4p12[4]	33178
t_LKASlewY_NmpSec_u4p12[5]	33587
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.84999999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.69999981
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.768999994	0.768999994 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.973000005	-2.973000005 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.583600004	5.583600004 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-2.75	-2.75 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-2.75	-2.75 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓	
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.82 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.949999988	
TrqArblim_ESC_State_Cnt_M_enum	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75	
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.09999999	
k_APAEnableRate_pSec_f32	480	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	1.23000002	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	32358	
t_APADisableRateX_HwNm_u4p12[1]	32768	
t_APADisableRateX_HwNm_u4p12[2]	33178	
t_APADisableRateX_HwNm_u4p12[3]	33587	
t_APADisableRateX_HwNm_u4p12[4]	33997	
t_APADisableRateX_HwNm_u4p12[5]	34406	
t_APADisableRateY_pSec_u7p9[0]	40448	
t_APADisableRateY_pSec_u7p9[1]	40960	
t_APADisableRateY_pSec_u7p9[2]	41472	
t_APADisableRateY_pSec_u7p9[3]	41984	
t_APADisableRateY_pSec_u7p9[4]	42496	
t_APADisableRateY_pSec_u7p9[5]	43008	
t_APASmoothX_Uls_u2p14[0]	12943	
t_APASmoothX_Uls_u2p14[1]	13107	
t_APASmoothX_Uls_u2p14[2]	13271	
t_APASmoothX_Uls_u2p14[3]	13435	
t_APASmoothX_Uls_u2p14[4]	13599	
t_APASmoothX_Uls_u2p14[5]	13763	
t_APASmoothX_Uls_u2p14[6]	13926	
t_APASmoothX_Uls_u2p14[7]	14090	
t_APASmoothX_Uls_u2p14[8]	14254	
t_APASmoothX_Uls_u2p14[9]	14418	
t_APASmoothY_Uls_u2p14[0]	16384	
t_APASmoothY_Uls_u2p14[1]	16384	
t_APASmoothY_Uls_u2p14[2]	16384	
t_APASmoothY_Uls_u2p14[3]	16384	
t_APASmoothY_Uls_u2p14[4]	16384	
t_APASmoothY_Uls_u2p14[5]	16384	
t_APASmoothY_Uls_u2p14[6]	16384	
t_APASmoothY_Uls_u2p14[7]	16384	
t_APASmoothY_Uls_u2p14[8]	16384	
t_APASmoothY_Uls_u2p14[9]	16384	
t_LKASlewX_Kph_u8p8[0]	39936	
t_LKASlewX_Kph_u8p8[1]	40448	
t_LKASlewX_Kph_u8p8[2]	40960	

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[3]	41472
t_LKASlewX_Kph_u8p8[4]	41984
t_LKASlewX_Kph_u8p8[5]	42496
t_LKASlewY_NmpSec_u4p12[0]	31949
t_LKASlewY_NmpSec_u4p12[1]	32358
t_LKASlewY_NmpSec_u4p12[2]	32768
t_LKASlewY_NmpSec_u4p12[3]	33178
t_LKASlewY_NmpSec_u4p12[4]	33587
t_LKASlewY_NmpSec_u4p12[5]	33997
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.5
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.56999993
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	489.792999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.60000038
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.69999981

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.791999996	0.791999996 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.68074989	-2.68074989 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75	1.75 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	6.09399986	6.09399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.38600004	1.38600004 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.83 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.975000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2
TrqArblim_PrelKATrqReq_HwNm_M_f32	6.5999999
k_APAEnableRate_pSec_f32	489.75
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1

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TrqArblim_Per1

Name	Input Value
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	32768
t_APADisableRateX_HwNm_u4p12[1]	33178
t_APADisableRateX_HwNm_u4p12[2]	33587
t_APADisableRateX_HwNm_u4p12[3]	33997
t_APADisableRateX_HwNm_u4p12[4]	34406
t_APADisableRateX_HwNm_u4p12[5]	34816
t_APADisableRateY_pSec_u7p9[0]	40960
t_APADisableRateY_pSec_u7p9[1]	41472
t_APADisableRateY_pSec_u7p9[2]	41984
t_APADisableRateY_pSec_u7p9[3]	42496
t_APADisableRateY_pSec_u7p9[4]	43008
t_APADisableRateY_pSec_u7p9[5]	43520
t_APASmoothX_Uls_u2p14[0]	13107
t_APASmoothX_Uls_u2p14[1]	13271
t_APASmoothX_Uls_u2p14[2]	13435
t_APASmoothX_Uls_u2p14[3]	13599
t_APASmoothX_Uls_u2p14[4]	13763
t_APASmoothX_Uls_u2p14[5]	13926
t_APASmoothX_Uls_u2p14[6]	14090
t_APASmoothX_Uls_u2p14[7]	14254
t_APASmoothX_Uls_u2p14[8]	14418
t_APASmoothX_Uls_u2p14[9]	14582
t_APASmoothY_Uls_u2p14[0]	12616
t_APASmoothY_Uls_u2p14[1]	12780
t_APASmoothY_Uls_u2p14[2]	12943
t_APASmoothY_Uls_u2p14[3]	13107
t_APASmoothY_Uls_u2p14[4]	13271
t_APASmoothY_Uls_u2p14[5]	13435
t_APASmoothY_Uls_u2p14[6]	13599
t_APASmoothY_Uls_u2p14[7]	13763
t_APASmoothY_Uls_u2p14[8]	13926
t_APASmoothY_Uls_u2p14[9]	14090
t_LKASlewX_Kph_u8p8[0]	40448
t_LKASlewX_Kph_u8p8[1]	40960
t_LKASlewX_Kph_u8p8[2]	41472
t_LKASlewX_Kph_u8p8[3]	41984
t_LKASlewX_Kph_u8p8[4]	42496
t_LKASlewX_Kph_u8p8[5]	43008
t_LKASlewY_NmpSec_u4p12[0]	32358
t_LKASlewY_NmpSec_u4p12[1]	32768
t_LKASlewY_NmpSec_u4p12[2]	33178
t_LKASlewY_NmpSec_u4p12[3]	33587
t_LKASlewY_NmpSec_u4p12[4]	33997
t_LKASlewY_NmpSec_u4p12[5]	34406
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.11999989
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.1500001
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	496.243011
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.10000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.90000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.38850021	-2.38849998 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.10000038	-8.10000038 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	6.58319998	6.58319998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-2.06588173	-2.06588006 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.84 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.699999988
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.352999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.84000015
TrqArblim_PrelKATrqReq_HwNm_M_f32	1.10000002
k_APAEnableRate_pSec_f32	382.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	24166
t_APADisableRateX_HwNm_u4p12[1]	24576
t_APADisableRateX_HwNm_u4p12[2]	24986
t_APADisableRateX_HwNm_u4p12[3]	25395
t_APADisableRateX_HwNm_u4p12[4]	25805
t_APADisableRateX_HwNm_u4p12[5]	26214
t_APADisableRateY_pSec_u7p9[0]	43008
t_APADisableRateY_pSec_u7p9[1]	43520
t_APADisableRateY_pSec_u7p9[2]	44032
t_APADisableRateY_pSec_u7p9[3]	44544
t_APADisableRateY_pSec_u7p9[4]	45056
t_APADisableRateY_pSec_u7p9[5]	45568
t_APASmoothX_Uls_u2p14[0]	8356
t_APASmoothX_Uls_u2p14[1]	8520
t_APASmoothX_Uls_u2p14[2]	8684
t_APASmoothX_Uls_u2p14[3]	8847
t_APASmoothX_Uls_u2p14[4]	9011
t_APASmoothX_Uls_u2p14[5]	9175
t_APASmoothX_Uls_u2p14[6]	9339
t_APASmoothX_Uls_u2p14[7]	9503
t_APASmoothX_Uls_u2p14[8]	9667
t_APASmoothX_Uls_u2p14[9]	9830
t_APASmoothY_Uls_u2p14[0]	5407
t_APASmoothY_Uls_u2p14[1]	5571
t_APASmoothY_Uls_u2p14[2]	5734
t_APASmoothY_Uls_u2p14[3]	5898
t_APASmoothY_Uls_u2p14[4]	6062
t_APASmoothY_Uls_u2p14[5]	6226
t_APASmoothY_Uls_u2p14[6]	6390
t_APASmoothY_Uls_u2p14[7]	6554
t_APASmoothY_Uls_u2p14[8]	6717
t_APASmoothY_Uls_u2p14[9]	6881

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[0]	34816
t_LKASlewX_Kph_u8p8[1]	35328
t_LKASlewX_Kph_u8p8[2]	35840
t_LKASlewX_Kph_u8p8[3]	36352
t_LKASlewX_Kph_u8p8[4]	36864
t_LKASlewX_Kph_u8p8[5]	37376
t_LKASlewY_NmpSec_u4p12[0]	27853
t_LKASlewY_NmpSec_u4p12[1]	28262
t_LKASlewY_NmpSec_u4p12[2]	28672
t_LKASlewY_NmpSec_u4p12[3]	29082
t_LKASlewY_NmpSec_u4p12[4]	29491
t_LKASlewY_NmpSec_u4p12[5]	29901
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCscillate_State_enum	target_TrqArblim_Per1_GMOSHOCscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10.2299995
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOCscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.07000017
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	425.292999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	6.9000001
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.70000005

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.60325003	-5.60325003 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	6.9000001	6.9000001 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	1.08539987	1.08539999 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.58001709	0.140015006 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.58001709	0.140015006 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.08539987	1.08539987 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.08539987	1.08539987 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.89788222	-2.06588006 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.58001709	0.140015006 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.85 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.725000024
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.363999993
TrqArblim_PosSrvcCmd_MtrNm_M_f32	9.13000011
TrqArblim_PrelKATrqReq_HwNm_M_f32	1.60000002

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TrqArblim_Per1

Name	Input Value
k_APAEnableRate_pSec_f32	392.25
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	24576
t_APADisableRateX_HwNm_u4p12[1]	24986
t_APADisableRateX_HwNm_u4p12[2]	25395
t_APADisableRateX_HwNm_u4p12[3]	25805
t_APADisableRateX_HwNm_u4p12[4]	26214
t_APADisableRateX_HwNm_u4p12[5]	26624
t_APADisableRateY_pSec_u7p9[0]	43520
t_APADisableRateY_pSec_u7p9[1]	44032
t_APADisableRateY_pSec_u7p9[2]	44544
t_APADisableRateY_pSec_u7p9[3]	45056
t_APADisableRateY_pSec_u7p9[4]	45568
t_APADisableRateY_pSec_u7p9[5]	46080
t_APASmoothX_Uls_u2p14[0]	8520
t_APASmoothX_Uls_u2p14[1]	8684
t_APASmoothX_Uls_u2p14[2]	8847
t_APASmoothX_Uls_u2p14[3]	9011
t_APASmoothX_Uls_u2p14[4]	9175
t_APASmoothX_Uls_u2p14[5]	9339
t_APASmoothX_Uls_u2p14[6]	9503
t_APASmoothX_Uls_u2p14[7]	9667
t_APASmoothX_Uls_u2p14[8]	9830
t_APASmoothX_Uls_u2p14[9]	9994
t_APASmoothY_Uls_u2p14[0]	5571
t_APASmoothY_Uls_u2p14[1]	5734
t_APASmoothY_Uls_u2p14[2]	5898
t_APASmoothY_Uls_u2p14[3]	6062
t_APASmoothY_Uls_u2p14[4]	6226
t_APASmoothY_Uls_u2p14[5]	6390
t_APASmoothY_Uls_u2p14[6]	6554
t_APASmoothY_Uls_u2p14[7]	6717
t_APASmoothY_Uls_u2p14[8]	6881
t_APASmoothY_Uls_u2p14[9]	7045
t_LKASlewX_Kph_u8p8[0]	35328
t_LKASlewX_Kph_u8p8[1]	35840
t_LKASlewX_Kph_u8p8[2]	36352
t_LKASlewX_Kph_u8p8[3]	36864
t_LKASlewX_Kph_u8p8[4]	37376
t_LKASlewX_Kph_u8p8[5]	37888
t_LKASlewY_NmpSec_u4p12[0]	28262
t_LKASlewY_NmpSec_u4p12[1]	28672
t_LKASlewY_NmpSec_u4p12[2]	29082
t_LKASlewY_NmpSec_u4p12[3]	29491
t_LKASlewY_NmpSec_u4p12[4]	29901
t_LKASlewY_NmpSec_u4p12[5]	30310
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64999962
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.71999979

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TrqArblim_Per1

Name	Input Value
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	431.743011
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.0999999
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.9000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.555000007	0.555000007 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.31099987	-5.31099987 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011	9.13000011 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.59399998	1.59399998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	4.28000021	4.28000021 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.80018139	4.80017996 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1		✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2		✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1		✓

Test Step 3.86 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.75	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.375	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.42000008	
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0999999	
k_APAEnableRate_pSec_f32	402	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	3.25	
k_LKAUseSlewCal_lgc	1	
t_APADisableRateX_HwNm_u4p12[0]	24986	
t_APADisableRateX_HwNm_u4p12[1]	25395	
t_APADisableRateX_HwNm_u4p12[2]	25805	
t_APADisableRateX_HwNm_u4p12[3]	26214	
t_APADisableRateX_HwNm_u4p12[4]	26624	
t_APADisableRateX_HwNm_u4p12[5]	27034	
t_APADisableRateY_pSec_u7p9[0]	42496	
t_APADisableRateY_pSec_u7p9[1]	43008	
t_APADisableRateY_pSec_u7p9[2]	43520	
t_APADisableRateY_pSec_u7p9[3]	44032	
t_APADisableRateY_pSec_u7p9[4]	44544	
t_APADisableRateY_pSec_u7p9[5]	45056	
t_APASmoothX_Uls_u2p14[0]	8684	
t_APASmoothX_Uls_u2p14[1]	8847	
t_APASmoothX_Uls_u2p14[2]	9011	
t_APASmoothX_Uls_u2p14[3]	9175	
t_APASmoothX_Uls_u2p14[4]	9339	
t_APASmoothX_Uls_u2p14[5]	9503	
t_APASmoothX_Uls_u2p14[6]	9667	
t_APASmoothX_Uls_u2p14[7]	9830	
t_APASmoothX_Uls_u2p14[8]	9994	
t_APASmoothX_Uls_u2p14[9]	10158	
t_APASmoothY_Uls_u2p14[0]	5734	
t_APASmoothY_Uls_u2p14[1]	5898	
t_APASmoothY_Uls_u2p14[2]	6062	
t_APASmoothY_Uls_u2p14[3]	6226	
t_APASmoothY_Uls_u2p14[4]	6390	
t_APASmoothY_Uls_u2p14[5]	6554	
t_APASmoothY_Uls_u2p14[6]	6717	

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TrqArblim_Per1

Name	Input Value
t_APASmoothY_Uls_u2p14[7]	6881
t_APASmoothY_Uls_u2p14[8]	7045
t_APASmoothY_Uls_u2p14[9]	7209
t_LKASlewX_Kph_u8p8[0]	35840
t_LKASlewX_Kph_u8p8[1]	36352
t_LKASlewX_Kph_u8p8[2]	36864
t_LKASlewX_Kph_u8p8[3]	37376
t_LKASlewX_Kph_u8p8[4]	37888
t_LKASlewX_Kph_u8p8[5]	38400
t_LKASlewY_NmpSec_u4p12[0]	28672
t_LKASlewY_NmpSec_u4p12[1]	29082
t_LKASlewY_NmpSec_u4p12[2]	29491
t_LKASlewY_NmpSec_u4p12[3]	29901
t_LKASlewY_NmpSec_u4p12[4]	30310
t_LKASlewY_NmpSec_u4p12[5]	30720
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	7.30000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.09999999

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.01875019	-5.01875019 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.30000019	7.30000019 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	2.0849998	2.08500004 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.21201801	3.21201992 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.87 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.774999976
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005

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TrqArblim_Per1

Name	Input Value
TrqArblim_HwTorqueSV_HwNm_M_Str_K_Uls_f32	0.386000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004
TrqArblim_PrelKATrqReq_HwNm_M_f32	2.5999999
k_APAEnableRate_pSec_f32	411.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.1400001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	25395
t_APADisableRateX_HwNm_u4p12[1]	25805
t_APADisableRateX_HwNm_u4p12[2]	26214
t_APADisableRateX_HwNm_u4p12[3]	26624
t_APADisableRateX_HwNm_u4p12[4]	27034
t_APADisableRateX_HwNm_u4p12[5]	27443
t_APADisableRateY_pSec_u7p9[0]	43008
t_APADisableRateY_pSec_u7p9[1]	43520
t_APADisableRateY_pSec_u7p9[2]	44032
t_APADisableRateY_pSec_u7p9[3]	44544
t_APADisableRateY_pSec_u7p9[4]	45056
t_APADisableRateY_pSec_u7p9[5]	45568
t_APASmoothX_Uls_u2p14[0]	8847
t_APASmoothX_Uls_u2p14[1]	9011
t_APASmoothX_Uls_u2p14[2]	9175
t_APASmoothX_Uls_u2p14[3]	9339
t_APASmoothX_Uls_u2p14[4]	9503
t_APASmoothX_Uls_u2p14[5]	9667
t_APASmoothX_Uls_u2p14[6]	9830
t_APASmoothX_Uls_u2p14[7]	9994
t_APASmoothX_Uls_u2p14[8]	10158
t_APASmoothX_Uls_u2p14[9]	10322
t_APASmoothY_Uls_u2p14[0]	5898
t_APASmoothY_Uls_u2p14[1]	6062
t_APASmoothY_Uls_u2p14[2]	6226
t_APASmoothY_Uls_u2p14[3]	6390
t_APASmoothY_Uls_u2p14[4]	6554
t_APASmoothY_Uls_u2p14[5]	6717
t_APASmoothY_Uls_u2p14[6]	6881
t_APASmoothY_Uls_u2p14[7]	7045
t_APASmoothY_Uls_u2p14[8]	7209
t_APASmoothY_Uls_u2p14[9]	7373
t_LKASlewX_Kph_u8p8[0]	36352
t_LKASlewX_Kph_u8p8[1]	36864
t_LKASlewX_Kph_u8p8[2]	37376
t_LKASlewX_Kph_u8p8[3]	37888
t_LKASlewX_Kph_u8p8[4]	38400
t_LKASlewX_Kph_u8p8[5]	38912
t_LKASlewY_NmpSec_u4p12[0]	29082
t_LKASlewY_NmpSec_u4p12[1]	29491
t_LKASlewY_NmpSec_u4p12[2]	29901
t_LKASlewY_NmpSec_u4p12[3]	30310
t_LKASlewY_NmpSec_u4p12[4]	30720
t_LKASlewY_NmpSec_u4p12[5]	31130
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHocillate_State_enum	target_TrqArblim_Per1_GMOSHocillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.19999981
target_TrqArblim_Per1_ESCState_State_enum.value	4

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TrqArblim_Per1

Name	Input Value		
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.94999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	444.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.606999993	0.606999993 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	4	4	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.72650003	-4.72650003 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59399986	2.5940001 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.51673651	2.51674008 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.88 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t_APADisableRateY_pSec_u7p9[0]	43520
t_APADisableRateY_pSec_u7p9[1]	44032
t_APADisableRateY_pSec_u7p9[2]	44544
t_APADisableRateY_pSec_u7p9[3]	45056
t_APADisableRateY_pSec_u7p9[4]	45568
t_APADisableRateY_pSec_u7p9[5]	46080
t_APASmoothX_Uls_u2p14[0]	9011
t_APASmoothX_Uls_u2p14[1]	9175
t_APASmoothX_Uls_u2p14[2]	9339
t_APASmoothX_Uls_u2p14[3]	9503
t_APASmoothX_Uls_u2p14[4]	9667
t_APASmoothX_Uls_u2p14[5]	9830
t_APASmoothX_Uls_u2p14[6]	9994
t_APASmoothX_Uls_u2p14[7]	10158
t_APASmoothX_Uls_u2p14[8]	10322
t_APASmoothX_Uls_u2p14[9]	10486
t_APASmoothY_Uls_u2p14[0]	6062
t_APASmoothY_Uls_u2p14[1]	6226
t_APASmoothY_Uls_u2p14[2]	6390
t_APASmoothY_Uls_u2p14[3]	6554

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TrqArblim_Per1

Name	Input Value
t_APASmoothY_Uls_u2p14[4]	6717
t_APASmoothY_Uls_u2p14[5]	6881
t_APASmoothY_Uls_u2p14[6]	7045
t_APASmoothY_Uls_u2p14[7]	7209
t_APASmoothY_Uls_u2p14[8]	7373
t_APASmoothY_Uls_u2p14[9]	7537
t_LKASlewX_Kph_u8p8[0]	36864
t_LKASlewX_Kph_u8p8[1]	37376
t_LKASlewX_Kph_u8p8[2]	37888
t_LKASlewX_Kph_u8p8[3]	38400
t_LKASlewX_Kph_u8p8[4]	38912
t_LKASlewX_Kph_u8p8[5]	39424
t_LKASlewY_NmpSec_u4p12[0]	29491
t_LKASlewY_NmpSec_u4p12[1]	29901
t_LKASlewY_NmpSec_u4p12[2]	30310
t_LKASlewY_NmpSec_u4p12[3]	30720
t_LKASlewY_NmpSec_u4p12[4]	31130
t_LKASlewY_NmpSec_u4p12[5]	31539
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	7.69999981
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.43424988	-4.43424988 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.54216909	3.54217005 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.89 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim

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TrqArblim_Per1

Name	Input Value
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.5999999
k_APAEnableRate_pSec_f32	470.25
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22118
t_APADisableRateX_HwNm_u4p12[1]	22528
t_APADisableRateX_HwNm_u4p12[2]	22938
t_APADisableRateX_HwNm_u4p12[3]	23347
t_APADisableRateX_HwNm_u4p12[4]	23757
t_APADisableRateX_HwNm_u4p12[5]	24166
t_APADisableRateY_pSec_u7p9[0]	33280
t_APADisableRateY_pSec_u7p9[1]	33792
t_APADisableRateY_pSec_u7p9[2]	34304
t_APADisableRateY_pSec_u7p9[3]	34816
t_APADisableRateY_pSec_u7p9[4]	35328
t_APADisableRateY_pSec_u7p9[5]	35840
t_APASmoothX_Uls_u2p14[0]	9175
t_APASmoothX_Uls_u2p14[1]	9339
t_APASmoothX_Uls_u2p14[2]	9503
t_APASmoothX_Uls_u2p14[3]	9667
t_APASmoothX_Uls_u2p14[4]	9830
t_APASmoothX_Uls_u2p14[5]	9994
t_APASmoothX_Uls_u2p14[6]	10158
t_APASmoothX_Uls_u2p14[7]	10322
t_APASmoothX_Uls_u2p14[8]	10486
t_APASmoothX_Uls_u2p14[9]	10650
t_APASmoothY_Uls_u2p14[0]	6226
t_APASmoothY_Uls_u2p14[1]	6390
t_APASmoothY_Uls_u2p14[2]	6554
t_APASmoothY_Uls_u2p14[3]	6717
t_APASmoothY_Uls_u2p14[4]	6881
t_APASmoothY_Uls_u2p14[5]	7045
t_APASmoothY_Uls_u2p14[6]	7209
t_APASmoothY_Uls_u2p14[7]	7373
t_APASmoothY_Uls_u2p14[8]	7537
t_APASmoothY_Uls_u2p14[9]	7700
t_LKASlewX_Kph_u8p8[0]	39424
t_LKASlewX_Kph_u8p8[1]	39936
t_LKASlewX_Kph_u8p8[2]	40448
t_LKASlewX_Kph_u8p8[3]	40960
t_LKASlewX_Kph_u8p8[4]	41472
t_LKASlewX_Kph_u8p8[5]	41984
t_LKASlewY_NmpSec_u4p12[0]	31539
t_LKASlewY_NmpSec_u4p12[1]	31949
t_LKASlewY_NmpSec_u4p12[2]	32358
t_LKASlewY_NmpSec_u4p12[3]	32768
t_LKASlewY_NmpSec_u4p12[4]	33178
t_LKASlewY_NmpSec_u4p12[5]	33587
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.69999981
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.795000017	0.795000017 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.97300005	-2.97300005 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58360004	5.58360004 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.626373291	0.626372993 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.626373291	0.626372993 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.560440063	0.560440004 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.626373291	0.626372993 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.90 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.949999988
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.0999999
k_APAEnableRate_pSec_f32	480
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938
t_APADisableRateX_HwNm_u4p12[2]	23347
t_APADisableRateX_HwNm_u4p12[3]	23757
t_APADisableRateX_HwNm_u4p12[4]	24166
t_APADisableRateX_HwNm_u4p12[5]	24576
t_APADisableRateY_pSec_u7p9[0]	34304
t_APADisableRateY_pSec_u7p9[1]	34816
t_APADisableRateY_pSec_u7p9[2]	35328
t_APADisableRateY_pSec_u7p9[3]	35840
t_APADisableRateY_pSec_u7p9[4]	36352
t_APADisableRateY_pSec_u7p9[5]	36864
t_APASmoothX_Uls_u2p14[0]	9339
t_APASmoothX_Uls_u2p14[1]	9503
t_APASmoothX_Uls_u2p14[2]	9667
t_APASmoothX_Uls_u2p14[3]	9830
t_APASmoothX_Uls_u2p14[4]	9994
t_APASmoothX_Uls_u2p14[5]	10158
t_APASmoothX_Uls_u2p14[6]	10322
t_APASmoothX_Uls_u2p14[7]	10486
t_APASmoothX_Uls_u2p14[8]	10650
t_APASmoothX_Uls_u2p14[9]	10813
t_APASmoothY_Uls_u2p14[0]	6390

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[1]	6554		
t_APASmoothY_Uls_u2p14[2]	6717		
t_APASmoothY_Uls_u2p14[3]	6881		
t_APASmoothY_Uls_u2p14[4]	7045		
t_APASmoothY_Uls_u2p14[5]	7209		
t_APASmoothY_Uls_u2p14[6]	7373		
t_APASmoothY_Uls_u2p14[7]	7537		
t_APASmoothY_Uls_u2p14[8]	7700		
t_APASmoothY_Uls_u2p14[9]	7864		
t_LKASlewX_Kph_u8p8[0]	39936		
t_LKASlewX_Kph_u8p8[1]	40448		
t_LKASlewX_Kph_u8p8[2]	40960		
t_LKASlewX_Kph_u8p8[3]	41472		
t_LKASlewX_Kph_u8p8[4]	41984		
t_LKASlewX_Kph_u8p8[5]	42496		
t_LKASlewY_NmpSec_u4p12[0]	31949		
t_LKASlewY_NmpSec_u4p12[1]	32358		
t_LKASlewY_NmpSec_u4p12[2]	32768		
t_LKASlewY_NmpSec_u4p12[3]	33178		
t_LKASlewY_NmpSec_u4p12[4]	33587		
t_LKASlewY_NmpSec_u4p12[5]	33997		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.5		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.56999993		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	489.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.60000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.69999981		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.815999985	0.815999985 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	1	1	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.68074989	-2.68074989 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75	1.75 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.09399986	6.09399986 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.140000001	-0.140000001 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.140000001	-0.140000001 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.685412109	0.68541199 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 3.91 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.975000024
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.59999999
k_APAEnableRate_pSec_f32	489.75
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22938
t_APADisableRateX_HwNm_u4p12[1]	23347
t_APADisableRateX_HwNm_u4p12[2]	23757
t_APADisableRateX_HwNm_u4p12[3]	24166
t_APADisableRateX_HwNm_u4p12[4]	24576
t_APADisableRateX_HwNm_u4p12[5]	24986
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	9503
t_APASmoothX_Uls_u2p14[1]	9667
t_APASmoothX_Uls_u2p14[2]	9830
t_APASmoothX_Uls_u2p14[3]	9994
t_APASmoothX_Uls_u2p14[4]	10158
t_APASmoothX_Uls_u2p14[5]	10322
t_APASmoothX_Uls_u2p14[6]	10486
t_APASmoothX_Uls_u2p14[7]	10650
t_APASmoothX_Uls_u2p14[8]	10813
t_APASmoothX_Uls_u2p14[9]	10977
t_APASmoothY_Uls_u2p14[0]	6554
t_APASmoothY_Uls_u2p14[1]	6717
t_APASmoothY_Uls_u2p14[2]	6881
t_APASmoothY_Uls_u2p14[3]	7045
t_APASmoothY_Uls_u2p14[4]	7209
t_APASmoothY_Uls_u2p14[5]	7373
t_APASmoothY_Uls_u2p14[6]	7537
t_APASmoothY_Uls_u2p14[7]	7700
t_APASmoothY_Uls_u2p14[8]	7864
t_APASmoothY_Uls_u2p14[9]	8028
t_LKASlewX_Kph_u8p8[0]	40448
t_LKASlewX_Kph_u8p8[1]	40960
t_LKASlewX_Kph_u8p8[2]	41472
t_LKASlewX_Kph_u8p8[3]	41984
t_LKASlewX_Kph_u8p8[4]	42496
t_LKASlewX_Kph_u8p8[5]	43008
t_LKASlewY_NmpSec_u4p12[0]	29901
t_LKASlewY_NmpSec_u4p12[1]	30310
t_LKASlewY_NmpSec_u4p12[2]	30720
t_LKASlewY_NmpSec_u4p12[3]	31130
t_LKASlewY_NmpSec_u4p12[4]	31539
t_LKASlewY_NmpSec_u4p12[5]	31949
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.1500001
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	496.243011
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-8.10000038
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.9000001

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.38850021	-2.38849998 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	-8.10000038	-8.10000038 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.5843997	6.58440018 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.510009766	0.510010004 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.510009766	0.510010004 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.75	0.75 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.75	0.75 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.931078911	0.93107897 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.510009766	0.510010004 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.92 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.850000024
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.649999998
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqArblim_PosSrvcCmd_MtrNm_M_f32	0.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.0999999
k_APAEnableRate_pSec_f32	441
k_APAUseADmpScale_Igc	1
k_APAUseAsstScale_Igc	1
k_APAUseRetScale_Igc	1
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_Igc	1
t_APADisableRateX_HwNm_u4p12[0]	23347
t_APADisableRateX_HwNm_u4p12[1]	23757
t_APADisableRateX_HwNm_u4p12[2]	24166
t_APADisableRateX_HwNm_u4p12[3]	24576
t_APADisableRateX_HwNm_u4p12[4]	24986
t_APADisableRateX_HwNm_u4p12[5]	25395
t_APADisableRateY_pSec_u7p9[0]	30208
t_APADisableRateY_pSec_u7p9[1]	30720
t_APADisableRateY_pSec_u7p9[2]	31232
t_APADisableRateY_pSec_u7p9[3]	31744
t_APADisableRateY_pSec_u7p9[4]	32256
t_APADisableRateY_pSec_u7p9[5]	32768
t_APASmoothX_Uls_u2p14[0]	9667
t_APASmoothX_Uls_u2p14[1]	9830
t_APASmoothX_Uls_u2p14[2]	9994
t_APASmoothX_Uls_u2p14[3]	10158
t_APASmoothX_Uls_u2p14[4]	10322
t_APASmoothX_Uls_u2p14[5]	10486
t_APASmoothX_Uls_u2p14[6]	10650
t_APASmoothX_Uls_u2p14[7]	10813

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TrqArblim_Per1

Name	Input Value		
t_APASmoothX_Uls_u2p14[8]	10977		
t_APASmoothX_Uls_u2p14[9]	11141		
t_APASmoothY_Uls_u2p14[0]	6717		
t_APASmoothY_Uls_u2p14[1]	6881		
t_APASmoothY_Uls_u2p14[2]	7045		
t_APASmoothY_Uls_u2p14[3]	7209		
t_APASmoothY_Uls_u2p14[4]	7373		
t_APASmoothY_Uls_u2p14[5]	7537		
t_APASmoothY_Uls_u2p14[6]	7700		
t_APASmoothY_Uls_u2p14[7]	7864		
t_APASmoothY_Uls_u2p14[8]	8028		
t_APASmoothY_Uls_u2p14[9]	8192		
t_LKASlewX_Kph_u8p8[0]	37888		
t_LKASlewX_Kph_u8p8[1]	38400		
t_LKASlewX_Kph_u8p8[2]	38912		
t_LKASlewX_Kph_u8p8[3]	39424		
t_LKASlewX_Kph_u8p8[4]	39936		
t_LKASlewX_Kph_u8p8[5]	40448		
t_LKASlewY_NmpSec_u4p12[0]	30310		
t_LKASlewY_NmpSec_u4p12[1]	30720		
t_LKASlewY_NmpSec_u4p12[2]	31130		
t_LKASlewY_NmpSec_u4p12[3]	31539		
t_LKASlewY_NmpSec_u4p12[4]	31949		
t_LKASlewY_NmpSec_u4p12[5]	32358		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.9000001		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.96999979		
target_TrqArblim_Per1_LKASState_State_enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	463.993011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.10000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.731999993	0.731999993 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	3	3	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.84975004	-3.84975004 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75	0.75 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.08419991	4.08419991 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.634000003	0.634000003 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.634000003	0.634000003 ± 0000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.274500012	0.274500012 ± 0000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.634000003	0.634000003 ± 0000390625	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Per1

Test Step 3.93 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.875
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.29999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqArblim_PosSrvcCmd_MtrNm_M_f32	1
TrqArblim_PrelKATrqReq_HwNm_M_f32	4.59999999
k_APAEnableRate_pSec_f32	450.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	19661
t_APADisableRateX_HwNm_u4p12[1]	20070
t_APADisableRateX_HwNm_u4p12[2]	20480
t_APADisableRateX_HwNm_u4p12[3]	20890
t_APADisableRateX_HwNm_u4p12[4]	21299
t_APADisableRateX_HwNm_u4p12[5]	21709
t_APADisableRateY_pSec_u7p9[0]	30720
t_APADisableRateY_pSec_u7p9[1]	31232
t_APADisableRateY_pSec_u7p9[2]	31744
t_APADisableRateY_pSec_u7p9[3]	32256
t_APADisableRateY_pSec_u7p9[4]	32768
t_APADisableRateY_pSec_u7p9[5]	33280
t_APASmoothX_Uls_u2p14[0]	9830
t_APASmoothX_Uls_u2p14[1]	9994
t_APASmoothX_Uls_u2p14[2]	10158
t_APASmoothX_Uls_u2p14[3]	10322
t_APASmoothX_Uls_u2p14[4]	10486
t_APASmoothX_Uls_u2p14[5]	10650
t_APASmoothX_Uls_u2p14[6]	10813
t_APASmoothX_Uls_u2p14[7]	10977
t_APASmoothX_Uls_u2p14[8]	11141
t_APASmoothX_Uls_u2p14[9]	11305
t_APASmoothY_Uls_u2p14[0]	6881
t_APASmoothY_Uls_u2p14[1]	7045
t_APASmoothY_Uls_u2p14[2]	7209
t_APASmoothY_Uls_u2p14[3]	7373
t_APASmoothY_Uls_u2p14[4]	7537
t_APASmoothY_Uls_u2p14[5]	7700
t_APASmoothY_Uls_u2p14[6]	7864
t_APASmoothY_Uls_u2p14[7]	8028
t_APASmoothY_Uls_u2p14[8]	8192
t_APASmoothY_Uls_u2p14[9]	8356
t_LKASlewX_Kph_u8p8[0]	38400
t_LKASlewX_Kph_u8p8[1]	38912
t_LKASlewX_Kph_u8p8[2]	39424
t_LKASlewX_Kph_u8p8[3]	39936
t_LKASlewX_Kph_u8p8[4]	40448
t_LKASlewX_Kph_u8p8[5]	40960
t_LKASlewY_NmpSec_u4p12[0]	30720
t_LKASlewY_NmpSec_u4p12[1]	31130
t_LKASlewY_NmpSec_u4p12[2]	31539
t_LKASlewY_NmpSec_u4p12[3]	31949
t_LKASlewY_NmpSec_u4p12[4]	32358
t_LKASlewY_NmpSec_u4p12[5]	32768
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.55000019
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.61999989
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	470.442993
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.30000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.0999999

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.55749989	-3.55749989 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.30000019	8.30000019 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	4.59399986	4.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.33308125	8.33308029 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.94 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.8999999976
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.949999988
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.25
TrqArblim_PrelKATrqReq_HwNm_M_f32	5.0999999
k_APAEnableRate_pSec_f32	460.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	20070
t_APADisableRateX_HwNm_u4p12[1]	20480
t_APADisableRateX_HwNm_u4p12[2]	20890
t_APADisableRateX_HwNm_u4p12[3]	21299
t_APADisableRateX_HwNm_u4p12[4]	21709
t_APADisableRateX_HwNm_u4p12[5]	22118
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	9994
t_APASmoothX_Uls_u2p14[1]	10158
t_APASmoothX_Uls_u2p14[2]	10322
t_APASmoothX_Uls_u2p14[3]	10486
t_APASmoothX_Uls_u2p14[4]	10650
t_APASmoothX_Uls_u2p14[5]	10813

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TrqArblim_Per1

Name	Input Value		
t_APASmoothX_Uls_u2p14[6]	10977		
t_APASmoothX_Uls_u2p14[7]	11141		
t_APASmoothX_Uls_u2p14[8]	11305		
t_APASmoothX_Uls_u2p14[9]	11469		
t_APASmoothY_Uls_u2p14[0]	7045		
t_APASmoothY_Uls_u2p14[1]	7209		
t_APASmoothY_Uls_u2p14[2]	7373		
t_APASmoothY_Uls_u2p14[3]	7537		
t_APASmoothY_Uls_u2p14[4]	7700		
t_APASmoothY_Uls_u2p14[5]	7864		
t_APASmoothY_Uls_u2p14[6]	8028		
t_APASmoothY_Uls_u2p14[7]	8192		
t_APASmoothY_Uls_u2p14[8]	8356		
t_APASmoothY_Uls_u2p14[9]	8520		
t_LKASlewX_Kph_u8p8[0]	38912		
t_LKASlewX_Kph_u8p8[1]	39424		
t_LKASlewX_Kph_u8p8[2]	39936		
t_LKASlewX_Kph_u8p8[3]	40448		
t_LKASlewX_Kph_u8p8[4]	40960		
t_LKASlewX_Kph_u8p8[5]	41472		
t_LKASlewY_NmpSec_u4p12[0]	31130		
t_LKASlewY_NmpSec_u4p12[1]	31539		
t_LKASlewY_NmpSec_u4p12[2]	31949		
t_LKASlewY_NmpSec_u4p12[3]	32358		
t_LKASlewY_NmpSec_u4p12[4]	32768		
t_LKASlewY_NmpSec_u4p12[5]	33178		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.1800003		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.26999998		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	476.893005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	8.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.26525021	-3.26524997 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	8.5	8.5 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.08379984	5.08379984 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.479980469	0.479979992 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.479980469	0.479979992 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.72016621	8.72017002 ± 0.000390625	✔
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.479980469	0.479979992 ± 0.000390625	✔

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TrqArblim_Per1

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.95 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.949999988
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqArblim_PosSrvcCmd_MtrNm_M_f32	1.75
TrqArblim_PrelKATrqReq_HwNm_M_f32	6.0999999
k_APAEnableRate_pSec_f32	480
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	5
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938
t_APADisableRateX_HwNm_u4p12[2]	23347
t_APADisableRateX_HwNm_u4p12[3]	23757
t_APADisableRateX_HwNm_u4p12[4]	24166
t_APADisableRateX_HwNm_u4p12[5]	24576
t_APADisableRateY_pSec_u7p9[0]	34304
t_APADisableRateY_pSec_u7p9[1]	34816
t_APADisableRateY_pSec_u7p9[2]	35328
t_APADisableRateY_pSec_u7p9[3]	35840
t_APADisableRateY_pSec_u7p9[4]	36352
t_APADisableRateY_pSec_u7p9[5]	36864
t_APASmoothX_Uls_u2p14[0]	9339
t_APASmoothX_Uls_u2p14[1]	9503
t_APASmoothX_Uls_u2p14[2]	9667
t_APASmoothX_Uls_u2p14[3]	9830
t_APASmoothX_Uls_u2p14[4]	9994
t_APASmoothX_Uls_u2p14[5]	10158
t_APASmoothX_Uls_u2p14[6]	10322
t_APASmoothX_Uls_u2p14[7]	10486
t_APASmoothX_Uls_u2p14[8]	10650
t_APASmoothX_Uls_u2p14[9]	10813
t_APASmoothY_Uls_u2p14[0]	6390
t_APASmoothY_Uls_u2p14[1]	6554
t_APASmoothY_Uls_u2p14[2]	6717
t_APASmoothY_Uls_u2p14[3]	6881
t_APASmoothY_Uls_u2p14[4]	7045
t_APASmoothY_Uls_u2p14[5]	7209
t_APASmoothY_Uls_u2p14[6]	7373
t_APASmoothY_Uls_u2p14[7]	7537
t_APASmoothY_Uls_u2p14[8]	7700
t_APASmoothY_Uls_u2p14[9]	7864
t_LKASlewX_Kph_u8p8[0]	39936
t_LKASlewX_Kph_u8p8[1]	40448
t_LKASlewX_Kph_u8p8[2]	40960
t_LKASlewX_Kph_u8p8[3]	41472
t_LKASlewX_Kph_u8p8[4]	41984
t_LKASlewX_Kph_u8p8[5]	42496
t_LKASlewY_NmpSec_u4p12[0]	31949
t_LKASlewY_NmpSec_u4p12[1]	32358
t_LKASlewY_NmpSec_u4p12[2]	32768
t_LKASlewY_NmpSec_u4p12[3]	33178
t_LKASlewY_NmpSec_u4p12[4]	33587
t_LKASlewY_NmpSec_u4p12[5]	33997
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_Per1_ESCcmd_HwNm_f32	target_TrqArblim_Per1_ESCcmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCcmd_HwNm_f32.value	-3.75999999
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.5
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.56999993
target_TrqArblim_Per1_LKASState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	489.792999
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-8.60000038
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.69999981

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.815999985	0.815999985 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.68074989	-2.68074989 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	1.75	1.75 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.09399986	6.09399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.75999999	-0.140000001 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.685412109	0.68541199 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.96 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.449999988
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001
TrqArblim_PosSrvcCmd_MtrNm_M_f32	5.94000006
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002
k_APAEnableRate_pSec_f32	285
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	20890
t_APADisableRateX_HwNm_u4p12[1]	21299
t_APADisableRateX_HwNm_u4p12[2]	21709
t_APADisableRateX_HwNm_u4p12[3]	22118
t_APADisableRateX_HwNm_u4p12[4]	22528
t_APADisableRateX_HwNm_u4p12[5]	22938
t_APADisableRateY_pSec_u7p9[0]	33280
t_APADisableRateY_pSec_u7p9[1]	33792
t_APADisableRateY_pSec_u7p9[2]	34304
t_APADisableRateY_pSec_u7p9[3]	34816
t_APADisableRateY_pSec_u7p9[4]	35328

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	35840		
t_APASmoothX_Uls_u2p14[0]	10813		
t_APASmoothX_Uls_u2p14[1]	10977		
t_APASmoothX_Uls_u2p14[2]	11141		
t_APASmoothX_Uls_u2p14[3]	11305		
t_APASmoothX_Uls_u2p14[4]	11469		
t_APASmoothX_Uls_u2p14[5]	11633		
t_APASmoothX_Uls_u2p14[6]	11796		
t_APASmoothX_Uls_u2p14[7]	11960		
t_APASmoothX_Uls_u2p14[8]	12124		
t_APASmoothX_Uls_u2p14[9]	12288		
t_APASmoothY_Uls_u2p14[0]	7864		
t_APASmoothY_Uls_u2p14[1]	8028		
t_APASmoothY_Uls_u2p14[2]	8192		
t_APASmoothY_Uls_u2p14[3]	8356		
t_APASmoothY_Uls_u2p14[4]	8520		
t_APASmoothY_Uls_u2p14[5]	8684		
t_APASmoothY_Uls_u2p14[6]	8847		
t_APASmoothY_Uls_u2p14[7]	9011		
t_APASmoothY_Uls_u2p14[8]	9175		
t_APASmoothY_Uls_u2p14[9]	9339		
t_LKASlewX_Kph_u8p8[0]	35328		
t_LKASlewX_Kph_u8p8[1]	35840		
t_LKASlewX_Kph_u8p8[2]	36352		
t_LKASlewX_Kph_u8p8[3]	36864		
t_LKASlewX_Kph_u8p8[4]	37376		
t_LKASlewX_Kph_u8p8[5]	37888		
t_LKASlewY_NmpSec_u4p12[0]	28262		
t_LKASlewY_NmpSec_u4p12[1]	28672		
t_LKASlewY_NmpSec_u4p12[2]	29082		
t_LKASlewY_NmpSec_u4p12[3]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901		
t_LKASlewY_NmpSec_u4p12[5]	30310		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.13000011		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.02000005		
target_TrqArblim_Per1_LKASState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	360.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.90000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.699999988		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.319999993	0.310000002 ± 0.000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	4	4	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.24166012	-4.24166012 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006	5.94000006 ± 0.000244140625	✔
TrqArblim_PrelKATrqReq_HwNm_M_f32	-1.09399998	-1.09399998 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✔
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✔
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✔
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	✔
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.9123469	0.883835971 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.97 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t_APADisableRateY_pSec_u7p9[0]	43520
t_APADisableRateY_pSec_u7p9[1]	44032
t_APADisableRateY_pSec_u7p9[2]	44544
t_APADisableRateY_pSec_u7p9[3]	45056
t_APADisableRateY_pSec_u7p9[4]	45568
t_APADisableRateY_pSec_u7p9[5]	46080
t_APASmoothX_Uls_u2p14[0]	9011
t_APASmoothX_Uls_u2p14[1]	9175
t_APASmoothX_Uls_u2p14[2]	9339
t_APASmoothX_Uls_u2p14[3]	9503
t_APASmoothX_Uls_u2p14[4]	9667
t_APASmoothX_Uls_u2p14[5]	9830
t_APASmoothX_Uls_u2p14[6]	9994
t_APASmoothX_Uls_u2p14[7]	10158
t_APASmoothX_Uls_u2p14[8]	10322
t_APASmoothX_Uls_u2p14[9]	10486
t_APASmoothY_Uls_u2p14[0]	6062
t_APASmoothY_Uls_u2p14[1]	6226
t_APASmoothY_Uls_u2p14[2]	6390
t_APASmoothY_Uls_u2p14[3]	6554
t_APASmoothY_Uls_u2p14[4]	6717
t_APASmoothY_Uls_u2p14[5]	6881
t_APASmoothY_Uls_u2p14[6]	7045
t_APASmoothY_Uls_u2p14[7]	7209
t_APASmoothY_Uls_u2p14[8]	7373
t_APASmoothY_Uls_u2p14[9]	7537
t_LKASlewX_Kph_u8p8[0]	36864
t_LKASlewX_Kph_u8p8[1]	37376
t_LKASlewX_Kph_u8p8[2]	37888
t_LKASlewX_Kph_u8p8[3]	38400
t_LKASlewX_Kph_u8p8[4]	38912
t_LKASlewX_Kph_u8p8[5]	39424
t_LKASlewY_NmpSec_u4p12[0]	29491
t_LKASlewY_NmpSec_u4p12[1]	29901
t_LKASlewY_NmpSec_u4p12[2]	30310
t_LKASlewY_NmpSec_u4p12[3]	30720
t_LKASlewY_NmpSec_u4p12[4]	31130
t_LKASlewY_NmpSec_u4p12[5]	31539
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32

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TrqArblim_Per1

Name	Input Value
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.59999999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	7.69999981
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.43424988	-4.43424988 ± 0.03125	✓
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.54216909	3.54217005 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.98 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.34999999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvcCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.09999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t_APADisableRateY_pSec_u7p9[0]	28160
t_APADisableRateY_pSec_u7p9[1]	28672

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	29184		
t_APADisableRateY_pSec_u7p9[3]	29696		
t_APADisableRateY_pSec_u7p9[4]	30208		
t_APADisableRateY_pSec_u7p9[5]	30720		
t_APASmoothX_Uls_u2p14[0]	9011		
t_APASmoothX_Uls_u2p14[1]	9175		
t_APASmoothX_Uls_u2p14[2]	9339		
t_APASmoothX_Uls_u2p14[3]	9503		
t_APASmoothX_Uls_u2p14[4]	9667		
t_APASmoothX_Uls_u2p14[5]	9830		
t_APASmoothX_Uls_u2p14[6]	9994		
t_APASmoothX_Uls_u2p14[7]	10158		
t_APASmoothX_Uls_u2p14[8]	10322		
t_APASmoothX_Uls_u2p14[9]	10486		
t_APASmoothY_Uls_u2p14[0]	6062		
t_APASmoothY_Uls_u2p14[1]	6226		
t_APASmoothY_Uls_u2p14[2]	6390		
t_APASmoothY_Uls_u2p14[3]	6554		
t_APASmoothY_Uls_u2p14[4]	6717		
t_APASmoothY_Uls_u2p14[5]	6881		
t_APASmoothY_Uls_u2p14[6]	7045		
t_APASmoothY_Uls_u2p14[7]	7209		
t_APASmoothY_Uls_u2p14[8]	7373		
t_APASmoothY_Uls_u2p14[9]	7537		
t_LKASlewX_Kph_u8p8[0]	36864		
t_LKASlewX_Kph_u8p8[1]	37376		
t_LKASlewX_Kph_u8p8[2]	37888		
t_LKASlewX_Kph_u8p8[3]	38400		
t_LKASlewX_Kph_u8p8[4]	38912		
t_LKASlewX_Kph_u8p8[5]	39424		
t_LKASlewY_NmpSec_u4p12[0]	29491		
t_LKASlewY_NmpSec_u4p12[1]	29901		
t_LKASlewY_NmpSec_u4p12[2]	30310		
t_LKASlewY_NmpSec_u4p12[3]	30720		
t_LKASlewY_NmpSec_u4p12[4]	31130		
t_LKASlewY_NmpSec_u4p12[5]	31539		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCoscillate_State_enum	target_TrqArblim_Per1_GMOSHOCoscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOCoscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	2	2	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.43424988	-4.43424988 ± 0.03125	✔
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	✔
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	✔
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	✔
target TrqArblim Per1 ESCIsLimited Cnt lgc.value	1	1	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4	-4 ± 0.000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.54216909	3.54217005 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntrlVarXY_u16_u16Xu16Y_Cnt	3	IntrlVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.99 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.75
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.375
TrqArblim_PosSrvcCmd_MtrNm_M_f32	9.42000008
TrqArblim_PrelKATrqReq_HwNm_M_f32	2.09999999
k_APAEnableRate_pSec_f32	402
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	24986
t_APADisableRateX_HwNm_u4p12[1]	25395
t_APADisableRateX_HwNm_u4p12[2]	25805
t_APADisableRateX_HwNm_u4p12[3]	26214
t_APADisableRateX_HwNm_u4p12[4]	26624
t_APADisableRateX_HwNm_u4p12[5]	27034
t_APADisableRateY_pSec_u7p9[0]	26112
t_APADisableRateY_pSec_u7p9[1]	26624
t_APADisableRateY_pSec_u7p9[2]	27136
t_APADisableRateY_pSec_u7p9[3]	27648
t_APADisableRateY_pSec_u7p9[4]	28160
t_APADisableRateY_pSec_u7p9[5]	28672
t_APASmoothX_Uls_u2p14[0]	8684
t_APASmoothX_Uls_u2p14[1]	8847
t_APASmoothX_Uls_u2p14[2]	9011
t_APASmoothX_Uls_u2p14[3]	9175
t_APASmoothX_Uls_u2p14[4]	9339
t_APASmoothX_Uls_u2p14[5]	9503
t_APASmoothX_Uls_u2p14[6]	9667
t_APASmoothX_Uls_u2p14[7]	9830
t_APASmoothX_Uls_u2p14[8]	9994
t_APASmoothX_Uls_u2p14[9]	10158
t_APASmoothY_Uls_u2p14[0]	5734
t_APASmoothY_Uls_u2p14[1]	5898
t_APASmoothY_Uls_u2p14[2]	6062
t_APASmoothY_Uls_u2p14[3]	6226
t_APASmoothY_Uls_u2p14[4]	6390
t_APASmoothY_Uls_u2p14[5]	6554
t_APASmoothY_Uls_u2p14[6]	6717
t_APASmoothY_Uls_u2p14[7]	6881
t_APASmoothY_Uls_u2p14[8]	7045
t_APASmoothY_Uls_u2p14[9]	7209
t_LKASlewX_Kph_u8p8[0]	35840
t_LKASlewX_Kph_u8p8[1]	36352
t_LKASlewX_Kph_u8p8[2]	36864
t_LKASlewX_Kph_u8p8[3]	37376
t_LKASlewX_Kph_u8p8[4]	37888
t_LKASlewX_Kph_u8p8[5]	38400
t_LKASlewY_NmpSec_u4p12[0]	28672
t_LKASlewY_NmpSec_u4p12[1]	29082
t_LKASlewY_NmpSec_u4p12[2]	29491
t_LKASlewY_NmpSec_u4p12[3]	29901
t_LKASlewY_NmpSec_u4p12[4]	30310

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[5]	30720
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHoscillate_State_enum	target_TrqArblim_Per1_GMOSHoscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHoscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.30000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.09999999

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.01875019	-5.01875019 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.30000019	7.30000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0849998	2.08500004 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.21201801	3.21201992 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.100 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.34999999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.09999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_Igc	0
k_APAUseAsstScale_Igc	0
k_APAUseRetScale_Igc	0
k_ESCMax_HwNm_f32	1
k_LKAUseSlewCal_Igc	0
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938
t_APADisableRateX_HwNm_u4p12[2]	23347
t_APADisableRateX_HwNm_u4p12[3]	23757
t_APADisableRateX_HwNm_u4p12[4]	24166

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	24576		
t_APADisableRateY_pSec_u7p9[0]	29184		
t_APADisableRateY_pSec_u7p9[1]	29696		
t_APADisableRateY_pSec_u7p9[2]	30208		
t_APADisableRateY_pSec_u7p9[3]	30720		
t_APADisableRateY_pSec_u7p9[4]	31232		
t_APADisableRateY_pSec_u7p9[5]	31744		
t_APASmoothX_Uls_u2p14[0]	10158		
t_APASmoothX_Uls_u2p14[1]	10322		
t_APASmoothX_Uls_u2p14[2]	10486		
t_APASmoothX_Uls_u2p14[3]	10650		
t_APASmoothX_Uls_u2p14[4]	10813		
t_APASmoothX_Uls_u2p14[5]	10977		
t_APASmoothX_Uls_u2p14[6]	11141		
t_APASmoothX_Uls_u2p14[7]	11305		
t_APASmoothX_Uls_u2p14[8]	11469		
t_APASmoothX_Uls_u2p14[9]	11633		
t_APASmoothY_Uls_u2p14[0]	6717		
t_APASmoothY_Uls_u2p14[1]	6881		
t_APASmoothY_Uls_u2p14[2]	7045		
t_APASmoothY_Uls_u2p14[3]	7209		
t_APASmoothY_Uls_u2p14[4]	7373		
t_APASmoothY_Uls_u2p14[5]	7537		
t_APASmoothY_Uls_u2p14[6]	7700		
t_APASmoothY_Uls_u2p14[7]	7864		
t_APASmoothY_Uls_u2p14[8]	8028		
t_APASmoothY_Uls_u2p14[9]	8192		
t_LKASlewX_Kph_u8p8[0]	34816		
t_LKASlewX_Kph_u8p8[1]	35328		
t_LKASlewX_Kph_u8p8[2]	35840		
t_LKASlewX_Kph_u8p8[3]	36352		
t_LKASlewX_Kph_u8p8[4]	36864		
t_LKASlewX_Kph_u8p8[5]	37376		
t_LKASlewY_NmpSec_u4p12[0]	27853		
t_LKASlewY_NmpSec_u4p12[1]	28262		
t_LKASlewY_NmpSec_u4p12[2]	28672		
t_LKASlewY_NmpSec_u4p12[3]	29082		
t_LKASlewY_NmpSec_u4p12[4]	29491		
t_LKASlewY_NmpSec_u4p12[5]	29901		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008		
target_TrqArblim_Per1_LKASState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	7.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	4	4	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.43424988	-4.43424988 ± 0.03125	✔
TrqArblim_PosSrvcCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	✔
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.09399986	3.0940001 ± 0.03125	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.8499999	3.8499999 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.101 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.5999999
k_APAEnableRate_pSec_f32	470.25
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22938
t_APADisableRateX_HwNm_u4p12[1]	23347
t_APADisableRateX_HwNm_u4p12[2]	23757
t_APADisableRateX_HwNm_u4p12[3]	24166
t_APADisableRateX_HwNm_u4p12[4]	24576
t_APADisableRateX_HwNm_u4p12[5]	24986
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	10813
t_APASmoothX_Uls_u2p14[1]	10977
t_APASmoothX_Uls_u2p14[2]	11141
t_APASmoothX_Uls_u2p14[3]	11305
t_APASmoothX_Uls_u2p14[4]	11469
t_APASmoothX_Uls_u2p14[5]	11633
t_APASmoothX_Uls_u2p14[6]	11796
t_APASmoothX_Uls_u2p14[7]	11960
t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_Uls_u2p14[9]	12288
t_APASmoothY_Uls_u2p14[0]	6881
t_APASmoothY_Uls_u2p14[1]	7045
t_APASmoothY_Uls_u2p14[2]	7209
t_APASmoothY_Uls_u2p14[3]	7373
t_APASmoothY_Uls_u2p14[4]	7537
t_APASmoothY_Uls_u2p14[5]	7700
t_APASmoothY_Uls_u2p14[6]	7864
t_APASmoothY_Uls_u2p14[7]	8028
t_APASmoothY_Uls_u2p14[8]	8192
t_APASmoothY_Uls_u2p14[9]	8356
t_LKASlewX_Kph_u8p8[0]	35328
t_LKASlewX_Kph_u8p8[1]	35840
t_LKASlewX_Kph_u8p8[2]	36352
t_LKASlewX_Kph_u8p8[3]	36864
t_LKASlewX_Kph_u8p8[4]	37376
t_LKASlewX_Kph_u8p8[5]	37888
t_LKASlewY_NmpSec_u4p12[0]	28262
t_LKASlewY_NmpSec_u4p12[1]	28672

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TrqArblim_Per1

Name	Input Value
t_LKASlewY_NmpSec_u4p12[2]	29082
t_LKASlewY_NmpSec_u4p12[3]	29491
t_LKASlewY_NmpSec_u4p12[4]	29901
t_LKASlewY_NmpSec_u4p12[5]	30310
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOScillate_State_enum	target_TrqArblim_Per1_GMOSHOScillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOScillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008
target_TrqArblim_Per1_LKASState_State_enum.value	1
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.69999981
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.809000015	0.809000015 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.97300005	-2.97300005 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	5.58520031	5.58519983 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.618896842	0.618897021 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.102 (Repeat Count = 1)

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998
TrqArblim_PrelKATrqReq_HwNm_M_f32	-8.80000019
k_APAEnableRate_pSec_f32	0
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	0
t_APADisableRateX_HwNm_u4p12[1]	0

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TrqArblim_Per1

Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	0		
t_APADisableRateX_HwNm_u4p12[3]	0		
t_APADisableRateX_HwNm_u4p12[4]	0		
t_APADisableRateX_HwNm_u4p12[5]	0		
t_APADisableRateY_pSec_u7p9[0]	0		
t_APADisableRateY_pSec_u7p9[1]	0		
t_APADisableRateY_pSec_u7p9[2]	0		
t_APADisableRateY_pSec_u7p9[3]	0		
t_APADisableRateY_pSec_u7p9[4]	0		
t_APADisableRateY_pSec_u7p9[5]	0		
t_APASmoothX_Uls_u2p14[0]	0		
t_APASmoothX_Uls_u2p14[1]	0		
t_APASmoothX_Uls_u2p14[2]	0		
t_APASmoothX_Uls_u2p14[3]	0		
t_APASmoothX_Uls_u2p14[4]	0		
t_APASmoothX_Uls_u2p14[5]	0		
t_APASmoothX_Uls_u2p14[6]	0		
t_APASmoothX_Uls_u2p14[7]	0		
t_APASmoothX_Uls_u2p14[8]	0		
t_APASmoothX_Uls_u2p14[9]	0		
t_APASmoothX_Uls_u2p14[0]	0		
t_APASmoothY_Uls_u2p14[1]	0		
t_APASmoothY_Uls_u2p14[2]	0		
t_APASmoothY_Uls_u2p14[3]	0		
t_APASmoothY_Uls_u2p14[4]	0		
t_APASmoothY_Uls_u2p14[5]	0		
t_APASmoothY_Uls_u2p14[6]	0		
t_APASmoothY_Uls_u2p14[7]	0		
t_APASmoothY_Uls_u2p14[8]	0		
t_APASmoothY_Uls_u2p14[9]	0		
t_LKASlewX_Kph_u8p8[0]	0		
t_LKASlewX_Kph_u8p8[1]	0		
t_LKASlewX_Kph_u8p8[2]	0		
t_LKASlewX_Kph_u8p8[3]	0		
t_LKASlewX_Kph_u8p8[4]	0		
t_LKASlewX_Kph_u8p8[5]	0		
t_LKASlewY_NmpSec_u4p12[0]	0		
t_LKASlewY_NmpSec_u4p12[1]	0		
t_LKASlewY_NmpSec_u4p12[2]	0		
t_LKASlewY_NmpSec_u4p12[3]	0		
t_LKASlewY_NmpSec_u4p12[4]	0		
t_LKASlewY_NmpSec_u4p12[5]	0		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCssillate_State_enum	target_TrqArblim_Per1_GMOSHOCssillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvcCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10.2299995		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOCssillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998		
target_TrqArblim_Per1_LKASState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvcCmd_MtrNm_f32.value	-8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	✔
TrqArblim_ESC_State_Cnt_M_enum	0	0	✔

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10	-10 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998	-10.2399998 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.77299976	-8.77299976 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0.000390625	✓

Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.103 (Repeat Count = 1) ✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	1
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390027
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.2299995
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019
k_APAEnableRate_pSec_f32	500
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	5
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	40960
t_APADisableRateX_HwNm_u4p12[1]	40960
t_APADisableRateX_HwNm_u4p12[2]	40960
t_APADisableRateX_HwNm_u4p12[3]	40960
t_APADisableRateX_HwNm_u4p12[4]	40960
t_APADisableRateX_HwNm_u4p12[5]	40960
t_APADisableRateY_pSec_u7p9[0]	65535
t_APADisableRateY_pSec_u7p9[1]	65535
t_APADisableRateY_pSec_u7p9[2]	65535
t_APADisableRateY_pSec_u7p9[3]	65535
t_APADisableRateY_pSec_u7p9[4]	65535
t_APADisableRateY_pSec_u7p9[5]	65535
t_APASmoothX_Uls_u2p14[0]	16384
t_APASmoothX_Uls_u2p14[1]	16384
t_APASmoothX_Uls_u2p14[2]	16384
t_APASmoothX_Uls_u2p14[3]	16384
t_APASmoothX_Uls_u2p14[4]	16384
t_APASmoothX_Uls_u2p14[5]	16384
t_APASmoothX_Uls_u2p14[6]	16384
t_APASmoothX_Uls_u2p14[7]	16384
t_APASmoothX_Uls_u2p14[8]	16384
t_APASmoothX_Uls_u2p14[9]	16384
t_APASmoothY_Uls_u2p14[0]	16384
t_APASmoothY_Uls_u2p14[1]	16384
t_APASmoothY_Uls_u2p14[2]	16384
t_APASmoothY_Uls_u2p14[3]	16384
t_APASmoothY_Uls_u2p14[4]	16384
t_APASmoothY_Uls_u2p14[5]	16384
t_APASmoothY_Uls_u2p14[6]	16384
t_APASmoothY_Uls_u2p14[7]	16384
t_APASmoothY_Uls_u2p14[8]	16384
t_APASmoothY_Uls_u2p14[9]	16384
t_LKASlewX_Kph_u8p8[0]	56320
t_LKASlewX_Kph_u8p8[1]	56320
t_LKASlewX_Kph_u8p8[2]	56320
t_LKASlewX_Kph_u8p8[3]	56320
t_LKASlewX_Kph_u8p8[4]	56320

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TrqArblim_Per1

Name	Input Value
t_LKASlewX_Kph_u8p8[5]	56320
t_LKASlewY_NmpSec_u4p12[0]	55296
t_LKASlewY_NmpSec_u4p12[1]	55296
t_LKASlewY_NmpSec_u4p12[2]	55296
t_LKASlewY_NmpSec_u4p12[3]	55296
t_LKASlewY_NmpSec_u4p12[4]	55296
t_LKASlewY_NmpSec_u4p12[5]	55296
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOCillate_State_enum	target_TrqArblim_Per1_GMOSHOCillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKASState_State_enum	target_TrqArblim_Per1_LKASState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOCillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	10
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995
target_TrqArblim_Per1_LKASState_State_enum.value	4
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	511
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0.000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	10 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.000244140625	✓
TrqArblim_PrelKATrqReq_HwNm_M_f32	8.77299976	8.77299976 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0.000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0.000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0	0 ± 0.000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

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TrqArblim_Init



Project	TrqArblim
Module	TrqArblim
Test Object	TrqArblim_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_TrqArblim
Configuration File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqArblim\src\Ap_TrqArblim.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_OFF -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utpl\contract\Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utpl\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtLib\src\interpolation.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_OFF -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utpl\contract\Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utpl\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'TrqArblim'	*****Unit Test Report***** Name of Tester:Priti Mangalekar Code File(s) Under Test: Ap_TrqArblim.c Code File(s) Version:5 Module Design Document:TrqArblim_MDD.docx Module Design Document Version:6 Data Dictionary Version:5 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):1456 Total RAM Used (Bytes):33 Total CALS Used (Bytes):104 Special Test Requirements: Test Date:9/16/2014 Comments: Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested. Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 3:In function ""TrqArblim_Per1"" path ""(D_ONE_ULS_F32 <= ((D_2MS_SEC_F32 * APALowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" cannot be covered as ""((D_2MS_SEC_F32 * APALowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" always gets updated with a value less than 1." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a

TEST DETAILS REPORT

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TrqArblim_Init



Attributes	
Name	Value
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Time Enabled	false
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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TrqArblim_Init



Test Case 1: Boundary Test

Specification Performance Metrics:
(With "None" Instrumentation and "WithPS"
Environment)

CPU Cycles:

TS1.1	515.00	Cycles
TS1.2	514.00	Cycles
TS1.3	475.00	Cycles
TS1.4	461.00	Cycles
TS1.5	447.00	Cycles
TS1.6	432.00	Cycles
TS1.7	432.00	Cycles
TS1.8	475.00	Cycles
TS1.9	475.00	Cycles
TS1.10	457.00	Cycles
TS1.11	457.00	Cycles
TS1.12	447.00	Cycles
TS1.13	457.00	Cycles

Description Vector Description:

TS1.1 k_APASmoothHwTrqLPFKn_Hz_f32 = MIN
TS1.2 k_APASmoothHwTrqLPFKn_Hz_f32 = MAX
TS1.3 k_APASmoothHwTrqLPFKn_Hz_f32 = POS
TS1.4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV= MIN
TS1.5 TrqArblim_HwTorqueSV_HwNm_M_Str.SV= MAX
TS1.6 TrqArblim_HwTorqueSV_HwNm_M_Str.SV= ZERO
TS1.7 TrqArblim_HwTorqueSV_HwNm_M_Str.SV= NEG
TS1.8 TrqArblim_HwTorqueSV_HwNm_M_Str.SV= POS
TS1.9 TrqArblim_HwTorqueSV_HwNm_M_Str.K = MIN
TS1.10 TrqArblim_HwTorqueSV_HwNm_M_Str.K = MAX
TS1.11 TrqArblim_HwTorqueSV_HwNm_M_Str.K =POS
TS1.12 ALL MIN
TS1.13 ALL MAX

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.63		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.023		
k_APASmoothHwTrqLPFKn_Hz_f32	0.1		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.00125585 ± 0.03125	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.087		
k_APASmoothHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.71539 ± 0.03125	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.151		
k_APASmoothHwTrqLPFKn_Hz_f32	50.65		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470851779	0.470852 ± 0.03125	✓

Test Step 1.4 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.215		
k_APASmoothHwTrqLPFKn_Hz_f32	5.41		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0657246709	0.0657246 ± 0.03125	✓

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Test Step 1.5 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.279		
k_APASmoothHwTrqLPFKn_Hz_f32	11.42		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133686125	0.133686 ± 0.03125	✔

Test Step 1.6 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.343		
k_APASmoothHwTrqLPFKn_Hz_f32	15.85		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.18059516	0.180595 ± 0.03125	✓

Test Step 1.7 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407		
k_APASmoothHwTrqLPFKn_Hz_f32	20		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.222232342	0.222232 ± 0.03125	✓

Test Step 1.8 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.68		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.471		
k_APASmoothHwTrqLPFKn_Hz_f32	28.61		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.301991463	0.301991 ± 0.03125	✔

Test Step 1.9 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125585		
k_APASmoothHwTrqLPFKn_Hz_f32	36.86		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.370730996	0.370731 ± 0.03125	✔

Test Step 1.10 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.71539		
k_APASmoothHwTrqLPFKn_Hz_f32	42		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✔
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.4100914	0.410091 ± 0.03125	✔

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Test Step 1.11 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.24		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.01		
k_APASmoothHwTrqLPFKn_Hz_f32	48.34		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.455266416	0.455266 ± 0.03125	✓

Test Step 1.12 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125585		
k_APASmoothHwTrqLPFKn_Hz_f32	0.1		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.00125585 ± 0.03125	✓

Test Step 1.13 (Repeat Count = 1)

Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.71539		
k_APASmoothHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.71539 ± 0.03125	✓