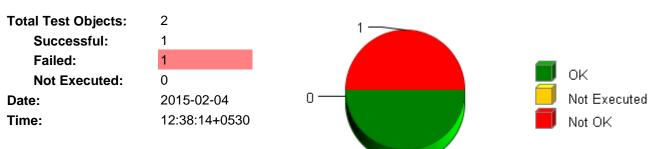


Summary

Overall Test Object Results (including Coverage)



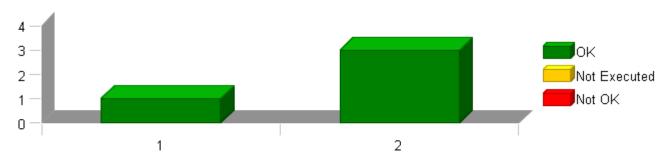
Selected Project Items

Test Object "CBD_UnitTest/TrqOvlSta/TrqOvlSta_Init" Test Object "CBD_UnitTest/TrqOvlSta/TrqOvlSta_Per1"

Used Test Environments

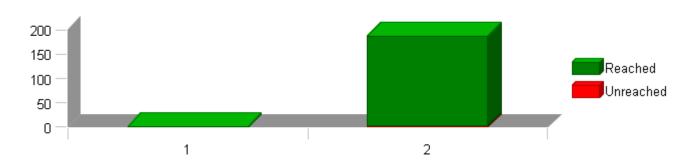
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

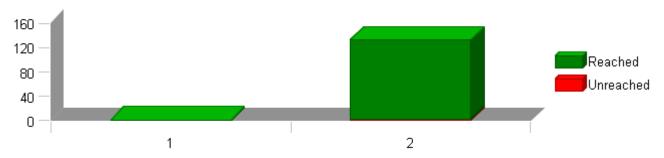
Statement (C0) Coverage: Total Statements for Each Test Object





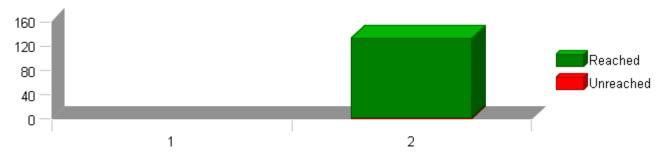
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

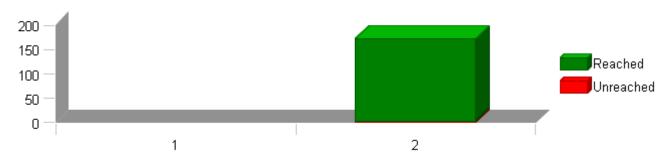
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object

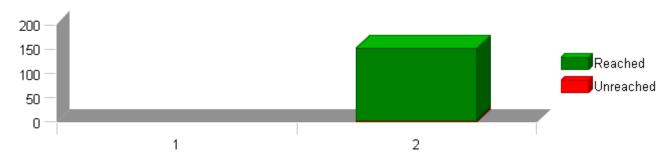


The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

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Project CBD_UnitTest

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	МСС	Test Cases	Result
	CBD_UnitTest	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	*
	CBD_UnitTest	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	×
	TrqOvlSta	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	*
1	TrqOvlSta_Init	100 %	100 %	-	-	-	1 of 1 passed	~
2	TrqOvlSta_Per1	98.93 %	98.49 %	98.49 %	98.25 %	98.02 %	3 of 3 passed	×

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 Project
 CBD_UnitTest

 Module
 TrqOvlSta

 Test Object
 TrqOvlSta_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\TrqOvlSta_CBD
Configuration File	D:\Synergy_Work_Area\TrqOvlSta_CBD\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqOvlSta\src\Ap_TrqOvlSta.c
Compiler Options	-DSKIP_MAGIC_NUMBER= -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqOvista\utp\contract -I\$(PROJECTROOT)\TrqOvista \utp\contract -I\$(PROJECTROOT)\TrqOvista -I\$(PROJECTROOT)\NtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments \ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Description	n/Specification
Name	Text
Comments/Description Name Module 'TrqOvISta'	Name of Tester:Imran desai Code File(s) Under Test:Ap_TrqOvISta_c Code File(s) Under Test:Ap_TrqOvISta_c Code File(s) Under Test:Ap_TrqOvISta_c Code File(s) Under Test:Ap_TrqOvISta_C Code File(s) Under Test:Ap_TrqOvISta_MDD.docx Module Design Document Version:7 Data Dictionary Version:6 Unit Test Plan Version:5 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):2128 Total RAM Used (Bytes):44 Total CALS Used (Bytes):45 Special Test Requirements: Test Date:2/4/2015 Comments:"NOTE 1:- Inline functions defined in ""GlobalMacro.h"" are not unit tested. NOTE 2:- ""CBD_Sandbox_dbg.map" map file is embedded for reference. NOTE 3:- In function ""TrqOvISta_Per1"", for 100% path coverage, Variable ""TrqOvISta_APAState_State_M_enum"" and ""APAState_State_enum" is giong Out of Range. NOTE 4:- In function ""TrqOvISta_Per1"", for 100% path coverage Variable ""TrqOvISta_APAState_State_M_enum" and ""TrqOvISta_ESCState_State_M_enum" is given out of range value at Vector Number ""TrqOvISta_ESCState_State_M_enum" is given out of range value at Vector Number ""TrqOvISta_ESCState_State_M_enum" is given out of range value at Vector Number ""TrqOvISta_ESCState_State_M_enum" is given out of range value at Vector Number ""Ts" in Path sheet, Hence at this test vector, output variable ""TrqOvISta_ESCState_State_M_enum" is given out of range value at Vector Number ""78" in Path sheet, Hence at this test vector, output variable ""TrqOvISta_ESCState_State_M_enum" in Path sheet, Hence at this test vector, output variable signing Out of Range. NOTE 5:- In function ""TrqOvISta_Per1"", 100% MCDC coverage is not achieved because (TrqOvISta_APAState_State_M_enum = APA_Active) >> True and (PosTrajEnable_Cnt_T_igc == FALSE) >> False cannot be covered as control goes to if loop. NOTE 6:- When value of TrqOvISta_LAPAFermFault_Cnt_M_u_D08' is greater than three, then the value of

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5

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TrqOvlSta_Init



Attributes	
Name	Value
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\Trq0vlSta_CBD\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 114.00 Cycles TS1.2 116.00 Cycles TS1.3 115.00 Cycles TS1.3 115.00 Cycles TS1.4 113.00 Cycles TS1.5 113.00 Cycles TS1.6 113.00 Cycles TS1.7 113.00 Cycles TS1.8 115.00 Cycles TS1.9 115.00 Cycles TS1.10 115.00 Cycles TS1.11 115.00 Cycles TS1.12 113.00 Cycles

Description

Vector Description:

TS1.1k_APAHwTrqLPFKn_Hz_f32 = MIN
TS1.2k_APAHwTrqLPFKn_Hz_f32 = MAX
TS1.3k_APAHwTrqLPFKn_Hz_f32 = POS
TS1.4TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV= MIN
TS1.5TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = MAX
TS1.6TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = ZERO
TS1.7TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = POS
TS1.8TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV = NEG
TS1.9TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = MIN
TS1.10TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = MAX
TS1.11TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = ZERO
TS1.12ALL MIN

TS1.12ALL MIN TS1.13ALL MAX

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.53999996		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.023		
k_APAHwTrqLPFKn_Hz_f32	0.10000001		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TraOvISta HwTorqueSV HwNm M Str K Llls f32	0.00125586987	0.00125584798 + 0.00000009	

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.32000017		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0869999975		
k_APAHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.715390444 ± 0.00000009	✓

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.38000011		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.150999993		
k_APAHwTrqLPFKn_Hz_f32	50.6500015		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470851779	0.470851779 ± 0.00000009	~

Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.215000004		
k_APAHwTrqLPFKn_Hz_f32	5.40999985		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0657246709	0.0657246411 ± 0.00000009	✓





Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.279000014		
k_APAHwTrqLPFKn_Hz_f32	11.4200001		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133686125	0.133686095 ± 0.00000009	✓

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342999995		
k_APAHwTrqLPFKn_Hz_f32	15.8500004		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.18059516	0.18059513 ± 0.00000009	✓

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407000005		
k_APAHwTrqLPFKn_Hz_f32	20		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.222232342	0.222232327 ± 0.00000009	~

Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470999986		
k_APAHwTrqLPFKn_Hz_f32	28.6100006		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.301991463	0.301991403 ± 0.0000009	✓

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.56999993		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798		
k_APAHwTrqLPFKn_Hz_f32	36.8600006		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.370730996	0.370730966 ± 0.0000009	~

Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444		
k_APAHwTrqLPFKn_Hz_f32	42		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.4100914	0.410091341 ± 0.0000009	✓



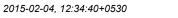
TrqOvlSta_Init

Test Step 1.11 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0099999978		
k_APAHwTrqLPFKn_Hz_f32	48.3400002		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.455266416	0.455266356 ± 0.0000009	✓

Test Step 1.12 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798		
k_APAHwTrqLPFKn_Hz_f32	0.100000001		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.00125584798 ± 0.00000009	•

Test Step 1.13 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444		
k_APAHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.715390444 ± 0.00000009	✓

TrqOvlSta_Per1





Project CBD_UnitTest Module TrqOvlSta Test Object TrqOvlSta_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	98.93 %
Decision Coverage	98.49 %
Branch (C1) Coverage	98.49 %
MCC Coverage	98.02 %
MC/DC Coverage	98.25 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\TrqOvlSta_CBD
Configuration File	D:\Synergy_Work_Area\TrqOvlSta_CBD\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqOvlSta\src\Ap_TrqOvlSta.c
Compiler Options	-DSKIP_MAGIC_NUMBER= -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqOvista\utp\contract -I\$(PROJECTROOT)\TrqOvista \utp\contract\Ap_TrqOviSta -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments \ccsv4\tools\compiler\tms470 4.9.5\include

Comments/Description/Spe	ecification
Name	Text



Module 'TrqOvISta'

Name of Tester:Imran desai Code File(s) Under Test:Ap_TrqOvlSta.c Code File(s) Version:8

Module Design Document:TrqOvlSta_MDD.docx Module Design Document Version:7

Data Dictionary Version:6 Unit Test Plan Version:5

Ont test Plan Version:3
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):2128
Total RAM Used (Bytes):44 Total CALS Used (Bytes):65 Special Test Requirements:

Test Date:2/4/2015
Comments:"NOTE 1:- Inline functions defined in ""GlobalMacro.h"" are not unit tested.

NOTE 2:- ""CBD_Sandbox_dbg.map"" map file is embedded for reference.

NOTE 3:- In function ""TrqOvlSta_Per1"", for 100% path coverage, Variable ""TrqOvlSta_APAState_State_M_enum"" is given out of range value at Vector Number ""76"" in Path sheet, Hence at this test vector output variable ""TrqOvlSta_APAState_State_M_enum"" and ""APAState_State_enum" is going Out of Range.

NOTE 4:-In function ""TrqOvISta_Per1"", for 100% path coverage Variable ""TrqOvISta_HapticState_State_M_enum"" and ""TrqOvISta_ESCState_State_M_enum""s given out of range value at Vector Number ""78"" in Path sheet, Hence at this test vector, output variable
""TrqOvlSta_ESCState_State_M_enum"",""TrqOvlSta_HapticState_State_M_enum"",""ESCState_State_enum""and""GMOSHOscillateState_State_State_State_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_

Indovota_Escate_state_state_in_enum, indovosta_napreciate_m_enum, become _state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_state_stat

NOTE 7:-In function ""TrqOvlSta_Per1"", (SWARTrgtAngRequest_HwDeg_T_f32<=(CCWPosition_HwDeg_T_f32 + k_SWARLimiter_HwDeg_f32)) = True and (SWARTrgtAngRequest_HwDeg_T_f32 >=(CWPosition_HwDeg_T_f32 - k_SWARLimiter_HwDeg_f32)) = True cannot be covered."

Attributes				
Name	Value			
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl			
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2			
Time Unit	Cycles			
Timer Enable	false			
Timer Prescale	0			
Timer Resolution	1.0			
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	D:\Synergy_Work_Area\TrqOvlSta_CBD\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP			



```
Test Case 1: Metrics Test
```

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 874.00 Cycles TS1.2 1053.00 Cycles

Description

Vector Description:

TS1.1"Shrotest Excution Path==>
(LKARequest_Cnt_T_f32 > D_ZERO_ULS_F32)=> TRUE
(HapticRequest_Cnt_T_gc == TRUE)=>FALSE
(ShiftLeverIsInReverse_Cnt_T_gc == TRUE)=>FALSE
(SecureVehicleSpeed_Kph_T_f32 >= k_StandstillThresh_Kph_f32)=>FALSE
(TrqOvISta_HapticState_State_M_enum != Haptic_Inactive)=>TRUE
(HwTrqFilt_HwNm_T_f32 >= k_APAMaxHwTrq_HwNm_f32)=>FALSE
((LKAFault_Cnt_T_lgc == TRUE) && (TrqOvISta_LKAPermFault_Cnt_M_lgc == FALSE))=>FALSE
(TrgOvISta_LKAMfgEnableTvH_Cnt_G_lgc == FALSE)=>FALSE

(TrqOvISta_LKAMfgEnableTyH_Cnt_G_lgc ==FALSE)=>FALSE (TrqOvISta_APAMfgEnableTyH_Cnt_G_lgc ==TRUE)=>FALSE Select (TrqOvISta_APAState_State_M_enum)

Case: Default

Select (TrqOvISta LKAState State M enum)

Case: Default

(TrqOvISta_HapticState_State_M_enum==Haptic_Active)=>TRUE (TrqOvISta_APAState_State_M_enum==APA_Active)=>FALSE

```
TS1.2"Longest Excution Path==>
(LKARequest_Cnt_T_f32 > D_ZERO_ULS_F32)=> FALSE
(HapticRequest_Cnt_T_g2 == TRUE)=>TRUE
(TrqOvlSta_Haptictime_Sec_M_f32 >= (k_MaxHapticReqTime_Sec_f32))=>FALSE
(ShiftLeverIsInReverse_Cnt_T_lgc == TRUE)=>TRUE
(TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 > (k_ShiftLevRevTime_Sec_f32))=>FALSE
(SecureVehicleSpeed_Kph_T_f32 >= k_StandstillTime_Sec_M_f32)=>TRUE
(TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 >= k_StandstillTime_Sec_f32)=>FALSE
(TrqOvlSta_HapticState_State_M_enum!= Haptic_Inactive)=>FALSE
(Abs_f32_m(HwTrqFilt_HwNm_T_f32) >= k_APAMaxHwTrq_HwNm_f32)=>TRUE
(TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 >= ((k_APAMaxHwTrqTime_Sec_f32)))=>FALSE
((LKAFault_Cnt_T_lgc == TRUE) && (TrqOvlSta_LKAPermFault_Cnt_M_lgc == FALSE) && (TrqOvlSta_LKAFault_Cnt_M_lgc == FALSE))=>TRUE
  FALSE))=>TRUE
 (TrqOvlSta_LKAPermFault_Cnt_M_u08 >= 3U)=>True && (*Rte_Pim_TrqOvlStaMfgEnable_Cnt_b08() & D_LKAMFGENABLE_CNT_U08) != D_LKAMFGENABLE_CNT_U08) ==>True && ((*Rte_Pim_TrqOvlStaMfgEnable_Cnt_b08() & D_APAMFGENABLE_CNT_U08) != D_APAMFGENABLE_CNT_U08) ==>False
 (TrqOvISta_LKAMfgEnableTyH_Cnt_G_lgc ==FALSE)=>FALSE
(TrqOvISta_APAMfgEnableTyH_Cnt_G_lgc ==TRUE)=>FALSE
Select (TrqOvISta_APAState_State_M_enum)
 Case(APA_AvailCntrl):
 Case(AFA_Availoriti),

(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=>FALSE
((SecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
((HapticRequest_Cnt_T_lgc==TRUE)&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)))=>FALSE
(k_APAIncludeHaptic_Cnt_lgc == FALSE)=>FALSE
Select (TrqOvISta_HapticState_State_M_enum)
 Case(Haptic_Inactive):

((HapticRequest_Cnt_T_Igc==FALSE) && (TrqOvlSta_ReadytoPulse_Cnt_M_Igc==FALSE))=>FALSE

(TrqOvlSta_ReadytoPulse_Cnt_M_Igc== TRUE)=>FALSE

((StandstillTime_Cnt_T_Igc==TRUE) && (APARequest_Cnt_T_Igc
 ==TRUE)&&(HapticRequest_Cnt_T_lgc==TRUE)&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))=>FALSE (HapticRequest_Cnt_T_lgc==TRUE)=>TRUE Select (TrqOvISta_LKAState_State_M_enum):
 DEFAULT
 (TrqOvISta_HapticState_State_M_enum==Haptic_Active)
 (TrqOvISta_APAState_State_M_enum==APA_Active)=>FALSE
```

Test Step 1.1 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	4	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	

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Name	Input Value
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	5270
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.37666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	84.5999985
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k HapticReacttime Sec f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_Apadrv$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfqEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target TrqOvlSta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value	200.04004
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-390.01001 1
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target TrqOvlSta_Per1_CWPosition HwDeg f32.value	1 360.980011
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCFault_Ont_Igc.value	0
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
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TrgOvlSta Per1

TrqOviSta_Per1			TUACITUD
Name	Input Value		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta HapticState State M enum	1	1	✓

Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.69000006	5.69000006 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 1.2 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	
TrqOvlSta_Haptictime_Sec_M_f32	0	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	
TrqOvlSta_LKAState_State_M_enum	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	
k_APAIncludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.601666689	
k_APAMaxHwTrq_HwNm_f32	0	
k_APAMaxVehSpd_Kph_f32	138.600006	
k_ESCMaxVehSpd_Kph_f32	12	
k_HapticAmplitude_MtrNm_f32	0.462000012	
k_HapticDuration_Sec_f32	1.15499997	

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Name	Input Value		
k_HapticFreq_Hz_f32	32.3019981		
k HapticReacttime Sec f32	1.23199999		
k_LKAMaxVehSpd_Kph_f32	146.300003		
k_LKAMinVehSpd_Kph_f32	1		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	0		
k_ShiftLevRevTime_Sec_f32	0.308999985		
k_StandstillThresh_Kph_f32	0		
k_StandstillTime_Sec_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cnt	0.308999985 target TrqOvlSta Per1 APADrvrIntervention	Detected Cat Inc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	· - ·	,	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target TrqOvlSta Per1 APARecoverableFa		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lge	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>j_</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_l	lae	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEi		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc	20	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscErtable_Ont_igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_ target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0	_	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-388.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target TrqOvlSta Per1 ESCRequest Cnt lgc.value	1		
target_IrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓

0.00200000009

TrqOvlSta_ElapsedStandstillTime_Sec_M_f32

0.00200000009 ± 1

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Name	Actual Value	Expected Value	Result
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

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Test Case 2: Boundary Test

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Specification

TrqOvlSta_Per1

Performance Metrics (With "None"
Instrumentation and WithPS Environment)

CPU Cycles:
TS2.1 906.00 Cycles
TS2.2 891.00 Cycles
TS2.3 887.00 Cycles
TS2.3 887.00 Cycles
TS2.5 887.00 Cycles
TS2.5 887.00 Cycles
TS2.6 905.00 Cycles
TS2.7 884.00 Cycles
TS2.10 932.00 Cycles
TS2.10 932.00 Cycles
TS2.11 883.00 Cycles
TS2.12 912.00 Cycles
TS2.13 889.00 Cycles
TS2.14 890.00 Cycles
TS2.14 890.00 Cycles
TS2.15 893.00 Cycles
TS2.16 892.00 Cycles
TS2.17 903.00 Cycles
TS2.18 893.00 Cycles
TS2.19 900.00 Cycles
TS2.19 900.00 Cycles TS2.18 893.00 Cycles TS2.19 900.00 Cycles TS2.20 906.00 Cycles TS2.21 896.00 Cycles TS2.23 879.00 Cycles TS2.24 886.00 Cycles TS2.24 886.00 Cycles TS2.25 916.00 Cycles TS2.26 904.00 Cycles TS2.27 892.00 Cycles TS2.28 895.00 Cycles TS2.29 904.00 Cycles TS2.30 889.00 Cycles TS2.31 931.00 Cycles TS2.31 931.00 Cycles TS2.31 931.00 Cycles TS2.30 889.00 Cycles TS2.31 931.00 Cycles TS2.32 887.00 Cycles TS2.33 903.00 Cycles TS2.34 909.00 Cycles TS2.35 915.00 Cycles TS2.36 893.00 Cycles TS2.37 891.00 Cycles TS2.37 891.00 Cycles TS2.38 913.00 Cycles TS2.39 911.00 Cycles TS2.40 889.00 Cycles TS2.41 900.00 Cycles TS2.42 902.00 Cycles TS2.42 902.00 Cycles TS2.43 911.00 Cycles TS2.44 921.00 Cycles TS2.45 913.00 Cycles TS2.46 907.00 Cycles TS2.47 907.00 Cycles TS2.48 903.00 Cycles TS2.49 907.00 Cycles TS2.50 896.00 Cycles TS2.51 899.00 Cycles TS2.52 900.00 Cycles TS2.53 915.00 Cycles TS2.53 915.00 Cycles TS2.54 898.00 Cycles TS2.55 918.00 Cycles TS2.56 894.00 Cycles TS2.57 924.00 Cycles TS2.58 887.00 Cycles TS2.59 934.00 Cycles TS2.60 890.00 Cycles TS2.60 890.00 Cycles TS2.61 923.00 Cycles TS2.62 940.00 Cycles TS2.63 893.00 Cycles TS2.64 919.00 Cycles TS2.65 888.00 Cycles TS2.66 894.00 Cycles TS2.66 894.00 Cycles TS2.69 894.00 Cycles TS2.69 894.00 Cycles TS2.70 913.00 Cycles TS2.71 893.00 Cycles TS2.72 907.00 Cycles TS2.73 897.00 Cycles TS2.74 906.00 Cycles TS2.75 893.00 Cycles TS2.76 889.00 Cycles TS2.77 896.00 Cycles TS2.78 883.00 Cycles TS2.79 906.00 Cycles TS2.80 899.00 Cycles TS2.81 917.00 Cycles TS2.82 908.00 Cycles TS2.83 913.00 Cycles TS2.84 905.00 Cycles TS2.84 905.00 Cycles TS2.85 887.00 Cycles TS2.86 890.00 Cycles TS2.87 896.00 Cycles TS2.88 924.00 Cycles TS2.89 888.00 Cycles TS2.90 892.00 Cycles TS2.91 908.00 Cycles TS2.92 900.00 Cycles TS2.92 900.00 Cycles
TS2.93 888.00 Cycles
TS2.94 907.00 Cycles
TS2.95 907.00 Cycles
TS2.96 920.00 Cycles
TS2.96 920.00 Cycles
TS2.98 933.00 Cycles
TS2.98 930.00 Cycles
TS2.98 930.00 Cycles
TS2.100 912.00 Cycles
TS2.101 897.00 Cycles
TS2.101 897.00 Cycles
TS2.103 893.00 Cycles
TS2.104 953.00 Cycles
TS2.105 903.00 Cycles
TS2.105 903.00 Cycles
TS2.106 933.00 Cycles
TS2.106 933.00 Cycles
TS2.107 900.00 Cycles
TS2.108 890.00 Cycles TS2.108 890.00 Cycles TS2.109 900.00 Cycles TS2.110 883.00 Cycles

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TS2.111 903.00 Cycles
TS2.112 883.00 Cycles
TS2.113 894.00 Cycles
TS2.114 895.00 Cycles
TS2.115 898.00 Cycles
TS2.115 898.00 Cycles
TS2.117 915.00 Cycles
TS2.118 883.00 Cycles
TS2.118 883.00 Cycles
TS2.119 899.00 Cycles
TS2.121 889.00 Cycles
TS2.122 883.00 Cycles
TS2.123 905.00 Cycles
TS2.124 883.00 Cycles
TS2.125 898.00 Cycles
TS2.125 898.00 Cycles
TS2.126 892.00 Cycles
TS2.127 902.00 Cycles
TS2.128 883.00 Cycles
TS2.131 889.00 Cycles
TS2.131 889.00 Cycles
TS2.132 896.00 Cycles
TS2.133 893.00 Cycles
TS2.134 883.00 Cycles
TS2.135 910.00 Cycles
TS2.136 883.00 Cycles
TS2.141 898.00 Cycles
TS2.142 896.00 Cycles
TS2.143 914.00 Cycles
TS2.144 883.00 Cycles
TS2.144 883.00 Cycles
TS2.145 899.00 Cycles
TS2.146 896.00 Cycles
TS2.147 899.00 Cycles
TS2.148 881.00 Cycles
TS2.149 903.00 Cycles
TS2.149 903.00 Cycles
TS2.149 899.00 Cycles
TS2.149 899.00 Cycles
TS2.150 893.00 Cycles
TS2.151 913.00 Cycles
TS2.153 999.00 Cycles
TS2.154 889.00 Cycles
TS2.155 899.00 Cycles
TS2.156 883.00 Cycles
TS2.157 908.00 Cycles
TS2.158 892.00 Cycles
TS2.159 904.00 Cycles
TS2.150 890.00 Cycles
TS2.151 890.00 Cycles
TS2.153 900.00 Cycles
TS2.154 883.00 Cycles
TS2.155 899.00 Cycles
TS2.156 883.00 Cycles
TS2.157 900.00 Cycles
TS2.158 892.00 Cycles
TS2.159 904.00 Cycles
TS2.159 904.00 Cycles





Description Vector Description:

TS2.1APANonRecoverableFaults_Cnt_lgc = MIN TS2.2APANonRecoverableFaults_Cnt_lgc = MAX TS2.2APANonRecoverableFaults_Cnt_lgc = MIN
TS2.3APARecoverableFaults_Cnt_lgc = MIN
TS2.4APARecoverableFaults_Cnt_lgc = MAX
TS2.5APARequest_Cnt_lgc = MIN
TS2.6APARequest_Cnt_lgc = MAX
TS2.7HapticRequest_Cnt_lgc = MIN
TS2.8HapticRequest_Cnt_lgc = MIN
TS2.9ShiftLeverIsInReverse_Cnt_lgc = MIN
TS2.10ShiftLeverIsInReverse_Cnt_lgc = MAX
TS2.11 KAFault_Cnt_lgc = MIN TS2.11LKAFault_Cnt_lgc = MIN TS2.11LKAFault_Cnt_lgc = MIN
TS2.12LKAFault_Cnt_lgc = MAX
TS2.13LKAInhibit_Cnt_lgc = MIN
TS2.14LKAInhibit_Cnt_lgc = MIN
TS2.15LKARequest_Cnt_lgc = MIN
TS2.16LKARequest_Cnt_lgc = MAX
TS2.17HwTorque_HwNm_f32 = MIN
TS2.18HwTorque_HwNm_f32 = MAX
TS2.19HwTorque_HwNm_f32 = ZERO
TS2.20HwTorque_HwNm_f32 = POS
TS2.21HwTorque_HwNm_f32 = NEG
TS2.22TrrQVISta_HanticState_State TS2.22TrqOvlSta_HapticState_State_M_enum = Haptic_Inactive TS2.23TrqOvlSta_HapticState_State_M_enum = Haptic_Active TS2.24TrqOvlSta_HapticState_State_M_enum = Haptic_Waiting TS2.25TrqOvlSta_LKAState_State_M_enum = LKA_Inactive
TS2.26TrqOvlSta_LKAState_State_M_enum = LKA_Active
TS2.27TrqOvlSta_LKAState_State_M_enum = LKA_TemporarilyLimited TS2.28TrqOvlSta_LKAState_State_M_enum = LKA_PermanentlyFailed TS2.29TrqOvISta_LKAState_State_Menum = LKA_ManufacturingDisable
TS2.39TrqOvISta_APAState_State_M_enum = APA_TempInhibit
TS2.31TrqOvISta_APAState_State_M_enum = APA_AvailCntrl
TS2.32TrqOvISta_APAState_State_M_enum = APA_Active
TS2.33TrqOvISta_APAState_State_M_enum = APA_PermanentlyFailed
TS2.33TrqOvISta_APAState_State_M_enum = APA_ManufacturingDisable
TS2.34TrqOvISta_APAState_State_M_enum = APA_ManufacturingDisable TS2.35TrqOvlSta_Haptictime_Sec_M_f32 = MIN TS2.36TrqOvlSta_Haptictime_Sec_M_f32 = MAX TS2.37TrqOvlSta_Haptictime_Sec_M_f32 = POS TS2.38TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 = MIN TS2.39TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32= MAX TS2.40TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TS2.41TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = MIN TS2.42TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = MAX TS2.43TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = POS TS2.44TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = MIN
TS2.45TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = MAX
TS2.46TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = POS IS2.46 I rQOVISta_LiapsedAPAMAXHW I rq I ime_Sec_M_132
TS2.47 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = MIN
TS2.48 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = MAX
TS2.49 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = ZERO
TS2.50 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = POS
TS2.51 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = NEG
TS2.52 I rqOVISta_HwTorqueSV_HwNm_M_Str.K = MIN
TS2.53 I rqOVISta_HwTorqueSV_HwNm_M_Str.K = MAX TS2.54TrqOvISta_HwTorqueSV_HwNm_M_Str.K = POS TS2.55TrqOvISta_LKAPermFault_Cnt_M_lgc = MIN TS2.56TrqOvISta_LKAPermFault_Cnt_M_lgc= MAX TS2.57TrqOvISta_LKAFault_Cnt_M_lgc = MIN TS2.57TrqOvISta_LKAFault_Cnt_M_lgc = MIN TS2.58TrqOvISta_LKAFault_Cnt_M_lgc = MAX TS2.59TrqOvISta_LKAPermFault_Cnt_M_u08 = MIN TS2.60TrqOvISta_LKAPermFault_Cnt_M_u08 = MAX TS2.61TrqOvISta_LKAPermFault_Cnt_M_u08 = POS TS2.62TrqOvISta_ReadytoPulse_Cnt_M_igc = MIN TS2.63TrqOvISta_ReadytoPulse_Cnt_M_igc= MAX TS2.64TrqOvISta_Hapticdur_Cnt_M_u16 = MIN TS2.65TrqOvISta_Hapticdur_Cnt_M_u16= MAX TS2.66TrqOvISta_Hapticdur_Cnt_M_u16 = POS TS2.67k_MaxHapticReqTime_Sec_f32 = MIN TS2.68k_MaxHapticReqTime_Sec_f32= MAX TS2.69k_MaxHapticReqTime_Sec_f32 = POS TS2.70k ShiftLevRevTime Sec f32 = MIN TS2.71k ShiftLevRevTime Sec f32 = MAX TS2.72k ShiftLevRevTime Sec f32 = POS TS2.73k_StandstillThresh_Kph_f32 = MIN TS2.74k_StandstillThresh_Kph_f32 = MAX TS2.75k_StandstillThresh_Kph_f32 = POS TS2.76k_StandstillTime_Sec_f32 = MIN TS2.77k_StandstillTime_Sec_f32= MAX TS2.78k_StandstillTime_Sec_f32 = POS TS2.79k_APAMaxHwTrq_HwNm_f32 = MIN TS2.80k_APAMaxHwTrq_HwNm_f32= MAX TS2.81k_APAMaxHwTrq_HwNm_f32 = POS TS2.82k_APAMaxHwTrqTime_Sec_f32 = MIN TS2.83k_APAMaxHwTrqTime_Sec_132 = MiN
TS2.83k_APAMaxHwTrqTime_Sec_132 = MAX
TS2.84k_APAMaxHwTrqTime_Sec_132 = POS
TS2.85k_APAMaxVehSpd_Kph_132 = MIN
TS2.86k_APAMaxVehSpd_Kph_132 = MAX
TS2.87k_APAMaxVehSpd_Kph_132 = POS
TS2.88k_HapticDuration_Sec_132 = MIN TS2.89k_HapticDuration_Sec_f32= MAX TS2.90k_HapticDuration_Sec_f32 = POS TS2.91k_HapticReacttime_Sec_f32 = MIN TS2.92k_HapticReacttime_Sec_f32= MAX TS2.93k_HapticReacttime_Sec_f32 = POS TS2.94k_LKAMinVehSpd_Kph_f32 = MIN





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TS2.95k_LKAMinVehSpd_Kph_f32= MAX
 TS2.96k_LKAMinVehSpd_Kph_f32 = POS
TS2.97k_LKAMaxVehSpd_Kph_f32 = MIN
 TS2.98k_LKAMaxVehSpd_Kph_f32= MAX
 TS2.99k_LKAMaxVehSpd_Kph_f32 = POS
TS2.100k_HapticAmplitude_MtrNm_f32 = MIN
TS2.101k_HapticAmplitude_MtrNm_f32= MAX
 TS2.102k_HapticAmplitude_MtrNm_f32 = POS
TS2.103k_APAIncludeHaptic_Cnt_lgc = MIN
TS2.104k_APAIncludeHaptic_Cnt_lgc= MAX
TS2.105k_HapticFreq_Hz_f32=MIN
TS2.106k_HapticFreq_Hz_f32=MAX
TS2.107k_HapticFreq_Hz_f32=POS
TS2.108MaxSecureVehicleSpeed_Kph_f32=Min
TS2.109MaxSecureVehicleSpeed_Kph_f32=Max
TS2.110MaxSecureVehicleSpeed_Kph_f32=Pos
 TS2.111MinSecureVehicleSpeed_Kph_f32=Min
 TS2.112MinSecureVehicleSpeed_Kph_f32=Max TS2.113MinSecureVehicleSpeed_Kph_f32=Pos
 TS2.114ESCRequest_Cnt_lgc=FALSE
IS2.114ESCRequest_Cnt_lgc=FALSE
TS2.115ESCRequest_Cnt_lgc=TRUE
TS2.116ESCFault_Cnt_lgc=FALSE
TS2.117ESCFault_Cnt_lgc=TRUE
TS2.118ESCISLimited_Cnt_lgc=FALSE
TS2.119ESCISLimited_Cnt_lgc=FRUE
TS2.120CWFound_Cnt_lgc=FALSE
TS2.121CWFound_Cnt_lgc=TRUE
TS2.123CCWFound_Cnt_lgc=FALSE
TS2.123CCWFound_Cnt_lgc=TRUE
TS2.124CPS_Trailbu_Cnt_lgc=TRUE
IS2.123CCWFound_Cnt_Igc=1RUE
TS2.124PoSTrajHwAngle_HwDeg_f32=Min
TS2.125PoSTrajHwAngle_HwDeg_f32=Max
TS2.125PoSTrajHwAngle_HwDeg_f32=Pos
TS2.127SWARTrgtAngRequest_HwDeg_f32=Min
TS2.128SWARTrgtAngRequest_HwDeg_f32=Max
TS2.129SWARTrgtAngRequest_HwDeg_f32=Pos
TS2.130SWARTrgtAngRequest_HwDeg_f32=Pos
 TS2.131SWARTrgtAngRequest_HwDeg_f32=Neg
TS2.132PosTrajEnable_Cnt_lgc=FALSE
TS2.133PosTrajEnable_Cnt_lgc=TRUE
 TS2.134CWPosition_HwDeg_f32=Min
TS2.135CWPosition_HwDeg_f32=Max
TS2.136CWPosition_HwDeg_f32=Pos
 TS2.137CCWPosition_HwDeg_f32=Min
TS2.138CCWPosition_HwDeg_f32=Max
TS2.139CCWPosition_HwDeg_f32=Neg
IS2.139CCWPosition_Hwbeg_f32=Neg
TS2.140GMOSH_APAMfgEnable_Cnt_lgc=FALSE
TS2.141GMOSH_APAMfgEnable_Cnt_lgc=TRUE
TS2.142GMOSH_LKAMfgEnable_Cnt_lgc=FALSE
TS2.143GMOSH_LKAMfgEnable_Cnt_lgc=FALSE
TS2.144GMOSH_ESCMfgEnable_Cnt_lgc=TRUE
TS2.145GMOSH_ESCMfgEnable_Cnt_lgc=TRUE
TS2.145TrqOvlSta_ESCState_State_M_enum=ESC_Inactive
 TS2.147TrqOvlSta_ESCState_State_M_enum=ESC_Active
TS2.148TrqOvlSta_ESCState_State_M_enum=ESC_TemporarilyLimited
TS2.149TrqOvlSta_ESCState_State_M_enum=ESC_PermanentlyFailed
 TS2.150TrqOvlSta_ESCState_State_M_enum=ESC_ManufacturingDisable TS2.151k_ESCMaxVehSpd_Kph_f32=Min TS2.152k_ESCMaxVehSpd_Kph_f32=Max
 TS2.153k_ESCMaxVehSpd_Kph_f32=Pos
TS2.154k_SWARLimiter_HwDeg_f32=Min
TS2.155k_SWARLimiter_HwDeg_f32=Max
 TS2.156k_SWARLimiter_HwDeg_f32=Pos
 TS2.157TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32=Min
TS2.158TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32=Max
TS2.159TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32=Pos
 TS2.160AII MIN
TS2.161ALL MAX
```

Test Step 2.1 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.850000024
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	15
TrqOvlSta_Haptictime_Sec_M_f32	75.6800003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85999966
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.109999999

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Apption Appt		
i. APABANCHYSIQUE SIGN. 19. (2000000000000000000000000000000000000		·
	k_APAMaxHwTrq_HwNm_f32	0.100000001
Ligar-Continue Description Ligar-Continue Description Description Ligar-Continue Description Ligar-Continue Description Ligar-Continue Description Ligar-Continue Description Description Ligar-Continue Description Ligar-Continue Description Ligar-Continue Description Ligar-Continue Desc	k_APAMaxVehSpd_Kph_f32	0.200000003
Application No. 2012	k_ESCMaxVehSpd_Kph_f32	10.1000004
Lipspace Lips	k_HapticAmplitude_MtrNm_f32	0
Linguistic Plance 1.00000000000000000000000000000000000	k_HapticDuration_Sec_f32	0.50999999
L.Markinyonispot, Nr., 192 L.Markinyonispot, Str., 192 L.Markinyonispotes Time, Sec., 192 L.Markinyonispotes Time, Sec., 192 L.Shinkowith Time, Sec., 192 Langer, Rein, Ind., 1970/Sills, Time, 192 Langer, Re	k_HapticFreq_Hz_f32	3
M. Audarplicht-Park Sec. 12	k_HapticReacttime_Sec_f32	0.159999996
M. MANTENDER 128 20000005	k_LKAMaxVehSpd_Kph_f32	0
S.SWARDINER HORSE, 122	k_LKAMinVehSpd_Kph_f32	0.400000006
K.Simbell/Time-Sec, 12 K.Simbell/Time-Sec, 12 K.Simbell/Time-Sec, 12 K.Simbell/Time-Sec, 12 Larger, Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. APADor/intervention/Detected_Cot. participe, Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. APADor/intervention/Detected_Cot. participe, Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. APADor/intervention/Detected_Cot. participe, Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. APADor/intervention/Detected_Cot. participe, Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. APADor/intervention/Detected_Cot. participe, Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. APADor/intervention/Detected_Cot. participe. participe. participe. Pert. APADor/intervention/Detected_Cot. participe. participe. Pert. APADor/intervention/Detected_Cot. participe. participe. participe. Pert. APADor/intervention/Detected_Cot. participe. p	k_MaxHapticReqTime_Sec_f32	0.0149999997
k. Sandardimen, Jedr. 122 langel, Rie, Jed. A. Trody-Sta. Frody-Sta. Perf. ARAborotex-vestionDefeded. Cnt. particular River River. A Trody-Sta. Trody-Sta. Perf. ARAborotex-vestionDefeded. Cnt. particular River. A Trody-Sta. Trody-Sta. Perf. ARAborotex-vestion Ratio. Cnt. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Cnt. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Cnt. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Pp. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Pp. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Pp. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. SCORDA Long. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. SCORDA Long. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. SCORDA Long. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. SCORDA Long. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Sci. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Sci. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Rev. Stat. Pp. Trody-Sta.	k_SWARLimiter_HwDeg_f32	2.20000005
k Satestatime, Sec. 12 larget Rin Int. An Trochista Trochista Pert APADrottenevertionDebedded Cot. 1 to tracet Rin Int. An Trochista Trochista Pert APADrottenevertionDebedded Cot. 1 to tracet Rin Int. An Trochista Trochista Pert APADrottenevertionDebedded Cot. 1 to tracet Rin Int. An Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1	k_ShiftLevRevTime_Sec_f32	0.00100000005
Larget Rise Inst AD TROVISIA TOROVISE Port I APADOMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APADOMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APADOMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APAROMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APAROMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APAROMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APAROMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT TOROVISE COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT TOR	k_StandstillThresh_Kph_f32	0
tissed Rin Ind AD TROVISTA TORONS Pert J APANORecoversitération Cit ly target, Trojotists, Trovista Trovista pert J APARocoversitération Cit ly target, Trojotists (Trovista Trovista Pert J APARocoversitération) arranget, Trojotists (Trovista Trovista Trovista Pert J APARocoversitération) arranget, Trojotists (Trovista Trovista Pert J APARocoversitération) arranget, Trojotists (Trovista Trovista Trovista Pert J APARocoversitération) arrangement (Trovista Trovista Trovista Trovista Pert J APARocoversitération) arrangement (Trovista Trovista Trovista Pert J APARocoversitération) arrangement (Trovista Trovista Trovista Trovista Pert J APARocoversitération) arrangement (Trovista Trovista Trovist	k_StandstillTime_Sec_f32	0.00100000005
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target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvchiwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvchiwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SviAPTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SviAPTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SviAPTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_NinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrovEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrovEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SWARTrgMnghequet_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SWARTrgMnghequet_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SWARTrgMnghequet_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Pert_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_		
target_Rte_Inst_Ap_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_PosTrajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_Ent_Des_Trajhabg_E		
target_Rte_Inst_Ap_TrqOv/Sta_Pert_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_DosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Pert_SwARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOv/Sta_Pert_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_TrqOsCAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_TrqOsCAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_TrqOsCAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_TrqOsCAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_TrqOsCANp_MtrNm_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_		
target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajHwAngie_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajHwAngie_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrqAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrqAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrqAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOsCahnp_MtrNm_132 target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_132_value target_TrqOviSta_Per1_APANangrecoverableFaults_Cnt_Igc_value 0 target_TrqOviSta_Per1_Cnt_Qr_Cnt_Igc_value 0 target_TrqOviSta_Per1_Cnt_Qr_Cnt_Igc_value 1140.10999 target_TrqOviSta_Per1_Cnt_Qr_Cnt_Igc_value 0 target_TrqOviSta_Per1_Cnt_Qr_Cnt_Igc_value 0 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value 0 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value 0 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOviSta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc_value 0 target_TrqOviSta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc_value 1 target_TrqOviSta_Per1_InstRequest_Cnt_Igc_value 1 target_TrqOviSta_Per1_HapticRequest_Cnt_Igc_value 1 target_TrqOviSta_Per1_PasTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_posTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_sWRATrgiAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SwiRteverlsnReverse_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwiRteverlsnReverse_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscErred_Hz_f32 target_TrqOviSta_Per1_AP_APONGRecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_APARecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_APARequest_Cnt_igc.value target_TrqOviSta_Per1_CWPCound_Cnt_igc.value target_TrqOviSta_Per1_CWPCound_Cnt_igc.value target_TrqOviSta_Per1_CWPCound_Cnt_igc.value target_TrqOviSta_Per1_CWPCound_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCREquest_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_APAMfignable_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_APAMfignable_Cnt_igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value target_TrqOviSta_Per1_LKARault_Cnt_igc.value target_TrqOviSta_Per1_LKARault_Cnt_igc.value target_TrqOviSta_Per1_LKARault_Cnt_igc.value target_TrqOviSta_Per1_LKARault_Cnt_igc.value target_TrqOviSta_Per1_LKARault_Cnt_igc.value target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value		
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_SWARTrgtAngRequest_HwDeg_i31 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_strintLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscAmp_MtrNm_i32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscAmp_MtrNm_i32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscAmp_MtrNm_i32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscFreq_Hz_i32 target_TrqOvIsta_Per1_APANoRecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_EXCRault_Cnt_lgc.value target_TrqOvIsta_Per1_EXCRault_Cnt_lgc.value target_TrqOvIsta_Per1_EXCRault_Cnt_lgc.value target_TrqOvIsta_Per1_EXCRault_Cnt_lgc.value target_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvIsta_Per1_GMOSH_EXCMfgEnable_Cnt_lgc.value target_TrqOvIsta_Per1_GMOSH_EXCMfgEnable_Cnt_lgc.value target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value target_TrqOvIsta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvIsta_Per1_PostrajEnable_Cnt_lgc.value target_TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvIsta_Per1_PostrajEnable_Cnt_lgc.value target_TrqOvIsta_Per1_PostrajEnable_Cnt_lgc.value target_TrqOvIsta_Per1_PostrajEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_CvWFound_Cnt_Igc.value 0 target_TrqOviSta_Per1_CvWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_CvWFound_Cnt_Igc.value 0 target_TrqOviSta_Per1_CvWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_ExCEPault_Cnt_Igc.value 0 target_TrqOviSta_Per1_ExCEPault_Cnt_Igc.value 0 target_TrqOviSta_Per1_ExCEPault_Cnt_Igc.value 0 target_TrqOviSta_Per1_ExCEPault_Cnt_Igc.value 0 target_TrqOviSta_Per1_ExCRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOviSta_Per1_GMOSH_ExCMfgEnable_Cnt_Igc.value 1 target_TrqOviSta_Per1_GMOSH_ExCMfgEnable_Cnt_Igc.value 1 target_TrqOviSta_Per1_HwTorque_HwNm_f32.value 1 target_TrqOviSta_Per1_HwTorque_HwNm_f32.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_MARequest_Cnt_Igc.value 2 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 3 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 3 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 4 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 5 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 4 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 5 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 5 target_TrqOviSta_Per1_Marequest_		
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target_Rte_Inst_Ap_TrqOvlSta_Prq_TrqOvsCnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_VrlSta_PrqT_TrqOscFreq_Hz_f32 target_TrqOvlSta_Prq_APARcoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HubricRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HubricRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HubricRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HubricRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HxAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MinsecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinsecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 0.100000001		
target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APARoncecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APAReccoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APAReccoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARequest_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_ManSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_ManSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_ManSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
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target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 360 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.20999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARquest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.29999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.2999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.2999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.2999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.099985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.099985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
	· · · · · · · · · · · · · · · · · · ·	
target_rrqUviSta_Per1_SWAR1rgtAngRequest_HwDeg_r32.value -32.5		
target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_Igc.value	target_rrqUviSta_Peri_SnittLeverIsinReverse_Cnt_lgc.value	<u>I</u>

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Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	Result
TrgOvISta ESCState State M enum	4	4	-
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0.460000008	0.460000008 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	45	45 ± 1	-
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	
	0	0	•
TrqOvISta_HapticState_State_M_enum	15	15 ± 1	
TrqOvISta_Hapticdur_Cnt_M_u16	1.4	· · · · ·	
TrqOvISta_Haptictime_Sec_M_f32	75.6800003	75.6800003 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.83985996	-9.83985996 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005	1.20000005 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	0	0 ± 1	•
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	1.20000005	1.20000005 ± 0.0009765625	✓
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0	0 ± 0.0009765625	~
target TrgOvlSta Per1 TrgOscEnable Cnt Igc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrgOvISta APAState State M enum	2
TrgOvISta ESCState State M enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	7.11999989
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	132.839996
TrqOvlSta ElapsedStandstillTime Sec M f32	8
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	240
TrqOvlSta_Haptictime_Sec_M_f32	150
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrgOvISta LKAPermFault Cnt M u08	2
TrgOvISta LKAState State M enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	2.2999995
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.119999997
k_APAMaxHwTrq_HwNm_f32	0.079999982
k_APAMaxVehSpd_Kph_f32	1.79999995
k_ESCMaxVehSpd_Kph_f32	12.5
k_HapticAmplitude_MtrNm_f32	0.00600000005
K_HapticDuration_Sec_f32	0.0149999997
K_HapticFreq_Hz_f32	3.53999996
K_HapticReacttime_Sec_f32	0.0160000008
<pre>LKAMaxVehSpd_Kph_f32</pre>	1.8999998
k_LKAMinVehSpd_Kph_f32	1.75
<pre>k_MaxHapticReqTime_Sec_f32</pre>	0.029999993
k_SWARLimiter_HwDeg_f32	3.5999999
k_ShiftLevRevTime_Sec_f32	0.0049999989
k_StandstillThresh_Kph_f32	0.039999991
<pre>c_StandstillTime_Sec_f32</pre>	0.00499999989
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cr	nt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt Igc	target TrqOvlSta Per1 APARequest Cnt Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc$	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>_</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlOta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH LKAMfgEn		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HwTorque HwNm f32	target TrqOvISta Per1 HwTorque HwNm f		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1130.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	364.940002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.560000002		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.2000008		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004 0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value		Function Value	Descrit
Name Tracoulste ADAState State M. cours	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime Sec_M_f32 TrqOvlSta ElapsedShiftLevRevTime Sec_M f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_EtapsedStatidstill*Iffle_Sec_ivi_132 TrqOvISta_HapticState_State_M_enum	2	2	_
TrqOvISta Hapticdur Cnt M u16	240	240 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrgOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25	-9.25 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TraCulSto BoodytoBulgo Cot M Igo	1	1 1 1	

0

3

target_TrqOvlSta_Per1_APAState_State_enum.value

target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value

TrqOvlSta_ReadytoPulse_Cnt_M_lgc

1 ± 1

0 ± 1

3

3

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.00600000005	0.00600000005 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.53999996	3.53999996 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvISta_APAState_State_M_enum	1
FrqOvISta_ESCState_State_M_enum	2
FrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	85
FrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	220.679993
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	68.3000031
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	530
rqOvlSta_Haptictime_Sec_M_f32	860.26001
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.64000034
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	1
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3.400001
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.129999995
_APAMaxHwTrq_HwNm_f32	0.159999996
APAMaxVehSpd Kph f32	3.5999999
_ESCMaxVehSpd_Kph_f32	14.8999996
HapticAmplitude_MtrNm_f32	0.0120000001
_HapticDuration_Sec_f32	0.029999993
	4
HapticReacttime_Sec_f32	0.0320000015
 _LKAMaxVehSpd_Kph_f32	3.79999995
LKAMinVehSpd Kph f32	3.5
	0.0450000018
SWARLimiter_HwDeg_f32	5
ShiftLevRevTime_Sec_f32	0.00899999961
StandstillThresh Kph f32	0.079999982
_StandstillTime_Sec_f32	0.00899999961
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOviSta_Per1_APARequest_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APAState State enum	target TrqOvISta Per1 APAState State enum
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1119.91003		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	369.880005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.910000026		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	35.0999985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	44.9000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997 -28.7000008		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
		Francisco d Volum	Daguile
Name	Actual Value	Expected Value 4	Result
TrqOvISta_APAState_State_M_enum	4	4	~
TrqOvISta_ESCState_State_M_enum	0		
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1 0	-
		1	~
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	530	530 ± 1	-
TrqOvISta_Haptictime_Sec_M_f32	860.26001	860.26001 ± 1	-
TrqOvISta_Haptictime_Sec_im_is2 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.64000034	-8.64000034 ± 0.000009	
TrgOvISta LKAFault Cnt M Igc	0	0 ± 1	9
TrgOvISta LKAPermFault Cnt M Igc	1	1 ± 1	,
TrgOvlSta LKAPermFault Cnt M u08	1	1±1	~
TrgOvlSta LKAState State M enum	4	4	
TrgOvlSta PosSrvoHwAngle HwDeg M f32	3.4000001	3.4000001 ± 0.0009765625	~
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	-
target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	_
		1	-
target TrgOvISta Per1 GMOSHOscillateState State enum.value	1		1
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrgOvlSta_Per1_LKAState_State_enum.value		4	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4		~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	4	0 ± 1	\ \ \ \ \ \ \
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	4		~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	4 0 3.4000001	0 ± 1 3.4000001 ± 0.0009765625	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	4 0 3.4000001 0.0120000001	0±1 3.4000001±0.0009765625 0.0120000001±0.0009765625	, , , , , , , , , , , , , , , , , , ,

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•



Test Step 2.4 (Repeat Count = 1)	▼
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	225.639999
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	684
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrgOvlSta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvISta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	4.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.14000001
k_APAMaxHwTrq_HwNm_f32 k_APAMaxVehSpd_Kph_f32	0.239999995 5.4000001
k_ESCMaxVehSpd_Kph_f32	17.2999992
k_HapticAmplitude_MtrNm_f32	0.0179999992
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	4.55999994
k_HapticReacttime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.69999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.059999987
k_SWARLimiter_HwDeg_f32	6.4000001
k_ShiftLevRevTime_Sec_f32	0.0130000003
k_StandstillThresh_Kph_f32 k StandstillTime Sec f32	0.119999997 0.0130000003
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. \\$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvIsta_Per1_APARecuverablePaulis_Crit_Igc.value target_TrqOvIsta_Per1_APARequest_Cnt_Igc.value	1
target_TrqOvlota_rer1_crWFound_Cnt_lgc.value	1
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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1109.81006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	374.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.25999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	-
TrqOvlSta_ESCState_State_M_enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	308.519989	308.519989 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	-
TrqOvlSta_Hapticdur_Cnt_M_u16	684	684 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973	-8.02999973 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0179999992	0.0179999992 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.55999994	4.55999994 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	450.359985
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	396.359985
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	540
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	960
TrqOvlSta_Haptictime_Sec_M_f32	1830.43005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.42000008
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.150000006
k_APAMaxHwTrq_HwNm_f32	0.319999993
k APAMaxVehSpd Kph f32	7.1999981
k_ESCMaxVehSpd_Kph_f32	19.7000008
k HapticAmplitude MtrNm f32	0.0240000002
k_HapticDuration_Sec_f32	0.0599999987
k HapticFreq Hz f32	5.05999994
k HapticReacttime Sec f32	0.064000003
k_LKAMaxVehSpd_Kph_f32	7.5999999
k LKAMinVehSpd Kph f32	7
k_MaxHapticReqTime_Sec_f32	0.075000003
k SWARLimiter HwDeg f32	7.80000019
k_ShiftLevRevTime_Sec_f32	0.0170000009
k StandstillThresh Kph f32	0.159999996
k_StandstillTime_Sec_f32	0.0170000009
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target TrgOvlSta Per1 APADrvrInterventionDetected Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrqOvlSta Per1 APAState State enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrqOvlSta Per1 CWFound Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target TrqOvISta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreg Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0
target TrqOvlSta Per1 APARecoverableFaults Cnt Igc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target TrgOvlSta Per1 CCWPosition HwDeg f32.value	-1099.70996
target TrgOvlSta Per1 CWFound Cnt Igc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	379.76001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.61000001
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	54.7000008
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-24.8999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	396.359985	396.359985 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	960	960 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.42000008	-7.42000008 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999	5.5999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	5.5999999	5.5999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0240000002	0.0240000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.05999994	5.05999994 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	1050
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.400000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.029999993
k_HapticDuration_Sec_f32	0.075000003
k_HapticFreq_Hz_f32	5.57399988
k_HapticReacttime_Sec_f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k_LKAMinVehSpd_Kph_f32	8.75
k_MaxHapticReqTime_Sec_f32	0.090000036
k_SWARLimiter_HwDeg_f32	9.19999981
k_ShiftLevRevTime_Sec_f32	0.0209999997
k_StandstillThresh_Kph_f32	0.200000003
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	r32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	_*	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrgOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-1089.60999		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	384.700012		
	1		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta_Per1_ESCIsLimited Cnt_lgc.value	1		
	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.96000004		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	-
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991	2315.65991 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc		1 ± 1	•
. = = = = = =	1		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
		1±1 3±1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	1		-
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 3	3 ± 1	

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TrqOvlSta_Per1

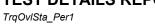
Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.029999993	0.0299999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.7 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrgOvISta ESCState State M enum	4
TrqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	1750
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	572.039978
TrqOvISta ElapsedStandstillTime Sec M f32	1800.45996
TrqOvISta_HapticState_State_M_enum	1
TrgOvISta Hapticdur Cnt M u16	3575
TrqOvISta_Haptictime_Sec_M_f32	2800.93994
TrgOvISta HwTorqueSV HwNm M Str.SV UIs f32	-6.19999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0152000003
TrqOvISta_KKAFault_Cnt_M_lgc	1
· ·	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	5.5999999 1
k APAIncludeHaptic Cnt Igc	0
k_APAMicudenapiic_Gnt_igc k_APAMaxHwTrqTime_Sec_f32	0.170000002
k APAMaxHwTrq HwNm f32	0.479999989
	10.8000002
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	19.7000008
k HapticAmplitude MtrNm f32	0.0359999985
_ : :	0.090000036
k_HapticDuration_Sec_f32	6.08799982
k_HapticFreq_Hz_f32	0.0960000008
k_HapticReacttime_Sec_f32 k_LKAMaxVehSpd_Kph_f32	11.3999996
_ : - : -	10.5
k_LKAMinVehSpd_Kph_f32	0.104999997
k_MaxHapticReqTime_Sec_f32 k SWARLimiter HwDeg f32	7.80000019
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	0.23999995
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APAState State enum	target TrqOvISta Per1 APAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target TrqOvlSta Per1 CWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_ESCFault_Cnt_loc
target_Rte_Inst_Ap_InqOvista.InqOvista_Peri_ESCPauli_Chi_igc target_Rte_Inst_Ap_InqOviSta.TrqOviSta_Peri_ESCIsLimited_Chi_igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
	target TrqOvlSta Per1 ESCRequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	
target_Rte_Inst_Ap_IrqOviSta.IrqOviSta_PerI_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_PerI_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_IrqOviSta.IrqOviSta_PerI_GiviOSHOSciliateState_State_enun target Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH APAMfgEnable Cnt Igc	
	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lge	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSt	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- •	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvISta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_l target TrqOvlSta Per1 TrqOscFreq Hz f32	gc	
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1079.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	389.640015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.30999994		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	44.900015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	54.7000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001 -24.8999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
	Actual Value	Expected Value	Result
Name TraChilSta ADAStata Stata Michael TraChilSta ADASTATA		Expected value	
TrqOvlSta_APAState_State_M_enum	1.4	4	Result
Tranvista Escetate State Miller	4	4	~
TrqOvISta_ESCState_State_M_enum TrqOvISta_Elapsed&PAMayHwTrqTime_Sec_M_f32	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4 0	4 0±1	**************************************
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4 0 0	4 0±1 0±1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4 0 0 0	4 0±1 0±1 0	**************************************
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4 0 0	4 0±1 0±1	y
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	4 0 0 0 0 1	4 0±1 0±1 0	y
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	4 0 0 0 1 3575	4 0±1 0±1 0 1 3575±1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	4 0 0 0 1 3575 0	4 0±1 0±1 0 1 3575±1 0±1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4 0 0 0 1 3575 0 -6.19999981	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	4 0 0 0 1 3575 0 -6.19999981 0	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	4 0 0 0 1 3575 0 -6.19999981 0	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.00009 0±1 1±1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	4 0 0 0 1 3575 0 -6.19999981 0 1 3	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.00009 0±1 1±1 3±1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	4 0 0 0 1 3575 0 -6.19999981 0 1 3	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.00009 0±1 1±1 3±1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1 1±1 3±1 4 5.5999999±0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1 0 4	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1 1±1 3±1 4 5.5999999±0.0009765625 1±1 0±1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1 0 4	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1 1±1 3±1 4 5.5999999±0.0009765625 1±1 0±1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1 0 4 4	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1 1±1 3±1 4 5.5999999±0.0009765625 1±1 0±1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1 0 4 4 1	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1 1±1 3±1 4 5.5999999±0.0009765625 1±1 0±1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticitime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_Pearl_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1 0 4 4 1 4	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1 1±1 3±1 4 5.5999999±0.0009765625 1±1 0±1 4 4 1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_HKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1 0 4 4 1 4 0 5.5999999	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1 1±1 3±1 4 5.5999999±0.0009765625 1±1 0±1 4 4 1 4 5.5999999±0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAPemlFault_Cnt_M_lgc TrqOvlSta_LKAPemFault_Cnt_M_lgc TrqOvlSta_LKAPemFault_Cnt_M_u08 TrqOvlSta_LKAPemFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_HKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1 0 4 4 1 4 0 5.5999999 0.0359999985	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.00009 0±1 1±1 3±1 4 5.5999999±0.0009765625 1±1 0±1 4 4 1 4 0±1 5.5999999±0.0009765625 0.035999998±0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_HKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	4 0 0 0 1 3575 0 -6.19999981 0 1 3 4 5.5999999 1 0 4 4 1 4 0 5.5999999	4 0±1 0±1 0 1 3575±1 0±1 -6.19999981±0.000009 0±1 1±1 3±1 4 5.5999999±0.0009765625 1±1 0±1 4 4 1 4 5.5999999±0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓





Name	Test Step 2.8 (Repeat Count = 1)	▼
No. Inc. Proposition P	Name	Input Value
Topichies Decision Seak American 0		·
TropOste Bispeed National Work Time, See J. 192 TropOste Bispeed See J. 192 TropOste Industrial Work J. 192 TropOste Industrial Work J. 192 TropOste Industrial Conf.	TrqOvlSta_APAState_State_M_enum	· · · ·
TROMOSE BispectShirt-Sher Time Sec. M. 202 TROMOSE BispectShirt-Sher Sec. M. 203 TROMOSE Sher Sher Sher Sher Sher Sher Sher Sher	TrqOvlSta_ESCState_State_M_enum	0
Topockas Supposed Service Survey	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TOLORISE SINS AND WASTERS (C.M. 1915) TOLORISE SINS AND WASTERS (C.M. 1925) TOLORISE SINS AND WASTERS (C.M.	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
Tagodista psignostic Crit M. rst 9	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	
TROMBIS Informacy Hole M SIV UIS D2		i e
TERDATES INFORMED Mark M \$1.00 M; E. 12		
TROWSE MARTINES LANGEMENT LOT M. 1908 TOPOSISE JAKSHER SIME M. FROM 1908 TOPOSISE JAKSHER SIME SIME TOPOSISE JAKSHER SIME SIME SIME SIME SIME SIME SIME SIME		
TROM/Sils JAPAPent Crit M, gr. TROM/Sils JAPAPent Sils Crit M, gr. TROM/Sils JAPAPent Sils Crit M, gr. TROM/Sils JAPAPent Sils M, gr. mm. 1 TROM/Sils JAPAPent Sils JAPAPent JAPAPent Sils JAPAPent Sils JAPAPent JA		
TEONISE JAPPame Saul Cort 19		
Trick/Sile Disk Ordering Horizon M. 12	TrqOvlSta_LKAPermFault_Cnt_M_lgc	
1000/0003 Pastylothic Cyl. M. 190	TrqOvISta_LKAPermFault_Cnt_M_u08	2
TOO/HIS ReadyoPulse CM My Type A PAPANAHVITTIME Sec, 52 A PAPANAHVIT	TrqOvlSta_LKAState_State_M_enum	1
A.PAMacHor Type Sec. 12	TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	
A. PAMAMAN Trail Time, Soc., 192 A. Schlandwinsky Kin, 192	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	·
A. PAMAWAYEN Proc Pamaway		
L. PADMANNIANS K. Ph. 12 22 22 20 20 20 20 20		
K. ESCHAAVENSED, KON. 12 K. Hapitch/Touristin Step. (32 K. Shiftch/Touristin Step. (32 K. Shiftch/Touristin Hybridge) K. Sandidalliffune, Step. (32 K. Shiftch/Touristin Hybridge) K. Sandidalliffune, Step. (32 K. Shiftch/Touristin Hybridge) K. Sandidalliffune, Hybridge K. Sandidalliffune, H		
k, Hapschrand, Buthin, 122 k, Hapschrand, Hz, 128 k, LAMANIVENS, Hz, 128 k, LAMANIVENS, Hz, 128 k, LAMANIVENS, Hz, 128 k, LAMANIVENS, Hz, 128 k, Shandaithrea, H		
Kapalicheau Ft. 122 6.60000014	k_HapticAmplitude_MtrNm_f32	
IL KAMANIVARSA KAN JASA KANANIVARSA KAN JASA KANANIVARSA KANANIV	k_HapticDuration_Sec_f32	
L KAMAINVerSpd Kpn, 132 k L KAMINVerSpd Kpn, 132 k L KAMINVerSpd Kpn, 132 k L KAMINVerSpd Kpn, 132 k SIMILK-RevTime, 5cc, 132 0.28999991 k Similk-RevTime, 5cc, 132 0.28999991 k Similk-RevTime, 5cc, 132 0.28999991 k Similk-RevTime, 5cc, 132 0.289999992 k Similk-RevTime, 5cc, 132 0.2899999992 k Similk-RevTime, 5cc, 132 0.289999992 k Similk-RevTime, 5cc, 132 0.28999991 k Similk-RevTime, 5	k_HapticFreq_Hz_f32	6.60200024
k, LMAMPHONEOR (pt.) 12 k. Marthoplic Real Time, Sec. (13 c.) 119999981 c. 119999997 c. 119999997 c. 119999999 c. 1199999999 c. 119999999999	k_HapticReacttime_Sec_f32	0.112000003
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target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HaviDrque_HwNm_f32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARstate_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1		
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target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovIbanble_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_Mithm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_Mithm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_Mithm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_Mithm_f32 target_TrqOviSta_Perl_APAReqoverableFaults_Cnt_Igc_value 0 target_TrqOviSta_Perl_APARequest_Cnt_Igc_value 1	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	
target_Rte_Inst_Ap_TrqOvlSta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_LKAFault_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_SwartrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Perl_SwARtrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOsSenable_Cnt_lgc target_TrqOvlSta_Perl_TrqOsSenable_Cnt_lgc target_TrqOvlSta_Perl_TrqOsSenable_Cnt_lgc target_TrqOvlSta_Perl_TrqOsSenable_Cnt_lgc target_TrqOvlSta_Perl_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvl		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASItat_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASitat_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASitat_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SorAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SorAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_APANceoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_DesServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServIstable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovIstaDe_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvISta_Per1_APANenRecoverableFaults_Cnt_Igc_value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecurevehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecurevehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Tr	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_DosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_DosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsinReverse_Cnt_igc target_TrqOvlSta_Per1_ShiftLeverlsinReverse_Cnt_igc target_TrqOvlSta_Per1_ShiftLeverlsinReverse_Cnt_igc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShifftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 1		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscErable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlS		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1	target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1

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rrqOviSta_Peri		IMAC	71640
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-1069.41003		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	394.579987		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target TrqOvlSta Per1 GMOSHOscillateState State enum.value		0	✓
target_TrqOvista_Ferr_GiviOSHOscillateState_State_enum.value	0	U	
target_TrqOvIsta_Fe11_GWOSHOScillateState_State_enum.value	0 3	3	•
			· ·
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	· · ·
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3	3 1±1	· ·
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3 1 0.150000006	3 1 ± 1 0.150000006 ± 0.0009765625	· ·

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~	
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓	

Test Step 2.9 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	4	
TrqOvlSta_ESCState_State_M_enum	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3529.32007	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	747.719971	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3539.93994	
TrqOvlSta_HapticState_State_M_enum	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	8625	
TrqOvlSta_Haptictime_Sec_M_f32	3770.09009	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.98000002	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0196000002	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	10	

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TrqOvlSta_Per1



Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1.
k_APAMaxHwTrqTime_Sec_f32	0.18999998
k_APAMaxHwTrq_HwNm_f32	0.639999986
k APAMaxVehSpd Kph f32	14.3999996
k_ESCMaxVehSpd_Kph_f32	29.2999992
k HapticAmplitude MtrNm f32	0.0480000004
k_HapticDuration_Sec_f32	0.119999997
k_HapticFreq_Hz_f32	7.11600018
k_HapticReacttime_Sec_f32	0.128000006
k LKAMaxVehSpd Kph f32	15.1999998
k LKAMinVehSpd Kph f32	14
k_MaxHapticReqTime_Sec_f32	0.135000005
k SWARLimiter HwDeg f32	13.399996
- · · · · ·	0.0329999998
k_ShiftLevRevTime_Sec_f32	0.319999993
k_StandstillThresh_Kph_f32	0.0329999998
k_StandstillTime_Sec_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enunceters$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ShiftLeverIsInReverse Cnt Igc	
	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1059.31006
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	399.519989
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.00999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	64.5
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	74.3000031
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007
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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	8625	8625 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.98000002	-4.98000002 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	10	10 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	10	10 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0480000004	0.0480000004 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.11600018	7.11600018 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1.
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	14.8000002
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	:	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	ite_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	.lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1049.20996		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	404.459991		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta APAState State M enum	0	0	- Nosuit
TrqOvISta_ESCState_State_M_enum	4	4	-
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	4418.97998	4418.97998 ± 1	
TrqOvISta ElapsedShiftLevRevTime Sec M f32	835.559998	835.559998 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	425564	425564 ± 1	
		-4.20148611 ± 0.000009	
Tracovista Hwitoranesv Hwnm M Str SV 118 130	1-4 20148611	7.20170011 ± 0.000003	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAEquit_Cnt_M_loc	-4.20148611 1	1+1	-4
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1 0 + 1	
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 0	0 ± 1	-
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 0 2	0 ± 1 2 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 0	0 ± 1	-





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Fest Step 2.11 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	0
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5308.64014
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	923.400024
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	5279.41992
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	13675
rqOvlSta_Haptictime_Sec_M_f32	4740.43018
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75999999
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0240000002
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	12.1999998
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
_APAMaxHwTrqTime_Sec_f32	0.20999993
APAMaxHwTrg HwNm f32	0.800000012
APAMaxVehSpd Kph f32	18
ESCMaxVehSpd Kph f32	34.0999985
_Eoolmaxvenopd_Npn_io2 _HapticAmplitude_MtrNm_f32	0.0599999987
HapticDuration Sec f32	0.150000006
HapticFreq Hz f32	8.1440005
- •	
_HapticReacttime_Sec_f32	0.159999996
_LKAMaxVehSpd_Kph_f32	19
_LKAMinVehSpd_Kph_f32	17.5
_MaxHapticReqTime_Sec_f32	0.165000007
_SWARLimiter_HwDeg_f32	16.2000008
_ShiftLevRevTime_Sec_f32	0.0410000011
_StandstillThresh_Kph_f32	0.40000006
_StandstillTime_Sec_f32	0.0410000011
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
urget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
urget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enui	
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfqEnable Cnt Igc	
irget_Rte_inst_Ap_1rq0viSta.1rq0viSta_Per1_GM0SH_APAMigEnable_Cnt_igc irget_Rte_inst_Ap_Trq0viSta.Trq0viSta_Per1_GM0SH_ESCMfgEnable_Cnt_igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
	target_TrqOvlSta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiffLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRevers target TrqOvlSta Per1 TrqOscAmp MtrNm		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_l	_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_Fer1_TrqOscFreq Hz f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1039.10999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	409.399994		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.71000004		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	74.3000031 84.0999985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-13.5		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓ ×
TrqOvISta_ESCState_State_M_enum	4	4	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	~
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	13675	13675 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75999999	-3.75999999 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	12.1999998	12.1999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	0 + 1	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	12.1999998 0.0599999987	12.1999998 ± 0.0009765625 0.0599999987 ± 0.0009765625	
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0.0099999997 ± 0.0009765625	-
target_11q0viota_r cr1_11q0scEnable_crit_ige.value	V	U ± 1	1
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	8.14400005	8.14400005 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Fest Step 2.12 (Repeat Count = 1)	Innut Value
ta let An TraOviSta	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta 0
rqOvlSta_APAState_State_M_enum rqOvlSta_ESCState_State_M_enum	1
rqOvISta_E3C3tate_state_ivi_entitiii rqOvISta_E3c3tate_state_ivi_entitiii rqOvISta_E3c3tate_state_ivi_entitiii	6198.2998
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999
rqOvISta_ElapsedStandstillTime_Sec_M_f32	6149.16016
rqOvISta_HapticState_State_M_enum	1
rqOvISta_Hapticdut_Cnt_M_u16	16200
rqOvISta_Haptictime_Sec_M_f32	5225.33984
rqOvISta HwTorqueSV HwNm M Str.SV UIs f32	-3.1500001
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	13.3000002
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.219999999
APAMaxHwTrq_HwNm_f32	0.87999995
_APAMaxVehSpd_Kph_f32	19.7999992
_ESCMaxVehSpd_Kph_f32	36.5
_HapticAmplitude_MtrNm_f32	0.0659999996
_HapticDuration_Sec_f32	0.165000007
_HapticFreq_Hz_f32	8.65799999
_HapticReacttime_Sec_f32	0.175999999
_LKAMaxVehSpd_Kph_f32	20.8999996
_LKAMinVehSpd_Kph_f32	19.25
_MaxHapticReqTime_Sec_f32	0.180000007
_SWARLimiter_HwDeg_f32	17.6000004
_ShiftLevRevTime_Sec_f32	0.0450000018
_StandstillThresh_Kph_f32	0.439999998
_StandstillTime_Sec_f32	0.0450000018
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target TrqOvlSta_Per1_ESCIsLimited Cnt_lgc
	target TrqOvISta Per1 ESCRequest Cnt Igc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvISta_Per1_ESCRequest_Clit_igc target TrqOvISta Per1 ESCState State enum
rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	
inget_Rte_inst_Ap_frqOviSta.frqOviSta_Feff_GMOSH_APAMfgEnable_Cnt_lgc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
rget_Rte_Inst_Ap_frqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget Rte Inst Ap TrgOvISta.TrgOvISta Per1 HapticReguest Cnt Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HwTorque HwNm f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAInhibit Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target TrqOvlSta Per1 LKARequest Cnt Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAState State enum	target TrqOvISta Per1 LKAState State enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
rget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
irget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
	1
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	

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TrqOviSta_Peri		Ida	TIMO
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	414.339996		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.05999994		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	89		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-11.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999	1011.23999 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	16200	16200 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001	-3.1500001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value		0.109999999 ± 0.0009765625	✓
3 - 2 - 4	0.109999999	0.109999999 ± 0.0009703023	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.109999999 0.0659999996	0.0659999996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	0	
TrqOvlSta_ESCState_State_M_enum	2	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7087.95996	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1099.07996	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	7018.8999	
TrqOvlSta_HapticState_State_M_enum	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	18725	
TrqOvlSta_Haptictime_Sec_M_f32	5710.16992	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53999996	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0284000002	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996	

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TrqOvlSta_Per1

TrqOviSta_Peri	THE CITY OF THE CONTRACT OF TH
Name	Input Value
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
C_APAIncludeHaptic_Cnt_lgc	1
C_APAMaxHwTrqTime_Sec_f32	0.230000004
C_APAMaxHwTrq_HwNm_f32	0.959999979
<_APAMaxVehSpd_Kph_f32	21.6000004
<_ESCMaxVehSpd_Kph_f32	38.9000015
_HapticAmplitude_MtrNm_f32	0.0719999969
_HapticDuration_Sec_f32	0.180000007
_HapticFreq_Hz_f32	9.17199993
_HapticReacttime_Sec_f32	0.192000002
_LKAMaxVehSpd_Kph_f32	22.7999992
	21
C_MaxHapticReqTime_Sec_f32	0.194999993
SSWARLimiter_HwDeg_f32	19
 ShiftLevRevTime_Sec_f32	0.0489999987
StandstillThresh Kph f32	0.479999989
standstillTime_Sec_f32	0.0489999987
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc	target TrqOvISta Per1 APARecoverableFaults Cnt Igc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	
arget Rte Inst Ap TrqOviSta.TrqOviSta_Peri_APARequest_Cht_igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
	target_TrqOvISta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target TrgOvlSta Per1 PosTrajHwAngle HwDeg f32
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 SWARTrgtAngRequest HwDeg f3	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PeTI_SwARTrgiArigkequest_nwbeg_ts arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_igc	target_TrqOviSta_Per1_SWARTigtArigRequest_HwDeg_i32 target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1018.90997
arget_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	419.279999
arget_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.40999985
arget_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0
arget_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	84.099985
arget_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	93.9000015
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0 0.119999997

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.69999981		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	18725	18725 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	5710.16992	5710.16992 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53999996	-2.53999996 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996	14.3999996 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	14.3999996	14.3999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0719999969	0.0719999969 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	9.17199993	9.17199993 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7977.62012
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1186.92004
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	7888.64014
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	21250
TrqOvlSta_Haptictime_Sec_M_f32	6195
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.92999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	15.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.239999995
k_APAMaxHwTrq_HwNm_f32	1.03999996
k_APAMaxVehSpd_Kph_f32	23.3999996
k_ESCMaxVehSpd_Kph_f32	41.2999992
k_HapticAmplitude_MtrNm_f32	0.0780000016
k_HapticDuration_Sec_f32	0.194999993
k_HapticFreq_Hz_f32	9.68599987
k_HapticReacttime_Sec_f32	0.208000004
k_LKAMaxVehSpd_Kph_f32	24.7000008
k_LKAMinVehSpd_Kph_f32	22.75
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	20.3999996
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	0.519999981
k_StandstillTime_Sec_f32	0.0529999994
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cnd$	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lc	target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lq	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_er	ium	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgE	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	iest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1008.81		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	424.220001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.76000023		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	89		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	98.8000031		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.80000019 0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value			- "
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	Y
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	V
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	· ·
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	V
TrqOvlSta_HapticState_State_M_enum	2	2	V
TrqOvISta_Hapticdur_Cnt_M_u16	21250	21250 ± 1	V
TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32	1 02000005	0 ± 1	*
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.92999995	-1.92999995 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1±1	· ·
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1±1	· ·
TrqOvISta_LKAPermFault_Cnt_M_u08	4	4 ± 1	V
TrqOvISta_LKAState_State_M_enum	3	0.120000005 + 0.0000765625	~
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0780000016	0.0780000016 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	9.68599987	9.68599987 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
	1
FrqOvISta_APAState_State_M_enum	4
FrgOvISta_ESCState_State_M_enum	
FrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8867.28027 1274.76001
FrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	8758.37988
FrqOvISta_ElapsedStandstillTime_Sec_M_f32	1
FrqOvISta_HapticState_State_M_enum	23775
FrgOvISta_Hapticdur_Cnt_M_u16	
FrqOvISta_Haptictime_Sec_M_f32	6679.83008
FrgOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.32000005 0.0328000002
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	
FrqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	1 2
FrgOvISta_LKAPermFault_Cnt_M_u08	2 2
rqOvISta_LKAState_State_M_enum	
FrgOvISta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	
x_APAMaxHwTrqTime_Sec_f32	0.25
x_APAMaxHwTrq_HwNm_f32	1.12
x_APAMaxVehSpd_Kph_f32	25.2000008
C_ESCMaxVehSpd_Kph_f32	43.700008
:_HapticAmplitude_MtrNm_f32	0.083999989
c_HapticDuration_Sec_f32	0.20999993 10.199998
K_HapticFreq_Hz_f32	
C_HapticReacttime_Sec_f32	0.224000007
_LKAMaxVehSpd_Kph_f32	26.6000004
C_LKAMinVehSpd_Kph_f32	24.5
(_MaxHapticReqTime_Sec_f32	0.224999994
SSWARLimiter_HwDeg_f32	21.7999992
_ShiftLevRevTime_Sec_f32	0.057
StandstillThresh_Kph_f32	0.560000002
x_StandstillTime_Sec_f32	0.057
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HwTorque HwNm f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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TrqOvlSta_Per1





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3:			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	3 target_TrqOvlSta_Per1_SWARTrgtAngRequertarget_TrqOvlSta_Per1_ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNn		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1	-	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-998.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	429.160004		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.11000013		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1274.76001	1274.76001 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	8758.37988	8758.37988	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	23775	23775 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	6679.83008	6679.83008 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005	-1.32000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004	16.6000004 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	*
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	Y
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	*
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	*
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	16.6000004	16.6000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0839999989	0.0839999989 ± 0.0009765625	V
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	10.1999998	10.1999998 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.16 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum rqOvlSta_ESCState_State_M_enum	0
rqOviSta_ESCState_state_ivi_eritiiii rqOviSta_EscState_state_ivi_eritiiii rqOviSta_EscState_state_ivi_eritiiii	9756.94043
rqOvISta_ElapsedArAwaxhwTrqTime_Sec_IM_f32	1362.59998
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	9628.12012
rqOvlSta_HapticState_State_M_enum	2
rqOvISta_Hapticdur_Cnt_M_u16	26300
rqOvISta Haptictime Sec M f32	7164.66016
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.709999979
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0350000001
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	17.7000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.25999999
_APAMaxHwTrq_HwNm_f32	1.20000005
_APAMaxVehSpd_Kph_f32	27
_ESCMaxVehSpd_Kph_f32	46.0999985
_HapticAmplitude_MtrNm_f32	0.090000036
_HapticDuration_Sec_f32	0.224999994
_HapticFreq_Hz_f32	10.7139997
_HapticReacttime_Sec_f32	0.23999995
_LKAMaxVehSpd_Kph_f32	28.5
_LKAMinVehSpd_Kph_f32	26.25
_MaxHapticReqTime_Sec_f32	0.239999995
_SWARLimiter_HwDeg_f32	23.2000008
_ShiftLevRevTime_Sec_f32	0.0610000007
_StandstillThresh_Kph_f32	0.600000024
_StandstillTime_Sec_f32	0.0610000007
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Iq	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_Igc arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWPosition HwDeg f32	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOviSta.TrqOviSta_Peri_CwPountd_Cnt_gc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 ESCFault Cnt Igc	target TrqOvISta Per1 ESCFault Cnt Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt Igc
rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target TrqOvISta Per1 ESCRequest Cnt Igc
riget Rte Inst Ap TrqOvlSta.TrqOvlSta_FeT1_ESCRequest_Crit_igc	target TrgOvlSta Per1 ESCState State enum
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_E3c5tate_6tate_entiti irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_entit	
arget_Rte_Inst_Ap_TrqOviota.TrqOviota_rer1_GMOSH_APAMfgEnable_Cnt_lgc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
liget_Rte_Inst_Ap_TrqOviota.TrqOviota_Fer1_GMOSH_ESCMfgEnable_Cnt_lgc	
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
riget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
rget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
rget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-988.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	434.100006		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	98.8000031		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	108.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	
TrgOvlSta ESCState State M enum	3	3	_
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	_
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	-
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	_
TrgOvlSta HapticState State M enum	0	0	_
TrqOvlSta_Hapticdur_Cnt_M_u16	26300	26300 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.709999979	-0.709999979 ± 0.0000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	_
TrgOvlSta LKAPermFault Cnt M lgc	1	1±1	
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	_
TrqOvlSta_LKAState_State_M_enum	3	3	_
TrgOvlSta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	_
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target TrqOvlSta Per1 LKAState State enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	·
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.090000036	0.0900000036 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13315.5801
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13107.0801
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	36400
TrqOvlSta_Haptictime_Sec_M_f32	9103.98047
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.30000012
k_APAMaxHwTrq_HwNm_f32 k_APAMaxVehSpd_Kph_f32	1.51999998 34.2000008
k_ESCMaxVehSpd_Kph_f32	55.7000008
k_HapticAmplitude_MtrNm_f32	0.114
k_HapticDuration_Sec_f32	0.284999996
k_HapticFreq_Hz_f32	12.7700005
k_HapticReacttime_Sec_f32	0.30399999
k_LKAMaxVehSpd_Kph_f32	36.0999985
k_LKAMinVehSpd_Kph_f32	33.25
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	28.7999992
k_ShiftLevRevTime_Sec_f32	0.0769999996
k_StandstillThresh_Kph_f32	0.75999999
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0769999996
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrgOvlSta Per1 PosTrajHwAngle HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-978.51001 4
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1 439.040009
target_TrqOvISta_Per1_CWPosition_HwDeg_t3z.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	439.040009
target_rrqOvlSta_Per1_ESCFauit_Crit_igc.value target_rrqOvlSta_Per1_ESCIsLimited_Crit_igc.value	1
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-10
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	118.400002
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	128.199997
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.5999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996	1713.95996 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	36400	36400 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002	1.73000002 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.114	0.114 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	12.7700005	12.7700005 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.18 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta APAState State M enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	14205.2402
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1801.80005
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13976.8203
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	38925
TrqOvlSta_Haptictime_Sec_M_f32	9588.80957
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	23.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.310000002
k_APAMaxHwTrq_HwNm_f32	1.60000002
k_APAMaxVehSpd_Kph_f32	36
k_ESCMaxVehSpd_Kph_f32	58.0999985
k_HapticAmplitude_MtrNm_f32	0.119999997
k_HapticDuration_Sec_f32	0.300000012
k_HapticFreq_Hz_f32	13.2840004
k_HapticReacttime_Sec_f32	0.319999993
k_LKAMaxVehSpd_Kph_f32	38
k_LKAMinVehSpd_Kph_f32	35
k_MaxHapticReqTime_Sec_f32	0.314999998
k_SWARLimiter_HwDeg_f32	30.2000008
k_ShiftLevRevTime_Sec_f32	0.0810000002
k_StandstillThresh_Kph_f32	0.800000012
k_StandstillTime_Sec_f32	0.0810000002
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgauge and the property of the prope$	target_TrqOvlSta_Per1_APANonRecoverable	Faults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	lts_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_102	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	e_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEn	able_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f3	32	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lgr target_TrqOvlSta_Per1_LKAState_State_enu		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MaxSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosServEnable Cnt Igc	target TrgOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H	- •	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers	e_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-968.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0 443.980011		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESGIsLimited_Cnt_lqc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	10		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value			
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1 1 123.300003		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1 1 123.300003 133.100006		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1 1 123.300003 133.100006 0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	1 1 123.300003 133.100006 0 0.100000001		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1 1 123.300003 133.100006 0 0.100000001 5.5		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1 1 123.300003 133.100006 0 0.100000001 5.5	Expected Value	Result
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1 1 123.300003 133.100006 0 0.100000001 5.5	Expected Value 4 4	Result
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value	4	Result
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4	4	~
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4	4 4 0 ± 1	<i>y</i>
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4 0 1801.80005	4 4 0±1 1801.80005±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4 4 0 1801.80005	4 4 0±1 1801.80005±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Lnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4 4 0 1801.80005 0 1 38925 9588.80957	4 4 0 ± 1 1801.80005 ± 1 0 1 38925 ± 1 9588.80957 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4 4 0 1801.80005 0 1 38925 9588.80957 2.33999991	4 4 0 ± 1 1801.80005 ± 1 0 1 38925 ± 1 9588.80957 ± 1 2.33999991 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Lnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedApAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4 4 0 1801.80005 0 1 38925 9588.80957 2.33999991 0	4 4 0 ± 1 1801.80005 ± 1 0 1 38925 ± 1 9588.80957 ± 1 2.33999991 ± 0.000009 0 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Lmt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAFault_Cnt_M_Igc	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4 4 0 1801.80005 0 1 38925 9588.80957 2.33999991 0 1	4 4 0±1 1801.80005±1 0 1 38925±1 9588.80957±1 2.33999991±0.000009 0±1 1±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Lmt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4 4 0 1801.80005 0 1 38925 9588.80957 2.33999991 0 1 1	4 4 0±1 1801.80005±1 0 1 38925±1 9588.80957±1 2.33999991±0.000009 0±1 1±1 1±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Lmt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAFault_Cnt_M_Igc	1 1 123.300003 133.100006 0 0.100000001 5.5 1 Actual Value 4 4 0 1801.80005 0 1 38925 9588.80957 2.33999991 0 1	4 4 0±1 1801.80005±1 0 1 38925±1 9588.80957±1 2.33999991±0.000009 0±1 1±1	· · · · · · · · · · · · · · · · · · ·

 $TrqOvlSta_ReadytoPulse_Cnt_M_lgc$

1 ± 1

TrqOvlSta_Per1





Name	Actual Value	Expected Value	Result
target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	1100 11 1 1 1 1 1 1 1 1
target TrqOvlSta Per1 APAState State enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	23.2000008	23.2000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.119999997	0.119999997 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	13.2840004	13.2840004 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Fest Step 2.19 (Repeat Count = 1)	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	0
rqOvlSta_ESCState_State_M_enum	1
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15094.9004
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1889.64001
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	14846.5596
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	41450
rqOvlSta_Haptictime_Sec_M_f32	10073.6396
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.95000005
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0482000001
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	4
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.319999993
APAMaxHwTrg HwNm f32	1.67999995
APAMaxVehSpd Kph f32	37.7999992
ESCMaxVehSpd Kph f32	60.5
LapticAmplitude MtrNm f32	0.126000002
_HapticDuration_Sec_f32	0.314999998
HapticFreq Hz f32	13.7980003
napticFreq_nz_isz	
	0.335999995
_LKAMaxVehSpd_Kph_f32	39.9000015
_LKAMinVehSpd_Kph_f32	36.75
_MaxHapticReqTime_Sec_f32	0.33000013
_SWARLimiter_HwDeg_f32	31.6000004
_ShiftLevRevTime_Sec_f32	0.0850000009
_StandstillThresh_Kph_f32	0.839999974
_StandstillTime_Sec_f32	0.0850000009
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
lirget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
riget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enur	
arget Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH APAMfgEnable Cnt Igc	
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 GMOSH ESCMfgEnable Cnt Igc	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_FeT1_GMOSH_ESCINIGENable_Cit_igc arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_igc	target TrqOvISta Per1 GMOSH LKAMfgEnable Cnt lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-958.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 448.920013		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_rer1_ESCFault_Crit_tgc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvISta_rer1_ESCISEIIIIIted_Off_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	0		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	128.199997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	138		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	7.4000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	41450	41450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	10073.6396	10073.6396 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.95000005	2.95000005 ± 0.000009	V
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	
TrqOvISta_LKAState_State_M_enum	0.109999999	4	*
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32		0.109999999 ± 0.0009765625	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1 0 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target_IrqoviSta_Peri_GMOSHOscillateState_State_enum.value target_TrqOviSta_Peri_LKAState_State_enum.value	4	4	-
target_rrqOvlSta_Per1_ENAState_State_enum.value target_rrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target_rrqOvlSta_Per1_PosServEnable_Cnt_igc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_igc.value	0.109999999	0.109999999 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.126000002	0.126000002 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	· ·
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	13.7980003	13.7980003 ± 0.0009765625	-
2. 2. 7. 4			

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~	
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V	



Test Step 2.20 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrgOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15984.5596
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1977.47998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	15716.2998
TrqOvlSta_HapticState_State_M_enum	0
TrqOvISta_Haptictime_See_M_f22	43975 10558.4697
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.55999994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0504000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvISta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.3999996
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	0.330000013 1.75999999
k_APAMaxVehSpd_Kph_f32	39.5999985
k_ESCMaxVehSpd_Kph_f32	62.9000015
k_HapticAmplitude_MtrNm_f32	0.131999999
k_HapticDuration_Sec_f32	0.330000013
k_HapticFreq_Hz_f32	14.3120003
k_HapticReacttime_Sec_f32	0.351999998
k_LKAMaxVehSpd_Kph_f32	41.7999992
k_LKAMinVehSpd_Kph_f32	38.5
k_MaxHapticReqTime_Sec_f32	0.344999999
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	33 0.0890000015
k StandstillThresh Kph f32	0.879999995
k StandstillTime Sec f32	0.0890000015
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_In$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_inst_Ap_irqovista.irqovista_Peri_SwaRirgiangRequest_Hwbeg_rstarget_Rte_inst_Ap_TrqoviSta.TrqoviSta_Peri_ShiftLeverIsInReverse_Cnt_lgc	target_IrqOvlSta_Per1_SwARTrgtAngRequest_Hwbeg_132 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-948.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	453.859985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.4000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	9.30000019		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	-
TrgOvlSta ESCState State M enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15984.5596	15984.5596 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	1977.47998	1977.47998 ± 1	•
TrgOvlSta ElapsedStandstillTime Sec M f32	15716.2998	15716.2998	-
TrgOvlSta HapticState State M enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	43975	43975 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.60233593	3.60233593 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.3999996	25.3999996 ± 0.0009765625	·
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	25.3999996	25.3999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.131999999	0.131999999 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value		0.4	-
target_TrqOvi3ta_Fet1_TrqOscEriable_Crit_igc.value	0	0 ± 1	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16874.2207
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	16586.0391
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	46500
TrqOvlSta_Haptictime_Sec_M_f32	11043.2998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	26.5

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.34000004
k_APAMaxHwTrq_HwNm_f32 k_APAMaxVehSpd_Kph_f32	1.84000003 41.4000015
k_ESCMaxVehSpd_Kph_f32	65.3000031
k_HapticAmplitude_MtrNm_f32	0.137999997
k_HapticDuration_Sec_f32	0.344999999
k_HapticFreq_Hz_f32	14.8260002
k_HapticReacttime_Sec_f32	0.368000001
k_LKAMaxVehSpd_Kph_f32	43.7000008
k_LKAMinVehSpd_Kph_f32	40.25
k_MaxHapticReqTime_Sec_f32	0.360000014
k_SWARLimiter_HwDeg_f32	34.4000015
k_ShiftLevRevTime_Sec_f32	0.0930000022
k_StandstillThresh_Kph_f32	0.920000017
k_StandstillTime_Sec_f32	0.0930000022
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvIsta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrqtAngRequest HwDeg_f3	
target_rite_inst_Ap_riqovista.riqovista_reri_swartrigiangrequest_riwbeg_is target Rte Inst Ap Trq0vlSta.Trq0vlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrgOvlSta Per1 TrgOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-938.109985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	458.799988
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOvlSta_Per1_ESCRequest_Crit_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Fe1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.4000001
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	138
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007	2065.32007 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	46500	46500 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008	4.17000008 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.137999997	0.137999997 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	14.8260002	14.8260002 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	17763.8809
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2153.15991
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	17455.7793
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	49025
TrqOvlSta_Haptictime_Sec_M_f32	11528.1299
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.78000021
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.349999994
k_APAMaxHwTrq_HwNm_f32	1.91999996
k_APAMaxVehSpd_Kph_f32	43.2000008
k_ESCMaxVehSpd_Kph_f32	67.6999969
k_HapticAmplitude_MtrNm_f32	0.143999994
k_HapticDuration_Sec_f32	0.360000014
k_HapticFreq_Hz_f32	15.3400002
k_HapticReacttime_Sec_f32	0.384000003
k_LKAMaxVehSpd_Kph_f32	45.5999985
k_LKAMinVehSpd_Kph_f32	42
k_MaxHapticReqTime_Sec_f32	0.375
k_SWARLimiter_HwDeg_f32	35.7999992
k_ShiftLevRevTime_Sec_f32	0.0970000029
k_StandstillThresh_Kph_f32	0.959999979
k_StandstillTime_Sec_f32	0.0970000029
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H	- ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvlSta Per1 PosTrajEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lg</u> c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	463.73999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	142.899994		
target TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	152.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target TrgOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	- Troount
TrqOvISta_ESCState_State_M_enum	4	4	·
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	17763.8809	17763.8809 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	-
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvISta_HapticState_State_M_enum	0	0	·
TrqOvISta_Hapticdur_Cnt_M_u16	49025	49025 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	11528.1299	11528.1299 ± 1	·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.38105631	4.38105583 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004	27.6000004 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
			-

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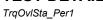
Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	27.6000004	27.6000004 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.143999994	0.143999994 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.3400002	15.3400002 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Name	Input Value
	•
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18653.5391
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2241
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	18325.5195
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	51550
rqOvlSta_Haptictime_Sec_M_f32	12012.96
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	1
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.360000014
APAMaxHwTrg HwNm f32	2
_APAMaxVehSpd_Kph_f32	45
ESCMaxVehSpd Kph f32	70.0999985
_HapticAmplitude_MtrNm_f32	0.150000006
_HapticDuration_Sec_f32	0.375
_HapticFreq_Hz_f32	15.8540001
HapticReacttime Sec f32	0.40000006
- ·	47.5
_LKAMaxVehSpd_Kph_f32	
_LKAMinVehSpd_Kph_f32	43.75
_MaxHapticReqTime_Sec_f32	0.389999986
_SWARLimiter_HwDeg_f32	37.2000008
_ShiftLevRevTime_Sec_f32	0.101000004
_StandstillThresh_Kph_f32	1
_StandstillTime_Sec_f32	0.101000004
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetecte	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_state_enum
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_C	
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_C	
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Ci	
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 HapticRequest Cnt Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HapticRequest_Ont_igc	target_TrqOvlSta_Per1_HapticRequest_Ont_gc
1901_110_1101_74_114Oviota.114Oviota_rei1_11w1014ue_11w14III_132	wast-1140410ta_1 til_11W1014ut_11W14III_102

@ Report created by TESSY V3.1.9, report template V2.1

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lg</u> c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-917.909973		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	468.679993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	1		
0 = 1 = = = = = = = = = = = = = = = = =	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	51550	51550 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987	5.38999987 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	_
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	V
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	- 4
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	- 4
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006 0.150000006	0.150000006 ± 0.0009765625 0.150000006 ± 0.0009765625	V
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	V
target_TIQOVISta_F ETT_TIQOSCETTADIE_CITL_IGG.Valide		1 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.8540001	15.8540001 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Fest Step 2.24 (Repeat Count = 1)	Innut Value
lame	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum rqOvlSta_ESCState_State_M_enum	1
rqOvISta_ESCState_state_M_entitif	19543.1992
rqOvISta_ElapsedAhiftLevRevTime_Sec_IM_f32	2328.84009
rqOvISta_ElapsedStandstillTime_Sec_M_f32	19195.2598
rqOvlSta_HapticState_State_M_enum	2
rqOvISta_Hapticdur_Cnt_M_u16	54075
rqOvISta_Haptictime_Sec_M_f32	12497.79
rgOvISta HwTorqueSV HwNm M Str.SV UIs f32	6
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.370000005
APAMaxHwTrq_HwNm_f32	2.07999992
APAMaxVehSpd_Kph_f32	46.7999992
ESCMaxVehSpd_Kph_f32	72.5
HapticAmplitude_MtrNm_f32	0.156000003
HapticDuration_Sec_f32	0.389999986
HapticFreq_Hz_f32	16.368
HapticReacttime_Sec_f32	0.416000009
_LKAMaxVehSpd_Kph_f32	49.4000015
_LKAMinVehSpd_Kph_f32	45.5
_MaxHapticReqTime_Sec_f32	0.405000001
_SWARLimiter_HwDeg_f32	38.5999985
_ShiftLevRevTime_Sec_f32	0.104999997
_StandstillThresh_Kph_f32	1.03999996
_StandstillTime_Sec_f32	0.104999997
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lo	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
${\tt arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc}$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
${\tt rrget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc}$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
rrget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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TrqOviSta_Peri		1	MACILAID
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-907.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	473.619995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	152.699997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2328.84009	2328.84009 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	54075	54075 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6	6 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvISta_LKAState_State_M_enum	=		
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	4	4	~
			•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	4	4	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	4 29.7999992	4 29.7999992 ± 0.0009765625	~
	4 29.7999992 1	4 29.7999992 ± 0.0009765625 1 ± 1	•
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	4 29.7999992 1 0 4	4 29.7999992 ± 0.0009765625 1 ± 1 0 ± 1 4	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	4 29.7999992 1 0 4 4	4 29.7999992 ± 0.0009765625 1 ± 1 0 ± 1 4 4	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	4 29.7999992 1 0 4	4 29.7999992 ± 0.0009765625 1 ± 1 0 ± 1 4	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	4 29.7999992 1 0 4 4 2 4 0	4 29.7999992 ± 0.0009765625 1 ± 1 0 ± 1 4 4	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	4 29.7999992 1 0 4 4 2	4 29.7999992 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	4 29.7999992 1 0 4 4 2 4 0 29.7999992 0.156000003	4 29.7999992 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2 4 0 ± 1 29.7999992 ± 0.0009765625 0.156000003 ± 0.0009765625	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	4 29.7999992 1 0 4 4 2 4 0 29.7999992	4 29.7999992 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2 4 0 ± 1 29.7999992 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.25 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	20432.8594
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2416.67993
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	20065
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	56600
TrqOvlSta_Haptictime_Sec_M_f32	12982.6201
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.61000013
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0614
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	30.8999996

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.37999995
k_APAMaxHwTrq_HwNm_f32	2.16000009
k_APAMaxVehSpd_Kph_f32	48.5999985
k_ESCMaxVehSpd_Kph_f32	74.9000015
k HapticAmplitude MtrNm f32	0.162
k_HapticDuration_Sec_f32	0.405000001
k_HapticFreq_Hz_f32	16.882
k_HapticReacttime_Sec_f32	0.432000011
k_LKAMaxVehSpd_Kph_f32	51.2999992
k LKAMinVehSpd Kph f32	47.25
k_MaxHapticReqTime_Sec_f32	0.419999987
k SWARLimiter HwDeg f32	40
-	
k_ShiftLevRevTime_Sec_f32	0.108999997
k_StandstillThresh_Kph_f32	1.08000004 0.108999997
k_StandstillTime_Sec_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_state_enumerators and the property of the pr$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ShiftLeverIsInReverse Cnt Igc	
	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-897.710022
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	478.559998
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.8999998
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlota_rer1_Ervarvequest_Ont_ge.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.399994
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
g.sqo.nota_r or i_r oo raji iiw iigio_r iibog_loz.valido	

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	20432.8594	20432.8594 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	20065	20065	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	56600	56600 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	12982.6201	12982.6201 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.08748627	6.08748579 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.162	0.162 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	16.882	16.882 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.26 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21322.5195
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2504.52002
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	20934.7402
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	59125
TrqOvlSta_Haptictime_Sec_M_f32	13467.4502
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.21999979
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0636000037
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	32
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.389999986
k_APAMaxHwTrq_HwNm_f32	2.24000001
k_APAMaxVehSpd_Kph_f32	50.4000015
k_ESCMaxVehSpd_Kph_f32	77.3000031
k_HapticAmplitude_MtrNm_f32	0.167999998
k_HapticDuration_Sec_f32	0.419999987
k_HapticFreq_Hz_f32	17.3959999
k_HapticReacttime_Sec_f32	0.448000014
k_LKAMaxVehSpd_Kph_f32	53.2000008
k_LKAMinVehSpd_Kph_f32	49
k_MaxHapticReqTime_Sec_f32	0.435000002
k_SWARLimiter_HwDeg_f32	41.4000015
k_ShiftLevRevTime_Sec_f32	0.112999998
k_StandstillThresh_Kph_f32	1.12
k_StandstillTime_Sec_f32	0.112999998
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ılts_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
	target_TrqOvlSta_Per1_APAState_State_enu		
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_	f32	
	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_132	
	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lg	nc.	
	target TrqOvISta Per1 ESCRequest Cnt Ig		
	target_TrqOvISta_Per1_ESCState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateStar	te_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
	target_TrqOvISta_Per1_LKAFault_Cnt_lgc		
*	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	2	
	target_TrqOvlSta_Per1_LKARequest_Cnt_lgr target_TrqOvlSta_Per1_LKAState_State_enu		
·	target_TrqOvISta_Per1_MaxSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
	target TrqOvISta Per1 PosServEnable Cnt		
0 = = = 1 = 1 = = = = = = = = = = = = =	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw	vDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvISta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	$target_TrqOvlSta_Per1_ShiftLeverlsInRevers$	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_l	lgc	
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
	1		
0 = 1 = = = = = = = = = = = = = = = = =	1		
· - · · ·	0		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-887.609985		
	0		
	483.5		
	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
312 (1 112 1 21 11 2 11 3 11 2 2 1 1 1	0		
	0		
312 (1 112 1 2 1)11 1 (1 1 2 1 2 3 1 1 1 1	0		
	-1.70000005		
<u> </u>	1		
0 = 1 = = = = = = = = = = = = = = = = =	0		
target_TrqOvlSta_Per1_EnArkequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5		
	172.300003		
	0		
	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
	Actual Value	Expected Value	Result
	4	4	V
	4	4	~
	0	0 ± 1	✓
	0	0 ± 1 0	~
1	2	2	V
	59125	59125 ± 1	
	0	0 ± 1	~
		7.21999979 ± 0.000009	~
	7.21999979		✓
	7.21999979	1 ± 1	_
		1 ± 1 0 ± 1	-
TrqOvISta_LKAPermFault_Cnt_M_lgc	1		
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	1 0	0 ± 1	\ \ \
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	1 0 2	0 ± 1 2 ± 1	✓

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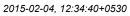
TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
	Actual Value	•	resuit
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	32	32 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.167999998	0.167999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	17.3959999	17.3959999 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.27 (Repeat Count = 1)	and the state of the
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrgOvISta ESCState State M enum	4
TrgOvISta ElapsedAPAMaxHwTrgTime Sec M f32	22212.1797
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2592.36011
TrgOvlSta ElapsedStandstillTime Sec M f32	21804.4805
TrqOvlSta_HapticState_State_M_enum	1
TrgOvISta Hapticdur Cnt M u16	61650
TrqOvlSta_Haptictime_Sec_M_f32	13952.2803
TrgOvISta HwTorqueSV HwNm M Str.SV UIs f32	7.82999992
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0658000037
TrgOvISta LKAFault Cnt M Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOviSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_ErvGstate_Gtate_M_endfil TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	33.099985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.400000006
k APAMaxHwTrg HwNm f32	2.31999993
k APAMaxVehSpd Kph f32	52.2000008
k ESCMaxVehSpd Kph f32	79.6999969
k_HapticAmplitude_MtrNm_f32	0.173999995
k_HapticDuration_Sec_f32	0.435000002
k_HapticFreq_Hz_f32	17.9099998
k_HapticReacttime_Sec_f32	0.463999987
k LKAMaxVehSpd Kph f32	55.099985
k_LKAMinVehSpd_Kph_f32	50.75
k_MaxHapticReqTime_Sec_f32	0.449999988
k SWARLimiter HwDeg f32	42.7999992
k_ShiftLevRevTime_Sec_f32	0.116999999
k_StandstillThresh_Kph_f32	1.15999997
k_StandstillTime_Sec_f32	0.116999999
k_Standstill rime_sec_is2 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_I	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARecoverabler autis_Ont_igc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_PerT_CCWPosition_Hwbeg_is2 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_PerT_CWFound_Cnt_Igc	target TrqOvISta Per1 CWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_PerT_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_PerT_CwPosition_Hwbeg_is2 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_PerT_ESCFault_Cnt_lgc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_PerT_ESCPault_Cnt_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_PerT_ESCIsLimited_Cnt_igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
	target TrqOvISta Per1 ESCRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_PerT_ESCState_State_enum target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enu	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	
aiyelinle iiislap iiyovisia.iiyovisia Peli Hadiickeddesi Oil ICC	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_6		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_0 target_TrqOvlSta_Per1_PosSrvoHwAngle		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_C		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvISta_Per1_PosTrajHwAngle_		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 SWARTrgtAngRequest HwDeg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrN		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-877.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	488.440002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target TrqOvlSta_Per1_HwTorque HwNm f32.value	-1.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	167.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	177.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2592.36011	2592.36011 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	61650	61650 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	13952.2803	13952.2803 ± 1	Y
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.82999992	7.82999992 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	V
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	✓
TrqOvISta_LKAPermFault_Cnt_M_u08 TrgOvISta_LKAState_State_M_enum	4	4 ± 1	~
TrqOvISta_LKAState_State_M_enum TrqOvISta PosSrvoHwAngle HwDeg M f32	3 0.189999998	3 0.189999998 ± 0.0009765625	~
TrqOvISta ReadytoPulse Cnt M Igc	0.169999990	0.169999996 ± 0.0009765625	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	· ·
target TrqOvlSta Per1 APAState State enum.value	3	3	
target_TrqOvISta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvISta_Per1_ESCState_State_enum.value	1	1	
target_TrqOviSta_Per1_LKAState_State_enum.value	3	3	•
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.173999995	0.173999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	17.9099998	17.9099998 ± 0.0009765625	~
		-	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Fest Step 2.28 (Repeat Count = 1)	Input Value
	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
	4
	0
, _ , _ , ,	23101.8398
	2680.19995
. = . = = =	22674.2207
1	0
rqOvlSta_Hapticdur_Cnt_M_u16	64175
rqOvlSta_Haptictime_Sec_M_f32	14437.1104
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.43999958
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0680000037
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	1
rqOvlSta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
	0.40999996
·	2.4000001
	54
	82.0999985
_ , _ , _	0.180000007
	0.449999988
	18.4239998
- · · ·	0.47999989
	57
- '-'-	
	52.5
	0.465000004
	44.200008
	0.120999999
	1.20000005
	0.120999999
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_enum
	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target TrqOvISta_Per1_GWoSt1_ERAWIgEtrable_CIT_gc
0 = = = 1	target_rrqOvlSta_Per1_HapticRequest_Ont_lgc
	· - ·
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
	tanget_indexicat_i eni_enn anneatangnedaest_imbeg_iez
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-867.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	493.380005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	172.300003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	182.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	23101.8398	23101.8398 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	64175	64175 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.77767944	7.77767992 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008	34.2000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	34.2000008	34.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	18.4239998	18.4239998 ± 0.0009765625	~
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Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.29 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	23991.5
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2768.04004
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	23543.9609
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	2450
TrqOvlSta_Haptictime_Sec_M_f32	14921.9404
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0702000037
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	35.2999992

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TrqOvlSta_Per1

Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.41999987
k_APAMaxHwTrq_HwNm_f32	2.48000002
k_APAMaxVehSpd_Kph_f32	55.7999992
k_ESCMaxVehSpd_Kph_f32	84.5
k_HapticDuration_Sec_f32	0.186000004 0.465000004
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	18.9379997
k HapticReacttime Sec f32	0.495999992
k_LKAMaxVehSpd_Kph_f32	58.9000015
k LKAMinVehSpd Kph f32	54.25
k MaxHapticRegTime Sec f32	0.479999989
k_SWARLimiter_HwDeg_f32	45.5999985
k_ShiftLevRevTime_Sec_f32	0.125
k_StandstillThresh_Kph_f32	1.24000001
k_StandstillTime_Sec_f32	0.125
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrv$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enur target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviota_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-857.309998
target_IrqOvISta_Per1_CCWPosition_Hwbeg_t3z.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value	-857.309998 1
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	498.320007
target_TrqOvIsta_ren_Gvvrosition_nwbeg_isz.value	1
target_rrqOvIsta_rerr_ESCrauit_Crit_igc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.10000002
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	177.199997
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	187
target TraCulSta Bort BooTraiEnable Cat laggiculus	1
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	'

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2768.04004	2768.04004 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	2450	2450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019	9.05000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.186000004	0.186000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	18.9379997	18.9379997 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	24881.1602
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2855.87988
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	24413.6992
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	8460
TrqOvlSta_Haptictime_Sec_M_f32	15406.7695
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.65999985
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0724000037
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	36.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.430000007
k_APAMaxHwTrq_HwNm_f32	2.55999994
k_APAMaxVehSpd_Kph_f32	57.5999985
k_ESCMaxVehSpd_Kph_f32	86.9000015
k_HapticAmplitude_MtrNm_f32	0.192000002
k_HapticDuration_Sec_f32	0.479999989
k_HapticFreq_Hz_f32	19.4519997
k_HapticReacttime_Sec_f32	0.512000024
k_LKAMaxVehSpd_Kph_f32	60.7999992
k_LKAMinVehSpd_Kph_f32	56
k_MaxHapticReqTime_Sec_f32	0.495000005
k_SWARLimiter_HwDeg_f32	47
k_ShiftLevRevTime_Sec_f32	0.128999993
k_StandstillThresh_Kph_f32	1.27999997
k_StandstillTime_Sec_f32	0.128999993
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	f22	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt I	ac	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lo		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvISta Per1 MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-847.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	503.26001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.899999976		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target TrqOvISta Per1 LKARequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	191.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	28.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	V
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	24413.6992	0 ± 1 24413.6992	
TrqOvISta_ElapsedStafidstill*Iffle_Sec_Ivi_132 TrqOvISta_HapticState_State_M_enum	2	2	-
TrgOvlSta Hapticdur Cnt M u16	8460	8460 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	15406.7695	15406.7695 ± 1	*
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.65999985	9.65999985 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	36.4000015	36.4000015 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	

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TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
	o Actual Value	0 ± 1	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	UII	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	36.4000015	36.4000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.192000002	0.192000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	19.4519997	19.4519997 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Namo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	25770.8203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2943.71997
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	25283.4395
TrqOvlSta_HapticState_State_M_enum	0
FrqOvlSta_Hapticdur_Cnt_M_u16	14470
FrqOvISta_Haptictime_Sec_M_f32	15891.5996
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.97999954
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0746000037
FrqOvlSta_LKAFault_Cnt_M_lgc	1
FrqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	37.5
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime_Sec_f32	0.43999998
APAMaxHwTrq HwNm f32	2.6400001
CAPAMaxVehSpd_Kph_f32	59.4000015
CSCMaxVehSpd Kph f32	89.3000031
	0.197999999
_HapticAmplitude_MtrNm_f32	
<pre>(_HapticDuration_Sec_f32</pre>	0.49500005
X_HapticFreq_Hz_f32	19.9659996
x_HapticReacttime_Sec_f32	0.527999997
<pre>C_KAMaxVehSpd_Kph_f32</pre>	62.7000008
C_KAMinVehSpd_Kph_f32	57.75
x_MaxHapticReqTime_Sec_f32	0.50999999
C_SWARLimiter_HwDeg_f32	48.4000015
s_ShiftLevRevTime_Sec_f32	0.133000001
_StandstillThresh_Kph_f32	1.32000005
_StandstillTime_Sec_f32	0.133000001
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Ci	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrgOvlSta Per1 ESCRequest Cnt lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESGState_State_enum arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_en	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igr	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lge	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSt	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_I	gc	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	1		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-837.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	508.200012		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.69999988		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	187		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	196.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	
			V
TrqOvlSta_ESCState_State_M_enum	3	3	V
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3 25770.8203	3 25770.8203 ± 1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3 25770.8203 0	3 25770.8203 ± 1 0 ± 1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3 25770.8203 0	3 25770.8203 ± 1 0 ± 1 0	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	3 25770.8203 0 0	3 25770.8203 ± 1 0 ± 1 0	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	3 25770.8203 0 0 0 14470	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1	\rightarrow \right
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	3 25770.8203 0 0 0 14470 15891.5996	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3 25770.8203 0 0 0 14470 15891.5996 9.18327141	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009	\rightarrow \right
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	3 25770.8203 0 0 0 14470 15891.5996 9.18327141	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1	\rightarrow \right
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.1299999995 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.129999995 ± 0.0009765625 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995 0	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.129999995 ± 0.0009765625 0 ± 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995 0	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.1299999995 ± 0.0009765625 0 ± 1 1 ± 1 0	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995 0 1	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.1299999995 ± 0.0009765625 0 ± 1 1 ± 1 0 3	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995 0 1	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.1299999995 ± 0.0009765625 0 ± 1 1 ± 1 0 3 0	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995 0 1 0 3	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.129999995 ± 0.0009765625 0 ± 1 1 ± 1 0 3 0 0 1 ± 1 0.129999995 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAPault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_BSCState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_HKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995 0 1 1 0 3 0 0 1.29999995 0 1	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.129999995 ± 0.0009765625 0 ± 1 1 ± 1 0 3 0 0 1 ± 1 0.129999995 ± 0.0009765625 0.129999999 ± 0.0009765625 0.1979999999 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3 25770.8203 0 0 0 14470 15891.5996 9.18327141 1 0 3 0 0.129999995 0 1 0 3 0 0	3 25770.8203 ± 1 0 ± 1 0 0 14470 ± 1 15891.5996 ± 1 9.18327236 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0.129999995 ± 0.0009765625 0 ± 1 1 ± 1 0 3 0 0 1 ± 1 0.129999995 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

TrqOvlSta_Per1



Test Step 2.32 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrgOvISta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	26660.4805
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3031.56006
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	26153.1797
TrqOvlSta_HapticState_State_M_enum	2
TrqOvISta_Haptictime_See_M_f22	20480 16376.4297
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0768000036
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvISta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	0.449999988 2.72000003
k_APAMaxVehSpd_Kph_f32	61.2000008
k_ESCMaxVehSpd_Kph_f32	91.6999969
k_HapticAmplitude_MtrNm_f32	0.203999996
k_HapticDuration_Sec_f32	0.50999999
k_HapticFreq_Hz_f32	20.4799995
k_HapticReacttime_Sec_f32	0.54400003
k_LKAMaxVehSpd_Kph_f32	64.5999985
k_LKAMinVehSpd_Kph_f32	59.5
k_MaxHapticReqTime_Sec_f32	0.524999976
k_SWARLimiter_HwDeg_f32 k ShiftLevRevTime Sec f32	49.7999992 0.136999995
k StandstillThresh Kph f32	1.36000001
k StandstillTime Sec f32	0.136999995
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWPosition HwDeg f32	target TrqOvISta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_Inst_Ap_TrqOvlSta_Rte_In$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_inst_Ap_irqovista.irqovista_Peri_SwaRirgiangRequest_Hwbeg_rstarget_Rte_inst_Ap_TrqoviSta.TrqoviSta_Peri_ShiftLeverIsInReverse_Cnt_lgc	target_IrqOvlSta_Per1_SwARTrgtAngRequest_Hwbeg_132 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-827.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	513.140015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	191.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	201.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	32.0999985		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3031.56006	3031.56006 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	20480	20480 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9	-9 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.5999985	38.5999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	38.5999985	38.5999985 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.203999996	0.203999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	20.4799995	20.4799995 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.33 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	27550.1406
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3119.3999
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	27022.9199
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	26490
TrqOvlSta_Haptictime_Sec_M_f32	16861.2598
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0790000036
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	39.7000008

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.460000008
k_APAMaxHwTrq_HwNm_f32	2.7999995
k_APAMaxVehSpd_Kph_f32	63
k_ESCMaxVehSpd_Kph_f32	94.0999985
k_HapticAmplitude_MtrNm_f32	0.20999993
k_HapticDuration_Sec_f32	0.524999976
k_HapticFreq_Hz_f32	20.9939995
k_HapticReacttime_Sec_f32	0.560000002
k_LKAMinVohSpd_Kph_f32	66.5 61.25
k_LKAMinVehSpd_Kph_f32 k MaxHapticReqTime Sec f32	0.540000021
k SWARLimiter HwDeg f32	51.2000008
k ShiftLevRevTime Sec f32	0.141000003
k StandstillThresh Kph f32	1.3999998
k_StandstillTime_Sec_f32	0.141000003
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target TrqOvISta Per1 ESCFault Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target TrqOvlSta Per1 TrqOscEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Crit_igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-816.909973
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	518.080017
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.300000012
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	196.800003
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	206.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1 0.150000006
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.130000000

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3119.3999	3119.3999 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	26490	26490 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	16861.2598	16861.2598 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.5	0.5 ± 0.0000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.209999993	0.209999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	20.9939995	20.9939995 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrgOviSta	target Rte Inst Ap TrgOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrgOvlSta ESCState State M enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	28439.8008
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3207.23999
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	27892.6602
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	32500
TrqOvlSta_Haptictime_Sec_M_f32	17346.0898
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0812000036
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	40.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.469999999
k_APAMaxHwTrq_HwNm_f32	2.88000011
k_APAMaxVehSpd_Kph_f32	64.8000031
k_ESCMaxVehSpd_Kph_f32	96.5
k_HapticAmplitude_MtrNm_f32	0.216000006
k_HapticDuration_Sec_f32	0.540000021
k_HapticFreq_Hz_f32	21.5079994
k_HapticReacttime_Sec_f32	0.575999975
k_LKAMaxVehSpd_Kph_f32	68.4000015
k_LKAMinVehSpd_Kph_f32	63
k_MaxHapticReqTime_Sec_f32	0.555000007
k_SWARLimiter_HwDeg_f32	52.5999985
k_ShiftLevRevTime_Sec_f32	0.144999996
k_StandstillThresh_Kph_f32	1.44000006
k_StandstillTime_Sec_f32	0.144999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	9_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	f22	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_le	ne.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun	target TrqOvlSta Per1 GMOSHOscillateSta		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	- ' -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvISta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvISta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target TrgOvlSta Per1 TrgOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-806.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	523.02002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	-0.100000001		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value	0		
target TrgOvISta_Per1_LKARIIIIIIIL_CITL_Igc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	201.699997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	211.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-33.0999985		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvISta_Hapticdur_Cnt_M_u16	32500	32500 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	17346.0898	17346.0898 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015	6.34000015 ± 0.000009	•
T 0 101 11/15 11 0 1 M 1		4 . 4	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1±1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 0 2	0 ± 1 2 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 0	0 ± 1	•

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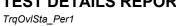


Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	40.7999992	40.7999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.216000006	0.216000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	21.5079994	21.5079994 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Fest Step 2.35 (Repeat Count = 1)	Input Value
	· ·
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvISta_APAState_State_M_enum	0
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	29329.4609
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3295.08008
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	28762.4004
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	38510
rqOvISta_Haptictime_Sec_M_f32	0
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.86000013
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0834000036
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	4
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41.9000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.479999989
_APAMaxHwTrq_HwNm_f32	2.96000004
_APAMaxVehSpd_Kph_f32	66.5999985
_ESCMaxVehSpd_Kph_f32	98.9000015
_HapticAmplitude_MtrNm_f32	0.222000003
_HapticDuration_Sec_f32	0.555000007
_HapticFreq_Hz_f32	22.0219994
_HapticReacttime_Sec_f32	0.592000008
_LKAMaxVehSpd_Kph_f32	70.3000031
_LKAMinVehSpd_Kph_f32	64.75
_MaxHapticReqTime_Sec_f32	0.569999993
_SWARLimiter_HwDeg_f32	54
_ShiftLevRevTime_Sec_f32	0.149000004
_StandstillThresh_Kph_f32	1.48000002
_StandstillTime_Sec_f32	0.149000004
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-796.710022		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	527.960022		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta_rer1_ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_FeT_ESCRequest_Cnt_lgc.value	1		
target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	0.100000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	216.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-31.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3295.08008	3295.08008 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	28762.4004	28762.4004	
TrqOvlSta_HapticState_State_M_enum	1	1	V
TrqOvlSta_Hapticdur_Cnt_M_u16	38510	38510 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.86000013	-5.86000013 ± 0.000009	✓ ✓
TrqOvISta_LKAFault_Cnt_M_lgc	1	1±1 1±1	
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	3	3±1	-
TrgOvISta_LKAState State M enum	4	4	
TrqOvlSta_ExAstate_state_injeriorii TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	-
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	-
target TrqOvISta Per1 ESCState State enum.value	3	3	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	V
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.222000003	0.222000003 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	22.0219994	22.0219994 ± 0.0009765625	~

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓



Fest Step 2.36 (Repeat Count = 1)	Innut Value
	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
	4
	3
	30219.1191
	3382.91992
	29632.1406
	2 44520
	4.28999987e+009
	0.649999976
	0.0526000001
	0
·	1
	4
	0
	43
0 _ 0	1
4	0
1 = =0	0.49000001
·	3.03999996
	68.4000015
1 - 1 -	101.300003
_	0.228
	0.569999993
	22.5359993
·	0.60799998
	72.1999969
	66.5
	0.584999979
	55.4000015
	0.152999997
	1.51999998
- · -	0.152999997
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
irget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 lrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 lrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc lrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 lrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc lrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiffLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 1

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TrqOvISta_APAState_State_M_enum	TrqOviSta_Peri		(G	201600
Image_Tropoxists_Pert_COMPosation_HarDeg_TS2 value 766.000085	Name	Input Value		
Integro TroOviSta Pert Division HyDeg SZ-value	target TrqOvlSta Per1 CCWPosition HwDeg f32.value	The second secon		
target_TroCviSta_Pert_ESCReut_Cnt_lgc.value orget_TroCviSta_Pert_ESCReutest_Cnt_lgc.value orget_TroCviSta_Pert_ESCReutest_Cnt_lgc.value orget_TroCviSta_Pert_ESCReutest_Cnt_lgc.value orget_TroCviSta_Pert_ESCReutest_Cnt_lgc.value orget_TroCviSta_Pert_ESCReutest_Cnt_lgc.value orget_TroCviSta_Pert_ESCReutest_Cnt_lgc.value orget_TroCviSta_Pert_ESCReutest_Cnt_lgc.value orget_TroCviSta_Pert_ESCReutest_Cnt_lgc.value orget_TroCviSta_Pert_Indocst_ESCRESCRESCRESCRESCRESCRESCRESCRESCRESCR	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
Image_TropOrSta_Pert_ESCRequest_Cnt_lgx_value		532.900024		
target_TrqOvSta_Pert_ESCRequest_Ort_lg-cvalue target_TrqOvSta_Pert_GMOSH_APAMfgEnable_Ort.lg-cvalue target_TrqOvSta_Pert_GMOSH_ESCMfgEnable_Ort.lg-cvalue 0 target_TrqOvSta_Pert_GMOSH_ESCMfgEnable_Ort.lg-cvalue 0 target_TrqOvSta_Pert_MorSta_Pert_Morst_EscMfgEnable_Ort.lg-cvalue 0 target_TrqOvSta_Pert_Hortorue_Hown_Fig2-value 0 target_TrqOvSta_Pert_Morst_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_Hortorue_	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
large_TrqOvSta_Pert_GMOSH_APAM(Enable_Cnt_Igo-value 0	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
larget_TrqOvSta_Pert_GMOSH_ESCME_penable_Cnt_lgc_value	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOviSta_Pert_HarpticRequest_Cnt_Igc.value	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
larget_TrQOVISta_Pert_LKAFaut_Cnt_(gc_value 1 1 1 1 1 1 1 1 1	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvISta_Pert_LKAFault_Cnt_lgc_value 1 target_TrqOvISta_Pert_LKAFault_Cnt_lgc_value 1 target_TrqOvISta_Pert_LKAFault_Cnt_lgc_value 1 target_TrqOvISta_Pert_LKAFault_Cnt_lgc_value 2 target_TrqOvISta_Pert_LKAFault_Cnt_lgc_value 2 target_TrqOvISta_Pert_LMArsecureVehicleSpeed_Kph_132 value 2 target_TrqOvISta_Pert_PostTajehable_Cnt_lgc_value 0 target_TrqOvISta_Pert_PostTajehable_Cnt_lgc_value 0 target_TrqOvISta_Pert_PostTajehable_Cnt_lgc_value 0 target_TrqOvISta_Pert_Shrift_evertsinfeverse_Cnt_lgc_value 1 target_TrqOvISta_Pert_ANAState_State_Menum 2 target_TrqOvISta_Pert_ANAState_State_Menum 2 target_TrqOvISta_Pert_ANAState_State_enum_value 1 target_TrqOvISta_Pert_ANAState_State_enum_value 1 target_TrqOvISta_Pert_ANAState_State_enum_value 1 target_TrqOvISta_Pert_ANAState_State_enum_value 1 target_TrqOvISta_Pert_PosStrvehwAngle_HwDeg_132 value 1 target_TrqOvISta_Pert_PosStrvehwAngle_HwDeg_132 value 1 target_TrqOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_IndOvISta_Pert_	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvISta_Per1_LKARnequest_Cnt_lgc-value 1 target_TrqOvISta_Per1_LKARnequest_Cnt_lgc-value 0 target_TrqOvISta_Per1_MASecureVehicleSpeed_Kph_f32_value 211.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32_value 221.300003 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc-value 0 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc-value 0 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc-value 0 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc-value 0 target_TrqOvISta_Per1_SWARTrgIdva_Request_HwDeg_f32_value 29.8999996 target_TrqOvISta_Per1_SWIRETrgIdva_Request_HwDeg_f32_value 29.8999996 target_TrqOvISta_Per1_SWIRETrgIdva_Request_HwDeg_f32_value 4 4 4 4 4 4 4 4 4	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.300000012		
target_TrqOvIsta_PerrI_KARequest_Cnt_lgc.value	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvISta_Perf_MaxSecureVehicleSpeed_Kph_f32 value 221.5 target_TrqOvISta_Perf_Destrajhabe_Cnt_Lgovalue 0 target_TrqOvISta_Perf_Destrajhabe_Cnt_Lgovalue 0.180000007 target_TrqOvISta_Perf_StrifteVersInReverse_Cnt_Igovalue 29.98999996 target_TrqOvISta_Perf_ShiftLeversinReverse_Cnt_Igovalue 1 Name Actual Value Expected Value Result TrqOvISta_PAState_State_M_enum 4 4 4 4 4 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32_value 221.300003 target_TrqOvISta_Per1_PostrajEnable_Cnt_Igc value 0 target_TrqOvISta_Per1_PostrajEnable_Cnt_Igc value 0.180000007 target_TrqOvISta_Per1_SimitLeverIsinReverse_Cnt_Igc.value 29.8999996 target_TrqOvISta_Per1_SimitLeverIsinReverse_Cnt_Igc.value 1 Name Actual Value Expected Value Result TrqOvISta_APAState_State_Menum 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 <t< td=""><td>target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value</td><td>0</td><td></td><td></td></t<>	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc.value 0.180000007	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	211.5		
target_TrqOvISta_Pert_SvARTrgIAngRequest_HvDeg_f32.value 0.180000007 target_TrqOvISta_Pert_SWARTrgIAngRequest_HvDeg_f32.value 2.9.8999996 target_TrqOvISta_Pert_ShiffLeversinReverse_Cnt_Igc_value 1 Name Actual Value Expected Value Result TrqOvISta_APAState_State_M_enum 4 4 4 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 1 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 3882.91992 3882.91992 ± 1 2 TrqOvISta_HapticState_State_M_enum 2 2 2 2 7 2 2 2 2 2 7 1 1 1 1 1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	221.300003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 29.8999996 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 1 Name Actual Value Expected Value Result TrqOvlSta_APAState_State_M_enum 4 4 4 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 7 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 0 TrqOvlSta_Bapticstare_State_M_enum 2 3382.91992 3382.91992 ± 1 TrqOvlSta_Hapticstime_Sec_M_f32 0 0 0 TrqOvlSta_Hapticstime_Sec_M_f32 42899987e+009 42899987e+009 ± 42899997e+009 ± 1 TrqOvlSta_Hapticstime_Sec_M_f32 428999987e+009 42899997e+009 ± 42899997e+0	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
Targot TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
Name Actual Value Expected Value Result TrqOvISta_APAState_State_Menum 4 4 4 TrqOvISta_ElapsedSpate_Menum 4 4 4 TrqOvISta_ElapsedSpathMtLevRevTime_Sec_M_f32 0 0±1 9 TrqOvISta_ElapsedSpathIttlevRevTime_Sec_M_f32 3382.91992 3382.91992±1 3382.91992±1 TrqOvISta_BapticState_State_Menum 2 0 0 0 TrqOvISta_HapticCtur_Cnt_M_u16 44520 44520±1 44520±1 TrqOvISta_HapticCtur_Cnt_M_u16 44520 428999987e+009±1 428999987e+009±1 TrqOvISta_HapticCtur_Cnt_M_u16 0 0±1 0 TrqOvISta_HapticCtur_Cnt_M_lgc 0 0±1 0 TrqOvISta_LKAFault_Cnt_M_lgc 0 0±1 0 TrqOvISta_LKAFault_Cnt_M_lgc 1 1±1 1 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4±1 4 TrqOvISta_PersMandle_HwDeg_M_f32 43 43±0.0009765625 4 TrqOvISta_PersLAPADrivinterventionDetected_Cnt_lgc.value 0 0±1 4 </td <td>target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value</td> <td>-29.8999996</td> <td></td> <td></td>	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-29.8999996		
TrqOvISta_APAState_State_M_enum	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
TrqOvISta_ESCState_State_M_enum 4 4 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedStainftLevRevTime_Sec_M_f32 388.91992 3382.91992 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_M_enum 2 2 TrqOvISta_Hapticotur_Cnt_M_u16 44520 44520 ± 1 TrqOvISta_Hapticotur_Cnt_M_u16 442999987e+009 4.28999987e+009 ± 1 TrqOvISta_HwTorqueSV_HwMm_M_Str.SV_UIs_f32 0.649999976 0.64999997e+0.0000009 TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 TrqOvISta_LKAState_State_Menum 4 4 4 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 4 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009766625 7 TrqOvISta_Perl_APADrvrinterventionDetected_Cnt_lgc.value 0 0 ± 1 4 target_TrqOvISta_Perl_APAState_State	Name	Actual Value	Expected Value	Result
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 3382.91992 3382.91992 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_Menum 2 2 TrqOvISta_Hapticdur_Cnt_M_u16 44520 44520 ± 1 TrqOvISta_Haptictime_Sec_M_f32 4.28999987e+009 4.28999987e+009 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.649999976 0.64999976 ± 0.0000009 TrqOvISta_LKAPetmFault_Cnt_M_lgc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAState_State_M_enum 4 4 ± 1 TrqOvISta_LKAState_State_M_enum 4 4 ± 1 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 TrqOvISta_Perl_APADrvrinterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Perl_APAState_State_enum.value 4 4 target_TrqOvISta_Perl_APAState_State_enum.value 4 4 target_TrqOvISta_Perl_GMOSHOscillateState_State_enum.value 4 4 target_TrqOvISta_Perl_PosSrvoHwAngle_HwDeg_f32.value <td>TrqOvlSta_APAState_State_M_enum</td> <td>4</td> <td>4</td> <td>✓</td>	TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 3382.91992 3382.91992 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvlSta_HapticState_State_Menum 2 2 TrqOvlSta_HapticClime_Sec_M_f32 44520 44520 ± 1 TrqOvlSta_HapticClime_Sec_M_f32 4.28999987e+009 4.28999987e+009 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.649999976 0.649999976 ± 0.0000009 TrqOvlSta_LKAFault_Cnt_M_igc 0 0 ± 1 1 TrqOvlSta_LKAPermFault_Cnt_M_igc 1 1 ± 1 1 TrqOvlSta_LKAPermFault_Cnt_M_igc 4 4 ± 1 4 TrqOvlSta_LKAState_State_M_enum 4 4 ± 1 4 TrqOvlSta_LKAState_State_M_enum 4 4 ± 1 4 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 4 TrqOvlSta_ReadytoPulse_Cnt_M_igc 1 1 ± 1 1 ± 1 4 target_TrqOvlSta_Per1_APAState_State_enum_value 0 0 ± 1 4 4 target_TrqOvlSta_Per1_APAState_State_enum_value 4 4 4 4	TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvIsta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvIsta_HapticState_State_Menum 2 2 2 7 7 7 7 44520 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 44520 ± 1 428999987e+009 ± 1 428999987e+009 ± 1 428999987e+009 ± 1 428999987e+009 ± 1 428999997e ± 0.0000009 541 44520 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44620 ± 1 44	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HapticState_State_M_enum 2 2 TrqOvlSta_Hapticdur_Cnt_M_u16 44520 44520 ±1 TrqOvlSta_Haptictime_Sec_M_f32 4.28999987e+009 4.28999987e+009 ±1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.649999976 0.649999976 ±0.0000009 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ±1 TrqOvlSta_LKAPermFault_Cnt_M_lgc 1 1 ±1 TrqOvlSta_LKAPermFault_Cnt_M_u08 4 4 ±1 TrqOvlSta_LKAState_State_Menum 4 4 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ±0.0009765625 TrqOvlSta_PeadytoPulse_Cnt_M_lgc 1 1 ±1 target_TrqOvlSta_Per1_APAState_State_enum.value 0 0 ±1 target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 target_TrqOvlSta_Per1_ENGSCHable_State_enum.value 2 2 target_TrqOvlSta_Per1_FoSSrvoHwAngle_HwDeg_f32.value 4 4 target_TrqOvlSta_Per1_PoSSrvOhWAngle_HwDeg_f32.value 43 43 ±0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3382.91992	3382.91992 ± 1	~
TrqOvISta_Hapticdur_Cnt_M_u16 44520 44520 ± 1 TrqOvISta_Haptictime_Sec_M_f32 4.28999987e+009 4.28999987e+009 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.649999976 0.649999976 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u0g 1 1 ± 1 TrqOvISta_LKAState_State_M_enum 4 4 ± 1 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 1 1 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_DesServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_PosSvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvISta_Haptictime_Sec_M_132 4.28999987e+009 4.28999987e+009 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.649999976 0.649999976 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_Igc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_Igc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 TrqOvISta_LKAState_State_M_enum 4 4 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_igc 1 1 ± 1 target_TrqOvISta_Per1_APADrvrinterventionDetected_Cnt_igc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_PosServEnable_Cnt_igc.value 0 0 ± 1 target_TrqOvISta_Per1_PosServEnable_Cnt_igc.value 4 4 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_igc.value 0 0 ± 1	TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.649999976 0.649999976 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 TrqOvISta_LKAState_State_M_enum 4 4 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 1 1 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value 2 2 target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value 4 4 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_PosServoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	TrqOvlSta_Hapticdur_Cnt_M_u16	44520	44520 ± 1	~
TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 TrqOvISta_LKAState_State_Menum 4 4 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 1 1 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 4 4 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	TrqOvlSta_Haptictime_Sec_M_f32	4.28999987e+009	4.28999987e+009 ± 1	~
TrqOVISta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqOVISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 TrqOVISta_LKAState_State_Menum 4 4 TrqOVISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 1 1 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 4 4 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.649999976	0.649999976 ± 0.0000009	~
TrqOVISta_LKAPermFault_Cnt_M_u08 4 4±1 TrqOVISta_LKAState_State_Menum 4 4 TrqOVISta_PosSrvoHwAngle_HwDeg_M_f32 43 43±0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 1 1±1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0±1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 4 4 target_TrqOvISta_Per1_LKAState_State_enum.value 4 4 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0±1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43±0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228±0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0±1	TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAState_State_Menum 4 4 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 1 1 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 4 4 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 43 43 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 1 1 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 4 4 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOVISta_ReadytoPulse_Cnt_M_lgc 1 1 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 4 4 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	TrqOvlSta_LKAState_State_M_enum	4	4	~
target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_Igc.value 0 0 ± 1 target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvlSta_Per1_LKAState_State_enum.value 4 4 target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value 0 0 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value 0 0 ± 1	TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43	43 ± 0.0009765625	~
target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvlSta_Per1_LKAState_State_enum.value 4 4 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvlSta_Per1_LKAState_State_enum.value 4 4 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_GMOSHOscillateState_enum.value 2 2 target_TrqOvlSta_Per1_LKAState_State_enum.value 4 4 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	target_TrqOvlSta_Per1_APAState_State_enum.value		4	~
target_TrqOvlSta_Per1_LKAState_State_enum.value 4 4 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	target_TrqOvlSta_Per1_ESCState_State_enum.value			~
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value 0 0 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value 0 0 ± 1	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 43 43 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.228 0.228 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value		0 ± 1	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0±1	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	43	43 ± 0.0009765625	~
	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value		0.228 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value 22.5359993 22.5359993 ± 0.0009765625 ✓	· - ·			~
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	22.5359993	22.5359993 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.37 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31108.7793
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3470.76001
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	30501.8809
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	50530
TrqOvlSta_Haptictime_Sec_M_f32	35000.5391
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	44.0999985

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	l
Name TraCulSta ReaduteRules Cot M les	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.5
k_APAMaxHwTrq_HwNm_f32	3.11999989
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	103.699997
k_HapticAmplitude_MtrNm_f32	0.233999997
k_HapticDuration_Sec_f32	0.584999979
k_HapticFreq_Hz_f32	23.0499992 0.624000013
k_HapticReacttime_Sec_f32 k_LKAMaxVehSpd_Kph_f32	74.099985
k LKAMinVehSpd Kph f32	68.25
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	56.7999992
k_ShiftLevRevTime_Sec_f32	0.157000005
k_StandstillThresh_Kph_f32	1.55999994
k_StandstillTime_Sec_f32	0.157000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target TrqOvlSta Per1 APARecoverableFaults Cnt lgc.value	0
target_TrqOvISta_Per1_APARecoverablePaulis_Crit_igc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-776.51001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	537.840027
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_frqOviSta_Per1_GMOSH_APAMitgEnable_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value	1
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.5
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	216.399994 226.199997
target_IrqOviSta_Peri_minSecurevenicleSpeed_kpn_rsz.value target_TrqOviSta_Peri_PosTrajEnable_Cnt_lgc.value	226.199997
target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	50530	50530 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	1.21000004 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.233999997	0.233999997 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	23.0499992	23.0499992 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31998.4395
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	31371.6191
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	3240
TrqOvlSta_Haptictime_Sec_M_f32	18460.6309
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.8900001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0340000018
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.50999999
k_APAMaxHwTrq_HwNm_f32	3.3599999
k_APAMaxVehSpd_Kph_f32	75.5999985
k_ESCMaxVehSpd_Kph_f32	106.099998
k_HapticAmplitude_MtrNm_f32	0.252000004
k_HapticDuration_Sec_f32	0.629999995
k_HapticFreq_Hz_f32	23.5639992
k_HapticReacttime_Sec_f32	0.671999991
k_LKAMaxVehSpd_Kph_f32	79.8000031
k_LKAMinVehSpd_Kph_f32	73.5
k_MaxHapticReqTime_Sec_f32	0.644999981
k_SWARLimiter_HwDeg_f32	58.2000008
k_ShiftLevRevTime_Sec_f32	0.169
k_StandstillThresh_Kph_f32	1.67999995
k_StandstillTime_Sec_f32	0.169
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_In$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	·9_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	£2.2	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_102	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt I	nc .	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	able_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	_	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvISta Per1 MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc	target_TrqOvISta_Per1_PosServEnable_Cnt	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	.lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0 1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-766.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	542.780029		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.10000002		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	0		
target TrgOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	221.300003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	V
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	
TrqOvISta_ElapsedStafidstill*Iffle_Sec_M_132 TrqOvISta_HapticState_State_M_enum	0	0	-
TrgOvISta Hapticdur Cnt M u16	3240	3240 ± 1	-
TrqOvISta_Haptictime_Sec_M_f32	18460.6309	18460.6309 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.82914019	2.82913995 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008	45.2000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	

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Name	Actual Value	Expected Value	Result
target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	*
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	45.2000008	45.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.252000004	0.252000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	23.5639992	23.5639992 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
	•
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	32888.1016
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	32241.3594
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	46
TrqOvlSta_Haptictime_Sec_M_f32	21465
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.45000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0450000018
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	46.2999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.519999981
k_APAMaxHwTrq_HwNm_f32	3.44000006
k_APAMaxVehSpd_Kph_f32	77.4000015
k_ESCMaxVehSpd_Kph_f32	108.5
k_HapticAmplitude_MtrNm_f32	0.257999986
k_HapticDuration_Sec_f32	0.644999981
k_HapticFreq_Hz_f32	24.0779991
k_HapticReacttime_Sec_f32	0.688000023
k_LKAMaxVehSpd_Kph_f32	81.6999969
k_LKAMinVehSpd_Kph_f32	75.25
k_MaxHapticReqTime_Sec_f32	0.660000026
k_SWARLimiter_HwDeg_f32	59.599985
k_ShiftLevRevTime_Sec_f32	0.172999993
k_StandstillThresh_Kph_f32	1.72000003
k_StandstillTime_Sec_f32	0.172999993
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Ci	nt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	gc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrqOvlSta Per1 ESCRequest Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_s	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
target Rte Inst Ap TrqOvIsta.TrqOvISta Per1 HapticRequest Cnt Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
angot_rtto_mat_rp_rrqoviota.rrqoviota_rerr_riwrorque_rrwivin_132	target in onotation in invitate invitation

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	756 200000		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-756.309998 1		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	547.719971		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target_TrqOvlSta_FeT_ESCI auti_Cit_gc.value target TrqOvlSta_Per1_ESCIsLimited Cnt lqc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	1.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	226.199997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	236		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	46	46 ± 1	· ·
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.45000005	3.45000005 ± 0.000009	•
TrqOvISta_LKAFault_Cnt_M_lgc	1	1±1	V
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	3	1±1	-
TrgOvISta_LKAState State M enum	3	3 ± 1 3	
TrqOvISta_ExAState_State_M_entrin TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	-
TrqOvISta ReadytoPulse Cnt M Igc	0	0 ± 1	
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	J
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	-
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.109999999	0.109999999 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.257999986	0.257999986 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	24.0779991	24.0779991 ± 0.0009765625	~
	1		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-

TrqOvlSta_Per1



Fest Step 2.40 (Repeat Count = 1)	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	2
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	33777.7617
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	40350.6602
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	33111.1016
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	1352
rqOvlSta_Haptictime_Sec_M_f32	24470
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.01000023
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0560000017
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	47.4000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.529999971
_APAMaxHwTrq_HwNm_f32	3.51999998
_APAMaxVehSpd_Kph_f32	79.1999969
_ESCMaxVehSpd_Kph_f32	110.900002
_HapticAmplitude_MtrNm_f32	0.263999999
_HapticDuration_Sec_f32	0.660000026
_HapticFreq_Hz_f32	24.5919991
_HapticReacttime_Sec_f32	0.703999996
_LKAMaxVehSpd_Kph_f32	83.5999985
_LKAMinVehSpd_Kph_f32	77
_MaxHapticReqTime_Sec_f32	0.675000012
SWARLimiter_HwDeg_f32	1.29999995 0.177000001
ShiftLevRevTime_Sec_f32 StandstillThresh Kph f32	1.7599999
_StandstillTime_Sec_f32	0.177000001
_oraniostiin inte_oec_roz arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target TrgOvlSta Per1 CWFound Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target TrgOvlSta Per1 CWPosition HwDeg f32
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
	1
rrget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value rrget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0

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Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-746.210022		
target TrqOvlSta Per1 CWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	552.659973		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	240.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	-
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	33111.1016	33111.1016	
TrqOvlSta_HapticState_State_M_enum	1	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	1352	1352 ± 1	•
TrqOvISta_Haptictime_Sec_M_f32	24470	24470 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.01000023	4.01000023 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	47.4000015	47.4000015 ± 0.0009765625	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	· ·
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	47.4000015	47.4000015 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.263999999	0.263999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	24.5919991	24.5919991 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.41 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	5270
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	48.5

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TrqOvlSta_Per1

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.540000021
k APAMaxHwTrq HwNm f32	3,7599999
k APAMaxVehSpd Kph f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	113.300003
k HapticAmplitude MtrNm f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	25.1060009
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k LKAMinVehSpd Kph f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k SWARLimiter HwDeg f32	1.60000002
k_ShiftLevRevTime_Sec_f32	0.188999996
k StandstillThresh Kph f32	1.88
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target TrgOvlSta Per1 APADrvrInterventionDetected Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrqOvlSta Per1 CWFound Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvISta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-736.109985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	557.599976
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	236
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	245.800003
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-21.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293	27475.5293 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.1060009	25.1060009 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.42 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	35557.0781
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	8620
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	6576
TrqOvlSta_Haptictime_Sec_M_f32	30480
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.25
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.100000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.550000012
k_APAMaxHwTrq_HwNm_f32	3.83999991
k_APAMaxVehSpd_Kph_f32	86.4000015
k_ESCMaxVehSpd_Kph_f32	115.699997
k_HapticAmplitude_MtrNm_f32	0.287999988
k_HapticDuration_Sec_f32	0.720000029
k_HapticFreq_Hz_f32	25.6200008
k_HapticReacttime_Sec_f32	0.768000007
k_LKAMaxVehSpd_Kph_f32	91.1999969
k_LKAMinVehSpd_Kph_f32	84
k_MaxHapticReqTime_Sec_f32	0.735000014
k_SWARLimiter_HwDeg_f32	1.89999998
k_ShiftLevRevTime_Sec_f32	0.193000004
k_StandstillThresh_Kph_f32	1.91999996
k_StandstillTime_Sec_f32	0.193000004
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Section 1) and the properties of the properties$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	•	eFaults Cnt loc	
	target TrqOvISta Per1 APARecoverableFau		
	target_TrqOvlSta_Per1_APARequest_Cnt_lg	*	
	target TrqOvISta Per1 APAState State enu		
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
	target TrqOvISta Per1 CCWPosition HwDe		
·	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	5 _ ·	
	target_TrqOvlSta_Per1_CWPosition_HwDeg	f32	
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	qc	
	target TrqOvlSta Per1 ESCRequest Cnt Ig		
	target_TrqOvISta_Per1_ESCState_State_enu		
	target_TrqOvlSta_Per1_GMOSHOscillateStar		
	target TrqOvISta Per1 GMOSH APAMfgEn		
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn	able Cnt Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f3	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgr	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	e_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-726.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	562.539978		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.2999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1		
. 312 11 112 2 111112 23	0		
0 = 1 = = 1	240.899994		
	250.699997		
0 = 1 = = 7 = = 5	0		
	0.140000001		
	-20.2999992		
3.5 4	0		
	Actual Value	Expected Value	Result
	4	4	~
	4	4	~
	0	0 ± 1	~
	0	0 ± 1	~
	0	0	~
	2	2	~
	6576	6576 ± 1	~
	30480	30480 ± 1	~
	6.25	6.25 ± 0.000009	~
1, 1, 2, 2, 2, 2, 2, 3, 3, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4,	1	1 ± 1	~
	0	0 ± 1	~
	2	2 ± 1	~
1	4	4	~
, = 0 = 0 = 1	49.5999985	49.5999985 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	49.5999985	49.5999985 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.287999988	0.287999988 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.6200008	25.6200008 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

ame	Input Value
e_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
QOVISta_APAState_State_M_enum	2
OvISta_ESCState_State_M_enum	0
OvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36446.7383
OvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11790
OvlSta_ElapsedStandstillTime_Sec_M_f32	76432.1094
QOVISta_HapticState_State_M_enum	0
OvlSta_Hapticdur_Cnt_M_u16	7882
OvlSta_Haptictime_Sec_M_f32	33485
OvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.8099994
OvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.111000001
OvISta_LKAFault_Cnt_M_lgc	0
OvlSta_LKAPermFault_Cnt_M_lgc	1
OvlSta_LKAPermFault_Cnt_M_u08	3
OvlSta_LKAState_State_M_enum	1
OvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.7000008
OvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime_Sec_f32	0.560000002
APAMaxHwTrq_HwNm_f32	3.92000008
APAMaxVehSpd_Kph_f32	88.1999969
ESCMaxVehSpd_Kph_f32	118.099998
HapticAmplitude_MtrNm_f32	0.294
HapticDuration_Sec_f32	0.735000014
HapticFreq_Hz_f32	26.1340008
HapticReacttime_Sec_f32	0.783999979
LKAMaxVehSpd_Kph_f32	93.0999985
_KAMinVehSpd_Kph_f32	85.75
MaxHapticReqTime_Sec_f32	0.75
SWARLimiter_HwDeg_f32	2.20000005
ShiftLevRevTime_Sec_f32	0.196999997
StandstillThresh_Kph_f32	1.96000004
StandstillTime_Sec_f32	0.196999997
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_C	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt	_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_en	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lg	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lg	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lg	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-715.909973		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	567.47998		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_rrqovista_ren_Eschaut_cit_gc.value target Trqovista Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	2.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	245.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	255.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-18.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		_
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36446.7383	36446.7383 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11790	11790 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_M_enum	0	0	V
TrqOvlSta_Hapticdur_Cnt_M_u16	7882	7882 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.3315897	6.33159018 ± 0.000009	V
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	V
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	3	1±1	
TrgOvISta_LKAState State M enum	3	3±1 3	
TrqOvISta_ExAState_state_ivi_entititi TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	-
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1±1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	V
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.294	0.294 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	26.1340008	26.1340008 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.44 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	14960
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	12024
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11800
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	36490.3516 8.48999977
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.143999994
TrgOvlSta LKAFault Cnt M Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	51.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.56999993
k_APAMaxHwTrq_HwNm_f32	0.10000001
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	93.5999985 120.5
k_HapticAmplitude_MtrNm_f32	0.312000006
k HapticDuration Sec f32	0.779999971
k_HapticFreq_Hz_f32	26.6480007
k_HapticReacttime_Sec_f32	0.832000017
k_LKAMaxVehSpd_Kph_f32	98.8000031
k_LKAMinVehSpd_Kph_f32	91
k_MaxHapticReqTime_Sec_f32	0.795000017
k_SWARLimiter_HwDeg_f32	2.5
k_ShiftLevRevTime_Sec_f32	0.20900006
k_StandstillThresh_Kph_f32	2.07999992
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.209000006 target TroOvISta Per1 APADryInterventionDetected Cnt Inc.
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAStete_State_cours
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	0
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TrqOviSta_Per1		Ida	CILAB
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-705.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	572.419983		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	250.699997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	260.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	14960	14960 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11800	11800 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.71384001	7.71384001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32		51.7999992 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	51.7999992	31.7333332 ± 0.0003703023	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	51.7999992 0	0 ± 1	~
			✓ ✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0 ± 1 0 ± 1	~
	0 0 4	0 ± 1 0 ± 1 4	*
target_TrqOvlSta_Per1_ESCState_State_enum.value	0 0 4 4	0 ± 1 0 ± 1 4 4	\rightarrow \right
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value	0 0 4 4 0	0 ± 1 0 ± 1 4 4 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0 0 4 4 0 4	0 ± 1 0 ± 1 4 4 0 4	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 0 4 4 0 4 0	0 ± 1 0 ± 1 4 4 0 4 0 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 0 4 4 0 4 0 51.7999992	0 ± 1 0 ± 1 4 4 0 4 0 ± 1 51.7999992 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.45 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18130
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18570
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	13106
TrqOvlSta_Haptictime_Sec_M_f32	39495
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	52.9000015

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TrqOvlSta_Per1

TIQOVISIA_PELI	
Name	Input Value
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
a_APAIncludeHaptic_Cnt_Igc	0
_APAMaxHwTrqTime_Sec_f32	0.579999983
_APAMaxHwTrq_HwNm_f32	0.5
_APAMaxVehSpd_Kph_f32	95.4000015
ESCMaxVehSpd_Kph_f32	122.900002
_HapticAmplitude_MtrNm_f32	0.31799989
_HapticDuration_Sec_f32	0.795000017
_HapticFreq_Hz_f32	27.1620007
_HapticReacttime_Sec_f32	0.84799999
_LKAMaxVehSpd_Kph_f32	100.699997
_LKAMinVehSpd_Kph_f32	92.75 0.810000002
_MaxHapticReqTime_Sec_f32	2.7999995
_SWARLimiter_HwDeg_f32 _ShiftLevRevTime_Sec_f32	0.213
StandstillThresh Kph f32	2.11999989
StandstillTime_Sec_f32	0.213
_oraniostii11iiii0_occ_io2 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_ig	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOviota_ren_ArAnoninecoverableFaults_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target TrgOvlSta Per1 APAState State enum
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvIsta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_FeT1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_FeT1_CCWF0sition_FiwDeg_i52 arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_CWF0und_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWPosition HwDeg f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_FeT_EVVF0sition_TiwDeg_i32	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAState State enum	target TrqOvlSta Per1 LKAState State enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-695.710022
arget_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	577.359985
arget_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
rget_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.29999995
arget_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
arget_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	265.399994
1 T 0 101 D 1 D T 15 11 0 11 1	1
arget_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	ļ'.

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18130	18130 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18570	18570	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	13106	13106 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.15875053	8.15874958 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.317999989	0.317999989 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	27.1620007	27.1620007 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.46 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvISta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	684235.25
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	21300
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	25116
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	14412
TrqOvlSta_Haptictime_Sec_M_f32	42500
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.60999966
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.165999994
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	54
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.589999974
k_APAMaxHwTrq_HwNm_f32	7
k_APAMaxVehSpd_Kph_f32	97.1999969
k_ESCMaxVehSpd_Kph_f32	125.300003
k_HapticAmplitude_MtrNm_f32	0.324000001
k_HapticDuration_Sec_f32	0.810000002
k_HapticFreq_Hz_f32	27.6760006
k_HapticReacttime_Sec_f32	0.864000022
k_LKAMaxVehSpd_Kph_f32	102.599998
k_LKAMinVehSpd_Kph_f32	94.5
k_MaxHapticReqTime_Sec_f32	0.824999988
k_SWARLimiter_HwDeg_f32	3.0999999
k_ShiftLevRevTime_Sec_f32	0.216999993
k StandstillThresh Kph f32	2.16000009
k_StandstillTime_Sec_f32	0.216999993
	nDetected Cnt target TrgOviSta Per1 APADrvrInterventionDetected Cnt lgc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target TrgOvlSta Per1 APARecoverableFa	ults Cnt lac	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	0	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	ı f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_	
		lee.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	ium	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target TrqOvISta Per1 GMOSH APAMfgEr	nable Cnt lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable Cnt lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ic	
	target TrqOvISta Per1 LKAState State en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lac	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-685.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	582.299988		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	260.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	270.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f32.value	-13.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
		From a stand Well	
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	684235.25	684235.25 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	14412	14412 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	42500	42500 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.59573936	8.59574032 ± 0.000009	-
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	54	54 ± 0.0009765625	✓
	I.	1 ± 1	

 $TrqOvlSta_ReadytoPulse_Cnt_M_lgc$

1 ± 1

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	54	54 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.324000001	0.324000001 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	27.6760006	27.6760006 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

lamo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvISta_APAState_State_M_enum	4
FrqOvISta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	152460
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	24470
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	31662
FrqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	18330
FrqOvISta_Haptictime_Sec_M_f32	45505
FrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.199000001
FrqOvlSta_LKAFault_Cnt_M_lgc	1
FrqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	55.0999985
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.600000024
APAMaxHwTrq HwNm f32	4.55999994
APAMaxVehSpd_Kph_f32	102.599998
ESCMaxVehSpd Kph f32	127.699997
	0.342000008
_HapticAmplitude_MtrNm_f32	0.855000019
C_HapticDuration_Sec_f32	
x_HapticFreq_Hz_f32	28.1900005
x_HapticReacttime_Sec_f32	0.912
x_LKAMaxVehSpd_Kph_f32	108.300003
C_LKAMinVehSpd_Kph_f32	99.75
x_MaxHapticReqTime_Sec_f32	0.870000005
SWARLimiter_HwDeg_f32	3.4000001
_ShiftLevRevTime_Sec_f32	0.229000002
_StandstillThresh_Kph_f32	2.27999997
_StandstillTime_Sec_f32	0.229000002
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cr	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Tet1_ESCReguest_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
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arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_e	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_	_f32 target_TrqOvlSta_Per1_MaxSecureVehicle	Speed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_	f32 target_TrqOvlSta_Per1_MinSecureVehicles	Speed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_C	nt_lgc		
$target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosSrvoHwAngle_	HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn	t_lgc		
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosTrajHwAngle_I	HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDe	g_f3. target_TrqOvlSta_Per1_SWARTrgtAngReq	uest_HwDeg_f32		
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgcolor=0.00000000000000000000000000000000000$	target_TrqOvlSta_Per1_ShiftLeverIsInReve	arget_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNr	n_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cn			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-675.51001			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	587.23999			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target TrqOvlSta_Per1_HwTorque HwNm f32.value	4.0999999			
	0			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0			
target TrqOvlSta_rer1_ErxRequest_Crit_igc.value target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	265.399994			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	275.200012			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-12.3000002			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	4	4	- 100 u.i.	
TrqOvlSta_ESCState_State_M_enum	4	4	✓	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	152460	152460 ± 1		
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	✓	
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	_	
TrqOvlSta_HapticState_State_M_enum	0	0	~	
TrgOvlSta Hapticdur Cnt M u16	18330	18330 ± 1		
TrgOvlSta Haptictime Sec M f32	45505	45505 ± 1	~	
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-7.1940999	-7.1940999 ± 0.000009		
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~	
TrgOvISta LKAPermFault Cnt M Igc	0	0 ± 1		
TrgOvISta LKAPermFault Cnt M u08	2	2 ± 1	~	
TrqOvlSta_LKAState_State_M_enum	0	0		
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~	
torget TraOvISte Part ESCState State enum value		4	~	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4			
target_TrqOvISta_Per1_E3CState_State_entiff.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	0	0	~	
		0	✓	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	· ·	• • • • • • • • • • • • • • • • • • •	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	<i>y</i>	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 0 1 0.189999998 0.342000008	0 1 ± 1	· ·	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 0 1 0.189999998	0 1 ± 1 0.189999998 ± 0.0009765625	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Name	Test Step 2.48 (Repeat Count = 1)	✓
New Journal Process Pr	Name	Input Value
Trocklosis Designation 2		·
Tigo Albert Designed Abstantive Tige Tige Set JL 192 Tigo Albert Designed Market Time, Set JL 192 Tigo Albert Time	TrqOvlSta_APAState_State_M_enum	· · · ·
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Topological papersons State Agriculture State	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	27640
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A. APAMateVirTimer Sec 62	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	
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target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APAState_State_enum target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SCSCFault_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Moriorue_HwNm_f32 target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Moriorue_HwNm_f32 target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARiapuet_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARiapuet_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARiapuet_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFanable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviFa	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCScault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCScault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCScault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCScault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCScault_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCScault_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHD-ScaliateState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ScMGfenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMGfenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMGfenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMGfenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_RASecureVehicleSpeed_Kph_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_RASecureVehicleSpeed_Kph_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PSorapi+mAngle_HWDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Sorapi+mAngle_HWDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Sorapi+mAngle_HWDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Sorapi+mAngle_HWDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Sorapi+mAngle_HWDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqO	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWPooldOn_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPooldOn_Ligo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_EMOSH_Dev1_ESCMIgenable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_EMOSH_Dev1_ESCMIgenable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_EMOSH_Dev1_ESCMIgenable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAPnobiSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HAState_State_enum target_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HAState_State_enum target_TrqOviSta_Per1_HAState_State_enum target_TrqOviSta_Per1_HAState_State_Enum target_TrqOviSta_Per1_HAState_State_Enum target_TrqOviSta_Per1_HAState_State_Enum target_TrqOviSta_Per1_HAState_State_Enum target_TrqOviSta_Per1_PasservEnable_Cnt_Igo target_TrqOviSta_Per1_PasservEnable_Cnt_Igo target_TrqOviSta_Per1_PasservEnable_Cnt_Igo target_TrqOviSta_Per1_PasservEnable_Cnt_Igo target_TrqOviSta_Per1_PasservEnable_Cn	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CWFouston_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCMSHe_ESCMSHe_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMSE_Rnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Ber1_GMOSH_ESCMSE_Rnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HwTorque_HwMm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Marsecurest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Marsecurest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Marsecurest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Marsecurest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Marsecurest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Marsecurest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_DessrotAmalie_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PossrotAmalie_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PossrotAmalie_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Ins	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARSequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARSequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARSequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Descentable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrajenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrajenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrajenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrajenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrajenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrajenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrajenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrajen		
target_Rie_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCISLimited_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCISLimited_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCISLimited_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCIState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EMOSH_ESCMigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EMOSH_ESCMigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EMOSH_ESCMigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EMOSH_ESCMigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Hwforque_Hwhm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Livor_Ingc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Divor_Ingc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Divor_Pert_PosSevrEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSevrEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSevrEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSevrEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_PosSevrEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_PosSevrEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_PosSevrEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_PosSevrEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Ri		
target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCRequest_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCRequest_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_APAMfgEnable_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_CSCMfgEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HopticRequest_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HopticRequest_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HopticRequest_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HopticRequest_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARequest_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARequest_Ort_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrejEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrejEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrejEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrejEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrejEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrejEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrejEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SMRTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SMRTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_TrqOscRep_Hz_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_TrqOscRep_Hz_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_TrqOscRep_Hz_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosTrejEnable_Cnt_Igo tar		
target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARAult_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARstate_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARstate_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_DesSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SortrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SortrajEnable_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_DavaSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_Sort-NewTeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_Sort-NewTeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_Sort-NewTeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_Sort-NewTeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_Sort-NewTeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_Sort-NewTeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOvSca_Pert_NewDeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOvSca_Pert_NewDeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOvSca_Pert_NewDeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOvSca_Pert_NewDeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOvSca_Pert_NewDeg_f332 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOvSca_Pert_NewDeg_f332 target_R		
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_APARequest_Cnt_lgc.target_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOviSta_Per1_APARequest_Cnt_lgc.value	target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	
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target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvohwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_igc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_TrqOvlSta_Per1_TrqOscEreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscEreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscErable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0	target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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TIQONOIA_T ELT			- 10-10
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-665.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	592.179993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	270.299988		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	280.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.10000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-10.6999998		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	, 100 di
TrgOvlSta ESCState State M enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	186423	186423 ± 1	-
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	•
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	•
TrgOvlSta HapticState State M enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	19636	19636 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.80300045	8.80300045 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrgOvlSta LKAPermFault Cnt M lgc	1	1 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta PosSrvoHwAngle HwDeg M f32	56.2000008	56,2000008 ± 0,0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target TrqOvlSta Per1 LKAState State enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	56.2000008	56.2000008 ± 0.0009765625	•
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.34799999	0.34799999 ± 0.0009765625	•
0 _ 1, 1, 1, 1 _ 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,			
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.49 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	220386
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	30810
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	44754
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	20942
TrqOvlSta_Haptictime_Sec_M_f32	51515
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.221000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.2999992

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TrqOvlSta_Per1



Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k APAIncludeHaptic Cnt Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.620000005
k_APAMaxHwTrq_HwNm_f32	4.71999979
k_APAMaxVehSpd_Kph_f32	106.199997
k_ESCMaxVehSpd_Kph_f32	132.5
k_HapticAmplitude_MtrNm_f32	0.354000002
k_HapticDuration_Sec_f32	0.88499999 29.2180004
k_HapticFreq_Hz_f32 k HapticReacttime Sec f32	0.94400006
k_LKAMaxVehSpd_Kph_f32	112.099998
k_LKAMinVehSpd_Kph_f32	103.25
k_MaxHapticReqTime_Sec_f32	0.89999976
k_SWARLimiter_HwDeg_f32	4
k_ShiftLevRevTime_Sec_f32	0.237000003
k_StandstillThresh_Kph_f32	2.3599999
k_StandstillTime_Sec_f32	0.237000003
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverable Faults Cnt Igc	target TrqOvISta Per1 APARecoverable aults_Cit_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-655.309998
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 507 110005
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	597.119995
target_IrqOviSta_Peri_ESCFault_Cnt_igc.value target TrqOviSta_Peri_ESCIsLimited Cnt_igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.5
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1 275.200012
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	285
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.10000038		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	30810	30810 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	20942	20942 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	51515	51515 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.994499981	0.994499981 ± 0.0000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	2	2	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.354000002	0.354000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	29.2180004	29.2180004 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.50 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	254349
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	33980
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	51300
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	22248
TrqOvlSta_Haptictime_Sec_M_f32	54520
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.231999993
TrqOvlSta_LKAFault_Cnt_M_lgc	1.
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	58.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.62999995
k_APAMaxHwTrq_HwNm_f32	4.80000019
k_APAMaxVehSpd_Kph_f32	108
k_ESCMaxVehSpd_Kph_f32	134.899994
k_HapticAmplitude_MtrNm_f32	0.360000014
k_HapticDuration_Sec_f32	0.899999976
k_HapticFreq_Hz_f32	29.7320004
k_HapticReacttime_Sec_f32	0.959999979
k_LKAMaxVehSpd_Kph_f32	114
k_LKAMinVehSpd_Kph_f32	105
k_MaxHapticReqTime_Sec_f32	0.915000021
k_SWARLimiter_HwDeg_f32	4.30000019
k_ShiftLevRevTime_Sec_f32	0.240999997
k_StandstillThresh_Kph_f32	2.4000001
k_StandstillTime_Sec_f32	0.240999997
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lge		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgE	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-645.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	602.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 HapticRequest Cnt lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	4.69999981		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	289.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvISta APAState State M enum	4	4	- Nosuli
TrqOvISta_ESCState_State_M_enum	4	4	
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	254349	254349 ± 1	
TrqOvISta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	51300	51300	
TrqOvISta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	22248	22248 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	
	5.95951986	5.95951986 ± 0.000009	
TraOvISta HwTorqueSV HwNm M Str SV IIIs f32	0.0000 1000		
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_IKAFault_Cnt_M_loc	0		
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	0 ± 1 2 ± 1	·
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	

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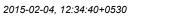
TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	58.4000015	58.4000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.360000014	0.360000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	29.7320004	29.7320004 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.51 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	288312
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	37150
TrqOvlSta ElapsedStandstillTime Sec M f32	57846
TrqOvlSta_HapticState_State_M_enum	0
TrqOvISta Hapticdur Cnt M u16	23554
TrqOvlSta_Haptictime_Sec_M_f32	57525.3203
TrqOvISta HwTorqueSV HwNm M Str.SV UIs f32	-5.86000013
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001
TrqOvISta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	59.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k APAMaxHwTrqTime Sec f32	0.639999986
k APAMaxHwTrq HwNm f32	4.88000011
k APAMaxVehSpd Kph f32	109.800003
k ESCMaxVehSpd Kph f32	137.300003
k HapticAmplitude MtrNm f32	0.365999997
k_HapticDuration_Sec_f32	0.915000021
k_HapticFreq_Hz_f32	30.2460003
k_HapticReacttime_Sec_f32	0.976000011
k LKAMaxVehSpd Kph f32	115.900002
k_LKAMinVehSpd_Kph_f32	106.75
k_MaxHapticReqTime_Sec_f32	0.930000007
k SWARLimiter HwDeg f32	4.5999999
k_ShiftLevRevTime_Sec_f32	0.245000005
k_StandstillThresh_Kph_f32	2.44000006
k_StandstillTime_Sec_f32	0.245000005
k_ctailustiiiTille_dec_loz target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_t	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target TrqOvIsta_Per1_CGWPosition_nwbeg_isz
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrgOvlSta Per1 CWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_PerT_ESCPault_Cht_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_ESCIsLimited_Cht_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
· · · · ·	target TrqOvISta_Per1_ESCISLIMITEd_CIT_tigc target TrqOvISta_Per1_ESCRequest Cnt_tigc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_ESCState_State_enum target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enui	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1	2	
target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-635.109985		
target TrqOvISta Per1 CWFound Cnt Igc.value	1		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	607		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	1		
target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	285		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	294.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
Transideta Ecociata Ciata M.		•	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0		
		3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	3 0±1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0 37150	3 0 ± 1 37150 ± 1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0 37150 0	3 0 ± 1 37150 ± 1 0	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	0 37150 0 0 23554	3 0 ± 1 37150 ± 1 0	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0 37150 0 0 23554 0 -3.24532008	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	0 37150 0 0 23554 0 -3.24532008	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0 37150 0 0 23554 0 -3.24532008	3 0±1 37150±1 0 0 23554±1 0±1 -3.24532008±0.000009 1±1 1±1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	0 37150 0 0 23554 0 -3.24532008 1 1	3 0±1 37150±1 0 0 23554±1 0±1 -3.24532008±0.000009 1±1 1±1 3±1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	0 37150 0 0 23554 0 -3.24532008 1 1 3	3 0±1 37150±1 0 0 23554±1 0±1 -3.24532008±0.000009 1±1 1±1 3±1 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995 1	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1 0 ± 1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995 1 0	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1 0 ± 1 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995 1 0 3	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1 0 ± 1 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995 1 0 3	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1 0 ± 1 3 0	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995 1 0 3	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1 0 ± 1 3 3 0 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HAPTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995 1 0 3 3 1	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1 0 ± 1 3 0 ± 1 3 1 ± 1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Hapticqnt_M_lgc TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_LKAState_State_Menum TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995 1 0 3 3 1 0.129999995	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1 0 ± 1 3 3 0 3 0 1 1 1 1 1 1 1 1 1 1 1 1 1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HAPTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	0 37150 0 0 23554 0 -3.24532008 1 1 3 3 0.129999995 1 0 3 3 1	3 0 ± 1 37150 ± 1 0 0 23554 ± 1 0 ± 1 -3.24532008 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1299999995 ± 0.0009765625 1 ± 1 0 ± 1 3 0 ± 1 3 1 ± 1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.52 (Repeat Count = 1)	√
Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	322275
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	40320
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	64392
TrqOvlSta_HapticState_State_M_enum	0
TrqOvISta_Haptictime_See M #22	24860 60530
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.9000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798
TrqOvISta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	4
TrqOvISta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	60.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	
k_APAIncludeHaptic_Cnt_lgc	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	0.649999976 4.96000004
k APAMaxVehSpd Kph f32	111.599998
k_ESCMaxVehSpd_Kph_f32	139.699997
k_HapticAmplitude_MtrNm_f32	0.372000009
k_HapticDuration_Sec_f32	0.930000007
k_HapticFreq_Hz_f32	30.7600002
k_HapticReacttime_Sec_f32	0.991999984
k_LKAMaxVehSpd_Kph_f32	117.800003
k_LKAMinVehSpd_Kph_f32	108.5
k_MaxHapticReqTime_Sec_f32	0.944999993
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	4.900001 0.24899998
k StandstillThresh Kph f32	2.48000002
k StandstillTime Sec f32	0.248999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_Igc target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target TrqOvISta Per1 LKAInhibit Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_inst_Ap_irqovista.irqovista_Peri_irqoscAmp_mtrnm_i32 target_Rte_inst_Ap_TrqoviSta.TrqoviSta_Peri_TrqOscEnable_Cnt_lgc	target_IrqOviSta_Per1_IrqOvsCAmp_intrivm_t32 target_TrqOviSta_Per1_TrqOvsCEnable_Cnt_Igc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-625.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	611.940002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	289.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	299.700012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4.30000019		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrgOvlSta ESCState State M enum	4	4	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	322275	322275 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	~
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrgOvlSta HapticState State M enum	0	0	-
TrqOvlSta_Hapticdur_Cnt_M_u16	24860	24860 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	60530	60530 ± 1	-
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.88493013	-6.88492966 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrgOvlSta LKAPermFault Cnt M lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	-
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	60.5999985	60.5999985 ± 0.0009765625	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target TrqOvlSta Per1 ESCState State enum.value	4	4	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
		4	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4		
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
		0 ± 1 60.5999985 ± 0.0009765625	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0		•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 60.5999985	60.5999985 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.53 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	356238
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	43490
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	70938
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	26166
TrqOvlSta_Haptictime_Sec_M_f32	63535
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.55000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	61.7000008

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.660000026
k_APAMaxHwTrq_HwNm_f32	5.03999996
k_APAMaxVehSpd_Kph_f32	113.400002
k_ESCMaxVehSpd_Kph_f32	142.100006
k_HapticAmplitude_MtrNm_f32	0.377999991
k_HapticDuration_Sec_f32	0.944999993 31.2740002
k_HapticFreq_Hz_f32 k HapticReacttime Sec f32	1.00800002
k_LKAMaxVehSpd_Kph_f32	119.699997
k_LKAMinVehSpd_Kph_f32	110.25
k_MaxHapticReqTime_Sec_f32	0.959999979
k_SWARLimiter_HwDeg_f32	5.19999981
k_ShiftLevRevTime_Sec_f32	0.252999991
k_StandstillThresh_Kph_f32	2.51999998
k_StandstillTime_Sec_f32	0.252999991
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target TrgOvlSta Per1 TrgOscEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-614.909973
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target TrqOvlSta Per1 CWPosition HwDeg f32.value	1 616.880005
target_IrqOviSta_Peri_CwPosition_HwDeg_132.value target TrqOviSta_Peri_ESCFault_Cnt_lgc.value	1
target TrqOvISta_Per1_ESGFatit_Off_igc.value	1
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.30000019
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1 0
target_TrqOvlSta_Per1_LtArkequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	294.799988
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value	304.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	26166	26166 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	63535	63535 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.92737675	1.92737687 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.377999991	0.377999991 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	31.2740002	31.2740002 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.54 (Repeat Count = 1)	▼ ·
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	390201
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	46660
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	77484
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	27472
TrqOvlSta_Haptictime_Sec_M_f32	66540.6328
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0500000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	62.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.670000017
k_APAMaxHwTrq_HwNm_f32	5.11999989
k_APAMaxVehSpd_Kph_f32	115.199997
k_ESCMaxVehSpd_Kph_f32	144.5
k_HapticAmplitude_MtrNm_f32	0.384000003
k_HapticDuration_Sec_f32	0.95999979
k_HapticFreq_Hz_f32	31.7880001
k_HapticReacttime_Sec_f32	1.02400005
k_LKAMaxVehSpd_Kph_f32	121.599998
k_LKAMinVehSpd_Kph_f32	112
k_MaxHapticReqTime_Sec_f32	0.975000024
k_SWARLimiter_HwDeg_f32	5.5
k_ShiftLevRevTime_Sec_f32	0.256999999
k_StandstillThresh_Kph_f32	2.55999994
k_StandstillTime_Sec_f32	0.256999999
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cnd$	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	. 122	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeq target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	<u>j_</u> 132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_	lac	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvIsta_Per1_ESCRequest_Cnt_l	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_er		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgE	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosSrveHwAngle_F		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_H target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrqOvlSta Per1 ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-604.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	621.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target TrgOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	5.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	299.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-1.10000002		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvISta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	390201	390201 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	46660	46660 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	27472	27472 ± 1	
TrqOvISta Haptictime Sec M f32	0	0±1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.61499977	-5.61499977 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
		1±1	
TrqOvISta LKAPermFault Cnt M u08	1	III	•
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	4	4	
	•		

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	62.7999992	62.7999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.384000003	0.384000003 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	31.7880001	31.7880001 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrgOvISta ESCState State M enum	2
FrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	424164
FrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	48750.6406
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	84030
FrqOvlSta_HapticState_State_M_enum	0
FrgOvlSta Hapticdur Cnt M u16	28778
FrqOvISta_Haptictime_Sec_M_f32	69545
FrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-5.8499999
FrgOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.287
FrgOviSta LKAFault Cnt M lgc	0
FrgOvISta_EKALauti_ont_wi_gc	0
FrqOvISta_LKAPermFault_Cnt_M_u08	0
FrqOvISta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	63.9000015
TrqOvlSta ReadytoPulse Cnt M lgc	0
APAIncludeHaptic Cnt Igc	0
<pre><_APAMaxHwTrqTime_Sec_f32</pre>	0.680000007
CAPAMaxHwTrq_HwNm_f32	5.1999981
CAPAMaxVehSpd_Kph_f32	117
CESCMaxVehSpd Kph f32	146.899994
C_EOSMAXVOROPA_, Kpr_102 C_HapticAmplitude_MtrNm_f32	0.38999986
<pre>C_HapticOuration_Sec_f32</pre>	0.975000024
<_hapticFreq_Hz_f32	32.3019981
<pre><_napito req_nz_or</pre> <pre><_HapticReacttime_Sec_f32</pre>	1.03999996
<pre><_napidoredadimo_eco_io2</pre> <pre><_LKAMaxVehSpd_Kph_f32</pre>	123.5
C_LKAMinVehSpd_Kph_f32	113.75
<pre>C_Ete timinverlepe_repri_es </pre> <pre>(_MaxHapticReqTime_Sec_f32</pre>	0.99000001
C_SWARLimiter_HwDeg_f32	5.8000019
<pre>C_OVV training_INVOG_IO2</pre> <pre>C_ShiftLevRevTime_Sec_f32</pre>	0.261000007
C_StandstillThresh_Kph_f32	2.5999999
StandstillTime Sec f32	0.261000007
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cr	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target TrqOvlSta Per1 CCWFound Cnt Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CWPosition HwDeg f32	target TrqOvlSta Per1 CWPosition HwDeg f32
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCFault Cnt Igc	target TrqOvlSta Per1 ESCFault Cnt Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fet1_ESGISLittlited_Cit_igc arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc	target TrqOvlSta Per1 ESCRequest Cnt Igc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Ont_ig0	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_E3CState_State_enum arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_inst_Ap_1rq0viSta.1rq0viSta_Per1_GiviOSH0ScillateState_State_ent arget_Rte_Inst_Ap_Trq0viSta.Trq0viSta_Per1_GM0SH_APAMfgEnable_Cnt_lgi	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_AFAMigEnable_Cnt_ig arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_ig	
.arget_Rte_Inst_Ap_11q0vlSta.11q0vlSta_Per1_GM0Sh_ES6MigEnable_Cht_ig :arget_Rte_Inst_Ap_Trq0vlSta.Trq0vlSta_Per1_GM0SH_LKAMfgEnable_Cht_ig	
arget_Rie_inst_Ap_11q0vista.11q0vista_Per1_Giii0Sn_t.KainigEriable_Chi_igt arget Rte Inst Ap Trq0viSta.Trq0viSta Per1 HapticRequest Cnt Igc	target_TrqOvlSta_Per1_GMOSH_EXAMIGENable_Cht_igc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HapticRequest_Crit_igc arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HwTorque HwNm f32	target_TrqOvlSta_Per1_mapticRequest_Crit_gic target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-594.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 626.76001		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	1		
target TrqOvISta_rer1_ESCISEIIIIIted_Off_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	5.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	314.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	48750.6406	48750.6406 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	84030	84030	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	28778	28778 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	69545	69545 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53515005	-2.53515005 ± 0.000009	V
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	
TrqOvlSta_LKAState_State_M_enum	2	2	✓
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625 0 ± 1	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_IrqoviSta_Peri_GMOSHOscillateState_State_enum.value target_TrqOviSta_Peri_LKAState_State_enum.value	2	2	-
target_rqovista_Peri_LRAState_state_enum.value target_TrqOviSta_Peri_LRAState_state_enum.value	1	1±1	-
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.389999986	0.389999986 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	-
O 4	1		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.56 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	458127
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	49550.3203
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	90576
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	30084
TrqOvlSta_Haptictime_Sec_M_f32	72550
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	0.298000008
TrqOvISta_LKAPermFault_Cnt_M_lgc	1
TrgOvlSta LKAPermFault Cnt M u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	65
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.689999998
k_APAMaxHwTrq_HwNm_f32	5.28000021
k_APAMaxVehSpd_Kph_f32	118.800003
k_ESCMaxVehSpd_Kph_f32	149.300003
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0.39599998 0.99000001
k_HapticFreq_Hz_f32	32.8160019
k HapticReacttime Sec f32	1.05599999
k_LKAMaxVehSpd_Kph_f32	125.400002
k_LKAMinVehSpd_Kph_f32	115.5
k_MaxHapticReqTime_Sec_f32	0.100000001
k_SWARLimiter_HwDeg_f32	6.0999999
k_ShiftLevRevTime_Sec_f32	0.264999986
k_StandstillThresh_Kph_f32	2.6400001
k_StandstillTime_Sec_f32	0.264999986
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_irqOvlSta_Peri_minSecurevenicleSpeed_kpn_t32 target_trqOvlSta_Peri_minSecurevenicleSpeed_kpn_t32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	0
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TrqOviSta_Peri		(OL	CILAB
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-584.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	631.700012		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	309.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	319.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	2.0999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	30084	30084 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	72550	72550 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5	-5.5 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	65	65 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	65	65 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.395999998	0.395999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.8160019	32.8160019 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.57 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	492090
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	50350
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	97122
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	31390
TrqOvlSta_Haptictime_Sec_M_f32	75555.5781
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.308999985
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	66.099985

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k APAIncludeHaptic Cnt Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.69999988
k_APAMaxHwTrq_HwNm_f32	5.36000013
k_APAMaxVehSpd_Kph_f32	120.599998
k_ESCMaxVehSpd_Kph_f32	151.699997
k_HapticAmplitude_MtrNm_f32	0.40200001
k_HapticDuration_Sec_f32	1.005
k_HapticFreq_Hz_f32	33.3300018
k_HapticReacttime_Sec_f32	1.07200003
k_LKAMaxVehSpd_Kph_f32	127.300003
k_LKAMinVehSpd_Kph_f32	117.25
k_MaxHapticReqTime_Sec_f32	0.20000003
k_SWARLimiter_HwDeg_f32	6.4000001
k_ShiftLevRevTime_Sec_f32	0.268999994
k_StandstillThresh_Kph_f32	2.68000007
k_StandstillTime_Sec_f32	0.268999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_state_entilin target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target TrgOvISta Per1 CCWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_Hwbeg_I32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_lgc	target_TrqOvIsta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-574.51001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	636.640015
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0
	1
target TrgOvlSta Per1 LKARequest Cnt lac.value	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	314.399994
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	314.39994 324.200012
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	324.200012
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	50350	50350 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	31390	31390 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.67375016	-1.67375004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.40200001	0.40200001 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	33.3300018	33.3300018 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.58 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	526053
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51149.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	103668
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	32696
TrqOvlSta_Haptictime_Sec_M_f32	78560
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.319999993
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	67.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.709999979
k_APAMaxHwTrq_HwNm_f32	5.44000006
k_APAMaxVehSpd_Kph_f32	122.400002
k_ESCMaxVehSpd_Kph_f32	154.100006
k_HapticAmplitude_MtrNm_f32	0.407999992
k_HapticDuration_Sec_f32	1.01999998
k_HapticFreq_Hz_f32	33.8440018
k_HapticReacttime_Sec_f32	1.08800006
k_LKAMaxVehSpd_Kph_f32	129.199997
k_LKAMinVehSpd_Kph_f32	119
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	6.69999981
k_ShiftLevRevTime_Sec_f32	0.273000002
k_StandstillThresh_Kph_f32	2.72000003
k_StandstillTime_Sec_f32	0.273000002
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_In$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_ target_TrqOvlSta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	52	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	ic.	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvlSta Per1 PosTrajEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-564.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	641.580017		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	319.299988		
target TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	329.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f32.value	5.30000019		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	- TOOUIT
TrqOvISta_ESCState_State_M_enum	4	4	✓
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	51149.6797	51149.6797 ± 1	·
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	2	2	·
TrqOvISta_Hapticdur_Cnt_M_u16	32696	32696 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019	-4.80000019 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	67.1999969	67.1999969 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	67.1999969	67.1999969 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.407999992	0.407999992 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	33.8440018	33.8440018 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

lame	Input Value
	target Rte Inst Ap TrqOvlSta
tte_Inst_Ap_TrqOvlSta	·
rqOvISta_APAState_State_M_enum	1
rqOvISta_ESCState_State_M_enum	1
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	560016 51949.3594
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	110214
rqOvISta_HapticState_State_M_enum	
rqOvISta_Hapticdur_Cnt_M_u16	34002
rqOvlSta_Haptictime_Sec_M_f32	81565.6406
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.331
rqOvISta_LKAFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	0
qOvlSta_LKAState_State_M_enum	0
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	68.3000031
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.720000029
_APAMaxHwTrq_HwNm_f32	5.51999998
_APAMaxVehSpd_Kph_f32	124.199997
_ESCMaxVehSpd_Kph_f32	156.5
_HapticAmplitude_MtrNm_f32	0.414000005
_HapticDuration_Sec_f32	1.03499997
_HapticFreq_Hz_f32	34.3580017
_HapticReacttime_Sec_f32	1.10399997
_LKAMaxVehSpd_Kph_f32	131.100006
_LKAMinVehSpd_Kph_f32	120.75
_MaxHapticReqTime_Sec_f32	0.400000006
_SWARLimiter_HwDeg_f32	7
_ShiftLevRevTime_Sec_f32	0.27700001
_StandstillThresh_Kph_f32	2.75999999
_StandstillTime_Sec_f32	0.27700001
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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		•	
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lc	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_F		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target TrqOvlSta Per1 APANonRecoverableFaults Cnt lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-554.309998		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	646.52002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target TrgOvlSta Per1 ESCReguest Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	324.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	334		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0	1	
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	Y
TrqOvlSta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	34002	34002 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	81565.6406	81565.6406 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981	-4.44999981 ± 0.000009 1 ± 1	Ž
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	1 0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_ugc	1	1±1	
TrqOvISta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	_
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0.103333333	0.109999999 1 0.0009703023	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value			
3 _ 1		0 ± 1	✓
target TrgOvlSta Per1 APAState State enum.value	0	0 ± 1	· ·
target_TrqOvlSta_Per1_APAState_State_enum.value target TrqOvlSta Per1_ESCState State enum.value	0 0		~
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 0 3	0 3	· ·
target_TrqOvlSta_Per1_ESCState_State_enum.value	0 0 3 0	0 3 0	
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0 0 3 0 0	0 3 0 0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 0 3 0 0	0 3 0 0 1 ± 1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 0 3 0 0 1 0.109999999	0 3 0 0 1±1 0.109999999±0.0009765625	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.60 (Repeat Count = 1)	J. Committee of the com
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	593979
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	52749.0391
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	116760
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	35308
TrqOvlSta_Haptictime_Sec_M_f32	84570
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342000008
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	69.4000015
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.730000019
k_APAMaxHwTrq_HwNm_f32	5.5999999
k_APAMaxVehSpd_Kph_f32	126
k_ESCMaxVehSpd_Kph_f32	158.899994
k_HapticAmplitude_MtrNm_f32	0.419999987
k_HapticDuration_Sec_f32	1.04999995
k_HapticFreq_Hz_f32	34.8720016
k_HapticReacttime_Sec_f32	1.12
k_LKAMaxVehSpd_Kph_f32	133
k_LKAMinVehSpd_Kph_f32	122.5
k_MaxHapticReqTime_Sec_f32	0.5 7.30000019
k_SWARLimiter_HwDeg_f32 k ShiftLevRevTime Sec f32	0.280999988
k StandstillThresh Kph f32	2.7999995
k StandstillTime Sec f32	0.280999988
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviota_TrqOviota_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	1.
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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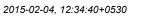


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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-544.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	651.460022		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	338.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	8.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	_
TrgOvISta ESCState State M enum	4	4	_
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	•
TrqOvlSta ElapsedStandstillTime Sec M f32	116760	116760	
TrqOvlSta HapticState State M enum	1	1	_
TrqOvlSta_Hapticdur_Cnt_M_u16	35308	35308 ± 1	_
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999	-4.0999999 ± 0.000009	_
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	_
TrqOvlSta LKAPermFault Cnt M lgc	1	1 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	4	4 ± 1	_
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	69.4000015	69.4000015 ± 0.0009765625	_
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	_
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	_
target TrqOvlSta Per1 LKAState State enum.value	4	4	•
			_
· - ·	0	0 ± 1	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value		0 ± 1 69.4000015 ± 0.0009765625	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	·	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 69.4000015	69.4000015 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.61 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	627942
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	53548.7188
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	123306
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	36614
TrqOvlSta_Haptictime_Sec_M_f32	87575
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.352999985
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.5

TrqOvlSta_Per1





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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.74000001
k_APAMaxHwTrq_HwNm_f32	5.67999983
k_APAMaxVehSpd_Kph_f32	127.800003
k_ESCMaxVehSpd_Kph_f32	161.300003
k_HapticAmplitude_MtrNm_f32	0.425999999
k_HapticDuration_Sec_f32	1.06500006
k_HapticFreq_Hz_f32	35.3860016
k_HapticReacttime_Sec_f32	1.13600004
k_LKAMaxVehSpd_Kph_f32	134.899994
k_LKAMinVehSpd_Kph_f32	124.25
k_MaxHapticReqTime_Sec_f32	0.60000024
k_SWARLimiter_HwDeg_f32	7.5999999
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32	0.284999996 2.83999991
k_StandstillTime_Sec_f32	0.284999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-534.109985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	656.400024
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_IrqOviSta_Peri_GMOSH_ESCMrgEnable_Cnt_igc.value target TrqOvlSta_Peri_GMOSH_LKAMfgEnable_Cnt_igc.value	1
target_TrqOvIsta_Per1_HapticRequest_Cnt_Igc.value	0
target TrqOvISta_Peri_HapticRequest_Crit_igc.value target TrqOvISta_Peri_HapticRequest_Crit_igc.value	6.9000001
target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value	1
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1
target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	334
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	343.799988
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

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 $target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value$

target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value

target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value

target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value

TrqOvlSta_Per1

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1 ± 1

0.129999995 ± 0.0009765625

0.425999999 ± 0.0009765625

35.3860016 ± 0.0009765625

Input Value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 $target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value$ Name **Actual Value Expected Value** Result TrqOvlSta_APAState_State_M_enum 0 0 TrqOvlSta_ESCState_State_M_enum 3 3 TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32 0 0 ± 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 53548.7188 53548.7188 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvlSta_HapticState_State_M_enum 0 0 TrqOvlSta_Hapticdur_Cnt_M_u16 36614 36614 ± 1 TrqOvlSta_Haptictime_Sec_M_f32 0 0 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 -3.75 -3.75 ± 0.000009 **V** TrqOvlSta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvlSta_LKAPermFault_Cnt_M_lgc 1 ± 1 ソソソソソソソソソソ TrqOvlSta_LKAPermFault_Cnt_M_u08 3 3 + 1 TrqOvlSta_LKAState_State_M_enum 3 3 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.129999995 0.129999995 ± 0.0009765625 TrqOvlSta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 $target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$ 0 0 ± 1 $target_TrqOvlSta_Per1_APAState_State_enum.value$ 0 0 target_TrqOvlSta_Per1_ESCState_State_enum.value 3 3 $target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value$ 0 0 target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

0.129999995

0.425999999

35.3860016

Test Step 2.62 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrgOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrgOvlSta APAState State M enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	797757
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	57547.1211
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	156036
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	43144
TrqOvlSta_Haptictime_Sec_M_f32	102600
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	74.9000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.779999971
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003
k_ESCMaxVehSpd_Kph_f32	170.899994
k_HapticAmplitude_MtrNm_f32	0.456
k_HapticDuration_Sec_f32	1.13999999
k_HapticFreq_Hz_f32	37.4420013
k_HapticReacttime_Sec_f32	1.21599996
k_LKAMaxVehSpd_Kph_f32	144.399994
k_LKAMinVehSpd_Kph_f32	133
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	8.80000019
k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.03999996
k_StandstillTime_Sec_f32	0.305000007
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results and all the properties of the properties o$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_	,	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequi	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-524.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	661.340027		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	7.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	353.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	363.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	57547.1211	57547.1211 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	43144	43144 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.03919983	2.03920007 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.456	0.456 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	37.4420013	37.4420013 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.63 (Repeat Count = 1)	Innut Value
lame	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	3
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	831720
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	58346.8008
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	162582
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	44450
rqOvlSta_Haptictime_Sec_M_f32	105605
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	3
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	76
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	1
_APAMaxHwTrqTime_Sec_f32	0.790000021
APAMaxHwTrg HwNm f32	6.15999985
APAMaxVehSpd Kph f32	138.600006
ESCMaxVehSpd Kph f32	173.300003
HapticAmplitude MtrNm f32	0.462000012
HapticDuration Sec f32	1.15499997
HapticFreq Hz f32	37.9560013
HapticReacttime Sec f32	
	1.23199999
_LKAMaxVehSpd_Kph_f32	146.300003
_LKAMinVehSpd_Kph_f32	134.75
_MaxHapticReqTime_Sec_f32	0.600000024
_SWARLimiter_HwDeg_f32	9.10000038
_ShiftLevRevTime_Sec_f32	0.308999985
_StandstillThresh_Kph_f32	3.07999992
_StandstillTime_Sec_f32	0.308999985
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lç	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur	
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH ESCMfgEnable Cnt Igc	
inget_rite_inst_Ap_friqOviSta_friqOviSta_Feff_GMOSH_LKAMfgEnable_Cnt_lgc	
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFau	ult_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInh	ibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARed				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKASta				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kp					
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kpl					
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvISta_Per1_PosSer				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc					
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Clit_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f3/	target_TrqOvlSta_Per1_PosTraj target_TrqOvlSta_Per1_PosTraj				
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwD					
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_I					
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target TrqOvlSta Per1 TrqOsc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOsc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOsc	Freq_Hz_f32			
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-513.909973				
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0				
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	666.280029 0				
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	0				
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	0				
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	0				
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038				
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	358.5				
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	368.299988				
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0				
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007 18.1000004				
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0				
Name	Actual Value	Expected Value	Popul		
TrqOvlSta_APAState_State_M_enum	4	Expected Value 4	Resul		
TrqOvISta_KFAState_State_M_enum TrqOvISta_ESCState_State_M_enum	4	4			
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1			
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1			
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0			
TrqOvlSta_HapticState_State_M_enum	0	0			
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	•		
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009			
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•		
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•		
TrqOvlSta_LKAState_State_M_enum	4	4	•		
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	76	76 ± 0.0009765625	•		
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1			
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target TrqOvlSta Per1 APAState State enum.value	0 4	0 ± 1			
target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	4	4			
target_TrqOvIsta_Per1_ESCState_State_enum.value	0	0			
target_TrqOvISta_Per1_LKAState_State_enum.value	4	4			
target_TrqOvlota_Fer1_PosServEnable_Cnt_Igc.value	0	0 ± 1			
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	76	76 ± 0.0009765625			
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625			
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•		
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	37.9560013	37.9560013 ± 0.0009765625			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~



est Step 2.64 (Repeat Count = 1)	Input Value
	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
	1
	4
, _ , _ , ,	865683
	59146.4805
	169128
	1
rqOvlSta_Hapticdur_Cnt_M_u16	0
rqOvlSta_Haptictime_Sec_M_f32	108610.617
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.407999992
	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	1
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	77.0999985
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.800000012
_APAMaxHwTrq_HwNm_f32	6.07999992
	136.800003
	175.699997
_	0.456
	1.13999999
	38.4700012
- · · · ·	1.21599996
LKAMaxVehSpd_Kph_f32	144.399994
LKAMinVehSpd_Kph_f32	133
	0.5
	9.39999962
	0.305000007
- · -	3.0399996
	0.305000007
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum
	target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target TrgOvlSta Per1 HapticRequest Cnt lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HwTorque HwNm f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAInhibit Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
	target_TrqOvISta_Fe11_EKAIIIIIIIII_CITI_gc
	target_TrqOvlSta_Per1_LKARtequest_Cnt_gc
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
a	tornet Trachilete Dard Traches Lie 622
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_riqovista_Peri_riqoscrieq_nz_isz
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-503.809998		
target TrgOvlSta Per1 CWFound Cnt Igc.value	1		
target TrgOvlSta Per1 CWPosition HwDeg f32.value	671.219971		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrgOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	363.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	373.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	19.7000008		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Resu
TrqOvlSta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	4	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	59146.4805	59146.4805 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	
TrqOvlSta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.456	0.456 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.65 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	899646
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	59946.1602
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	175674
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	65535
TrqOvlSta_Haptictime_Sec_M_f32	111615
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	78.1999969

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Name		
A. APAMANI-ANTER, 1999. A. APAMANI-ANTER	Name	Input Value
K. APAMANHATTITINE, Sec. 52 K. APAMANHATTITINE, Sec. 52 K. APAMANHATTITINE, Sec. 52 K. APAMANHATTITINE, Sec. 52 K. Haselschaminson, Mr. 12 K. Haselschaminson, Sec. 53 K. Sharkelschaminson, Sec. 53 K. Sharkelschaminson	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
R. PAPMANY Proceedings Process Section Process	k_APAIncludeHaptic_Cnt_lgc	1.
K. PAPMAVerSpot (ph. 12 138.000006	k_APAMaxHwTrqTime_Sec_f32	0.810000002
K. PAPMAVerSpot (ph. 12 138.000006		6.15999985
k. Hapschormlose, Mirhim, 192 k. Hapschormlose, 1932 k. Hapschormlos		138.600006
k. Hapschormlose, Mirhim, 192 k. Hapschormlose, 1932 k. Hapschormlos	k ESCMaxVehSpd Kph f32	178.100006
Management Man		
S. HaptoPreachine Sec. 123		
k LHOMENVERSOR (Np. 1)2 k LKOMENVERSOR (Np. 1)2 k SARABERTER (Np. 1)2 k SA		
k LKAMAVeNSpd Kin 122		
k. LKAM/HAMPORDQ Koh. 122 k. Kamidalinkreh, Sec. 132 k. Kamidalinkreh, Sec. 132 k. Kamidalinkreh, Koh.		
K, SMARLImiter HWDg. [32		
k. SindteuRevTime Sec_132		
k. SindcutilTrives Mp. 122 k. SiandcutilTrives Mp. 123 k. SiandcutilTrives Mp. 124 karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl APARocoverableFaults, Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl APARocuse Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl APARocoverable Mp. 124 karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl APARocoverable Mp. 125 karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl APARocoverable Mp. 125 karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault, Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault Cnt. Igo karget Rte. Inst. Ap. 17q0Vista Trq0Vista Perl ScFoault School Escottle School		
k, SlandstillTriens, Sep. 132 target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APADrvrinterventionDetected_ Crit. target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APADrvrinterventionDetected_ Crit. target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APADrvrinterventionDetected_ Crit. target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APARqueuset, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APARqueuset, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APARqueuset, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APARqueuset, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APARqueuset, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _APARqueuset, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _CWPosition, HwDeg, 132 target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _CWPosition, InvDeg, 132 target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _ESCISta, Intloned, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _ESCISta, Intloned, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _ESCISta, Intloned, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _GNOSH-ObcallateState, State, enum target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _GNOSH-ObcallateState, State, enum target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _GNOSH-DeschialeState, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _GNOSH-DeschialeState, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _ENGAL, LCM, Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _GNOSH-DeschialeState, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _GNOSH-DeschialeState, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _GNOSH-DeschialeState, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _GNOSH-DeschialeState, Crit. Igo target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _HAPrique, Hawhim, 132 target, Rei, Inst. Ap., TrgOviSta, TrgOviSta, Perl _HAPrique, Perl, Inst. Inst. Ap. TrgOviSta		
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target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Hartorque_HwNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARailt_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARailt_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARailt_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARailt_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARailt_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARailt_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARailt_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MarsecureVehicleSpeed_Kpf_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MinSecureVehicleSpeed_Kpf_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortiRieverisinReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SintReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_Mitnm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_PaReoverableFaults_Cnt_Igc_value 0 target_TrqOviSta_Per1_PaReoverableFaults_Cnt_Igc_value 1 target_TrqOviSta_Per1_PaReoverableFaults_Cnt_Igc_value 1 target_TrqOviSta_Per1_APAReoverableFaults_Cnt_Igc_value 1 target_TrqOvi	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_ESCState_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_GMOSH_APMIfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_GMOSH_ESCMIfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_GMOSH_ESCMIfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_GMOSH_ESCMIfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFacult_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFacult_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFacult_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFacult_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_DasServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_SortajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_SortajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_SortajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_SortajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOscEnable_Cnt_Igc target_TrqOvIsta_Perl_DaNonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvIsta_Perl_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvIsta_Perl_APARecove	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HarpticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HarpticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HarpticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HarpticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_STrqOviSta_Per1_ShiffLeverIsInReverse_Cnt_lgc target_TrqOviSta_Per1_DosTrajHoviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_PosTrajHoviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOviSta_Per1_APARequest_Cnt_lgc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
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target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HavTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCAMp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCAMp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_TrqOvlSta_Per1_APANequest_Cnt_lgc target_TrqOvlSta_Per1_APANequest_Cnt_lgc target_TrqOvlSta_Per1_APANequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APANequest_Cnt_lgc.value 1	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enun$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_Per1_BapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorMARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorMARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorMARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorMARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorMARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Per1_SorMARTgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SorMARTgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Per1_APANGCOVPRIDE_FAILSOVENUME 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAReduest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAReduest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCanp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCanp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCanp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCanp_MtrNm_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc_value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc_value target_TrqOvlSta_Per1_APARequest_Cnt_lgc_value target_TrqOvlSta_Per1_APARequest_Cnt_lgc_value target_TrqOvlSta_Per1_APARequest_Cnt_lgc_value target_TrqOvlSta_Per1_APARequest_Cnt_lgc_value	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_Per1_MAState_State_enum target_Rte_Inst_Ap_TrqOvISta_Per1_Mastate_State_enum target_Rte_Inst_Ap_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SifftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARequest_Cnt_lgc.value target_TrqOvISta_Per1_APARequest_Cnt_lgc.value target_TrqOvISta_Per1_APARequest_Cnt_lgc.value	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
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target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwaRtrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwaRtrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrgOvlSta Per1 LKAFault Cnt lgc
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target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_posTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_posTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCenable_Cnt_Igc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvScAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvScAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvScEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvScEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvScFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target TrqOvlSta Per1 CCWFound Cnt lgc.value 0		
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -493.709991		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 0	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 676.159973	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	676.159973
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value 0	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.10000038		8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0		
target TrgOvlSta Per1 LKAInhibit Cnt Igc.value 0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 378.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
g		1

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TrqOvlSta_Per1

Name
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32

Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	21.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	59946.1602	59946.1602 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	175674	175674	•
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	65535	65535 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	78.1999969	78.1999969 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	78.1999969	78.1999969 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	38.9840012	38.9840012 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.66 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	933609
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	60745.8398
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	182220
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	35640
TrqOvlSta_Haptictime_Sec_M_f32	114620
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	79.3000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.819999993
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	11.3000002
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	39.4980011
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	10
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	0 <u>2</u>	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	.lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequi	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-483.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	681.099976		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	373.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	383		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.8999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	60745.8398	60745.8398 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	35640	35640 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	V
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	

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TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	39.4980011	39.4980011 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Namo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	967572
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	61545.5195
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	188766
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	49674
TrqOvlSta_Haptictime_Sec_M_f32	117625.852
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	80.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.829999983
k APAMaxHwTrg HwNm f32	6.48000002
k_APAMaxVehSpd_Kph_f32	145.800003
C ESCMaxVehSpd Kph f32	16.3999996
k_HapticAmplitude_MtrNm_f32	0.48600001
<_HapticDuration_Sec_f32	1.21500003
	40.012001
k_HapticFreq_Hz_f32	1.296
k_HapticReacttime_Sec_f32	
k_LKAMaxVehSpd_Kph_f32	153.899994
k_LKAMinVehSpd_Kph_f32	141.75
k_MaxHapticReqTime_Sec_f32	0
k_SWARLimiter_HwDeg_f32	10.3000002
k_ShiftLevRevTime_Sec_f32	0.324999988
k_StandstillThresh_Kph_f32	3.24000001
k_StandstillTime_Sec_f32	0.324999988
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetec	ted_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFault	s_Cnt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cl	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_Sta	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_t	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvISta_Per1_GillOsh_LRAiniigEriable_v target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-473.51001		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	686.039978		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_rer1_ESCFatit_Crit_tgc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvISta_rer1_ESCISEIIIIIted_GIT_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	378.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	387.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	49674	49674 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	117625.852	117625.852 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25	-0.25 ± 0.0000009	V
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	
TrqOvISta_LKAState_State_M_enum	4	4	*
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	80.4000015	80.4000015 ± 0.0009765625 0 ± 1	
	0	0 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
	4	4	-
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	-
target_IrqoviSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOviSta_Per1_LKAState_State_enum.value	4	4	-
target_rqovista_Peri_LRAState_State_enum.value target_TrqOviSta_Peri_LRAState_State_enum.value	0	0 ± 1	-
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	80.4000015	80.4000015 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.486000001	0.4860000015 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	40.012001	40.012001 ± 0.0009765625	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.68 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1001535
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	62345.1992
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	195312
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	50980
TrqOvlSta_Haptictime_Sec_M_f32	120630
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.100000001 0.474000007
TrgOvlSta LKAFault Cnt M lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	0
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	81.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.839999974
k_APAMaxHwTrq_HwNm_f32	6.55999994
k_APAMaxVehSpd_Kph_f32	147.600006
k_ESCMaxVehSpd_Kph_f32	21.5 0.492000014
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	1.23000002
k_HapticFreq_Hz_f32	40.526001
k_HapticReacttime_Sec_f32	1.31200004
k_LKAMaxVehSpd_Kph_f32	155.800003
k_LKAMinVehSpd_Kph_f32	143.5
k_MaxHapticReqTime_Sec_f32	1
k_SWARLimiter_HwDeg_f32	10.6000004
k_ShiftLevRevTime_Sec_f32	0.328999996
k_StandstillThresh_Kph_f32	3.27999997
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.328999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc target TrqOvlSta Per1 ESCRequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	V - 1
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_IrqOviSta.IrqOviSta_Peri_MaxSecurevenicleSpeed_Kpn_132 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Peri_MinSecureVehicleSpeed_Kph_132	target_rqovista_Peri_maxsecureveniciespeed_kpn_rs2 target_rqovista_Peri_maxsecureveniciespeed_kpn_rs2
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1

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target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value			(- 10-10
target_TrqO/Sia_Pert_CWPosition_HVDeg_152 value 1 target_TrqO/Sia_Pert_ESCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_ESCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_ESCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_ESCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_ESCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_GSCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_GSCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_GSCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_GSCRau(Cnt_lgc_value 1 target_TrqO/Sia_Pert_GSCR_Value(Cnt_lgc_value 1 target_TrqO/Sia_Pert_LgcRAu(Cnt_lgc_value 1 target_TrqO/Sia_Pert_MacGarcevCenticSpeed_Kgh_TS_value 3 3 3 target_TrqO/Sia_Pert_Port_Igrapia(Cnt_lgc_value 1 target_TrqO/Sia_Pert_Sia_Pert_Port_Igrapia(Cnt_lgc_value 1 target_TrqO/Sia_Pert_Port_Igrapia(Cnt_lgc_value 1 target_TrqO/Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pert_Sia_Pe	Name	Input Value		
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-463.410004		
Larget_TrQONSia_Pert_ESCR-aut_Cnt_go:value 1	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOVSta_Pert_ESCRequest_Cnt_lgcvalue	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	690.97998		
Larget_TrqOVISta_Pert_CRORQUEN_CPL_Excause	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
Larget_TropOsta_Pert_GMOSH_ESCMESHable_Cnt_Igo:value 1	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
Larget_TrqOvSta_Perl_GMOSH_ESCM/gEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
Target_TrqOvISta_Pert_HaptoRequest_Ont_Igo_value	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
Larget_TrQO/Sta_Pert_HAVTATULE_HAVNIN_SZV-alue	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Perl_LKAFault_Cnt_lgc_value	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
Larget_TrqOvISta_Perl_LKAInhbit_Cnt_lgc_value	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
Larget_TrqOvISta_Perf_LkARequest_Cnt_lgc.value	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32.value 383 target_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32.value 1 target_TrqOvISta_Perl_PosTrajHwAngle_HwDeg_f32.value 0.129999995 target_TrqOvISta_Perl_SWARTrgdAngRequest_HwDeg_f32.value 26.1000004 target_TrqOvISta_Perl_ShiftLeverIsinReverse_Cnt_lgc value 1 Name Actual Value Expected Value Result TrqOvISta_PState_State_M_enum 0 0 0 0 TrqOvISta_EscSciate_State_Menum 3 3 3 1 TrqOvISta_ElapsedAPAMaxHvTrqTime_Sec_M_f32 0 0 ± 1 0 1 TrqOvISta_ElapsedShintGwRevTime_Sec_M_f32 62345.1992 62345.1992 ± 1 0 1 TrqOvISta_HapticState_State_Menum 1 1 1 1 1 TrqOvISta_HapticState_State_Menum 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 <td>target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value</td> <td>0</td> <td></td> <td></td>	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
LargeL_TrqOvISta_Perf_MinSecureVehicleSpeed_Kph_f32.value 1	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_132 value 0.129999995 target_TrqOvISta_Per1_ShiftLeverIshReverse_Cnt_Igc.value 26.1000004 target_TrqOvISta_Per1_ShiftLeverIshReverse_Cnt_Igc.value 1 Name Actual Value Expected Value Result TrqOvISta_PARAIState_State_Menum 0 0 0 V TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_132 0 0 ± 1 0 + V TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_132 62345.1992 62345.1992 ± 1 V TrqOvISta_ElapsedShiftLevRevTime_Sec_M_132 0 0 0 V TrqOvISta_ElapsedStandstillTime_Sec_M_132 0 0 0 V TrqOvISta_HalpicState_State_M_enum 1 1 1 1 V TrqOvISta_HalpicState_State_M_enum 1 1 1 V TrqOvISta_HalpicState_State_M_enum 1 1 1 V TrqOvISta_HalpicState_State_M_enum 1 1 1 1 1 1 1 1 1 1 1 1 1 1 <t< td=""><td>target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value</td><td>383</td><td></td><td></td></t<>	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	383		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_132 value 0.129999995 target_TrqOvISta_Per1_ShiftLeverIshReverse_Cnt_Igc.value 26.1000004 target_TrqOvISta_Per1_ShiftLeverIshReverse_Cnt_Igc.value 1 Name Actual Value Expected Value Result TrqOvISta_PARAIState_State_Menum 0 0 0 V TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_132 0 0 ± 1 0 + V TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_132 62345.1992 62345.1992 ± 1 V TrqOvISta_ElapsedShiftLevRevTime_Sec_M_132 0 0 0 V TrqOvISta_ElapsedStandstillTime_Sec_M_132 0 0 0 V TrqOvISta_HalpicState_State_M_enum 1 1 1 1 V TrqOvISta_HalpicState_State_M_enum 1 1 1 V TrqOvISta_HalpicState_State_M_enum 1 1 1 V TrqOvISta_HalpicState_State_M_enum 1 1 1 1 1 1 1 1 1 1 1 1 1 1 <t< td=""><td>target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32.value</td><td>392.799988</td><td></td><td></td></t<>	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	392.799988		
target_TrqOviSta_Perl_SWARTrgIAngRequest_HwDeg_f32.value 26.1000004 Arget_TrqOviSta_Perl_ShiftLeverIsinReverse_Cnt_Igc.value 1 Name Actual Value Expected Value Result TrqOviSta_PSCState_State_Menum 0 0 0 TrqOviSta_ElapsedAPAMAXHvTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOviSta_ElapsedStandstillTime_Sec_M_f32 0 0 0 0 TrqOviSta_HapticState_State_Menum 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOviSta_Perl_SWARTrgIAngRequest_HwDeg_f32.value 26.1000004 Arget_TrqOviSta_Perl_ShiftLeverIsinReverse_Cnt_Igc.value 1 Name Actual Value Expected Value Result TrqOviSta_PSCState_State_Menum 0 0 0 TrqOviSta_ElapsedAPAMAXHvTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOviSta_ElapsedStandstillTime_Sec_M_f32 0 0 0 0 TrqOviSta_HapticState_State_Menum 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		0.129999995		
Targot Targ		26.1000004		
Name Actual Value Expected Value Result TrqOvISta_PAPAState_State_M_enum 0 0 0 TrqOvISta_ESGState_State_M_enum 3 3 4 TrqOvISta_ElapsedAPAMAXHWTrqTime_Sec_M_f32 0 0±1 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 62345.1992 62345.1992±1 4 TrqOvISta_HapticState_State_M_enum 1 1 1 TrqOvISta_HapticState_State_M_enum 1 1 1 TrqOvISta_HapticState_State_M_enum 1 1 1 TrqOvISta_HapticState_State_M_f32 120630 120630±1 4 TrqOvISta_HapticState_State_M_f32 1 1 1 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 1 1 1 TrqOvISta_LKAPermFault_Cnt_M_u08 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		1		
TrqovISta_APAState_State_M_enum 0 0 0 TrqovISta_ESCState_State_M_enum 3 3 3 TrqovISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0±1 1 TrqovISta_ElapsedShiftLevRevTime_Sec_M_f32 62345.1992 62345.1992±1 2 TrqovISta_ElapsedStandstillTime_Sec_M_f32 0 0 0 TrqovISta_Hapticstate_State_M_enum 1 1 1 TrqovISta_Hapticstur_Crt_M_u16 50980 50980 ±1 1 TrqovISta_Hapticstur_Crt_M_u16 50980 120630 ±1 2 TrqovISta_Hapticstur_Sec_M_f32 120630 120630 ±1 2 TrqovISta_Hapticstur_Crt_M_u16 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		Actual Value	Expected Value	Result
TrqovISta_Elsate_Menum 3 3 TrqovISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqovISta_ElapsedShiftLevRevTime_Sec_M_f32 62345.1992 62345.1992 ± 1 TrqovISta_ElapsedStandstiliTime_Sec_M_f32 0 0 TrqovISta_HapticState_State_Menum 1 1 TrqovISta_HapticState_State_Menum 1 1 TrqovISta_HapticGtmc_Cnt_M_u16 50980 50980 ± 1 TrqovISta_HapticGtmc_Sec_M_f32 120630 120630 ± 1 TrqovISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.100000001 0.100000001 ± 0.0000009 TrqovISta_LKAPault_Cnt_M_lgc 1 1 ± 1 TrqovISta_LKAPermFault_Cnt_M_lgc 0 0 ± 1 TrqovISta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqovISta_LKAState_State_M_enum 0 0 0 0 0.129999995 ± 0.0009765625 TrqovISta_PosSrvoHwAngle_HwDeg_M_f32 0.129999995 0.129999995 ± 0.0009765625 TrqovISta_Perl_APAState_State_enum.value 0 0 ± 1 target_TrqovISta_Perl_APAState_State_enum.value 0 0 target_TrqovISta_Perl_PosS			•	1100011
TrqOvISta_ElapsedAPMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 62345.1992 62345.1992 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 0 TrqOvISta_HapticState_State_M_enum 1 1 1 TrqOvISta_Hapticdur_Cnt_M_uf6 50980 50980 ± 1 1 TrqOvISta_Hapticdur_Cnt_M_uf6 50980 120630 ± 1 2 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.100000001 0.100000001 ± 0.0000009 2 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 <td></td> <td></td> <td>•</td> <td></td>			•	
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_I32 62345.1992 62345.1992 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_I32 0 0 TrqOvISta_HapticState_State_Menum 1 1 TrqOvISta_Hapticdur_Cnt_M_u16 50980 50980 ± 1 TrqOvISta_Haptictime_Sec_M_I32 120630 120630 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_I32 0.100000001 0.100000001 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_Igc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_Igc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_PosSrvoHwAngle_HwDeg_M_I32 0.129999995 0.129999995 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_Igc 0 0 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 0 0 1 target_TrqOvISta_Per1_APAState_State_enum.value 3 3 3 target_TrqOvISta_Per1_ESCState_State_enum.value				
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_Menum 1 1 TrqOvISta_Hapticotr_Cnt_M_u16 50980 50980 ± 1 TrqOvISta_Haptictime_Sec_M_f32 120630 120630 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.100000001 0.100000001 ± 0.0000009 TrqOvISta_LKAPault_Cnt_M_igc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_igc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_PersonHwAngle_HwDeg_M_f32 0.129999995 0.129999995 ± 0.0009765625 TrqOvISta_PersonHwAngle_HwDeg_M_f32 0 0 ± 1 TrqOvISta_PersonHwAngle_HwDeg_M_f32 0 0 0 ± 1 TrqOvISta_PersonHw			·	
TrqOvISta_HapticState_State_Menum 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				
TrqOvISta_Hapticdur_Ont_M_u16 50980 50980 ± 1 TrqOvISta_Haptictime_Sec_M_f32 120630 120630 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.100000001 0.100000001 ± 0.0000009 TrqOvISta_LKAPeutl_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.129999995 0.129999995 ± 0.0009765625 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0 0 ± 1 TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 3 3 target_TrqOvISta_Per1_ESCState_State_tenum.value 1 1 target_TrqOvISta_Per1_CAState_State_enum.value 0 0 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosSrvOHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 target_TrqOvISta_Per1_TrqO	_ · ·		•	
TrqOvISta_Haptictime_Sec_M_f32 120630 120630 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.100000001 0.100000001 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_LKAState_State_Menum 0 0 TrqOvISta_LKAState_State_Menum 0 0 TrqOvISta_PeoSrvoHwAngle_HwDeg_M_f32 0.129999995 0.129999995 ± 0.0009765625 TrqOvISta_PeadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrivrinterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 0 0 0 target_TrqOvISta_Per1_BoundsHoscillateState_enum.value 1 1 1 target_TrqOvISta_Per1_CMOSHOscillateState_enum.value 0 0 0 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 1 target_TrqOvISta_Per1_PosSr				
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.100000001 0.100000001 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_Igc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_Igc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_DosSrvoHwAngle_HwDeg_M_f32 0.129999995 0.129999995 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_Igc 0 0 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 3 3 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvISta_Per1_BosServEnable_Cnt_Igc.value 0 0 target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value 1 1 ± 1 target_TrqOvISta_Per1_PosSrvOHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 target_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32.value 0.492000014 0.492000014 ± 0.0009765625				
TrqOvISta_LKAFault_Cnt_M_lgc 1 1±1 TrqOvISta_LKAPermFault_Cnt_M_lgc 0 0±1 TrqOvISta_LKAPermFault_Cnt_M_u08 1 1±1 TrqOvISta_LKAState_State_M_enum 0 0 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.129999995 0.129999995±0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0±1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0±1 target_TrqOvISta_Per1_APAState_State_enum.value 0 0 target_TrqOvISta_Per1_ESCState_State_enum.value 3 3 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvISta_Per1_LKAState_State_enum.value 0 0 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1±1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995±0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.492000014 0.492000014±0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1				
TrqOvISta_LKAPermFault_Cnt_M_lgc				
TrqOvlSta_LKAPermFault_Cnt_M_u08 1 1±1 TrqOvlSta_LKAState_State_M_enum 0 0 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.129999995 0.129999995±0.0009765625 TrqOvlSta_ReadytoPulse_Cnt_M_gc 0 0±1 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0±1 target_TrqOvlSta_Per1_APAState_State_enum.value 0 0 target_TrqOvlSta_Per1_ESCState_State_enum.value 3 3 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvlSta_Per1_LKAState_State_enum.value 0 0 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1±1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0.492000014 0.492000014 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1				
TrqOvlSta_LKAState_State_M_enum 0 0 129999995 0.129999995 ± 0.0009765625 0 129999995 ± 0.0009765625 0 129999995 ± 0.0009765625 0 0 129999995 ± 0.0009765625 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.129999995 0.129999995 ± 0.0009765625 TrqOvlSta_ReadytoPulse_Cnt_M_gc 0 0 ± 1 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_APAState_State_enum.value 0 0 target_TrqOvlSta_Per1_ESCState_State_enum.value 3 3 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvlSta_Per1_LKAState_State_enum.value 0 0 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1±1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.492000014 0.492000014 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1		·		
TrqOvlSta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_APAState_State_enum.value 0 0 target_TrqOvlSta_Per1_ESCState_State_enum.value 3 3 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvlSta_Per1_LKAState_State_enum.value 0 0 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.492000014 0.492000014 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 ± 1				
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value				
target_TrqOvlSta_Per1_APAState_State_enum.value 0 0 target_TrqOvlSta_Per1_ESCState_State_enum.value 3 3 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvlSta_Per1_LKAState_State_enum.value 0 0 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1±1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995±0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.492000014 0.492000014±0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1				
target_TrqOvlSta_Per1_ESCState_State_enum.value 3 3 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvlSta_Per1_LKAState_State_enum.value 0 0 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1±1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.492000014 0.492000014 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1				
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 1 target_TrqOvlSta_Per1_LKAState_State_enum.value 0 0 0 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 0.492000014 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0.492000014 0.492000014 ± 0.0009765625 0.492000014 ± 0.0009765625				
target_TrqOvlSta_Per1_LKAState_State_enum.value				
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1±1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.492000014 0.492000014 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1				
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.129999995 0.129999995 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.492000014 0.492000014 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 ± 1				
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value				
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 1 1	· · · · · · · · · · · · · · · · · · ·			
######################################		·		
	target_TrqOviota_Fet1_TrqOstFteq_Ftz_132.Value	40.020001	40.32000 I ± 0.0009/03023	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.69 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1035498
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63144.8789
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	201858
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	52286
TrqOvlSta_Haptictime_Sec_M_f32	123635
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.449999988
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	82.5999985

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.850000024
k_APAMaxHwTrq_HwNm_f32	6.63999987
k_APAMaxVehSpd_Kph_f32	149.399994
k_ESCMaxVehSpd_Kph_f32	26.6000004
k_HapticAmplitude_MtrNm_f32	0.497999996
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	1.245 41.0400009
k HapticReacttime Sec f32	1.32799995
k_LKAMaxVehSpd_Kph_f32	157.699997
k_LKAMinVehSpd_Kph_f32	145.25
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	10.8999996
k_ShiftLevRevTime_Sec_f32	0.333000004
k_StandstillThresh_Kph_f32	3.31999993
k_StandstillTime_Sec_f32	0.333000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-453.309998
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	695.919983 0
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-7.5999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LnArkequest_Cnt_gc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	387.899994
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	397.700012
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	27.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63144.8789	63144.8789 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	52286	52286 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	123635	123635 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988	0.449999988 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	82.5999985	82.5999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	82.5999985	82.5999985 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.497999996	0.497999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	41.0400009	41.0400009 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.70 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1069461
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63944.5586
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	208404
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	53592
TrqOvlSta_Haptictime_Sec_M_f32	126640.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.800000012
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	83.6999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.860000014
k_APAMaxHwTrq_HwNm_f32	6.71999979
k_APAMaxVehSpd_Kph_f32	151.199997
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.504000008
k_HapticDuration_Sec_f32	1.25999999
k_HapticFreq_Hz_f32	41.5540009
k_HapticReacttime_Sec_f32	1.34399998
k_LKAMaxVehSpd_Kph_f32	159.600006
k_LKAMinVehSpd_Kph_f32	147
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	11.1999998
k_ShiftLevRevTime_Sec_f32	0
k_StandstillThresh_Kph_f32	3.3599999
k_StandstillTime_Sec_f32	0.100000001
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target TrqOvlSta Per1 CCWPosition HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	.9_102	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_le	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target TrqOvISta Per1 GMOSH APAMfgEn		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_RAState_State_endin target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRevers target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-443.209991		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	700.859985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Fer1_GwloSn_ExAlwigErlable_Crit_igc.value target TrqOvlSta Per1 HapticRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-7.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	402.600006		
target_IrqOviSta_Per1_PosTrajEnable_Cnt_igc.value target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	29.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	63944.5586 208404	63944.5586 ± 1 208404	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdut_Cnt_M_u16	53592	53592 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	126640.641	126640.641 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.800000012	0.800000012 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	3	4 ± 1 3	~
TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	-

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.504000008	0.504000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	41.5540009	41.5540009 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	•
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.71 (Repeat Count = 1)	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrgOvlSta APAState State M enum	2
rqOvISta_ESCState_State_M_enum	1
rqOvISta_ESCState_State_M_endin	1103424
rqOvISta_EtapsedAFAMaXHWTIqTiffle_Sec_M_IS2	64744.2383
	214950
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	2
rqOvISta_HapticState_State_M_enum	
rqOvISta_Hapticdur_Cnt_M_u16	54898
rqOvISta_Haptictime_Sec_M_f32	129645
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.14999998
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	
rqOvISta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	84.8000031
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.870000005
_APAMaxHwTrq_HwNm_f32	6.80000019
_APAMaxVehSpd_Kph_f32	153
_ESCMaxVehSpd_Kph_f32	36.7999992
_HapticAmplitude_MtrNm_f32	0.50999999
_HapticDuration_Sec_f32	1.27499998
_HapticFreq_Hz_f32	42.0680008
_HapticReacttime_Sec_f32	1.36000001
_LKAMaxVehSpd_Kph_f32	161.5
_LKAMinVehSpd_Kph_f32	148.75
_MaxHapticReqTime_Sec_f32	0.69999988
_SWARLimiter_HwDeg_f32	11.5
_ShiftLevRevTime_Sec_f32	0.5
_StandstillThresh_Kph_f32	3.4000001
_StandstillTime_Sec_f32	0.479999989
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cr	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
.irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lg	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Ig	
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lg	3		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_l	gc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt	_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph	_f32 target_TrqOvlSta_Per1_MaxSecureVehic	eSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_	f32 target_TrqOvlSta_Per1_MinSecureVehicle	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_0	Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle	_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_C	nt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle	_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDe	g_f3: target_TrqOvlSta_Per1_SWARTrgtAngRe	quest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igo	target_TrqOvlSta_Per1_ShiftLeverIsInRev	verse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrN	lm_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_C	nt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_	f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-433.109985			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	705.799988			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0			
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.5999999			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	397.700012			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	407.5			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.8999996			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	4	4	-	
TrqOvlSta ESCState State M enum	4	4	•	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32				
1 2 2	0	0 ± 1		
TrgOvISta_ElapsedShiftLevRevTime_Sec_M_f32		0 ± 1 64744.2383 ± 1		
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	64744.2383	64744.2383 ± 1		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	64744.2383 0	64744.2383 ± 1	•	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	64744.2383 0 2	64744.2383 ± 1 0 2	•	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	64744.2383 0 2 54898	64744.2383 ± 1 0 2 54898 ± 1		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	64744.2383 0 2 54898 0	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	64744.2383 0 2 54898 0 1.14999998	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	64744.2383 0 2 54898 0 1.14999998	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	64744.2383 0 2 54898 0 1.14999998 1	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	64744.2383 0 2 54898 0 1.14999998 1 1	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 1 ± 1		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	64744.2383 0 2 54898 0 1.14999998 1 1	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 1 ± 1		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031 1	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1 0 ± 1		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031 1 0	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1 0 ± 1 4		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031 1 0 4	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1 0 ± 1 4		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031 1 0 4	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1 0 ± 1 4		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031 1 0 4 4	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1 0 ± 1 4		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031 1 0 4 4	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2 4 0 ± 1		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031 1 0 4 4 2 4 0 84.8000031	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2 4 0 ± 1 84.8000031 ± 0.0009765625		
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value	64744.2383 0 2 54898 0 1.14999998 1 1 1 4 84.8000031 1 0 4 4	64744.2383 ± 1 0 2 54898 ± 1 0 ± 1 1.14999998 ± 0.000009 1 ± 1 1 ± 1 4 84.8000031 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2 4 0 ± 1		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



est Step 2.72 (Repeat Count = 1)	Input Value
te_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1137387
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	65543.9219
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	221496
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	56204
rqOvlSta_Haptictime_Sec_M_f32	132650
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.518000007
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	85.9000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	0.87999995
APAMaxHwTrq_HwNm_f32	6.88000011
APAMaxVehSpd_Kph_f32	154.800003
ESCMaxVehSpd_Kph_f32	41.9000015 0.515999973
HapticAmplitude_MtrNm_f32 HapticDuration_Sec_f32	1.28999996
HapticFreq_Hz_f32	42.5820007
HapticReacttime_Sec_f32	1.37600005
_LKAMaxVehSpd_Kph_f32	163.399994
LKAMinVehSpd_Kph_f32	150.5
MaxHapticReqTime_Sec_f32	0.800000012
SWARLimiter_HwDeg_f32	11.8000002
ShiftLevRevTime_Sec_f32	0.300000012
StandstillThresh Kph f32	3.4400006
StandstillTime_Sec_f32	0.349999994
 irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
rget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
rrget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value rrget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
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Trigovista_r err			
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-423.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	710.73999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	402.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	412.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	32.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	rtooun
TrgOvISta ESCState State M enum	3	3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvISta ElapsedShiftLevRevTime Sec M f32	65543.9219	65543.9219 ± 1	
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrqOvlSta HapticState State M enum	0	0	_
TrqOvISta_Hapticdur_Cnt_M_u16	56204	56204 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5	1.5 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta LKAPermFault Cnt M Igc	0	0 ± 1	
	2	2 ± 1	
TrqOvISta_LKAStoto_State_M_agum	2	2	
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.170000002	0.170000002 ± 0.0009765625	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0.170000002	0.170000002 ± 0.0009763625	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1 0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
	3	3	
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	
target_TrqOvlSta_Per1_LKAState_State_enum.value			
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.515999973	0.515999973 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	42.5820007	42.5820007 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.73 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1171350
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	66343.6016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	228042
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	57510
TrqOvlSta_Haptictime_Sec_M_f32	135655.938
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.85000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.528999984
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	87

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***APAMANTOTION**, Sep. 12** ***APAMANTOTION**, Sep. 12** ***APAMANTOTION**, Prof. 12** ***APAMA	_	
A PAPAModeletagic Cell Jgs A PAPAModeletagic Min. Spc. 123 A Papamodeletagic Min. Spc. 1	Name	Input Value
A PAMAGHATOR THAN 1952 A PAMAGHATOR THAN 1952 A PAMAGHATOR THAN 1952 A PAMAGHATOR THAN 1952 A SCHAWAWSBI (Ap. 1952 A SCHAWAWSBI (Ap. 1952 A SCHAWAWSBI (Ap. 1952 A SCHAWAWSBI (Ap. 1952 A SUBJECTION OF THE APER SCHAWAWSBI (Ap. 1952 A SUBJECTION OF THE APER SCHAWAWSBI (Ap. 1952 A SUBJECTION OF THE APER SCHAWAWSBI (Ap. 1952 A SUMACHIME PAMAGHATOR SCH. 295 A SUMACHIME PAMAGHATOR SCH.	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APANAMANY APAN	k_APAIncludeHaptic_Cnt_lgc	0
APANAMANY APAN	k_APAMaxHwTrqTime_Sec_f32	0.889999986
R. SCHMANVENSEQ MP. II	k APAMaxHwTrq HwNm f32	6.96000004
Appachamiston Mrhtm, 152		156.600006
Appachamiston Mrhtm, 152	k ESCMaxVehSpd Kph f32	47
Separation Sec. 92 13,9499995 13,949995 18,940074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,94074 19,9		
N. Hapitching Her. 122 133 199996 1. 1. 1. 1. 1. 1. 1. 1		
k_JAMAWAYSBA_KP.12 k_JAMAWAYSAA_KP.12 k_JAMA		
k LXMAW/eNSpd Kph 12 k JAMAHydin Kph 13 k JAMAHydin		
LIAMANYONSOL (No.132 k. MoHalpicher M. 1962 p. 12 k. MoHalpicher M. 1962 p. 1962 p		
K_SMARLimber Hvi0eg 732 k_ SinhtLerNartime_Sec_922 k_ SinhtLerNartime_Sec_922 d_ 330000014 k_ SinhtLerNartime_Sec_922 d_ 330000014 d_ 33	_ : - : -	
k, SMMLR/ReVTM Sec. 122 1, StandatilTime, Sec. 123 1, St		
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target_Rie_Inst_Ap_TrqOviSta_Perl_GMOSHOscillateState_State_enum target_Rie_Inst_Ap_TrqOviSta_Perl_GMOSH_APAM/gEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Perl_GMOSH_ESOM/gEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESOM/gEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HavForque_HwNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAState_state_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAState_state_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAState_state_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAState_state_enum target_Rie_Inst_Ap_TrqOviSta_Perl_LKAState_state_enum target_Rie_Inst_Ap_TrqOviSta_Perl_LKAState_state_enum target_Rie_Inst_Ap_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajeInable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajeInable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajeInable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_StrajeHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_StrajeHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_StrajeHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_Mithim_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_Mithim_f32 target_TrqOviSta_Perl_TrqOscAmp_Mithim_f32 target_TrqOviSta_Perl_TrqOscAmp_Mithim_f32 target_TrqOviSta_Perl_TrqOscEnable_Cnt_Igc target_TrqOviSta_Perl_TrqOscEnable_Cnt_Igc target_Trq	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_GMOSH_CAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_HAPTICRQUEST_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kpf_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kpf_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PostrovPehicleSpeed_Kpf_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOscAmp_MtrN	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_HarbicRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKARstate_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKARstate_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKARstate_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_MassecureVehicleSpeed_Kpf_32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_MinSecureVehicleSpeed_Kpf_32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_PosTrqiEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_PosTrqiEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_PosTrqiEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_SortIttevertsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_SintReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOsCenable_Cnt_Igc target_TrqOvIsta_Per1_TrqOsCenable_Cnt_Igc targe	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate$	target_TrqOvISta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_HavTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_HavTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosSrvoHwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_ShiffLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_ShiffLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_TrqOvIsta_Perl_DrqOsCenable_Cnt_Igc target_TrqOvIsta_Perl_DrqOsCenable_Cnt_Igc target_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_TrqOvIsta_Perl_APARequest_Cnt_Igc.value target_TrqOvIsta_Perl_CCWPoontd_Cnt_Igc.value 1 target_TrqOvIsta_Perl_CCWPoontd_Cnt_Igc.value 1 target_TrqOvIsta_Perl_CCWPoontd_Cnt_Igc.value 1 0 1 target_TrqOvIsta_Perl_CCWPoontd_Cnt_Igc.value 1 0 1 target_TrqOvIsta_Perl_CCWPoontd_Cnt_Igc.value 1 0 1 target_TrqOvIsta_Perl_CCWPoontd_Cnt_Igc.value 1 0 1 15679993	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrqiHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrqiHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrqiHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SWARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SWARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value 0 target_TrqOviSta_Per1_CWFound_Cnt_Igc.value 10 target_TrqOviSta_Per1_CWFound_Cnt_Igc.value 10 target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value 11 target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value 11 t	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Starget_Request_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Starget_Request_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Starget_Request_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Starget_Request_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Starget_Request_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Starget_Request_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCFreq_Htz_f32 target_TrqOviSta_Perl_TrqOsCFreq_Htz_f32 target_TrqOviSta_Perl_APANonRecoverableFaults_Cnt_Igc_value 0 target_TrqOviSta_Perl_APARequest_Cnt_Igc_value 1 target_TrqOviSta_Perl_APARequest_Cnt_Igc_value 0 target_TrqOviSta_Perl_CWFound_Cnt_Igc_value 0 target_TrqOviSta_Perl_CWFound_Cnt_Igc_value 0 target_TrqOviSta_Perl_CWFound_Cnt_Igc_value 0 target_TrqOviSta_Perl_CWFound_Cnt_Igc_value 0 target_TrqOviSta_Perl_CWFound_Cnt_Igc_value 0 target_TrqOviSta_Perl_CWFound_Cnt_Value 0 target_TrqOviSta_Perl_CWFound_Cnt_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_StrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_StrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscFreq_Hz_f32 target_TrqOviSta_Perl_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Perl_APARecoverableFaults_Cnt_Igc_value 1 target_TrqOviSta_Perl_CWPound_Cnt_Igc_value 2 target_TrqOviSta_Perl_CWPound_Cnt_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_CCWPosition_HwDeg_f32_value 10 target_TrqOvlSta_Pert_CWPound_Cnt_lgc.value 21 target_TrqOvlSta_Pert_CWPound_Cnt_Ugc.value 22 target_TrqOvlSta_Pert_CWPound_Cnt_Ugc.value 23 tar	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_SorTajEnable_Cnt_lgc target_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_TrqOvlSta_Pert_CCWPosition_HwDeg_f32_value 10 target_TrqOvlSta_Pert_CWPound_Cnt_lgc.value 21 target_TrqOvlSta_Pert_CWPound_Cnt_Ugc.value 22 target_TrqOvlSta_Pert_CWPound_Cnt_Ugc.value 23 tar	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt lgc
target_Rte_Inst_Ap_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SHiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 2 target_TrqOvISta_Per1_CCWPound_Cnt_Igc.value 3 target_TrqOvISta_Per1_CCWPosition_HwDeg_f32_value 412.910004 target_TrqOvISta_Per1_CWPosition_HwDeg_f32_value 412.910004		
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target_Rte_Inst_Ap_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_PosSrotHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 2 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 2 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 3 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 4 112.910004		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 2 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 3 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 4 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 5 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 5 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 7 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	· · · · · · · · · · · · · · · · · · ·	
target_Rte_Inst_Ap_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOviSta_Per1_APARequest_Cnt_Igc.value target_TrqOviSta_Per1_APARequest_Cnt_Igc.value target_TrqOviSta_Per1_APARequest_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_Rte_Inst_Ap_TrqOvlSta_Per1_posTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscEnable_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value 412.910004 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value 0 target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value 0 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value 412.910004 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value 0 target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value 412.910004 target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value 0 target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value 10 target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value 715.679993	· · · · · · · · · · · · · · · · · · ·	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 412.910004 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 175.679993		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -412.910004 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value -412.910004 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -412.910004 target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value 0 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 715.679993	· · · · · · · · · · · · · · · · · · ·	-412.910004
	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	715.679993
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value 0	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value 1		
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-5.5999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0	· · · · - · · - · - · · · · · · · · · ·	
	target TrqOvlSta Per1 LKAlnhibit Cnt Igc.value	
target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 0	· - ·	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 407.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 417.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.180000007		
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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-33.0999985		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	57510	57510 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	135655.938	135655.938 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.09104967	-2.09104991 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	87	87 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	87	87 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.522000015	0.522000015 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	98.5240021	98.5240021 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.74 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOviSta
TrqOvISta APAState State M enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1205313
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	67143.2813
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	234588
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	58816
TrqOvlSta_Haptictime_Sec_M_f32	138660
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.9000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.540000021
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	88.0999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.899999976
k_APAMaxHwTrq_HwNm_f32	7.03999996
k_APAMaxVehSpd_Kph_f32	158.399994
k_ESCMaxVehSpd_Kph_f32	52.0999985
k_HapticAmplitude_MtrNm_f32	0.527999997
k_HapticDuration_Sec_f32	1.32000005
k_HapticFreq_Hz_f32	85
k_HapticReacttime_Sec_f32	1.40799999
k_LKAMaxVehSpd_Kph_f32	167.199997
k_LKAMinVehSpd_Kph_f32	154
k_MaxHapticReqTime_Sec_f32	0.0149999997
k_SWARLimiter_HwDeg_f32	12.3999996
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	5
k_StandstillTime_Sec_f32	0.00100000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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<u>-</u>			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	•	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	·g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	400	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target TrqOvISta Per1 ESCIsLimited Cnt Ig	nc .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target TrqOvlSta Per1 LKAState State enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	0 - 1		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvISta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	- -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-402.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	720.619995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target TrqOvlSta Per1 LKARequest Cnt lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	412.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	422.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-31.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlOta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	*
TrqOvlSta_HapticState_State_M_enum TrqOvlSta Hapticdur Cnt M u16	58816	58816 ± 1	Ž
TrqOvISta_Haptictime Sec M f32	0	0 ± 1	-
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.9000001	-6.9000001 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvISta_LKAState_State_M_enum	0	0	~
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.527999997	0.527999997 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	85	85 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.75 (Repeat Count = 1)	ر.
Name	Input Value
	•
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1239276
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	67942.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	241134
TrqOvISta_HapticState_State_M_enum	60122
TrqOvlSta_Hapticdur_Cnt_M_u16	
TrqOvlSta_Haptictime_Sec_M_f32	141665
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.55000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.550999999
TrqOvlSta_LKAFault_Cnt_M_lgc	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvISta_LKAState_State_M_enum	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	89.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.910000026
k_APAMaxHwTrq_HwNm_f32	7.11999989
k_APAMaxVehSpd_Kph_f32	160.199997
k_ESCMaxVehSpd_Kph_f32	57.2000008
k_HapticAmplitude_MtrNm_f32	0.533999979
k_HapticDuration_Sec_f32	1.33500004
k_HapticFreq_Hz_f32	72.697998
k_HapticReacttime_Sec_f32	1.42400002
k_LKAMaxVehSpd_Kph_f32	169.100006
k_LKAMinVehSpd_Kph_f32	155.75
k_MaxHapticReqTime_Sec_f32	0.029999993
k_SWARLimiter_HwDeg_f32	12.699998
k_ShiftLevRevTime_Sec_f32	0.0049999989
k_StandstillThresh_Kph_f32	3.5
k_StandstillTime_Sec_f32	0.0049999989
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f	32 target_TrqOvlSta_Per1_MaxSecureVehicles	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_from the property of t$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr	ıt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_I	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_			
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc$	target_TrqOvlSta_Per1_ShiftLeverIsInRever	rse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn	1_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-392.709991		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	725.559998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target TrqOvlSta_Per1_HwTorque HwNm f32.value	4.9000001		
	1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target TrqOvlSta Per1 LKAInhibit Cnt lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta_Fer1_ENARequest_Officingc.value target TrqOvlSta_Per1_MaxSecureVehicleSpeed Kph f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	427.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-29.8999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	67942.9609	67942.9609 ± 1	•
TrgOvlSta ElapsedStandstillTime Sec M f32	241134	241134	
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrgOvlSta Hapticdur Cnt M u16	60122	60122 ± 1	~
TrgOvISta Haptictime Sec M f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.55000019	-6.55000019 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc			
Transista LKADara Fault Cat M. Ira	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc		1±1 1±1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	1		· ·
·	1	1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_LKAPermFault_Cnt_M_u08	1 1 1	1±1 1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1 1 1 4	1 ± 1 1 ± 1 4	*
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1 1 1 4 89.1999969	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625	*
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1 1 1 4 89.1999969 1	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625 1 ± 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1 1 1 4 89.1999969 1	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625 1 ± 1 0 ± 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	1 1 1 4 89.1999969 1 0	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625 1 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	1 1 1 4 89.1999969 1 0 4	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625 1 ± 1 0 ± 1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	1 1 1 4 89.1999969 1 0 4 4	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625 1 ± 1 0 ± 1 4 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	1 1 1 4 89.1999969 1 0 4 4 2	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value	1 1 1 4 89.1999969 1 0 4 4 4 2 4 0 89.1999969 0.533999979	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2 4 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 1 1 4 89.1999969 1 0 4 4 2 4 0 89.1999969	1 ± 1 1 ± 1 4 89.1999969 ± 0.0009765625 1 ± 1 0 ± 1 4 4 2 4 0 ± 1 89.1999969 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.76 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1273239
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	68742.6406
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	247680
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	61428
TrqOvlSta_Haptictime_Sec_M_f32	144670
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.1999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.561999977
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAFault_Cnt_M_lga	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvISta_LKAState_State_M_enum	4
TrgOvlSta PosSrvoHwAngle HwDeg M f32	10.1999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.920000017
k_APAMaxHwTrq_HwNm_f32	7.19999981
k_APAMaxVehSpd_Kph_f32	162
k_ESCMaxVehSpd_Kph_f32	62.2999992
k_HapticAmplitude_MtrNm_f32	0.540000021
k_HapticDuration_Sec_f32	1.35000002
k_HapticFreq_Hz_f32	44.6380005
k_HapticReacttime_Sec_f32	1.44000006
k_LKAMaxVehSpd_Kph_f32	171
k_LKAMinVehSpd_Kph_f32	157.5 0.0450000018
k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32	13
k ShiftLevRevTime Sec f32	0.00899999961
k_StandstillThresh_Kph_f32	3.5999999
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFaults_Cnt_lgarget_RecoverableFau$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132	target_IrqOviSta_Per1_minSecure venicleSpeed_kpn_ts2 target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	1
	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

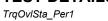
2015-02-04, 12:34:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	730.5		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	422.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	432		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	61428	61428 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	144670	144670 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981	-6.19999981 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	•
	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	~
		0.540000021 ± 0.0009765625 0 ± 1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.77 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1307202
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	69542.3203
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	254226
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	62734
TrqOvlSta_Haptictime_Sec_M_f32	147675.844
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.8499999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.573000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.6000004





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.930000007
k_APAMaxHwTrq_HwNm_f32	7.28000021
k_APAMaxVehSpd_Kph_f32	163.800003
k_ESCMaxVehSpd_Kph_f32	67.4000015
k_HapticAmplitude_MtrNm_f32	0.546000004
k_HapticDuration_Sec_f32	1.36500001
k_HapticFreq_Hz_f32	45.1520004
k_HapticReacttime_Sec_f32	1.45599997
k_LKAMaxVehSpd_Kph_f32	172.899994
k_LKAMinVehSpd_Kph_f32	159.25
k_MaxHapticReqTime_Sec_f32	0.0599999987 13.3000002
k_SWARLimiter_HwDeg_f32 k ShiftLevRevTime Sec f32	0.0130000003
k StandstillThresh Kph f32	3.6400001
k_StandstillTime_Sec_f32	0.5
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-372.51001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	735.440002 0
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.30000019
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	427.100006
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	436.899994
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0 0 110000007
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	62734	62734 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	147675.844	147675.844 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.8499999	-5.8499999 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.6000004	11.6000004 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	11.6000004	11.6000004 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.546000004	0.546000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	45.1520004	45.1520004 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.78 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	3	
TrqOvlSta_ESCState_State_M_enum	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1341165	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	70342	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	260772	
TrqOvlSta_HapticState_State_M_enum	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	64040	
TrqOvlSta_Haptictime_Sec_M_f32	150680	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.583999991	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	
TrqOvlSta_LKAState_State_M_enum	1	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	13	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAIncludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.93999998	
k_APAMaxHwTrq_HwNm_f32	7.36000013	
k_APAMaxVehSpd_Kph_f32	165.600006	
k_ESCMaxVehSpd_Kph_f32	72.5	
k_HapticAmplitude_MtrNm_f32	0.551999986	
k_HapticDuration_Sec_f32	1.38	
k_HapticFreq_Hz_f32	45.6660004	
k_HapticReacttime_Sec_f32	1.472	
k_LKAMaxVehSpd_Kph_f32	174.800003	
k_LKAMinVehSpd_Kph_f32	161	
k_MaxHapticReqTime_Sec_f32	0.075000003	
k_SWARLimiter_HwDeg_f32	13.6000004	
k_ShiftLevRevTime_Sec_f32	0.0170000009	
k_StandstillThresh_Kph_f32	3.68000007	
k_StandstillTime_Sec_f32	0.300000012	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	IC .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target TrqOvISta Per1 ESCIsLimited Cnt I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvlSta Per1 ESCRequest Cnt Ic	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc	target TrgOvISta Per1 GMOSH LKAMfgEn		
		,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	· •	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
		-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-362.410004		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	740.380005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target TrqOvlSta_Per1_MaxSecureVehicleSpeed Kph f32.value	432		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	441.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	•
TrqOvlSta_ESCState_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32		0 ± 1	•
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0		
		70342 ± 1	
TrgOvISta_ElapsedStandstillTime_Sec_M_f32	70342	70342 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HanticState_State_M_enum	70342 0	0	•
TrqOvlSta_HapticState_State_M_enum	70342 0 1	0	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	70342 0 1 64040	0 1 64040 ± 1	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	70342 0 1 64040 0	0 1 64040 ± 1 0 ± 1	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	70342 0 1 64040 0 -5.5	0 1 64040 ± 1 0 ± 1 -5.5 ± 0.000009	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	70342 0 1 64040 0 -5.5	0 1 64040 ± 1 0 ± 1 -5.5 ± 0.000009 0 ± 1	
TrqOvlSta_HapticState_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	70342 0 1 64040 0 -5.5 0	0 1 64040 ± 1 0 ± 1 -5.5 ± 0.000009	0
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	70342 0 1 64040 0 -5.5	0 1 64040 ± 1 0 ± 1 -5.5 ± 0.000009 0 ± 1	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	70342 0 1 64040 0 -5.5 0	0 1 64040 ± 1 0 ± 1 -5.5 ± 0.000009 0 ± 1 1 ± 1	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	70342 0 1 64040 0 -5.5 0 1	0 1 64040 ± 1 0 ± 1 -5.5 ± 0.000009 0 ± 1 1 ± 1 4 ± 1	

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.551999986	0.551999986 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	45.6660004	45.6660004 ± 0.0009765625	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

ame	Input Value
e_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
qOvISta_APAState_State_M_enum	4
qOvISta_ESCState_State_M_enum	4
qOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1375128
qOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	71141.6797
qOvlSta_ElapsedStandstillTime_Sec_M_f32	267318
qOvlSta_HapticState_State_M_enum	0
qOvlSta_Hapticdur_Cnt_M_u16	65346
qOvlSta_Haptictime_Sec_M_f32	153685.688
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.595000029
qOvlSta_LKAFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_u08	4
qOvlSta_LKAState_State_M_enum	0
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996
qOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	0.949999988
APAMaxHwTrg HwNm f32	0
APAMaxVehSpd_Kph_f32	167.399994
ESCMaxVehSpd_Kph_f32	77.5999985
HapticAmplitude_MtrNm_f32	0.558000028
HapticDuration_Sec_f32	1.39499998
HapticFreq_Hz_f32	46.1800003
	1.48800004
HapticReacttime_Sec_f32	
LKAMaxVehSpd_Kph_f32	176.699997
_LKAMinVehSpd_Kph_f32	162.75
MaxHapticReqTime_Sec_f32	0.090000036
SWARLimiter_HwDeg_f32	13.8999996
ShiftLevRevTime_Sec_f32	0.0209999997
StandstillThresh_Kph_f32	3.72000003
StandstillTime_Sec_f32	0.020999997
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_C	nt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lc	c target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCRequest Cnt lgc	target TrqOvlSta Per1 ESCRequest Cnt Igc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_t	
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_	
rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_ rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_	
rget_Rte_Inst_Ap_frqOviSta.TrqOviSta_Peff_GMOSH_ESCMigEnable_Crit_ rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Perf1_GMOSH_LKAMfgEnable_Crit_	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target TrqOvlSta Per1 HwTorque HwNm f32





Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	1			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-1140.10999			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	0			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	745.320007			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	0			
target TrqOvlSta_Fer1_ESCISEIIIIIted_GNT_igc.value target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0			
target_TrqOvlSta_Per1_ESCRequest_Clit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0			
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0			
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	0			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1			
target TrqOvlSta Per1 HwTorque HwNm f32.value	5.6999981			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0			
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1			
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	436.899994			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	446.700012			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23.5			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	4	4	~	
TrqOvlSta_ESCState_State_M_enum	4	4	~	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1375128	1375128 ± 1	~	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	71141.6797	71141.6797 ± 1	~	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~	
TrqOvlSta_HapticState_State_M_enum	0	0	~	
TrqOvlSta_Hapticdur_Cnt_M_u16	65346	65346 ± 1	~	
TrqOvlSta_Haptictime_Sec_M_f32	153685.688	153685.688 ± 1	~	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.30575037	1.30575001 ± 0.000009		
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	V	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1		
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	¥	
TrqOvISta_LKAState_State_M_enum	4 14.3999996	4	*	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	14.3999996 ± 0.0009765625		
	1	0 ± 1 1 ± 1	-	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	4	4		
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	-	
	0	0		
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	-	
target_rqovista_Peri_LkAState_State_enum.value target_TrqOviSta_Peri_LkAState_State_enum.value	0	0 ± 1	- 40	
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	14.3999996	14.3999996 ± 0.0009765625	-	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.558000028	0.558000028 ± 0.0009765625		
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	· ·	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	46.1800003	46.1800003 ± 0.0009765625	-	
2. 7. 4				

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-





Test Step 2.80 (Repeat Count = 1)	In the Walter
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2 0
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1409091
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	71941.3594
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	273864
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	1000
TrqOvlSta_Haptictime_Sec_M_f32	156690
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.606000006
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	15.8000002
TrqOvISta ReadytoPulse Cnt M Igc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.959999979
k_APAMaxHwTrq_HwNm_f32	10
k_APAMaxVehSpd_Kph_f32	169.199997
k_ESCMaxVehSpd_Kph_f32	82.6999969
k_HapticAmplitude_MtrNm_f32	0.56400001
k_HapticDuration_Sec_f32	1.40999997
k_HapticFreq_Hz_f32 k_HapticReacttime_Sec_f32	46.6940002 1.50399995
k_LKAMaxVehSpd_Kph_f32	178.600006
k_LKAMinVehSpd_Kph_f32	164.5
k_MaxHapticReqTime_Sec_f32	0.104999997
k_SWARLimiter_HwDeg_f32	14.1999998
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	3.75999999
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target TrqOvlSta Per1 CCWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvIsta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSiv0HwAngle_Hwbeg_isz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Dto Inst An Tranvista Tranvista Dord Transaction Cod Inc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
larget_tre_inst_Ap_11qOviota.11qOviota_Peri11qOscEnable_Ont_igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1130.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	750.26001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-21.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	1100011
TrgOvISta ESCState State M enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	
TrqOvlSta ElapsedStandstillTime Sec M f32	273864	273864	
TrqOvlSta HapticState State M enum	1	1	
TrqOvISta_Hapticdur_Cnt_M_u16	1000	1000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	156690	156690 ± 1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019	-4.8000019 ± 0.000009	
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	
TrqOvISta LKAPermFault Cnt M Igc	1	1±1	
TrqOvISta_LKAPermFault_Cnt_M_u08	1	1±1	
TrqOvISta_LKAState_State_M_enum	3	3	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	
target TrgOvISta Per1 ESCState State enum.value	3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	
target TrqOvlSta Per1 LKAState State enum.value	3	3	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.56400001	0.150000006 ± 0.0009765625 0.56400001 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.56400001	0.56400001 ± 0.0009765625 1 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	46.6940002	46.6940002 ± 0.0009765625	
targot_riqOviota_Ferr_riqOsorreq_Fiz_132.value	40.0340002	40.0340002 I 0.000370323	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.81 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1443054
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	72741.0391
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	280410
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	3000
TrqOvlSta_Haptictime_Sec_M_f32	159695.766
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.616999984
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	17.2000008

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.970000029
k_APAMaxHwTrq_HwNm_f32	7.6500001
k_APAMaxVehSpd_Kph_f32	171
k_ESCMaxVehSpd_Kph_f32	87.8000031
k_HapticAmplitude_MtrNm_f32	0.56999993
k_HapticDuration_Sec_f32	1.42499995
k_HapticFreq_Hz_f32	47.2080002
k_HapticReacttime_Sec_f32	1.51999998
k_LKAMaxVehSpd_Kph_f32	180.5
k_LKAMinVehSpd_Kph_f32	166.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	14.5
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	3.79999995 0.0289999992
k_StandstillTime_Sec_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 APAState State enum	target TrgOvlSta Per1 APAState State enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target TrqOvlSta Per1 CWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_enumerator and the property of the property $	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_Enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1119.91003
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	755.200012
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.0999999
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value	1 0
target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	446.700012
target_TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32.value	456.5
target_TrqOvIsta_Per1_PosTrajEnable_Cnt_Igc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-20.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	72741.0391	72741.0391 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	3000	3000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.05934954	2.05935001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	17.2000008	17.2000008 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	17.2000008	17.2000008 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.56999993	0.569999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	47.2080002	47.2080002 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.82 (Repeat Count = 1) Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	1477017
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	73540.7188
TrqOvlSta ElapsedStandstillTime Sec M f32	286956
TrqOvISta HapticState State M enum	0
TrqOvlSta Hapticdur Cnt M u16	5000
TrqOvlSta Haptictime Sec M f32	162700
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-4.099999
TrqOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.628000021
TrgOvISta LKAFault Cnt M lgc	0
TrqOvISta LKAPermFault Cnt M Igc	1
TrqOvISta LKAPermFault Cnt M u08	2
TrqOvISta_LKAState_State_M_enum	1
TrqOvlSta PosSrvoHwAngle HwDeg M f32	18.6000004
TrqOvlSta ReadytoPulse Cnt M lgc	1
k APAIncludeHaptic Cnt Igc	0
k APAMaxHwTrqTime Sec f32	0
k APAMaxHwTrq HwNm f32	0.69999988
k APAMaxVehSpd Kph f32	172.800003
k ESCMaxVehSpd Kph f32	92.9000015
k HapticAmplitude MtrNm f32	0.575999975
k HapticDuration Sec f32	1.4400006
k HapticFreq Hz f32	47.7220001
k HapticReacttime Sec f32	1.53600001
k LKAMaxVehSpd Kph f32	182.399994
k LKAMinVehSpd Kph f32	168
k MaxHapticRegTime Sec f32	0.135000005
k_SWARLimiter_HwDeg_f32	14.800002
k ShiftLevRevTime Sec f32	0.032999998
k StandstillThresh Kph f32	3.83999991
k_StandstillTime_Sec_f32	0.032999998
	Detected Cnt target TrqOvlSta Per1 APADrvrInterventionDetected Cnt lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlOta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg	_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	99	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_I target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSHOscillateState State enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target TrqOvlSta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1109.81006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	760.140015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	451.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	461.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-18.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1	I=	1
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1477017	1477017 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	73540.7188	73540.7188 ± 1	
TrqOvISta_EiapseoStanostill Ime_Sec_m_132 TrqOvISta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	5000	5000 ± 1	
TrgOvISta Haptictime Sec M f32	0	0 ± 1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.43120003	2.43120003 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	
,			
TrgOvISta LKAPermFault Cnt M lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 2	1 ± 1 2 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08		1 ± 1 2 ± 1 3	•
	2	2 ± 1	





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.575999975	0.575999975 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	47.7220001	47.7220001 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvISta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1510980
FrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	74340.3984
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	293502
FrqOvlSta_HapticState_State_M_enum	0
FrqOvISta_Hapticdate_State_W_enum FrqOvISta_Hapticdur_Cnt_M_u16	7000
	165705
rqOvISta_Haptictime_Sec_M_f32	-3.75
FrgOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.638999999
FrgOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	
FrqOvISta_LKAFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	3 0
rqOvISta_LKAState_State_M_enum	20
rqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0
rqOvISta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	
_APAMaxHwTrq_HwNm_f32	1.10000002
X_APAMaxVehSpd_Kph_f32	174.600006
C_ESCMaxVehSpd_Kph_f32	98
_HapticAmplitude_MtrNm_f32	0.582000017
c_HapticDuration_Sec_f32	1.45500004
K_HapticFreq_Hz_f32 K_HapticFlooritime_Sec_f23	48.2360001
(_HapticReacttime_Sec_f32	1.55200005
LKAMinVahCad Kab 533	184.300003
LKAMinVehSpd_Kph_f32	169.75
x_MaxHapticReqTime_Sec_f32	0.15000006
SWARLimiter_HwDeg_f32	15.1000004
_ShiftLevRevTime_Sec_f32	0.0370000005
_StandstillThresh_Kph_f32	3.88000011
StandstillTime_Sec_f32	0.0370000005
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Ci	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_en	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igr	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HwTorque HwNm f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_t	32 target_TrqOvlSta_Per1_MaxSecureVehicles	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_fa	target_TrqOvlSta_Per1_MinSecureVehicleS	speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr	ıt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_I	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn	:_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg	_f3. target_TrqOvlSta_Per1_SWARTrgtAngReq	uest_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc$	target_TrqOvlSta_Per1_ShiftLeverIsInReve	rse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn	1_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1099.70996		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	765.080017		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target TrqOvlSta_Per1_HwTorque HwNm f32.value	6.5		
	1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target TrqOvlSta_Per1_LKAInhibit Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	456.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	466.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	1510980	1510980 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	•
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	7000	7000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	165705	165705 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.79974985	2.79975009 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	20	20 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
		0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	-	
target_IrqOviSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOviSta_Per1_LKAState_State_enum.value	0 4	4	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	4 0	4 0±1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	4 0 20	4 0 ± 1 20 ± 0.0009765625	· ·
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	4 0 20 0.582000017	4 0 ± 1 20 ± 0.0009765625 0.582000017 ± 0.0009765625	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	4 0 20	4 0 ± 1 20 ± 0.0009765625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Fest Step 2.84 (Repeat Count = 1)	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum rqOvlSta_ESCState_State_M_enum	0 4
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1544943
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	75140.0781
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	300048
rqOvISta_HapticState_State_M_enum	0
rqOvISta_Hapticdur_Cnt_M_u16	9000
rqOvISta_Haptictime_Sec_M_f32	168710.594
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.4000001
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.649999976
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	4
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	21.3999996
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.550000012
_APAMaxHwTrq_HwNm_f32	2
_APAMaxVehSpd_Kph_f32	176.399994
_ESCMaxVehSpd_Kph_f32	103.099998
_HapticAmplitude_MtrNm_f32	0.588
_HapticDuration_Sec_f32	1.47000003
_HapticFreq_Hz_f32	48.75
_HapticReacttime_Sec_f32	1.56799996
_LKAMaxVehSpd_Kph_f32	186.199997
_LKAMinVehSpd_Kph_f32	171.5
_MaxHapticReqTime_Sec_f32	0.165000007
_SWARLimiter_HwDeg_f32	15.3999996
_ShiftLevRevTime_Sec_f32	0.0410000011
_StandstillThresh_Kph_f32	3.92000008
_StandstillTime_Sec_f32	0.0410000011
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 CCWPosition HwDeg f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 CWFound Cnt Igc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 CWPosition HwDeg f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target TrqOvlSta Per1 ESCFault Cnt Igc
riget Rte Inst Ap TrqOviSta.TrqOviSta Per1 ESCIsLimited Cnt Igc	target TrqOvlSta Per1 ESCIsLimited Cnt Igc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvlSta Per1 ESCRequest Cnt Igc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrqOvISta Per1 ESCState State enum
irget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enui	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc	
riget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target TrqOvlSta Per1 HapticRequest Cnt Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	2 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
rget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
angot_rido riota_r or r_oom ouna_ont_igo.valao	

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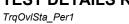


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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1089.60999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	770.02002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	461.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	471.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	-
TrqOvlSta_ESCState_State_M_enum	4	4	·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1544943	1544943 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	·
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	·
TrqOvlSta_Hapticdur_Cnt_M_u16	9000	9000 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.16499996	3.16499996 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.588	0.588 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	48.75	48.75 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.85 (Repeat Count = 1)	variable and the second of
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1578906
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	75939.7578
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	306594
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	11000
TrqOvlSta_Haptictime_Sec_M_f32	171715
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.661000013
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.7999992

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.980000019
k_APAMaxHwTrq_HwNm_f32	7.92000008
k_APAMaxVehSpd_Kph_f32	0
k_ESCMaxVehSpd_Kph_f32	108.199997
k_HapticAmplitude_MtrNm_f32	0.593999982
k_HapticDuration_Sec_f32	1.48500001
k_HapticFreq_Hz_f32	49.2639999
k HapticReacttime Sec f32	1.58399999
LKAMaxVehSpd_Kph_f32	188.100006
k_LKAMinVehSpd_Kph_f32	173.25
k_MaxHapticReqTime_Sec_f32	0.180000007
k_SWARLimiter_HwDeg_f32	15.699998
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	3.96000004
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target TrgOvISta Per1 PosTrajHwAngle HwDeg f32
	0 = 1 = 2 , 0 = 0=
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1079.51001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	774.960022
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target TrgOvlSta Per1 HwTorque HwNm f32.value	6.9000001
arget_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Fer1_EtKir atit_Off_ige.value	1
target_TrqOvlSta_Per1_LKARimibit_Ont_igc.value	1
target_TrqOvlSta_Per1_LKARequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
	476.100006
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	476.100006 0
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-13.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	75939.7578	75939.7578 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	306594	306594	•
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11000	11000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995	-3.04999995 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.7999992	22.7999992 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	22.7999992	22.7999992 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.593999982	0.593999982 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	49.2639999	49.2639999 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.86 (Repeat Count = 1) Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	1612869
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	76739.4375
TrqOvlSta ElapsedStandstillTime Sec M f32	313140
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	13000
TrqOvlSta_Haptictime_Sec_M_f32	174720.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.671999991
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.99000001
k_APAMaxHwTrq_HwNm_f32	8
k_APAMaxVehSpd_Kph_f32	220
k_ESCMaxVehSpd_Kph_f32	113.300003
k_HapticAmplitude_MtrNm_f32	0.600000024
k_HapticDuration_Sec_f32	1.5
k_HapticFreq_Hz_f32	49.7779999
k_HapticReacttime_Sec_f32	1.60000002
k_LKAMaxVehSpd_Kph_f32	190
k_LKAMinVehSpd_Kph_f32	175
k_MaxHapticReqTime_Sec_f32	0.194999993
k_SWARLimiter_HwDeg_f32	16
k_ShiftLevRevTime_Sec_f32	0.0489999987
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.0489999987
target Rte Inst An TrgOvISta TrgOvISta Per1 APADryrIntervention	nDetected Cnt target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFat	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlOta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg target TrqOvlSta Per1 ESCState State en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvISta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlOta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_ target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscErtable_Ont_igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvISta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1069.41003		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	779.900024		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	471.200012		
target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	481		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-12.3000002		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	V
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvISta_Hapticdur_Cnt_M_u16	13000	13000 ± 1	✓
TrqOvISta_Haptictime_Sec_M_f32	174720.641	174720.641 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	Y
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	1	1 ± 1 1 ± 1	-
TrqOvISta_LKAState_State_M_enum	3	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	V

0

0 ± 1

TrqOvlSta_ReadytoPulse_Cnt_M_lgc

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.600000024	0.600000024 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	49.7779999	49.7779999 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

est Step 2.87 (Repeat Count = 1)	
ame	Input Value
e_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
qOvlSta_APAState_State_M_enum	0
qOvlSta_ESCState_State_M_enum	2
qOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1646832
qOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	77539.1172
qOvlSta_ElapsedStandstillTime_Sec_M_f32	319686
qOvlSta_HapticState_State_M_enum	2
qOvlSta_Hapticdur_Cnt_M_u16	15000
qOvlSta_Haptictime_Sec_M_f32	177725
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.683000028
qOvlSta_LKAFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	3
qOvlSta_LKAState_State_M_enum	1
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.6000004
qOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	1
APAMaxHwTrq_HwNm_f32	8.07999992
APAMaxVehSpd_Kph_f32	100.360001
ESCMaxVehSpd_Kph_f32	118.400002
HapticAmplitude_MtrNm_f32	0.606000006
HapticDuration_Sec_f32	1.51499999
HapticFreq_Hz_f32	50.2919998
HapticReacttime_Sec_f32	1.61600006
LKAMaxVehSpd_Kph_f32	191.899994
LKAMinVehSpd_Kph_f32	176.75
MaxHapticReqTime_Sec_f32	0.0149999997
SWARLimiter_HwDeg_f32	16.2999992
ShiftLevRevTime_Sec_f32	0.00100000005
StandstillThresh_Kph_f32	4.03999996
StandstillTime_Sec_f32	0.00100000005
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target TrqOvlSta Per1 HwTorque HwNm f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f	32 target_TrqOvlSta_Per1_MaxSecureVehicles	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_fstarget_New SecureVehicleSpeed_Kph_fstarget_New SecureVehicleSpeed_New SecureVe$	target_TrqOvlSta_Per1_MinSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_I	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve	rse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn	1_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1059.31006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	784.840027		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMigEnable_Crit_igc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	7.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	476.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	485.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-10.6999998		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	
TrqOvlSta_ESCState_Ntate_M_enum	4	4	v
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	~
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	15000	15000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999	-2.3499999 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.6000004	25.6000004 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value		4	✓
	4		
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 25.6000004	0 ± 1 25.6000004 ± 0.0009765625	· ·
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0 25.6000004 0.606000006	0 ± 1 25.6000004 ± 0.0009765625 0.606000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 25.6000004	0 ± 1 25.6000004 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.88 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1680795
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	78338.7969
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	326232
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	180730
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_equip	2
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	27
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.123000003
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003
k_ESCMaxVehSpd_Kph_f32	123.5
k_HapticAmplitude_MtrNm_f32	0.456
k_HapticDuration_Sec_f32	0
k_HapticFreq_Hz_f32	50.8059998
k_HapticReacttime_Sec_f32	1.21599996
k_LKAMaxVehSpd_Kph_f32	144.399994
k_LKAMinVehSpd_Kph_f32	133
k_MaxHapticReqTime_Sec_f32	0.5 16.6000004
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.03999996
k_StandstillTime_Sec_f32	0.305000007
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target TrqOvlSta Per1 ESCIsLimited Cnt lgc
target_Rte_Inst_Ap_TrqOviota.TrqOviota_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132	target_TrqOvlSta_Per1_MinSecurevenicleSpeed_Kpn_t32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1049.20996		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	789.780029		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	481		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	490.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.10000038		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	78338.7969	78338.7969 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.456	0.456 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	50.8059998	50.8059998 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.89 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1714758
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79138.4766
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	332778
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	3650
TrqOvlSta_Haptictime_Sec_M_f32	183735
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.3999996

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TrqOvlSta_Per1



Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.134000003
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	128.600006
k_HapticAmplitude_MtrNm_f32	0.462000012 2
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	51.3199997
k HapticReacttime Sec f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	16.8999996
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target TrqOvlSta Per1 APARecoverableFaults Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target TrqOvISta Per1 TrqOscEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1039.10999
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0 704 740074
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	794.719971 0
target_IrqOviSta_Peri_ESCFault_Cnt_igc.value target TrqOviSta_Peri_ESCIsLimited Cnt_igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	485.899994
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	495.700012
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79138.4766	79138.4766 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	3650	3650 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.3999996	28.3999996 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	28.3999996	28.3999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.3199997	51.3199997 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.90 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	25475
TrqOvlSta_Haptictime_Sec_M_f32	186740.953
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.55999994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlOta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	192	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_I	ac	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target TrqOvlSta Per1 PosTrajHwAngle Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrqOvISta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 HapticRequest Cnt lgc.value	0		
target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value	0		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	•
TrqOvISta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	•
TrqOvlOta_HapticState_State_M_enum	2	2	•
TrqOvlOta_Hapticdur_Cnt_M_u16	0	0 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HuvTorqueSt/_HuvIm_M_Str_St/_Lile_f33	1.6400000	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998	-1.64999998 ± 0.000009	
		0 ± 1	
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAParmEault_Cnt_M_lgc	0	0 + 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	2 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0		•





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.91 (Repeat Count = 1) Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvISta_APAState_State_M_enum	1
rqOvISta_ESCState_State_M_enum	
rqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1782684
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	80737.8438
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	345870
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	0
rqOvISta_Haptictime_Sec_M_f32	189745
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	1
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	31.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.156000003
_APAMaxHwTrq_HwNm_f32	6.07999992
_APAMaxVehSpd_Kph_f32	136.800003
_ESCMaxVehSpd_Kph_f32	138.800003
_HapticAmplitude_MtrNm_f32	0.629999995
_HapticDuration_Sec_f32	0.5
_HapticFreq_Hz_f32	52.3479996
_HapticReacttime_Sec_f32	0
_LKAMaxVehSpd_Kph_f32	199.5
LKAMinVehSpd_Kph_f32	183.75
	0.5
SWARLimiter HwDeg f32	17.5
ShiftLevRevTime_Sec_f32	0.305000007
StandstillThresh_Kph_f32	3.03999996
standstillTime_Sec_f32	0.305000007
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target TrqOvlSta Per1 APARecoverableFaults Cnt Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target TrqOvlSta Per1 APAState State enum
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 CWFound Cnt Igc	
	target_TrqOvISta_Per1_CWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-1018.90997		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	804.599976		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	0		
target TrqOvISta_rer1_ESCISEIIIIIted_Off_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	7.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	495.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	505.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4.30000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	80737.8438	80737.8438 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2	-2 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	Y
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	· ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	31.2000008	31.2000008 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	- 4
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0 4	0 ± 1 4	V
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	0	0 ± 1	-
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	31.2000008	31.2000008 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.629999995	0.629999995 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value			1
	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	0 52.3479996	0 ± 1 52.3479996 ± 0.0009765625	*

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Fest Step 2.92 (Repeat Count = 1)	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_EScState_state_M_eritifi rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1816647
rqOvISta_ElapsedArAMaxHwTrqTime_Sec_IM_f32	81537.5234
rqOviSta_ElapsedStandstillTime_Sec_M_f32	352416
rqOvISta_HapticState_State_M_enum	2
rqOvISta_Hapticdur_Cnt_M_u16	3650
TrqOvISta_Haptictime_Sec_M_f32	192750
rgOvISta HwTorqueSV HwNm M Str.SV UIs f32	-1.64999998
rqOviSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	32.5999985
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.166999996
_APAMaxHwTrq_HwNm_f32	6.15999985
_APAMaxVehSpd_Kph_f32	138.600006
_ESCMaxVehSpd_Kph_f32	143.899994
_HapticAmplitude_MtrNm_f32	0.635999978
_HapticDuration_Sec_f32	0.870000005
_HapticFreq_Hz_f32	52.8619995
_HapticReacttime_Sec_f32	2
_LKAMaxVehSpd_Kph_f32	201.399994
_LKAMinVehSpd_Kph_f32	185.5
_MaxHapticReqTime_Sec_f32	0.600000024
_SWARLimiter_HwDeg_f32	17.7999992
_ShiftLevRevTime_Sec_f32	0.308999985
_StandstillThresh_Kph_f32	3.07999992
_StandstillTime_Sec_f32	0.308999985
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target TrgOvlSta Per1 ESCFault Cnt lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrgOvISta Per1 ESCIsLimited Cnt Igc
liget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target TrgOvISta Per1 ESCRequest Cnt Igc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrgOvISta Per1 ESCState State enum
inget_Rte_inst_Ap_11q0vi3ta.11q0vi3ta_Fe11_E3C3tate_state_entin irget_Rte_Inst_Ap_Trq0vlSta.Trq0vlSta_Per1_GMOSH0scillateState_State_enun	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
riget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 HapticReguest Cnt Igc	target TrgOvISta Per1 HapticRequest Cnt Iqc
riget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 HwTorque HwNm f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
lrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKAInhibit Cnt Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lgc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
riget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1008.81		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	809.539978		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	510.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	-
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	81537.5234	81537.5234 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	3650	3650 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	- − •
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	- − •
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.635999978	0.635999978 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	52.8619995	52.8619995 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.93 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1850610	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	82337.2031	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	358962	
TrqOvlSta_HapticState_State_M_enum	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	25475	
TrqOvlSta_Haptictime_Sec_M_f32	195755	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	
TrqOvlSta_LKAState_State_M_enum	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34	





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	. –	0.178000003
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target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SwARTrajEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SwARTrajEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SwARTrajEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SwARTrajEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SwARTrajEnable_Cnl_go target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAnp_MinM_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAnp_MinM_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAnp_MinM_132 target_TrqOviSta_Perl_ApARecoverableFaults_Cnl_go:value target_TrqOviSta_Perl_ApARecoverableFaults_Cnl_go:value target_TrqOviSta_Perl_ApARecoverableFaults_Cnl_go:value target_TrqOviSta_Perl_CWPosition_HwDeg_132.value target_TrqOviSta_Perl_CWPosition_HwDeg_132.value target_TrqOviSta_Perl_CWPosition_HwDeg_132.value target_TrqOviSta_Perl_CWPosition_HwDeg_132.value target_TrqOviSta_Perl_ESCPaul_Cnl_go:value target_TrqOviSta_Perl_ESCRequest_Cnl_go:value target_TrqOviSta_Perl_EGNOSH_ApAReficable_Cnl_go:value target_TrqOviSta_Perl_GNOSH_ApAReficable_Cnl_go:value target_TrqOviSta_Perl_GNOSH_ApAReficable_Cnl_go:value target_TrqOviSta_Perl_HwTorque_Hwnm_132.value target_TrqOviSta_Perl_HwTorque_Hwnm_132.value target_TrqOviSta_Perl_HwTorque_Hwnm_132.value target_TrqOviSta_Perl_HwTorque_Hwnm_132.value target_TrqOviSta_Perl_HwTorque_Hwnm_132.value target_TrqOviSta_Perl_HwTorque_Hwnm_132.value target_TrqOviSta_Perl_MxAeruevelicleSpeed_Kph_132.value target_TrqOviSta_Perl_MxAeruevelicleSp		
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target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Ont_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Ont_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Ont_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Ont_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Ont_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Ont_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Ont_Igo target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_TrqOvISta_Pert_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_TrqOvISta_Pert_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_TrqOvISta_Pert_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_TrqOvISta_Pert_TrqOvISta_Pert_TrqOsCap_Mirthm_g52 target_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvISta_Pert_TrqOvI	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rie_Inst_Ap_TrqOv/Sta_Perl_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Perl_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOv/Sta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOv/Sta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOv/Sta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOv/Sta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOv/Sta_Perl_TrqOsCAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_TrqOsCAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_TrqOsCAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_TrqOsCamp_MirNm_f32 target_Rie_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_TrqOsCamp_MirNm_f32 target_Rie_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_TrqOsCamp_MirNm_f32 target_Rie_Inst_Ap_TrqOv/Sta_Perl_TrqOsCamp_MirNm_f32 target_TrqOv/Sta_Perl_TrqOsCamp_MirNm_f32 target_TrqOv/Sta_Perl_MirNm_f32 target_TrqOv/Sta_Perl	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_SVMARTrglAngRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWMARTrglAngRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWMARTrglAngRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWMARTrglAngRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAnp_MtrMm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAnp_MtrMm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAnp_MtrMm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAnp_MtrMm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAnp_MtrMm_f32 target_TrqOvlSta_Per1_Ap_TrqOvlSta_Per1_TrqOscAnp_MtrMm_f32 target_TrqOvlSta_Per1_TrqOscAnp_MtrMm_f32 target_TrqOvlSta_Per1_Ap_And RecoverableFaults_Cnt_Igc_value 0 target_TrqOvlSta_Per1_Ap_And RecoverableFaults_Cnt_Igc_value 1 target_TrqOvlSta_Per1_Ap_And RecoverableFaults_Cnt_Igc_value 0 target_TrqOvlSta_Per1_CWPound_Cnt_Igc_value 1 target_TrqOvlSta_Per1_CWPound_Cnt_Igc_value 0 target_TrqOvlSta_Per1_CWPound_Cnt_Igc_value 1 target_TrqOvlSta_Per1_ESCAult_Cnt_Igc_value 0 target_TrqOvlSta_Per1_ESCAequest_Cnt_Igc_value 1 target_TrqOvlSta_Per1_ESCAequest_Cnt_Igc_value 0 target_TrqOvlSta_Per1_ESCAequest_Cnt_Igc_value 1 target_TrqOvlSta_Per1_ESCAequest_Cnt_Igc_value 0 target_TrqOvlSta_Per1_GMOSH_APAMIgEnable_Cnt_Igc_value 0 target_TrqOvlSta_Per1_GMOSH_EXCMIgEnable_Cnt_Igc_value 1 target_TrqOvlSta_Per1_GMOSH_EXCMIgEnable_Cnt_Igc_value 1 target_TrqOvlSta_Per1_HvTorque_HwMm_f32_value 1 target_TrqOvlSta_Per1_HvTorque_HwMm_f32_value 1 target_TrqOvlSta_Per1_HvTorque_HwMm_f32_value 1 target_TrqOvlSta_Per1_HvTorque_HwMm_f32_value 1 target_TrqOvlSta_Per1_HvTorque_HwMm_f32_value 1 target_TrqOvlSta_Per1_MaxSecureVeloicExpeed_Kph_f32_value 1 target_TrqOvlSta_Per1_MaxSecureVeloicExpeed_Kph_f32_value 1 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvlSta_Per1_PosTrajEvalue_Cn		
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target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MINm_f32 target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_TrqOvISta_Perl_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_APARequest_Cnt_Igc.value target_TrqOvISta_Perl_APARequest_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_ESCRequest_Cnt_Igc.value target_TrqOvISta_Perl_ESCRequest_Cnt_Igc.value target_TrqOvISta_Perl_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTorque_HwTor		
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target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_NinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_NinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_NinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.10000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 10 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 10 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 11 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 11 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0	·	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.1000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.1000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0	· - ·	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.10000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.10000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_HwTorque_HwNlm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 505.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.3000002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.180000007	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-1.10000002		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	82337.2031	82337.2031 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	25475	25475 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34	34 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	34	34 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.64200002	0.64200002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	53.3759995	53.3759995 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.94 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	35.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.188999996
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	154.100006
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	53.8899994
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	18.3999996
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_In$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	52	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	rc	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-988.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	819.419983		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 HapticRequest Cnt lgc.value	1		
target TrqOvISta_rer1_HapticRequest_Crit_igc.value target TrqOvISta Per1 HwTorque HwNm f32.value	8.6999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	510.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	3	3	~
TrqOvISta_ESCState_State_M_enum	4	4	•
TrgOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	83136.8828	83136.8828 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvISta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	53.8899994	53.8899994 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Fest Step 2.95 (Repeat Count = 1)	Input Value
	•
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvISta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	0
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1918536
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83936.5625
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	372054
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	31000
rqOvlSta_Haptictime_Sec_M_f32	201765
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	36.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.200000003
APAMaxHwTrg HwNm f32	8.72000027
APAMaxVehSpd Kph f32	196.199997
ESCMaxVehSpd Kph f32	159.199997
_HapticAmplitude_MtrNm_f32	0.653999984
HapticDuration Sec f32	1.63499999
HapticFreq Hz f32	54.4039993
HapticReacttime Sec f32	1.74399996
_LKAMaxVehSpd_Kph_f32	207.100006
_LKAMinVehSpd_Kph_f32	220
_MaxHapticReqTime_Sec_f32	0.135000005
_SWARLimiter_HwDeg_f32	18.7000008
_ShiftLevRevTime_Sec_f32	0.0329999998
_StandstillThresh_Kph_f32	4.36000013
_StandstillTime_Sec_f32	0.032999998
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enui	
rget_Rte_inst_Ap_frqovista.frqovista_Peri_GMOSHOscillatestate_state_entil rget_Rte_inst_Ap_frqoviSta.frqoviSta_Peri_GMOSH_APAMfgEnable_Cnt_lgc	
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
	target_TrqOvlSta_Per1_MaxSecureVehicleS	· -	
	target_TrqOvlSta_Per1_MinSecureVehicleSp	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	•	
	target_TrqOvISta_Per1_SWARTrgtAngReque		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	- Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-978.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	824.359985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	30.8999996		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.10000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	2.0999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		l
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	4	4	V
TrqOvlSta_ESCState_state_w_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	Ž
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0±1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	372054	372054	,
TrgOvISta HapticState State M enum	2	2	~
TrgOvlSta Hapticdur Cnt M u16	31000	31000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	201765	201765 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	36.7999992	36.7999992 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value		4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	V
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	-
target_rrqOvlSta_Per1_PosServErlable_Cnt_lgc.value target_rrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	36.7999992	36.7999992 ± 0.0009765625	~
target_TrqOvlSta_Per1_FosSivonwArigie_Hwbeg_isz.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.653999984	0.653999984 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.4039993	54.4039993 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.96 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.441000015
TrqOvISta_LKAFault_Cnt_M_lgc TrgOvISta_LKAFault_Cnt_M_lga	1 0
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvISta_LKAState_State_M_enum	0
TrgOvlSta PosSrvoHwAngle HwDeg M f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.6499998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001 0.150000006
k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32	19
k ShiftLevRevTime Sec f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_rer1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosSrvonwArigie_nwbeg_isz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvonwArigie_nwbeg_1s2 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_rer1_rosTrajEnable_Ont_ig0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTratAnaReauest HwDea f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse Cnt lgc
	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0 0
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0

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			10010
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-968.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	829.299988		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	Ī		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	Ī		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	✓
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008	38.2000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	38.2000008	38.2000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.97 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1986462
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	85535.9219
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	385146
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	207775
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	39.5999985

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.222000003
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	169.399994
k_HapticDuration_Soc_f32	0.666000009 1.62
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	55.4319992
k HapticReacttime Sec f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	0
k LKAMinVehSpd Kph f32	194.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	19.2999992
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-958.309998
target_rqoviSta_Peri_ccwPosition_Hwbeg_rsz.value target_rrqoviSta_Peri_ccwPosition_Hwbeg_rsz.value	-958.309998 0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	834.23999
target_TrqOvIsta_FeT1_GVVF0sition_TiwDeg_13z.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	0
target TrqOvISta Per1 ESCIsLimited Cnt Igc.value	0
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.6999981
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.8999996
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	51.5
	0
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	U .

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.30000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	85535.9219	85535.9219 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	207775	207775 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	39.5999985	39.5999985 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	39.5999985	39.5999985 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.666000009	0.666000009 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.4319992	55.4319992 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.98 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2020425
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	86335.6016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	391692
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	31000
TrqOvlSta_Haptictime_Sec_M_f32	210780
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.232999995
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	174.5
k_HapticAmplitude_MtrNm_f32	0.671999991
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	55.9459991
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	220
k_LKAMinVehSpd_Kph_f32	196
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	19.6000004
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cnd$	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_ig target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_ig target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Sf32 target_TrqOvlSta_Per1_CCWPosition_HwD target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_igc target_TrqOvlSta_Per1_CCWFound_Cnt_igc target_TrqOvlSta_Per1_CCWFound_Cnt_igc target_TrqOvlSta_Per1_CCWFound_Cnt_igc target_TrqOvlSta_Per1_CCWFound_Cnt_igc target_TrqOvlSta_Per1_CCWFound_Cnt_igc target_TrqOvlSta_Per1_CCWFound_Cnt_igc				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Ent_gc target_TrqOvlSta_Per1_CCWFound_Ent_gc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Ent_gc target_TrqOvlSta_Per1_CCWFound_Ent_gc target_TrqOvlSta_Per1_CCWFo	leFaults Cnt lgc			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_TrqOvlSta_Per1_CCWFound_Cnt_Igc				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_Tr				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc				
	09_102			
	n f32			
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	9_102			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_lgc	lao			
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target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_ESCState_State_enum				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_TrqOvlSta_Per1_GMOSHOscillateSt				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgE				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	,			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_HwTorque_HwNm_	,732			
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc				
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target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_LKAState_State_er				
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target	Speed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f33 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f33 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f33 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f33 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f33 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f33 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f33 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f33 target				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	t_lgc			
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_H	HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	uest_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverIsInRever	rse_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	1_f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt lgc target TrqOvlSta Per1 TrqOscEnable Cnt	lgc			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3.	2			
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0				
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0				
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0				
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1				
	-948.210022			
target TrgOvlSta Per1 CWFound Cnt Igc.value				
target TrgOvlSta Per1 CWPosition HwDeg f32.value 839.179993				
target TrgOvlSta Per1 ESCFault Cnt Igc.value				
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value				
magazinda yanzi ay zana ay dana Zan Zan ana				
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value				
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 41.2000008				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 41.2000008 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 61.7999992				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 41.2000008				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 41.2000008 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 61.7999992				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.129999995				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 6.9000001	Expected Value	Result		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value 0	Expected Value	Result		
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target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0 3 0±1 0±1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value 0 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 41.2000008 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 61.7999992 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.129999995 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 6.9000001 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value TrqOvISta_APAState_State_M_enum 0 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 TrqOvISta_HapticState_State_M_enum 0 TrqOvISta_HapticState_State_M_enum 0	0 3 0±1 0±1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.60000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 41.2000008 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 61.7999992 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.129999995 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 6.9000001 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 0 Name Actual Value TrqOvlSta_APAState_State_M_enum 0 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 TrqOvlSta_HapticState_State_M_enum 0 TrqOvlSta_HapticState_State_M_enum 0 TrqOvlSta_Hapticdur_Cnt_M_u16 31000	0 3 0±1 0±1 0 0 31000±1	· · · · · · · · · · · · · · · · · · ·		
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target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0 3 0 ± 1 0 ± 1 0 0 31000 ± 1 210780 ± 1 -5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 0	· · · · · · · · · · · · · · · · · · ·		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.129999995 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32.value 0.129999995 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 TrqOvlSta_APAState_State_M_enum 0 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 TrqOvlSta_HapticState_State_M_enum 0 TrqOvlSta_HapticState_State_M_enum 0 TrqOvlSta_HapticCnt_M_u16 31000 TrqOvlSta_HapticCnt_M_u16 TrqOvlSta_HapticCnt_M_sgc 1 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 2	0 3 0 ± 1 0 ± 1 0 0 31000 ± 1 210780 ± 1 -5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1	· · · · · · · · · · · · · · · · · · ·		

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Namo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2054388
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87135.2813
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	398238
TrqOvlSta_HapticState_State_M_enum	1
FrqOvISta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	213785
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
<pre>FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32</pre>	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	42.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.244000003
k APAMaxHwTrq HwNm f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k ESCMaxVehSpd Kph f32	179.600006
k_HapticAmplitude_MtrNm_f32	0.677999973
k_HapticDuration_Sec_f32	1.6499998
	56.4599991
k_HapticFreq_Hz_f32	
k_HapticReacttime_Sec_f32	1.7599999
k_LKAMaxVehSpd_Kph_f32	105.75
k_LKAMinVehSpd_Kph_f32	197.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	19.8999996
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_0	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt	
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cn	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Fer1_HapticRequest_Cnt_Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Fe1_HapitinRequest_Ont_igc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
argot_No_mot_Ap_riqoviota.riqoviota_rei1_riw10ique_riwiviii_132	Cargor_rigoriota_r or r_riw rorquo_riwinir_102

@ Report created by TESSY V3.1.9, report template V2.1

TrgOvISta Per1

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Input Value target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target TrgOvISta Per1 LKAFault Cnt lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc$ target TrgOvlSta Per1 LKAInhibit Cnt Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest Cnt lgc target TrgOvISta Per1 LKARequest Cnt lgc $target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum$ target_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32$ target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$ target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 | target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 | target_TrqO $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc$ target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc$ target TrgOvlSta Per1 TrgOscEnable Cnt lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 $target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value$ 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -938.109985 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value Λ target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 844.119995 target TrgOvlSta Per1 ESCFault Cnt lgc.value n target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 0 target TrgOvlSta Per1 ESCRequest Cnt lgc.value 0 $target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value$ 0 target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target TrqOvlSta Per1 HapticRequest Cnt Igc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -8.10000038 target TrqOvlSta Per1 LKAFault Cnt lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target TrqOvlSta Per1 LKARequest Cnt lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 51.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 72.0999985 $target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value$ 0 0.140000001 $target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value$ $target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value$ 8.5 $target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value$ Name **Actual Value Expected Value** TrgOvISta APAState State M enum 4 TrqOvISta_ESCState_State_M_enum 4 TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32 0 ± 1 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 87135.2813 87135.2813 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 ${\bf TrqOvlSta_HapticState_State_M_enum}$ TrqOvlSta_Hapticdur_Cnt_M_u16 33000 33000 ± 1 TrqOvlSta_Haptictime_Sec_M_f32 0 0 + 1TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 -4.80000019 -4.80000019 ± 0.000009 TrqOvlSta_LKAFault_Cnt_M_lgc 1 + 1TrqOvlSta LKAPermFault Cnt M lgc • 0 0 ± 1 TrqOvISta LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvlSta_LKAState_State_M_enum **~** TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 42.4000015 42.4000015 ± 0.0009765625 TrqOvlSta_ReadytoPulse_Cnt_M_lgc **~** 1 ± 1 **v** target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 $target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value$ 1 1 target_TrqOvlSta_Per1_LKAState_State_enum.value target TrgOvlSta Per1 PosServEnable Cnt Igc.value 0 ± 1 42.4000015 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 42.4000015 ± 0.0009765625 0.677999973 0.677999973 ± 0.0009765625 target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 56.4599991 56.4599991 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.100 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	216790.344 -3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrgOvlSta LKAFault Cnt M lgc	0
TrqOviSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvISta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	0.254999995 9.11999989
k APAMaxVehSpd Kph f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32	0.0529999994 4.55999994
k StandstillTime Sec f32	0.0529999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvlSta Per1 ESCRequest Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARiniibit_Crit_igc target_TrqOvlSta_Per1_LKARequest_Crit_igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target TrqOvlSta Per1 TrqOscFreq Hz f32
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	849.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrgOvlSta ESCState State M enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	87934.9609	87934.9609 ± 1	•
TrgOvlSta ElapsedStandstillTime Sec M f32	404784	404784	-
TrqOvlSta HapticState State M enum	1	1	-
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta LKAPermFault Cnt M Igc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	-
TrqOvlSta ReadytoPulse Cnt M lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	-
target TrqOvlSta Per1 ESCState State enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target TrqOvlSta Per1 LKAState State enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	-
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0	0 ± 0.0009765625	-
○ - · 1 1 1 1 1 1 - 1 1 1 1 1 1 1 1 1	1.5		
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.101 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2122314
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	88734.6406
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	411330
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	43000
TrqOvlSta_Haptictime_Sec_M_f32	219795.688
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008





	Input Value
, _ , = = = = = = = = = = = = = = =	1
	0
	0.26600003
	9.19999981 207
	19,2999992
2 11 1 1 PM2 F 21	0.75
- · ·	1.72500002
	57.487999
	1.84000003
k_LKAMaxVehSpd_Kph_f32	218.5
k_LKAMinVehSpd_Kph_f32	201.25
	0.224999994
	20.5
	0.057
	4.599999
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.057
	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvISta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_rer1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
0	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
0 = 1 = = = = = = = = = = = = = = = = =	0
0	0
v = 1	1
312 (1 112 1 21 1 1 1 2 2 2 3 1 1 1	0 -917.909973
3.5 1	-917.909973 0
	854
	0
	0
· - ·	0
	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
312 11 112 1 2 1111 1111 2 23 111	1
• - ·	-5.5999999
	0
	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0 72.0999985
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.6999998		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	43000	43000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	219795.688	219795.688 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.31479979	-4.31479979 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008	45.2000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	45.2000008	45.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	57.487999	57.487999 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.102 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2156277
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	89534.3203
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	417876
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	45000
TrqOvlSta_Haptictime_Sec_M_f32	222801.016
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	46.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.27700001
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	23
k_HapticAmplitude_MtrNm_f32	0.300000012
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	58.0019989
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	20.7999992
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0.0610000007
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	£2.2		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lg	ne.		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target TrqOvISta Per1 ESCRequest Cnt lo			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSHOscillateState State enun	target TrqOvlSta Per1 GMOSHOscillateSta			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	arget_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lgc</u>		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosSrvoHwAngle_H target_TrqOvlSta_Per1_PosTrajEnable_Cnt_			
target_Rte_Inst_Ap_trqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cht_gc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajEnable_Clit_ target_TrqOvISta_Per1_PosTrajHwAngle_Hv			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrqOvISta Per1 ShiftLeverIsInRevers			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm	,		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target TrqOvlSta Per1 TrqOscEnable Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-907.809998			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	858.940002			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1			
target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1			
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	1			
target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	-8.60000038			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	82.4000015			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.3000002			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	0	0	•	
TrqOvlSta_ESCState_State_M_enum	3	3	•	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•	
TrqOvlOta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•	
TrqOvlOta_ElapsedStandstillTime_Sec_M_f32	0	0	•	
TrqOvISta_HapticState_State_M_enum	0	45000 + 1		
TrqOvISta_Haptictime_Sec_M_f22	45000 222801.016	45000 ± 1		
TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNlm_M_Str.SV_Llls_f32	-2.70000005	222801.016 ± 1		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32		-2.70000005 ± 0.000009		
TrgOvISta LKAFault Cnt M Igc		1+1		
TrqOviSta_LKAFault_Cnt_M_lgc TrqOviSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1 1 + 1		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 1 3			
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1 3 ± 1	•	

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TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	58.0019989	58.0019989 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
	target Rte Inst Ap TrqOvlSta
Rte_Inst_Ap_TrqOvISta	
rqOvISta_APAState_State_M_enum	1
rqOvISta_ESCState_State_M_enum	3 2190240
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	90334
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	424422
rqOvISta_HapticState_State_M_enum	•
rqOvISta_Hapticdur_Cnt_M_u16	43000
rqOvISta_Haptictime_Sec_M_f32	225806.359
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992
rqOvISta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	4
rqOvISta_LKAState_State_M_enum	4
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	48
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.287999988
_APAMaxHwTrq_HwNm_f32	9.19999981
_APAMaxVehSpd_Kph_f32	207
_ESCMaxVehSpd_Kph_f32	26.7000008
_HapticAmplitude_MtrNm_f32	0.34999994
_HapticDuration_Sec_f32	1.72500002
_HapticFreq_Hz_f32	58.5159988
_HapticReacttime_Sec_f32	1.84000003
_LKAMaxVehSpd_Kph_f32	218.5
_LKAMinVehSpd_Kph_f32	201.25
_MaxHapticReqTime_Sec_f32	0.224999994
_SWARLimiter_HwDeg_f32	21.1000004
_ShiftLevRevTime_Sec_f32	0.057
_StandstillThresh_Kph_f32	4.5999999
_StandstillTime_Sec_f32	0.057
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	un target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgo	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgo	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Manage	Invest Value		
Name	Input Value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	C	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequi	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	$target_TrqOvlSta_Per1_TrqOscEnable_Cnt_$	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-897.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	863.880005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-5.5999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	92.6999969 113.300003		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlota_rer1_sWARTrgtAngRequest_HwDeg_f32.value	14.8999996		
target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	IXESUIT
TrqOvISta_ESCState_State_M_enum	4	4	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	_
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	43000	43000 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta HwTorgueSV HwNm M Str.SV UIs f32	-4.31479979	-4.31479979 ± 0.000009	
TrqOvlSta LKAFault Cnt M lgc	0	0 ± 1	✓
TrqOvISta LKAPermFault Cnt M Igc	1	1 ± 1	~
TrqOvISta LKAPermFault Cnt M u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	48	48 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	48	48 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.349999994	0.349999994 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	58.5159988	58.5159988 ± 0.0009765625	_

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V





Fest Step 2.104 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum rqOvlSta_ESCState_State_M_enum	4
rqOviSta_ESCState_State_w_enum rqOviSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
rqOvISta_ElapsedStandstillTime_Sec_M_f32	430968
rqOvISta_HapticState_State_M_enum	2
rqOvISta_Hapticdut_Cnt_M_u16	45000
rqOvISta_Haptictime_Sec_M_f32	228811.703
rqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.70000005
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvISta PosSrvoHwAngle HwDeg M f32	49.4000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.298999995
APAMaxHwTrq_HwNm_f32	9.27999973
_APAMaxVehSpd_Kph_f32	208.800003
_ESCMaxVehSpd_Kph_f32	30.3999996
_HapticAmplitude_MtrNm_f32	0.460000008
_HapticDuration_Sec_f32	1.74000001
_HapticFreq_Hz_f32	59.0299988
_HapticReacttime_Sec_f32	1.85599995
_LKAMaxVehSpd_Kph_f32	20
_LKAMinVehSpd_Kph_f32	203
_MaxHapticReqTime_Sec_f32	0.239999995
_SWARLimiter_HwDeg_f32	21.3999996
_ShiftLevRevTime_Sec_f32	0.0610000007
_StandstillThresh_Kph_f32	4.63999987
_StandstillTime_Sec_f32	0.0610000007
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
Irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target TrqOvlSta_Per1_ESCIsLimited Cnt_lgc
irget_Rte_inst_Ap_frqOviSta.TrqOviSta_Ferf_ESCRequest_Cnt_lgc	target TrqOvISta Per1 ESCRequest Cnt Igc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrqOvISta Per1 ESCState State enum
rget_Rte_inst_Ap_frqOviSta.TrqOviSta_Per1_ESCState_state_enum rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	
inget_Rte_inst_Ap_frqOviSta.frqOviSta_Feff_GMOSH_APAMfgEnable_Cnt_lgc	target TrqOvIsta Per1 GMOSH APAMfgEnable Cnt Igc
inget_Rte_inst_Ap_frqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc inget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
riget Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget Rte Inst Ap TrgOvISta.TrgOvISta Per1 HapticReguest Cnt Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HwTorque HwNm f32	target_TrqOvlota_reri_naparicquest_ont_ige target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target TrqOvlSta Per1 LKARequest Cnt Igc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAState State enum	target TrqOvISta Per1 LKAState State enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
rget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
irget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1

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TIQOVISIA_T ELT			
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	103		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.105 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.7999992





Name Input Value TrqOvlSta_ReadytoPulse_Cnt_M_lgc 1 k_APAIncludeHaptic_Cnt_lgc 0	
, _ ,	
k APAIngludgHaptic Cnt Igo	
K_AF Allicidder laptic_Crit_igc	
k_APAMaxHwTrqTime_Sec_f32 0.36500001	
k_APAMaxHwTrq_HwNm_f32 8.64000034	
k APAMaxVehSpd Kph f32 194.399994	
k_ESCMaxVehSpd_Kph_f32 34.0999985	
k HapticAmplitude MtrNm f32 0.648000002	
k_HapticDuration_Sec_f32 1.62	
k_HapticFreq_Hz_f32 3	
k_HapticReacttime_Sec_f32 1.72800004	
k_LKAMaxVehSpd_Kph_f32 205.199997	
k LKAMinVehSpd Kph f32 0	
k_SWARLimiter_HwDeg_f32 21.7000008	
k_ShiftLevRevTime_Sec_f32 0.0289999992	
k_StandstillThresh_Kph_f32 4.32000017	
k_StandstillTime_Sec_f32 0.0289999992	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_TrqOvlSta	
	a_Per1_APANonRecoverableFaults_Cnt_lgc
	a_Per1_APARecoverableFaults_Cnt_lgc
	a_Per1_APARequest_Cnt_lgc
	a_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta	a_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta	a_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta	a_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta	a_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	a_Per1_ESCFault_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc \\ target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc \\ target_$	a_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta	_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta	a_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_TrqOvlSta	_Per1_GMOSHOscillateState_State_enum
	a_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	a_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	a_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	a_Per1_HapticRequest_Cnt_lgc
	a_Per1_HwTorque_HwNm_f32
	a_Per1_LKAFault_Cnt_lgc
	n_Per1_LKAInhibit_Cnt_lgc
	n_Per1_LKARequest_Cnt_lgc
	Per1 LKAState State enum
	n_Per1_MaxSecureVehicleSpeed_Kph_f32
	n_Per1_MinSecureVehicleSpeed_Kph_f32
	n_Per1_PosServEnable_Cnt_lgc
	n_Per1_PosSrvoHwAngle_HwDeg_f32
	a_Per1_PosTrajEnable_Cnt_lgc
	a_Per1_PosTrajHwAngle_HwDeg_f32
	n_Per1_SWARTrgtAngRequest_HwDeg_f32
	a_Per1_ShiftLeverIsInReverse_Cnt_lgc
	n_Per1_TrqOscAmp_MtrNm_f32
	a_Per1_TrqOscEnable_Cnt_lgc
	a_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -877.51001	
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 873.76001	
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0	
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value 0	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0	
target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value 0	
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	
target TrqOvlSta_Fer1_HapticKequest_Crit_igc.value 1 target TrqOvlSta_Per1_HwTorque HwNm f32.value 8.69999981	
target_TrqOvlSta_Per1_Hw10rque_Hwnin_isz.value 0.659999961 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.400000006	
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 133.899994	
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001	

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508	365508	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.7999992	50.7999992 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	50.7999992	50.7999992 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrgOvISta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	1918536
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	83936.5625
TrqOvlSta ElapsedStandstillTime Sec M f32	372054
TrgOvlSta HapticState State M enum	2
TrgOvISta Hapticdur Cnt M u16	31000
TrgOvlSta Haptictime Sec M f32	201765
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-5.1500001
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.43000007
TrqOvISta LKAFault Cnt M lgc	1
TrgOvISta LKAPermFault Cnt M lgc	0
TrgOvISta LKAPermFault Cnt M u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	52.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.375999987
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	37.7999992
k_HapticAmplitude_MtrNm_f32	0.653999984
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	100
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	207.100006
k_LKAMinVehSpd_Kph_f32	220
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	22
k_ShiftLevRevTime_Sec_f32	0.032999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.032999998

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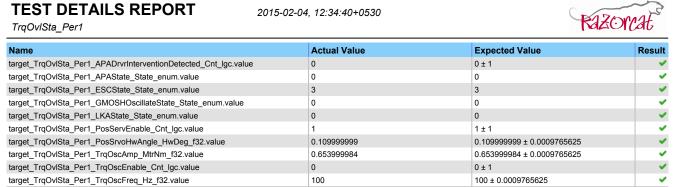
TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFat	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlOta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateSta target TrqOvlSta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target TrqOvISta Per1 GMOSH LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-867.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	878.700012		
target TrgOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	144.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	19.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	3	3	· ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1 0	Ž
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	201765	201765 ± 1	~
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	
TrqOviSta_HwTorqueSv_HwWin_w_Str.3v_Dis_132	1	1 ± 1	~
TrqOviSta_EtXi auti_oni_w_gc	0	0 ± 1	
TrqOvISta LKAPermFault Cnt M u08	2	2±1	-
TrqOvISta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
	I -	i ·	

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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvISta_APAState_State_M_enum	2
FrqOvISta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
FrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvISta_HapticState_State_M_enum	1
rqOvISta_Hapticdur_Cnt_M_u16	33000
rqOvISta_Haptictime_Sec_M_f32	204770
rqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019
rqOvISta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
rqOvISta_HwT0rquesv_HwNin_Iw_Sti.K_Ois_IS2	1
rqOviSta_LKAPault_Cit_M_igc	0
rqOviSta_LKAPermFault_Cnt_M_igc rqOviSta_LKAPermFault_Cnt_M_u08	3
rqOviSta_LKAFeffirFault_Crit_ivi_uo6 rqOviSta_LKAState_State_M_enum	0
rqOviSta_ErAState_state_injerium rqOviSta_PosSrvoHwAngle_HwDeg_M_f32	53.5999985
	33.3999963
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	
_APAMaxHwTrqTime_Sec_f32	0.38699995
_APAMaxHwTrq_HwNm_f32	8.80000019 198
_APAMaxVehSpd_Kph_f32 ESCMaxVehSpd_Kph_f32	41.5
_	0.660000026
_HapticAmplitude_MtrNm_f32	1.64999998
:_HapticDuration_Sec_f32 : HapticFreq Hz f32	60.5719986
- ·	1.75999999
_HapticReacttime_Sec_f32	
_LKAMaxVehSpd_Kph_f32	209
C_LKAMinVehSpd_Kph_f32	120.220001
_MaxHapticReqTime_Sec_f32	0.15000006
_SWARLimiter_HwDeg_f32	22.2999992
_ShiftLevRevTime_Sec_f32	0.037000005
_StandstillThresh_Kph_f32	4.400001
_StandstillTime_Sec_f32	0.0370000005
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cr	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_end	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HwTorque HwNm f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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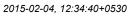


Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-857.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	883.640015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	133.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	154.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	21.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	53.5999985	53.5999985 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	53.5999985	53.5999985 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	60.5719986	60.5719986 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.108 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.26001
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	860.26001
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	860.26001
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	35000
TrqOvlSta_Haptictime_Sec_M_f32	860.26001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.4499981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	0
TrqOvISta_LNAState_State_W_entitin	62
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.398000002
k_APAMaxHwTrq_HwNm_f32	0.100000001
k_APAMaxVehSpd_Kph_f32	199.800003
k_ESCMaxVehSpd_Kph_f32	63.7000008
k_HapticAmplitude_MtrNm_f32	0.119999997
k_HapticDuration_Sec_f32	1.66499996
k_HapticFreq_Hz_f32	3.5
k_HapticReacttime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	0.40000006
k_MaxHapticReqTime_Sec_f32	0.165000007
k_SWARLimiter_HwDeg_f32	24.1000004
k_ShiftLevRevTime_Sec_f32	0.0379999988 4.44000006
k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
	target_TrqOvlSta_Per1_Swartngtarigrequest_nwbeg_132 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target Rie Inst An Trouvista Irouvista Peri Shitti everisin Peverse Chi Ide	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrgOvlSta_Per1_TrgOscAmp_MtrNm_f32	Tardet Trouvista Peri Trouscamp Mirring 132
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrgOvlSta_Per1_TrgOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-847.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	888.580017		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	216.300003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	-
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.26001	860.26001 ± 1	→
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	860.26001	860.26001	-
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	35000	35000 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.37099981	-6.37099981 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	62	62 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	→
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	62	62 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.119999997	0.119999997 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value		3.5 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.109 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta		
TrqOvlSta_APAState_State_M_enum	1		
TrqOvlSta_ESCState_State_M_enum	4		
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1345		
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1345		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1345		
TrqOvlSta_HapticState_State_M_enum	1		
TrqOvlSta_Hapticdur_Cnt_M_u16	37000		
TrqOvlSta_Haptictime_Sec_M_f32	1345		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463		
TrqOvlSta_LKAFault_Cnt_M_lgc	1		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1		
TrqOvlSta_LKAPermFault_Cnt_M_u08	1		
TrqOvlSta_LKAState_State_M_enum	1		
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	63.4000015		





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.409000009
k_APAMaxHwTrq_HwNm_f32	0.0799999982 201.600006
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	67.4000015
k_HapticAmplitude_MtrNm_f32	0.140000013
k_HapticDuration_Sec_f32	1.67999995
k_HapticFreq_Hz_f32	4.19999981
k_HapticReacttime_Sec_f32	0.0160000008
k_LKAMaxVehSpd_Kph_f32	1.8999998
k_LKAMinVehSpd_Kph_f32	1.75
k_MaxHapticReqTime_Sec_f32	0.180000007
k_SWARLimiter_HwDeg_f32	24.3999996
k_ShiftLevRevTime_Sec_f32	0.039000008
k_StandstillThresh_Kph_f32	4.4800002
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvIsta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_Fei1_F0sTrajnwArigte_nwbeg_i32 target Rte Inst Ap TrqOviSta.TrqOviSta Per1 SWARTrgtAngRequest HwDeg f3	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrgOvlSta Per1 ShiftLeverlsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-837.109985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	893.52002
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOvlSta_Per1_ESCRequest_Crit_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.30000019
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	514.799988
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	226.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	32.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1345	1345 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	37000	37000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	1345	1345 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999	-4.0999999 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.140000001	0.140000001 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.19999981	4.19999981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.110 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1830.43005
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1830.43005
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1830.43005
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	39000
TrqOvlSta_Haptictime_Sec_M_f32	1830.43005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	64.8000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.419999987
k_APAMaxHwTrq_HwNm_f32	0.159999996
k_APAMaxVehSpd_Kph_f32	203.399994
k_ESCMaxVehSpd_Kph_f32	71.0999985
k_HapticAmplitude_MtrNm_f32	0.159999996
k_HapticDuration_Sec_f32	1.69500005
k_HapticFreq_Hz_f32	4.9000001
k_HapticReacttime_Sec_f32	0.0320000015
k_LKAMaxVehSpd_Kph_f32	3.79999995
k_LKAMinVehSpd_Kph_f32	3.5
k_MaxHapticReqTime_Sec_f32	0.194999993
k_SWARLimiter_HwDeg_f32	24.7000008
k_ShiftLevRevTime_Sec_f32	0.0399999991
k_StandstillThresh_Kph_f32	4.51999998
k_StandstillTime_Sec_f32	0.0289999992
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (App_TrqOvlSta_Per1_APADrvrInterventionDetected_Cn$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lq	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>j_</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgE	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_	132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_en target_TrqOvlSta_Per1_MaxSecureVehicleS		
	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cn target_TrqOvlSta_Per1_PosSrvoHwAngle_F		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_H target TrqOvlSta Per1 SWARTrgtAngRequ		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscErtable_Ont_igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvISta_Per1_TrqOscFreq_Hz_f32		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0	.	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-827.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	898.460022		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	0		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-7.9000001		
target TrqOvlSta Per1 LKAFault Cnt lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	250.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	236.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1999998		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	v
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	_
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	39000	39000 ± 1	_
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75	-3.75 ± 0.000009	~
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
		0 ± 1 2 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0		
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	2 ± 1	~

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Name	Actual Value	Expected Value	Result
target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	o Actual Value	0 ± 1	Result
		0 = 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	v
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	64.8000031	64.8000031 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.159999996	0.159999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.9000001	4.9000001 ± 0.0009765625	~

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	-
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

lame	Input Value
	target Rte Inst Ap TrqOvlSta
Rte_Inst_Ap_TrqOvISta	· ·
rqOvISta_APAState_State_M_enum	3
rqOvISta_ESCState_State_M_enum	1
rqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2315.65991
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2315.65991
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	2315.65991
rqOvISta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	41000
rqOvISta_Haptictime_Sec_M_f32	2315.65991
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.400001
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
rqOvISta_LKAFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvISta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	66.1999969
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.430999994
_APAMaxHwTrq_HwNm_f32	0.239999995
_APAMaxVehSpd_Kph_f32	205.199997
_ESCMaxVehSpd_Kph_f32	74.8000031
_HapticAmplitude_MtrNm_f32	0.180000007
_HapticDuration_Sec_f32	1.71000004
_HapticFreq_Hz_f32	5.5999999
_HapticReacttime_Sec_f32	0.0480000004
_LKAMaxVehSpd_Kph_f32	5.69999981
_LKAMinVehSpd_Kph_f32	5.25
_MaxHapticReqTime_Sec_f32	0.20999993
_SWARLimiter_HwDeg_f32	25
_ShiftLevRevTime_Sec_f32	0.0410000011
_StandstillThresh_Kph_f32	4.55999994
_StandstillTime_Sec_f32	0.032999998
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-816.909973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	903.400024		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	1		
target_TrqOvlSta_rei1_ESCFatil_Crit_igc.value target TrqOvlSta Per1 ESCIsLimited Cnt Iqc.value	1		
target TrqOvlSta_Fer1_ESCISEIIIIIted_Crit_igc.value target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Clit_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-7.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	190.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.3000002		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2315.65991	2315.65991 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2315.65991	2315.65991 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991	2315.65991 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.38850021	-5.38850021 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	Y
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	V
TrqOvlSta_LKAState_State_M_enum	3	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1 3	1±1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	-
target_TrqOviSta_Per1_PosStvonwArigie_nwDeg_132.value target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.180000007	0.180000007 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.180000007	0 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.5999999	5.5999999 ± 0.0009765625	-
0	1		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.112 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2800.93994
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2800.93994
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	2800.93994
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	43000
TrqOvlSta_Haptictime_Sec_M_f32	2800.93994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.04999995 0.495999992
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	0.495999992
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrgOvlSta LKAPermFault Cnt M u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	67.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.442000002
k_APAMaxHwTrq_HwNm_f32	0.319999993
k_APAMaxVehSpd_Kph_f32	207
k_ESCMaxVehSpd_Kph_f32	78.5
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0.200000003 1.72500002
k_HapticFreq_Hz_f32	6.30000019
k HapticReacttime Sec f32	0.064000003
k_LKAMaxVehSpd_Kph_f32	7.5999999
k_LKAMinVehSpd_Kph_f32	7
k_MaxHapticReqTime_Sec_f32	0.224999994
k_SWARLimiter_HwDeg_f32	25.2999992
k_ShiftLevRevTime_Sec_f32	0.0419999994
k_StandstillThresh_Kph_f32	4.5999999
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Ont_igc target_Rte_Inst_Ap_TrqOvIsta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Crit_gc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
targot_rrqOviota_rerr_oovvrouniu_ont_lgc.value	U

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TrqOviSta_Peri		IGA	CIMO
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-806.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	908.340027		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-7.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	200.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	511		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	12.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	43000	43000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995	-3.04999995 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	67.5999985	67.5999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	67.5999985	67.5999985 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.30000019	6.30000019 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.113 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3285.03003
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3285.03003
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3285.03003
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	45000
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	69

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.453000009
k_APAMaxHwTrq_HwNm_f32	0.40000006
k_APAMaxVehSpd_Kph_f32	208.800003 82.1999969
k_ESCMaxVehSpd_Kph_f32	0.219999999
k_HapticAmplitude_MtrNm_f32 k_HapticDuration_Sec_f32	1.7400001
k_HapticFreq_Hz_f32	7
k HapticReacttime Sec f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k LKAMinVehSpd Kph f32	8.75
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	25.6000004
k_ShiftLevRevTime_Sec_f32	0.0430000015
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0.0410000011
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cn$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target_rtte_inst_Ap_TrqOviSta.TrqOviSta_Fe11_E3CState_State_entini target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Pe11_GMOSHOscillateState_State_enur	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_inst_Ap_rtq0vista.rtq0vista_Peri_rtq0screq_nz_isz target_Trq0vlSta_Peri_APANonRecoverableFaults_Cnt_lgc.value	aliget_riqovista_Peri_riqoscrieq_nz_isz
target_TrqOvIsta_FeT_APARecoverableFaults_Cnt_lgc.value	1
target TrqOvISta Per1 APARequest Cnt Igc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target TrqOvISta Per1 CCWPosition HwDeg f32.value	-796.710022
target TrgOvlSta Per1 CWFound Cnt Igc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	913.280029
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.69999981
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	211.199997
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	255.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	1
	0.129999995

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	45000	45000 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.219999999	0.219999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7	7 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.114 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3770.09009
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3770.09009
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3770.09009
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	47000
TrqOvlSta_Haptictime_Sec_M_f32	3770.09009
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.518000007
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.463999987
k_APAMaxHwTrq_HwNm_f32	0.479999989
k_APAMaxVehSpd_Kph_f32	210.600006
k_ESCMaxVehSpd_Kph_f32	85.9000015
k_HapticAmplitude_MtrNm_f32	0.239999995
k_HapticDuration_Sec_f32	1.755
k_HapticFreq_Hz_f32	7.69999981
k_HapticReacttime_Sec_f32	0.0960000008
k_LKAMaxVehSpd_Kph_f32	11.3999996
k_LKAMinVehSpd_Kph_f32	10.5
k_MaxHapticReqTime_Sec_f32	0.254999995
k_SWARLimiter_HwDeg_f32	25.8999996
k_ShiftLevRevTime_Sec_f32	0.043999998
k_StandstillThresh_Kph_f32	4.67999983
k_StandstillTime_Sec_f32	0.0450000018
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntorum and the properties of the properties o$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	•	Faults Cnt loc		
	target TrqOvISta Per1 APARecoverableFau			
	target_TrqOvlSta_Per1_APARequest_Cnt_lg			
	target TrqOvISta Per1 APAState State enu			
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc			
	target TrqOvlSta Per1 CCWPosition HwDeg f32			
·	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	9		
	target_TrqOvISta_Per1_CWPosition_HwDeg	f32		
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc			
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	ac		
	target TrqOvlSta Per1 ESCRequest Cnt Ig			
	target_TrqOvlSta_Per1_ESCState_State_enu			
	target_TrqOvlSta_Per1_GMOSHOscillateStar			
	target TrqOvlSta Per1 GMOSH APAMfgEn			
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn			
	target TrqOviSta Per1 GMOSH LKAMfgEnable Cnt lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f3	32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
	target TrqOvlSta Per1 LKARequest Cnt Igo	3		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enu	ım		
	target TrqOvlSta Per1 MaxSecureVehicleSp			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed Kph f32		
	target TrgOvlSta Per1 PosServEnable Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_Hv	wDeg_f32		
	target TrqOvlSta Per1 PosTrajEnable Cnt			
	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw	/Deg f32		
	target_TrqOvISta_Per1_SWARTrgtAngReque			
	target TrqOvlSta Per1 ShiftLeverlsInRevers			
	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	f32		
	target TrqOvlSta Per1 TrqOscEnable Cnt I			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32			
	0			
	0			
	0			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-786.609985			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	918.219971			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.30000019			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0			
	221.5			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	190.600006			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	14.6000004			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	4	4	~	
	4	4	~	
, = = ==	3770.09009	3770.09009 ± 1	~	
	0	0 ± 1	~	
	0	0	~	
	0	0	~	
	47000	47000 ± 1	~	
	0	0 ± 1	~	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.39610004	-4.39610004 ± 0.000009	~	
	0	0 ± 1	~	
	0	0 ± 1	~	
	1	1 ± 1	~	
	4	4	~	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.4000015	70.4000015 ± 0.0009765625	~	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~	

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	70.4000015	70.4000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.23999995	0.239999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.69999981	7.69999981 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.115 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvISta ESCState State M enum	0
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	425564
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	425564
TrqOvlSta ElapsedStandstillTime Sec M f32	425564
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta Hapticdur Cnt M u16	49000
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-2
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.528999984
TrgOvISta LKAFault Cnt M lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	71.8000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.474999994
k APAMaxHwTrq HwNm f32	0.560000002
k APAMaxVehSpd Kph f32	212.399994
k ESCMaxVehSpd Kph f32	89.5999985
k_HapticAmplitude_MtrNm_f32	0.25999999
k_HapticDuration_Sec_f32	1.76999998
k_HapticFreq_Hz_f32	8.39999962
k_HapticReacttime_Sec_f32	0.112000003
k LKAMaxVehSpd Kph f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.270000011
k SWARLimiter HwDeg f32	26.2000008
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	4.71999979
k_StandstillTime_Sec_f32	0.048999987
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_Entirin target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_lgc	target_TrqOvIsta_Per1_APAstate_state_enum target_TrqOvIsta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_ren_CCWPosition_Hwbey_i52 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc	target_TrqOvIsta_Per1_CGWPosition_nwbeg_isz
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target TrgOvISta Per1 ESCFault Cnt Igc
target_Rte_Irist_Ap_mqovista.mqovista_reri_Escratit_crit_igc target_Rte_Irist_Ap_mqovista.mqovista_reri_Escratit_crit_igc	target_TrqOvlSta_Per1_ESCFault_Ont_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCRequest_Cnt_lgc	target TrgOvISta Per1 ESCRequest Cnt Igc
target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_rerT_ESCRequest_Crit_igc target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCNequest_Ont_gc
target_Rte_linst_Ap_mqovista.mqovista_reri_E3cstate_state_entilin target Rte Inst Ap Trq0vlSta.Trq0vlSta Per1 GM0SH0scillateState State enur	
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSHOScillateState_State_entit target Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMilgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMigEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_GMOSH_ESCMIgEnable_Ctt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_GMOSH_LKAMfgEnable_Ctt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMigEnable_Crit_lgc
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GiviOSH_ExAwiigEnable_Crit_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
anger the met Ap Tryonola.Tryonola FCH Habile/Cuucer Oil Iuc	talgot_1140 nota_1 of 1_1 aption oducat_ont_igo
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Innut Value				
Name	Input Value				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc					
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!			
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-776.51001				
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	923.159973				
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1				
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1				
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1				
target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1				
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1				
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-5.9000001				
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1				
target TrqOvISta Per1 LKAInhibit Cnt Igc.value	1				
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5				
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	200.899994				
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006				
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15.699998				
target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc.value	1				
Name	Actual Value	Expected Value	Result		
TrqOvlSta_APAState_State_M_enum	3	3	rtoduit		
TrqOvISta_ESCState_State_M_enum	3	3			
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1			
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	425564	425564 ± 1			
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	425564	425564			
TrqOvlSta_HapticState_State_M_enum	1	1	✓		
TrgOvlSta Hapticdur Cnt M u16	49000	49000 ± 1			
TrqOvISta Haptictime Sec M f32	425564	425564 ± 1	· ·		
TrqOvISta HwTorqueSV HwNm M Str.SV UIs f32	-2	-2 ± 0.000009			
	1		~		
TrqOvISta_LKAFault_Cnt_M_lgc		1±1			
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	~		
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1			
TrqOvlSta_LKAState_State_M_enum	3	3	✓		
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	· ·		
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	✓		
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V		
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	V		
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~		
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~		
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~		
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	V		
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	V		
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.25999999	0.25999999 ± 0.0009765625	V		
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	V		
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	8.39999962	8.39999962 ± 0.0009765625	~		

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~	
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V	



Test Step 2.116 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrgOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvISta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4740.43018
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4740.43018
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4740.43018
TrqOvlSta_HapticState_State_M_enum TrqOvlSta Hapticdur Cnt M u16	2 51000
TrqOvISta_Haptictime_Sec_M_f32	4740.43018
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.540000021
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	73.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc k_APAMaxHwTrqTime_Sec_f32	0.486000001
k APAMaxHwTrq HwNm f32	0.639999986
k_APAMaxVehSpd_Kph_f32	214.199997
k_ESCMaxVehSpd_Kph_f32	93.3000031
k_HapticAmplitude_MtrNm_f32	0.280000001
k_HapticDuration_Sec_f32	1.78499997
k_HapticFreq_Hz_f32	9.10000038
k_HapticReacttime_Sec_f32	0.128000006
k_LKAMaxVehSpd_Kph_f32	15.1999998
k_LKAMinVehSpd_Kph_f32	14
k_MaxHapticReqTime_Sec_f32	0.284999996 26.5
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	0.0460000001
k StandstillThresh Kph f32	4.76000023
k StandstillTime Sec f32	0.0529999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWPosition HwDeg f32	target TrqOvISta_Per1_CWV outd_Cit_gc target TrqOvISta_Per1_CWPosition HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_results and the property of the propert$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_SwaRtrgiAngRequest_nwDeg_is target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_Swartngtarigkequest_Hwbeg_IS2 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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TrqOviSta_Peri		Ida	CILAB
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-766.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	928.099976		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-5.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	242.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	211.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	51000	51000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	73.1999969	73.1999969 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc		0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 1 1	
torrect TerrOulCto Dard ADACtate Ctate anum value	0 0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value			· ·
target_TrqOvlSta_rer1_AFAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0±1 4 4	~
	0 4	0 ± 1 4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	0 4 4	0±1 4 4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 4 4 2	0±1 4 4 2	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0 4 4 2 4	0±1 4 4 2 4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 4 4 2 4 0	0±1 4 4 2 4 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 4 4 2 4 0 73.1999969	0 ± 1 4 4 2 4 0 ± 1 73.1999969 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

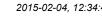
Test Step 2.117 (Repeat Count = 1)	van de la companya d
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5225.33984
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5225.33984
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	5225.33984
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	53000
TrqOvlSta_Haptictime_Sec_M_f32	5225.33984
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.29999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.550999999
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	74.5999985





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.497000009
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	216
k_ESCMaxVehSpd_Kph_f32	97
k_HapticAmplitude_MtrNm_f32	0.300000012
k_HapticDuration_Sec_f32	1.79999995
k_HapticFreq_Hz_f32	9.80000019
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	26.7999992
k_ShiftLevRevTime_Sec_f32	0.0469999984
k_StandstillThresh_Kph_f32	4.80000019
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APAState State enum	target TrgOvlSta Per1 APAState State enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target TrqOvISta Per1 CWFound Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target Rte Inst Ap TrqOviSta.TrqOviSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-756.309998
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	933.039978
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-5.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	252.399994
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	221.5
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002

TrqOvlSta_Per1



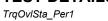


Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	17.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5225.33984	5225.33984 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5225.33984	5225.33984 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	53000	53000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	5225.33984	5225.33984 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.39380002	-3.39380002 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.30000012	0.300000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	9.80000019	9.80000019 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.118 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5710.16992
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5710.16992
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	5710.16992
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	55000
TrqOvlSta_Haptictime_Sec_M_f32	5710.16992
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.94999988
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.561999977
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	76
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.508000016
k_APAMaxHwTrq_HwNm_f32	0.800000012
k_APAMaxVehSpd_Kph_f32	217.800003
k_ESCMaxVehSpd_Kph_f32	100.699997
k_HapticAmplitude_MtrNm_f32	0.319999993
k_HapticDuration_Sec_f32	1.81500006
k_HapticFreq_Hz_f32	10.5
k_HapticReacttime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	19
k_LKAMinVehSpd_Kph_f32	17.5
k_MaxHapticReqTime_Sec_f32	0.314999998
k_SWARLimiter_HwDeg_f32	27.1000004
k_ShiftLevRevTime_Sec_f32	0.0480000004
k_StandstillThresh_Kph_f32	4.84000015
k_StandstillTime_Sec_f32	0.0610000007
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntarget_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntarget_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntarget_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntarget_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa_Rte_Inst_Apa$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWPosition_HwDe target_TrqOvlSta_Per1_CWFound_Cnt_lgc	g_132	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	дс	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn target TrqOvlSta Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f:		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_H target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-746.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	937.97998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.69999981		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	262.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta APAState State M enum	4	4	ixesuit
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	v
TrqOvlSta_Hapticdur_Cnt_M_u16	55000	55000 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.94999988	-0.949999988 ± 0.0000009	✓
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1 0 ± 1	Ž
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	V
TrqOvISta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	76	76 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	76	76 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.319999993	0.319999993 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	10.5	10.5 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.119 (Repeat Count = 1)	v v
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	6195
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	6195
TrqOvlSta ElapsedStandstillTime Sec M f32	6195
TrqOvlSta_HapticState_State_M_enum	2
TrqOvISta Hapticdur Cnt M u16	57000
TrqOvlSta_Haptictime_Sec_M_f32	6195
TrqOvlSta HwTorqueSV HwNm M Str.SV UIs f32	-0.600000024
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.573000014
TrgOvISta LKAFault Cnt M Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	77.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k APAMaxHwTrqTime Sec f32	0.518999994
k APAMaxHwTrg HwNm f32	0.879999995
k APAMaxVehSpd Kph f32	219.600006
k ESCMaxVehSpd Kph f32	104.400002
k HapticAmplitude MtrNm f32	0.340000004
k_HapticDuration_Sec_f32	1.83000004
k_HapticFreq_Hz_f32	11.1999998
k HapticReacttime Sec f32	0.175999999
k LKAMaxVehSpd Kph f32	20.8999996
k_LKAMinVehSpd_Kph_f32	19.25
k_MaxHapticReqTime_Sec_f32	0.330000013
k SWARLimiter HwDeg f32	27.3999996
k_ShiftLevRevTime_Sec_f32	0.0489999987
k_StandstillThresh_Kph_f32	4.88000011
k_StandstillTime_Sec_f32	0.0649999976
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOviSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOviSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOviota_t cri_xi Addate_ctate_ctatin target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOviota_i cri_covi osition_i wbog_isz
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOviota_i cri_GVV osition_i twocg_ioz
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOviSta Per1 ESCRequest Cnt Igc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target_TrqOvIsta_Per1_ESCState_state_enum
target_Inte_Inst_Ap_TrqOviSta.TrqOviSta_FeTSOState_State_Entiti target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enu	
target_Rie_Irist_Ap_TrqOviSta.TrqOviSta_FerT_GMOSH_ScillateState_State_eriu target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fet	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
goroor_np_ridonota.ridonota_i ori_inviolquo_riwinii_io2	goqoou_i oi i_im ioiquo_imimi_ioz





			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
	target_TrqOvlSta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_Fe1_TrqOscFreq Hz f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-736.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	942.919983		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	273 242.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	3	3	✓
TrqOvlSta_ESCState_State_M_enum	4	4	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	-
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	6195	6195 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	57000	57000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	6195	6195 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024	-0.600000024 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	V
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	- 4
target_TrqOvlSta_Per1_APADtvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	
target_TrqOvlSta_Per1_ESCState_State_enum.value		4 0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 3	3	V
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	-
target_rrqOvlSta_Per1_PosServErlable_Cnt_gc.value target_rrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.18999998	0.189999998 ± 0.0009765625	· ·
target_TrqOvlSta_Per1_FosSivonwArigie_Hwbeg_isz.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.340000004	0.340000004 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	11.1999998	11.1999998 ± 0.0009765625	~
			1

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.120 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6679.83008
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	6679.83008
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	6679.83008
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	59000
TrqOvlSta_Haptictime_Sec_M_f32	6679.83008
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-0.25 0.583999991
TrqOvISta_LKAFault_Cnt_M_lgc	0.00365661
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	78.8000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.529999971
k_APAMaxHwTrq_HwNm_f32	0.959999979
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	0.200000003 108.099998
k_HapticAmplitude_MtrNm_f32	0.360000014
k HapticDuration Sec f32	1.84500003
k_HapticFreq_Hz_f32	11.8999996
k_HapticReacttime_Sec_f32	0.192000002
k_LKAMaxVehSpd_Kph_f32	22.7999992
k_LKAMinVehSpd_Kph_f32	21
k_MaxHapticReqTime_Sec_f32	0.344999999
k_SWARLimiter_HwDeg_f32	27.7000008
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	4.92000008 0.0689999983
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_ESCRequest_Cnt_Igc	target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ESCState State enum	target TrqOvISta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	· - ·
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_inst_Ap_irqOviSta.irqOviSta_Peri_LKAState_state_enum target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Peri_MaxSecureVehicleSpeed_Kph_f32	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. \\$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0
target_TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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TIQOVISIA_T ELT			
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-726.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	947.859985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-3.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	252.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	21.2000008		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	1100011
TrgOvlSta ESCState State M enum	4	4	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6679.83008	6679.83008 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	-
TrqOvlSta ElapsedStandstillTime Sec M f32	6679.83008	6679.83008	
TrgOvISta HapticState State M enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	59000	59000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.38160014	-2.3815999 ± 0.000009	
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrgOvISta LKAPermFault Cnt M Igc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	
TrqOvISta_LKAState_State_M_enum	4	4	
TrqOvISta PosSrvoHwAngle HwDeg M f32	78.8000031	78.8000031 ± 0.0009765625	
TrqOvISta ReadytoPulse Cnt M lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target TrqOvISta Per1 ESCState State enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target TrqOvISta Per1 LKAState State enum.value	4	4	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	78.8000031	78.8000031 ± 0.0009765625	
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.360000014	0.360000011 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.300000014	0.5000000 14 ± 0.00097 05025	
target_TrqOvlSta_Per1_TrqOscEriable_Cit_gc.value	11.8999996	11.8999996 ± 0.0009765625	
target_11qOviota_F 611_11qOs6116q_11z_10z.value	11.0999990	11.0999990 ± 0.0009703023	

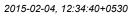
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.121 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7164.66016
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	7164.66016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	7164.66016
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	61000
TrqOvlSta_Haptictime_Sec_M_f32	7164.66016
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.595000029
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	80.1999969





APPANDATION Testing		
A.P.A.Masteristric Sec. 92 0.51100009	Name	Input Value
	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
A APANDATA In Territor See 102 A PANDATA IN TERRITOR SEE 1	k_APAIncludeHaptic_Cnt_lgc	0
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target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perf. LKAState_State_enum target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. MaxSecureVehicleSpeed (Kph. 132 target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. DesServEnable. Cnt. Igo: target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. PosServEnable. Cnt. Igo: target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. PosServEnable. Cnt. Igo: target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. PosServEnable. Cnt. Igo: target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. PosTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. PosTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. PosTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. TrqOviSta. Perf. PosTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. Perf. DesTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. Perf. DesTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. Perf. SoviSta. Perf. PosTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. Perf. DesTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. Perf. DesTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. Perf. SoviSta. Perf. SoviSta. Perf. SoviSta. Perf. DesTarjenable. Cnt. Igo: target. Ries. Lap. TrqOviSta. Perf. DesTarjenable. Cnt. Igo: target. TrqOviSta. Perf. ApARecoverableFaults. Cnt. Igo: value 1 target. TrqOviSta. Perf. DesTarjenable. Cnt. Igo: value 1 target. TrqOviSta. Perf. DesTarjenable. Cnt. Igo: value 1 target. TrqOviSta. Perf. DesTarjenable. Cnt. Igo: value 1 target. TrqOviSta. Perf. ExcPequest. Cnt. Igo: value 1 target. TrqOviSta. Perf. ExcPequest. Cnt. Igo: value 1 target. TrqOviSta. Perf. ExcPequest. Cnt. Igo: value 1 target. TrqOviSta. Perf. LKARSault.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Hz_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Hz_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Hz_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Hz_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCnep_Hz_132 target_TrqOviSta_Pert_APANnonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_ApARtecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_CWPound_Cnt_Igc_value target_TrqOviSta_Pert_CWPound_Cnt_Igc_value target_TrqOviSta_Pert_CWPound_Cnt_Igc_value target_TrqOviSta_Pert_CWPound_Cnt_Igc_value target_TrqOviSta_Pert_ESCRaut_Cnt_Igc_value target_TrqOviSta_Pert_ESCRaut_Cnt_Igc_value target_TrqOviSta_Pert_ESCRaut_Cnt_Igc_value target_TrqOviSta_Pert_HxAraut_Cnt_Igc_value target_TrqOviSta_Pert_MxAraut_Cnt_Igc_value target_TrqOviSta_Pert_MxAraut_Cnt_Igc_value target_TrqOviSta_Pert_MxAraut_Cnt_Igc_value target_TrqOviSta_Pert_MxAraut_Cnt_Igc_value target_TrqOviSta_Pert_MxAraut_Cnt_Igc_value target_TrqOviSta_Pert_MxAraut_Cnt_Igc_v	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target Rie Inst Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DosSrovEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SwRATrqiAngRequest_Hwbeg_g132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SwRATrqiAngRequest_Hwbeg_g132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsSer.Dulpc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsSer.Dulpc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsSer.Dulpc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsSer.Dulpc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsSer.Dulpc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsSer.Dulpc target_TrqOviSta_Perl_ApAnnonRecoverableFaults_Cnt_Ig_cvalue target_TrqOviSta_Perl_ApAnnonRecoverableFaults_Cnt_Ig_cvalue target_TrqOviSta_Perl_CWFound_Cnt_Ig_cvalue target_TrqOviSta_Perl_CWFound_Cnt_Ig_cvalue target_TrqOviSta_Perl_CWFound_Cnt_Ig_cvalue target_TrqOviSta_Perl_CWFound_Cnt_Ig_cvalue target_TrqOviSta_Perl_ESCRequest_Cnt_Ig_cvalue target_TrqOviSta_Perl_ESCRequest_Cnt_Ig_cvalue target_TrqOviSta_Perl_ESCRequest_Cnt_Ig_cvalue target_TrqOviSta_Perl_GMOSH_ESCMgEnable_Cnt_Ig_cvalue target_TrqOviSta_Perl_GMOSH_ESCMgEnable_Cnt_Ig_cvalue target_TrqOviSta_Perl_Mosh_Redules_Cnt_Ig_cvalue target_TrqOviSta_Perl_HalmSecurevelocieSpeed_Kpt_f32_value target_TrqOviSta_Perl_Mosh_Redules_Cnt_Ig_cvalue target_TrqOviSta_Perl_Mosh_Redules_Cnt_Ig_cvalue target_TrqOviSta_Perl_Mosh_Redules_Cnt_Ig_cvalue target_TrqOviSta_Perl_Mosh_Redules_Cnt_Ig_cvalue target_TrqOviSta_Perl_Mosh_Redules_Cnt_Ig_cvalue target_TrqOviSta_Perl_Mosh_Redules_Cnt_Ig_cvalue target_TrqOviSta_Perl_Mosh_Redules_Cnt_Ig_cvalue target_Trq	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rie_Inst_Ap_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Pert_PosSrovbhwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Prt_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_Rie_Inst_Ap_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Pr	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rie_Inst_Ap_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Pert_PosSrovbhwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Prt_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Prt_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_Rie_Inst_Ap_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_TrqOviSta_Prt_TrqOsChap_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Prt_Minhm_f32 target_TrqOviSta_Pr	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ShiftLeverisinReverse_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_RrqoviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_RrqoviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_RrqoviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_RrqoviSta_Pert_Ap_Rrqovista_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_RrqoviSta_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_Rrqovista_Pert_Ap_		
target_Rie_Inst_Ap_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Perl_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_MtrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_MtrNm_f32 target_Rie_Inst_Ap_TrqOviSta_Perl_TrqOscAmp_MtrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscFreq_Hz_f32 target_Rie_Inst_Ap_TrqOviSta_Perl_ApAngle_Revers_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Perl_TrqOscFreq_Hz_f32 target_TrqOviSta_Perl_ApAngle_Revers_Cnt_Igc_value target_TrqOviSta_Perl_ApAngle_Value target_TrqOviSta_Perl_ApAngle_Value target_TrqOviSta_Perl_CwRequest_Cnt_Igc_value target_TrqOviSta_Perl_CwRequest_Cnt_Igc_value target_TrqOviSta_Perl_CwRequest_Cnt_Igc_value target_TrqOviSta_Perl_CwRequest_Cnt_Igc_value target_TrqOviSta_Perl_CwRequest_Cnt_Igc_value target_TrqOviSta_Perl_CwRequest_Cnt_Igc_value target_TrqOviSta_Perl_EsCFault_Cnt_Igc_value target_TrqOviSta_Perl_EsCFault_Cnt_Igc_value target_TrqOviSta_Perl_EsCRequest_Cnt_Igc_value target_TrqOviSta_Perl_GwRequest_Cnt_Igc_value target_TrqOviSta_Perl_GwRequest_Cnt_Igc_value target_TrqOviSta_Perl_GwRequest_Cnt_Igc_value target_TrqOviSta_Perl_HwTorque_HwNm_f32_value target_TrqOviSta_Perl_HwTorque_HwNm_f32_value target_TrqOviSta_Perl_HwTorque_HwNm_f32_value target_TrqOviSta_Perl_MaxSecureVerliceSpeed_Kpn_f32_value 293.600006 target_TrqOviSta_Perl_MaxSecureVerliceSpeed_Kpn_f32_value target_TrqOviSta_Perl_DwSTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_DwSTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_DwSTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_DwSTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_DwSTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_DwSTrajEnable_		target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOviSta_Per1_SVRARTrgIAhangke_Hwbeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SVRARTrgIAhangkequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SMitLeverIshReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFnable_Cnt_Igc target_TrqOviSta_Per1_Cnt_Igc target_TrqOviSta_Per1_Cnt_Igc target_TrqOviSta_Per1_Cnt_Igc target_TrqOviSta_Per1_Cnt_Igc target_TrqOviSta_Per1_Cnt_Igc target_TrqOviSta_Per1_Cnt_Igc target_TrqOviSta_Per1_Cnt_Igc target_TrqOviSta_Per1_Encoperation_the properation_transport target_TrqOviSta_Per1_Encoperation_transport target_TrqOviSta_Per1_HvTorque_Hvbm_f32_value target_TrqOviSta_Per1_HvTorque_Hvbm_f32_value target_TrqOviSta_Per1_Encoperation_transport target_TrqOviSta_Per1_Encoperation_transport target_TrqOviSta_Per1_Encoperation_transport target_TrqOviSta_Per1_Encoperation_transport target_TrqOviSta_Per1_Encoperation_transport target_TrqOviSta_Per		
target_Re_Inst_Ap_TrqOviSta_Perd_SWARTgtAngRequest_HwDeg_f32 target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Perd_SkintLeverlsinReverse_Cnt_Igc target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtnNm_f32 target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtnNm_f32 target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtnNm_f32 target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtnNm_f32 target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtnNm_f32 target_TrqOviSta_Perd_ApAnonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Perd_ApAnonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Perd_ApAnonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Perd_ApAnonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Perd_ApAnequest_Cnt_Igc_value target_TrqOviSta_Perd_CwFound_Cnt_Igc_value target_TrqOviSta_Perd_CwFound_Cnt_Igc_value target_TrqOviSta_Perd_CwFound_Cnt_Igc_value target_TrqOviSta_Perd_CwFound_Cnt_Igc_value target_TrqOviSta_Perd_EsCFault_Cnt_Igc_value target_TrqOviSta_Perd_EsCFault_Cnt_Igc_value target_TrqOviSta_Perd_EsCFault_Cnt_Igc_value target_TrqOviSta_Perd_EsCFault_Cnt_Igc_value target_TrqOviSta_Perd_GwSh_Perd_EsCRequest_Cnt_Igc_value target_TrqOviSta_Perd_GwSh_EsCMfgEnable_Cnt_Igc_value target_TrqOviSta_Perd_MoSH_EsCMfgEnable_Cnt_Igc_value target_TrqOviSta_Perd_HapticRequest_Cnt_Igc_value target_TrqOviSta_Perd_HapticRequest_Cnt		
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target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	293.600006
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	262.700012
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.109999999	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.2999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	7164.66016	7164.66016 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	61000	61000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	7164.66016	7164.66016 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.10000001	0.100000001 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.379999995	0.379999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	12.6000004	12.6000004 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.122 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7649.49023
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	7649.49023
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	7649.49023
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	63000
TrqOvlSta_Haptictime_Sec_M_f32	7649.49023
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.606000006
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	81.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.551999986
k_APAMaxHwTrq_HwNm_f32	1.12
k_APAMaxVehSpd_Kph_f32	3.5999999
k_ESCMaxVehSpd_Kph_f32	115.5
k_HapticAmplitude_MtrNm_f32	0.400000006
k_HapticDuration_Sec_f32	1.875
k_HapticFreq_Hz_f32	13.3000002
k_HapticReacttime_Sec_f32	0.224000007
k_LKAMaxVehSpd_Kph_f32	26.6000004
k_LKAMinVehSpd_Kph_f32	24.5
k_MaxHapticReqTime_Sec_f32	0.375
k_SWARLimiter_HwDeg_f32	28.2999992
k_ShiftLevRevTime_Sec_f32	0.00899999961
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.0769999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr target TrqOvlSta Per1 GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvIsta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc	02	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAlnhibit Cnt Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-705.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	957.73999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 HapticRequest Cnt lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-3.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ErxNequest_Ont_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	303.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	273		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_Trq0vlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	23.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	63000	63000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988	0.449999988 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	81.5999985	81.5999985 ± 0.0009765625	v
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	81.5999985	81.5999985 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.40000006	0.400000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	13.3000002	13.3000002 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.123 (Repeat Count = 1) lame	
	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrgOvlSta
rqOvISta_APAState_State_M_enum	0
rqOvISta_ESCState_State_M_enum	3
rgOvISta ElapsedAPAMaxHwTrqTime Sec M f32	8134.31982
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	8134.31982
rqOvlSta ElapsedStandstillTime Sec M f32	8134.31982
rqOvISta_HapticState_State_M_enum	0
rgOvlSta Hapticdur Cnt M u16	1596
rqOvISta_Haptictime_Sec_M_f32	8134.31982
rgOvISta HwTorqueSV HwNm M Str.SV Uls f32	0.800000012
rqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.616999984
rgOviSta LKAFault Cnt M lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	0
rqOvISta_LKAState_State_M_enum	0
rqOviSta_ErxAstate_state_w_entitiii rqOviSta_PosSrvoHwAngle_HwDeg_M_f32	83
rqOviSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
APAMaxHwTrqTime Sec f32	0.563000023
: APAMaxHwTrq HwNm f32	1.2000005
APAMaxVehSpd Kph f32	5.4000001
ESCMaxVehSpd_Kph_f32	119.199997
	0.419999987
HapticDuration_Sec_f32	1.8899999
HapticFreq_Hz_f32	14
_HapticReacttime_Sec_f32	0.239999995
LKAMaxVehSpd Kph f32	28.5
_LKAMinVehSpd_Kph_f32	26.25
MaxHapticReqTime_Sec_f32	0.389999986
: SWARLimiter HwDeg f32	28.6000004
_SWARLINITIEL_NWBeg_132 _ShiftLevRevTime_Sec_f32	0.0130000003
_StandstillThresh_Kph_f32	0.039999991
_StandstillTime_Sec_f32	0.0049999989
standstiff inte_Sec_roz arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rer1_APARecoverableFaults_Cnt_igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_rerr_ArARecoverabler autis_Ont_igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOviSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target TrqOvlSta Per1 CWFound Cnt lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrqOvISta Per1 CWPosition HwDeg f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_rrqOviSta_Peri_CvvPosition_Hwbeg_is2 target_rrqOviSta_Peri_EsCFault_Cnt_lgc
arget_Rte_inst_Ap_rrqOviSta.rrqOviSta_Perr_ESCPault_Cnt_igc arget_Rte_inst_Ap_rrqOviSta.TrqOviSta_Perr_ESCIsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCFatilt_Ont_lgc target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
· · · · ·	target TrqOvISta Per1_ESCISLIMILEd_CNt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enu	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	Larger Tryonola Fell Invitique Invitil 132





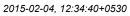
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgo		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target TrqOvlSta Per1 LKAInhibit Cnt Ig		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_e		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3	2 target_TrqOvlSta_Per1_MinSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_C	cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle	_HwDegf32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc$	target_TrqOvlSta_Per1_PosTrajEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_	HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_Inst_Rtproperties and the properties of the prope$	f3 target_TrqOvlSta_Per1_SWARTrgtAngRe	quest_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc$	target_TrqOvlSta_Per1_ShiftLeverIsInRev	erse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrN	m_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cr	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-695.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	962.679993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	-2.70000005		
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	314.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	283.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	-
TrqOvlSta_ESCState_State_M_enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8134.31982	8134.31982 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	8134.31982	8134.31982 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	1596	1596 ± 1	→
TrqOvlSta_Haptictime_Sec_M_f32	8134.31982	8134.31982 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.35949993	-1.35950005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	¥
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625 0.419999987 ± 0.0009765625	Ž
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value			
target TrgOvISta Per1 TrgOscEpable Cnt Igovalua	0.419999987		-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	0.419999987 0 14	0.±1 14±0.0009765625	7

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Name	Test Step 2.124 (Repeat Count = 1)	✓
Regional part Proposition		Input Value
Tricyclobs Scikios Buts M. erum		·
Trop/Sells Dissessed Anthonis Tropic Sept. 10 2 S		<u> </u>
TopOMSE Basedomin of the Propose Prop	TrqOvlSta_ESCState_State_M_enum	4
Topology Depote	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8619.15039
TOPOCOSE Harboriche, Sec. M. 20 TOPOCOSE Harboriche, Sec. M. 20 TOPOCOSE Harboriche, Sec. M. 20 TOPOCOSE JAMPACOSE JAM	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	8619.15039
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Troch/dell_LASA (Sept. M. pum)	TrqOvlSta_LKAFault_Cnt_M_lgc	0
Tropolisia Passibnething Helbook, M. 12 1 1 1 1 1 1 1 1 1	TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
March Marc	TrqOvISta_LKAPermFault_Cnt_M_u08	1
Troch/Sis Practo-Nise Cnt M yes	TrqOvISta_LKAState_State_M_enum	1
K. AMAccider Time Sec. 12	TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	
A.PAMaethVTE, Tell-Num. 12		·
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Manipulation Sec. 132	k_HapticAmplitude_MtrNm_f32	
1. Apaptic 1. Apaptic 2.	k_HapticDuration_Sec_f32	
k, LXMANVerSpd, Kon, 12 k, SWARLIMER, TMOBe, 132 k, Simklur, Raver Imm, Sec, 122 0.0799999992 k, Slandstillfrees, Kon, 122 k, Slandstillfrees, Kon, 122 k, Slandstillfrees, Kon, 122 0.0799999992 k, Slandstillfrees, Kon, 122 1.0994, Robe, Inst. Ap. TrqOviSta TrqOviSta, Perl APADrivinterventionDelacted Cnt., Ign target, Rive, Inst. Ap. TrqOviSta TrqOviSta, Perl APARcoverableFaults, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl APARcoverableFaults, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl APARcoverableFaults, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl APARcoverableFaults, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl APARcoverableFaults, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl APARcoverableFaults, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl APARcoverableFaults, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl CCWFound, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl CCWFound, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl CCWFound, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl ESCFault, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl ESCFault, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl ESCFault, Cnt. Ign target, Rive, Inst. Ap. TrqOviSta, Perl State, Perl ESCState, State, enum target, Rive, Inst. Ap. TrqOviSta, Perl ISCSState, State, enum target, Rive, Inst. Ap. TrqOviSta, Perl ISCSState, Perl ISCSState, State, enum target, Rive, Inst. Ap. TrqOviSta, Perl ISCSState, Perl ISCSState, State, enum target, Rive, Inst. Ap. TrqOviSta, Perl ISCSState, Perl ISCSState, State, enum target, Rive, Inst. Ap. TrqOviSta, Perl ISCSState, Perl ISCSSt	k_HapticFreq_Hz_f32	14.6999998
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target Rie Inst Ap_TrqOviSta_TrqOviSta_Perl_CWFound_Cnt_lgc target Rie. Inst Ap_TrqOviSta_TrqOviSta_Perl_ESCISLimited_Cnt_lgc target Rie. Inst Ap_TrqOviSta_TrqOviSta_Perl_GMOSH-DSCIIIdesState_State_enum target Rie. Inst Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCIMEDIALESCIATE_STATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_CNTOVISTA_PERL_GATE_C	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_CCWPosition_HwDeg_f32 target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target Rite Inst Ap_TrqOviSta_TrqOviSta_Per1_ENCRequest_Cnt_lgc target_Rite_Inst Ap_TrqOviSta_TrqOviSta_Per1_PosSrovibample_HwDeg_f32 target_Rite_Inst Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSroviba_Per1_PosSrovibample_Rite_Deg_f32 target_Rite_Inst Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSrov_PuMTNm_f32 target_Rite_Inst Ap_TrqOviSta	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
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target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMtgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMtgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMtgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLevertsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per3_ShiftLevertsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HavTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_Destrajenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_Destrajenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_Sort_aper1_Sort_aper1_Sort_aper1_Sort_aper1_Sort_aper1_PosTrajenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_Sort_aper1_Sort_aper1_Sort_aper1_Sort_aper1_Sort_aper1_Sort_aper1_Sort_aper1_Sort_aper1_Sort_aper1_TrqOvlSta_Per1_Sort_aper1_Sort_aper1_TrqOvlSta_Per1_Sort_aper1_Sort_aper1_TrqOvlSta_Per1_Sort_aper1_TrqOvlSta_Per1_Sort_aper1_Sort_aper1_TrqOvlSta_Per1_Sort_aper1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvl	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwMm_T32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_DosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajiEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajiEnable_Cnt_lgc target_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_TrqOvlSta_Per1_SortajiEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOvlSta_Per1	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
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target_Rte_Inst_Ap_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_NaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_NaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value utarget_TrqOvISta_Per1_APARequest_Cnt_lgc.value utarget_TrqOvISta_Per1_APARequest_Cnt_lgc.value utarget_TrqOvISta_Per1_APARequest_Cnt_lgc.value utarget_TrqOvISta_Per1_APARequest_Cnt_lgc.value utarget_TrqOvISta_Per1_APARequest_Cnt_lgc.value utarget_TrqOvISta_Per1_APARequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHvnMangle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHvnMangle_HwDeg_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_TrqOvl		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOvsC		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 targ	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOvsCamp_MtrNm_f32 target_TrqOvlS		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0		0
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-685.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	967.619995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	324.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	293.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	25.6000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	2578	2578 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.14999998	1.14999998 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	84.4000015	84.4000015 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
		4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4		
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
		1 4	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1		-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	1 4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1 4 0	4 0 ± 1	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 4 0 84.4000015	4 0 ± 1 84.4000015 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.125 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	9103.98047
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	9103.98047
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	9103.98047
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	3560
TrqOvlSta_Haptictime_Sec_M_f32	9103.98047
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.638999999
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	85.8000031

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Name		
k. APAMachaterine (Time. Sec. 12) k. Scolkavethold, Knih. 102 k. Scolkavethold, Knih. 102 k. Hapicholdson (Sec. 12) k. Hapicholdson (Sec. 1	Name	Input Value
K. APAMAMATVI Trime, Sec., 132 K. APAMAMATVI KHAMI, 132 K. SWANATHIMITE HIMDE, 132 K. SWANATH	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
K. APAMAWAYEN, HANDIN, 152 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998 126,599998	k_APAIncludeHaptic_Cnt_lgc	0
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target_Rte_Inst_Ap_TrqOviSta_Pert_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSHOSHOSHIdeState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSHOSHOSHIdeState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMIgfanable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMIgfanable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMIgfanable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqO		
target_Rie_Inst_Ap_TrqOv/Sta_Pref_APARequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_APAState_State_enum target_Rie_Inst_Ap_TrqOv/Sta_Pref_CCWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CCWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCStatulted_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCSMapenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_EMSCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_Po		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Pert_ESCReuts_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Des		
target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH-OscillateState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_LSCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosSrveEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosSrveEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_OscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_OscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_OscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HavForque_HmVm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_IMAScucreVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_IMAScucreVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_IMScucreVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrvchwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTra		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CSCPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LAPAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LAPAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_In		
target_Rte_Inst_Ap_TrqOvlSta_Prerl_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_PosSrvchwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_PosSrvchwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_SosTrqindengent_Sort_IndovlSta_Prerl_SosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_Sort_IndovlSta_Prerl_Sort_Indov		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_OScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-ault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-ault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-ault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTrajHayRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTrajHayRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTrajHayRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTrajHayRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInRe	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
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target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosT	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_HADTORQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_HADTORQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_HADTORQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosTrajHwAngle_HwDeg_f	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqO	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFacquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFacquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFacquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFacquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate_enuncestate$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_MinSecureVehicleSpeed_tpl target_TrqOvlSta_Per1_PosSrvehue_InsterverNellgc target_TrqOvlSta_Per1_PosSrvehue_InsterverNellgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosSrveInable_Cnt_lgc target_TrqOvlSta_Per1_PosSrveInable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt lgc	target TrgOvlSta Per1 LKAFault Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_Lnt_lgc target_TrqOvlSta_Per1_SwaRTrgtAngRequest_Lnt_lgc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_Cnt_Igc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShifftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShifftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target Rte inst Ap Irquvista.Irquvista Peri Irquscamp ivitrim 132 - target Irquvista Peri Irquscamp ivitrim 132		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -675.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 972.559998	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	972.559998
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -1.89999998		-1.89999998
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value 1	· · · · · · · · · · · · · · · · · · ·	
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value		
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1	~ - ,	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 303.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.200000003		
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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	9103.98047	9103.98047 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	3560	3560 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	9103.98047	9103.98047 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5	1.5 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.3999996	15.3999996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.126 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	9588.80957
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	9588.80957
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	9588.80957
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	4542
TrqOvlSta_Haptictime_Sec_M_f32	9588.80957
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.85000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.649999976
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	87.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.596000016
k_APAMaxHwTrq_HwNm_f32	1.44000006
k_APAMaxVehSpd_Kph_f32	10.8000002
k_ESCMaxVehSpd_Kph_f32	130.300003
k_HapticAmplitude_MtrNm_f32	0.479999989
k_HapticDuration_Sec_f32	1.93499994
k_HapticFreq_Hz_f32	16.1000004
k_HapticReacttime_Sec_f32	0.287999988
k_LKAMaxVehSpd_Kph_f32	34.2000008
k_LKAMinVehSpd_Kph_f32	31.5
k_MaxHapticReqTime_Sec_f32	0.435000002
k_SWARLimiter_HwDeg_f32	29.5
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	0.159999996
k_StandstillTime_Sec_f32	0.0170000009
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 _ 1		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH LKAMfgEnable Cnt lgc	target TrqOvISta Per1 GMOSH LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-665.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target TrgOvlSta Per1 CWPosition HwDeg f32.value	977.5		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	345.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	314.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.0099999978		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	27.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0	I=	
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	V
TrqOvlSta_ESCState_State_M_enum			V
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1 0	
TrqOvISta_ElapsedStandstill Time_Sec_M_T32 TrqOvISta_HapticState_State_M_enum	0	0	-
TrqOvISta_Hapticdur_Cnt_M_u16	4542	4542 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_Haptictime_Sec_M_i52 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.327499747	-0.327499986 ± 0.0000009	
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3±1	~
TrqOvISta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	87.1999969	87.1999969 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	87.1999969	87.1999969 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.479999989	0.479999989 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	16.1000004	16.1000004 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.127 (Repeat Count = 1)	Innut Value
lame	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	4
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	10073.6396
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	10073.6396
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	10073.6396
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	5524
rqOvlSta_Haptictime_Sec_M_f32	10073.6396
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.661000013
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	4
rqOvlSta_LKAState_State_M_enum	4
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	88.5999985
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
_APAMaxHwTrqTime_Sec_f32	0.606999993
APAMaxHwTrq HwNm f32	1.51999998
APAMaxVehSpd Kph f32	12.6000004
- , -, -	134
_ESCMaxVehSpd_Kph_f32	
_HapticAmplitude_MtrNm_f32	0.5
_HapticDuration_Sec_f32	1.95000005
_HapticFreq_Hz_f32	16.7999992
_HapticReacttime_Sec_f32	0.30399999
_LKAMaxVehSpd_Kph_f32	36.0999985
_LKAMinVehSpd_Kph_f32	33.25
_MaxHapticReqTime_Sec_f32	0.44999988
_SWARLimiter_HwDeg_f32	29.7999992
_ShiftLevRevTime_Sec_f32	0.0289999992
_StandstillThresh_Kph_f32	0.200000003
_StandstillTime_Sec_f32	0.0209999997
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
rget_Rte_inst_Ap_frqOviSta_frqOviSta_Perf_ESCState_State_enum rget_Rte_inst_Ap_frqOviSta_TrqOviSta_Perf_GMOSHOscillateState_State_enu	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta Per1 TrqOscFreq Hz f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-655.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	982.440002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.10000002		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	355.399994 324.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-34.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	3	3	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	-
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	10073.6396	10073.6396 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5524	5524 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	10073.6396	10073.6396 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005	2.20000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.100000001	0.100000001 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	- 4
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	V
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	_
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	*
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.100000001 0.5	0.100000001 ± 0.0009765625 0.5 ± 0.0009765625	-
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	-
target_TrqOviSta_Per1_TrqOscEriable_Crit_igc.value target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	16.7999992	16.7999992 ± 0.0009765625	
		500002 ± 0.00007 00020	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Fest Step 2.128 (Repeat Count = 1)	Innut Value
lame	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum rqOvlSta_ESCState_State_M_enum	3
rqOvISta_E3c3tate_state_w_entitif	10558.4697
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	10558.4697
rqOvISta_ElapsedStandstillTime_Sec_M_f32	10558.4697
rqOvISta_HapticState_State_M_enum	2
rqOvISta_Hapticdut_Cnt_M_u16	6506
rqOvISta_Haptictime_Sec_M_f32	10558.4697
rgOvISta HwTorqueSV HwNm M Str.SV UIs f32	2.54999995
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.671999991
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	10.1999998
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.617999971
_APAMaxHwTrq_HwNm_f32	1.60000002
_APAMaxVehSpd_Kph_f32	14.3999996
_ESCMaxVehSpd_Kph_f32	137.699997
_HapticAmplitude_MtrNm_f32	0.519999981
_HapticDuration_Sec_f32	1.96500003
_HapticFreq_Hz_f32	17.5
_HapticReacttime_Sec_f32	0.319999993
_LKAMaxVehSpd_Kph_f32	38
_LKAMinVehSpd_Kph_f32	35
_MaxHapticReqTime_Sec_f32	0.465000004
_SWARLimiter_HwDeg_f32	30.1000004
_ShiftLevRevTime_Sec_f32	0.032999998
_StandstillThresh_Kph_f32	0.239999995
_StandstillTime_Sec_f32	0.0250000004
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_Igc arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWPosition HwDeg f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target TrqOvlSta_Per1_CWPosition HwDeg f32
riget Rte Inst Ap TrqOviSta.TrqOviSta_Per1_EWPosition_Hwbeg_is2	target TrqOvISta Per1 ESCFault Cnt Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrqOvISta Per1 ESCState State enum
rget_Rte_inst_Ap_frqOviSta.TrqOviSta_Feff_ESCState_state_entiti rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enur	
inget_rite_inst_Ap_friqOviSta_friqOviSta_Feff_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOvlota_reri_GMOSH_LKAMfgEnable_Cnt_lgc
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 HapticReguest Cnt Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HwTorque HwNm f32	target TrqOvISta Per1 HwTorque HwNm f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
	0
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-645.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	987.380005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.699999988		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	365.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	334.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	2	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	6506	6506 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	· ·
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.54999995	2.54999995 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	10.1999998	10.1999998 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	10.1999998	10.1999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.519999981	0.519999981 ± 0.0009765625	•
	•	0.4	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.129 (Repeat Count = 1)		~
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11043.2998	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11043.2998	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	11043.2998	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	7488	
TrqOvlSta_Haptictime_Sec_M_f32	11043.2998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.9000001	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.683000028	
TrqOvlSta_LKAFault_Cnt_M_lgc	1.	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1.	
TrqOvlSta_LKAState_State_M_enum	1	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	20.2000008	

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APPANDATION		
A.P.A.Masteristric Sec. 92	Name	Input Value
APANData Front Test Park 192	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
A APAMentain Terf ten Ser D2 A D2	k_APAIncludeHaptic_Cnt_lgc	0
		0.629000008
ESCHARACHERY LENGTH 13.399994 1.3399994 1.3399994 1.3399994 1.399979979 1.399979979 1.399979979 1.399979979 1.399979999 1.3999799999 1.3999799999 1.3999799999 1.3999799999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.399979999999 1.39997999999 1.39997999999 1.399979999999 1.39997999999 1.399979999999 1.399979999999 1.399979999999 1.39997999999999999999999999999999999999		
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target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Pert_UKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Pert_OviSta_Pert_McSearcevethicleSpeed_Kpt_122 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecurevethicleSpeed_Kpt_122 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchable_Cnt_lgc target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchable_Cnt_lgc target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_TrqOviSta_Pert_SvARTrgdAngRequest_Ent_Ingovalue target_TrqOviSta_Pert_Ap_ArrgoviSta_Pert_TrqOsSchable_Cnt_Ingovalue 1 target_TrqOviSta_Pert_Ap_ArrgoviSta_Pert_TrqOsSchable_Cnt_Ingovalue 1 target_TrqOviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap_ArrgoviSta_Pert_Ap	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
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target Rie Inst Ap_TrqOviSta_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovEhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovEhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovEhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwRATrqiAngRequest_Hwbeg_g132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_TrqOviSta_Per1_ApAnnonRecoverableFaults_Cnt_lgc_value target_TrqOviSta_Per1_ApAnnonRecoverableFaults_Cnt_lgc_value target_TrqOviSta_Per1_CWFound_Cnt_lgc_value target_TrqOviSta_Per1_CWFound_Cnt_lgc_value target_TrqOviSta_Per1_CWFound_Cnt_lgc_value target_TrqOviSta_Per1_CWFound_Cnt_lgc_value target_TrqOviSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOviSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOviSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_lgc_value target_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_lgc_value target_TrqOviSta_Per1_Mosh_ApMgEnable_Cnt_lgc_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSt	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
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target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrovhwAngle_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOvlSta_Pert_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOvlSta_Pert_Ap_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOvlSta_Pert_Ap_TrqOsCenable_Cnt_Igc_value target_TrqOvlSta_Pert_Ap_ARRequest_Cnt_Igc_value target_TrqOvlSta_Pert_CoWPound_Cnt_Igc_value target_TrqOvlSta_Pert_CoWPound_Cnt_Igc_value target_TrqOvlSta_Pert_CoWPound_Cnt_Igc_value target_TrqOvlSta_Pert_CoWPound_Cnt_Igc_value target_TrqOvlSta_Pert_ESCRautic_Cnt_Igc_value target_TrqOvlSta_Pert_ESCRautic_Cnt_Igc_value target_TrqOvlSta_Pert_ESCRautic_Cnt_Igc_value target_TrqOvlSta_Pert_ESCRautic_Cnt_Igc_value target_TrqOvlSta_Pert_GMOSH_APAMfgenable_Cnt_Igc_value target_TrqOvlSta_Pert_GMOSH_APAMfgenable_Cnt_Igc_value target_TrqOvlSta_Pert_Hortorque_HnVbm_132_value target_TrqOvlSta_Pert_Hortorque_HnVbm_132_value target_TrqOvlSta_Pert_Hortorque_HnVbm_132_value target_TrqOvlSta_Pert_Hortorque_HnVbm_132_value target_TrqOvlSta_Pert_Hortorque_HnVbm_132_value target_TrqOvlSta_Pert_Hortorque_HnVbm_132_value target_TrqOvlSta_Pert_Hortorque_HnVbm_132_value target_TrqOvlSta_Pert_MasSecureVehicleSpeed_Kph_132_value target_TrqOvlSta_Pert_MosCall_Spert_Hortorque_HnVbm_132_value target_TrqO	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ShintLeverisinReverse_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Art_Ap_Ar		
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target_Rte_Inst_Ap_TrqOviSta_Per1_SVRARTrgIAhgRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SWRARTrgIAhgRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SMRARTrgIAhgRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFanable_Cnt_Ugc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_ApArtoquest_Ap_Artoquest_ApAnthecoverableFaults_Cnt_Ugc value target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Ugc value target_TrqOviSta_Per1_ApARecoverableFaults_Cnt_Ugc value target_TrqOviSta_Per1_CCWPound_Cnt_Ugc value target_TrqOviSta_Per1_CCWPound_Cnt_Ugc value target_TrqOviSta_Per1_CCWPosition_Hwbeg_f32_value target_TrqOviSta_Per1_CCWPosition_Hwbeg_f32_value target_TrqOviSta_Per1_ESCFault_Cnt_Ugc value target_TrqOviSta_Per1_ESCFault_Cnt_Ugc value target_TrqOviSta_Per1_ESCRequest_Cnt_Ugc value target_TrqOviSta_Per1_ESCRequest_Cnt_Ugc value target_TrqOviSta_Per1_ESCRequest_Cnt_Ugc value target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Ugc value target_TrqOviSta_Per1_GMOSH_ESCRMfgEnable_Cnt_Ugc value target_TrqOviSta_Per1_GMOSH_ESCRMfgEnable_Cnt_Ugc value target_TrqOviSta_Per1_GMOSH_ESCRMfgEnable_Cnt_Ugc value target_TrqOviSta_Per1_HvTorque_Hwhm_f32_value target_TrqOviSta_Per1_HvTorque_Hwhm_f32_value target_TrqOviSta_Per1_HvTorque_Hwhm_f32_value target_TrqOviSta_Per1_HvTorque_Hwhm_f32_value target_TrqOviSta_Per1_HxApthecquest_Cnt_Ugc value target_TrqOviSta_Per1_HxApthecquest_Cnt_Ugc valu		
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target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value 1. target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.30000012 target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value 1. target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1. target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1. target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1.		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	376
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	345.100006
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.119999997	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11043.2998	11043.2998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	7488	7488 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	11043.2998	11043.2998 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.714400053	0.714399993 ± 0.0000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.119999997	0.119999997 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.119999997	0.119999997 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	18.2000008	18.2000008 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.130 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11528.1299
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11528.1299
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	11528.1299
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	8470
TrqOvlSta_Haptictime_Sec_M_f32	11528.1299
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.25
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.694000006
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	30.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.639999986
k_APAMaxHwTrq_HwNm_f32	1.75999999
k_APAMaxVehSpd_Kph_f32	18
k_ESCMaxVehSpd_Kph_f32	145.100006
k_HapticAmplitude_MtrNm_f32	0.560000002
k_HapticDuration_Sec_f32	1.995
k_HapticFreq_Hz_f32	18.8999996
k_HapticReacttime_Sec_f32	0.351999998
k_LKAMaxVehSpd_Kph_f32	41.7999992
k_LKAMinVehSpd_Kph_f32	38.5
k_MaxHapticReqTime_Sec_f32	0.495000005
k_SWARLimiter_HwDeg_f32	30.7000008
k_ShiftLevRevTime_Sec_f32	0.0410000011
k_StandstillThresh_Kph_f32	0.319999993
k_StandstillTime_Sec_f32	0.0329999998
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_Results (A$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name		Input Value		
	_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	•	eFaults Cnt loc	
	TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target TrqOvlSta Per1 APARecoverableFau		
	_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	*	
	TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrgOvISta Per1 APAState State enu		
	TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt lgc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc		
	TrqOvlSta.TrqOvlSta Per1 CCWPosition HwDeg f32	target TrqOvISta Per1 CCWPosition HwDe		
	_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	9_102	
	_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_	f32	
	_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_102	
	_TrqOvIsta_TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_igc	70	
		target TrqOvISta Per1 ESCRequest Cnt Ig		
	_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc			
	_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enu		
	_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
	_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
	_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
	_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
	_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
	_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f3	32	
	_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
	_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
	_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
	_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
	_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSp	peed_Kph_f32	
0	_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw	vDeg_f32	
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	e_Cnt_lgc	
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_l	lgc	
target_Rte_Inst_Ap	_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_P	er1_APANonRecoverableFaults_Cnt_lgc.value	0		
	er1_APARecoverableFaults_Cnt_lgc.value	0		
target TrqOvlSta P	er1_APARequest_Cnt_lgc.value	0		
		0		
target TrgOviSta P	eri Covround Chi igc.value			
	ler1_CCWFound_Cnt_lgc.value ler1_CCWPosition_HwDeq_f32.value			
target_TrqOvlSta_P	er1_CCWPosition_HwDeg_f32.value	-625.01001 0		
target_TrqOvlSta_P target_TrqOvlSta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value	-625.01001 0		
target_TrqOvlSta_P target_TrqOvlSta_P target_TrqOvlSta_P	er1_CCWPosition_HwDeg_f32.value ler1_CWFound_Cnt_lgc.value ler1_CWPosition_HwDeg_f32.value	-625.01001 0 997.26001		
target_TrqOvlSta_P target_TrqOvlSta_P target_TrqOvlSta_P target_TrqOvlSta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value	-625.01001 0 997.26001		
target_TrqOvlSta_P target_TrqOvlSta_P target_TrqOvlSta_P target_TrqOvlSta_P target_TrqOvlSta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCIsLimited_Cnt_lgc.value	-625.01001 0 997.26001 0		
target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCIsLimited_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value	-625.01001 0 997.26001 0 0		
target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCIsLimited_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_EMOSH_APAMfgEnable_Cnt_lgc.value	-625.01001 0 997.26001 0 0		
target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCIsLimited_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0		
target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCIsLimited_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCIsLimited_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HwTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HwTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HwTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HwTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWPosition_HwDeg_f32.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HwTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HwTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HWTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value er1_PosTrajHwAngle_HwDeg_f32.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HWTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value er1_PosTrajHwAngle_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HWTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value er1_PosTrajHwAngle_HwDeg_f32.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0		
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HWTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value er1_PosTrajHwAngle_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0	Expected Value	Result
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HwTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LMaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value er1_PosTrajHwAngle_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value er1_ShiftLeverlsInReverse_Cnt_lgc.value	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0.5 355.399994 0 0.129999995 22.5	Expected Value 4	Result
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HWTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value er1_PosTrajHwAngle_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value er1_State_M_enum	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0	•	Result
target_TrqOvISta_P SAPAStat_TrqOvISta_ESCStat	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_GMOSH_LKAMfgEnable_Cnt_lgc.value er1_HApticRequest_Cnt_lgc.value er1_HWTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value er1_PosTrajHwAngle_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value er1_State_M_enum	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0	4	~
target_TrqOvISta_P	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value er1_CWFound_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCFault_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_ESCRequest_Cnt_lgc.value er1_GMOSH_APAMfgEnable_Cnt_lgc.value er1_GMOSH_ESCMfgEnable_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HapticRequest_Cnt_lgc.value er1_HwTorque_HwNm_f32.value er1_LKAFault_Cnt_lgc.value er1_LKAFault_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_LKARequest_Cnt_lgc.value er1_MaxSecureVehicleSpeed_Kph_f32.value er1_MinSecureVehicleSpeed_Kph_f32.value er1_PosTrajEnable_Cnt_lgc.value er1_PosTrajHwAngle_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value er1_SWARTrgtAngRequest_HwDeg_f32.value er1_State_M_enum te_State_M_enum	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0.100000001 0 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4	4	*
target_TrqOvISta_P target_TrqOvI	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_GMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_EKAMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HwTorque_HwNm_f32.value ter1_LKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MaxSecureVehicleSpeed_Kph_f32.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum te_State_M_enum APAMaxHwTrqTime_Sec_M_f32	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4	4 4 0±1	*
target_TrqOvISta_P target_TrqOvI	er1_CCWPosition_HwDeg_f32.value er1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_GMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_MostrajHwAngle_HwDeg_f32.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum APAMaxHwTrqTime_Sec_M_f32 StandstillTime_Sec_M_f32	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0	4 4 0±1 0±1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvISta_P target_TrqOvI	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_GMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_LKAMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HATorque_HwNm_f32.value ter1_LKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum te_State_M_enum te_State_M_enum APAMaxHwTrqTime_Sec_M_f32 StandstillTime_Sec_M_f32 StandstillTime_Sec_M_f32 tate_State_M_enum	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0 0	4 4 0±1 0±1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvISta_P target_TrqOvI	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_GMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_LKAMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HATorque_HwNm_f32.value ter1_LKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_State_M_enum te_State_M_enum APAMaxHwTrqTime_Sec_M_f32 ShiftLevRevTime_Sec_M_f32 StandstillTime_Sec_M_f32 state_State_M_enum ur_Cnt_M_u16	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0 0 0	4 4 0±1 0±1 0 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_P TrqOvISta_LescStal	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_GMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_LKAMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MaxSecureVehicleSpeed_Kph_f32.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum te_State_M_enum APAMaxHwTrqTime_Sec_M_f32 ShiftLevRevTime_Sec_M_f32 StandstillTime_Sec_M_f32 tate_State_M_enum ur_Cnt_M_u16 ne_Sec_M_f32	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0 0 0 1 8470 0	4 4 0±1 0±1 0 1 8470±1 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_P target_TrqOvI	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_GMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_LKAMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HwTorque_HwNm_f32.value ter1_HkAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MaxSecureVehicleSpeed_Kph_f32.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum te_State_M_enum APAMaxHwTrqTime_Sec_M_f32 StandstillTime_Sec_M_f32 StandstillTime_Sec_M_f32 tate_State_M_enum ur_Cnt_M_u16 till_State_State_M_enum ur_Cnt_M_u16 till_State_State_M_enum ur_Cnt_M_u16 till_State_State_M_enum	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0 0 0 0 1 8470 0 3.25	4 4 0±1 0±1 0 1 8470±1 0±1 3.25±0.000009	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_P target_TrqOvI	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_GMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_LKAMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HATorque_HwNm_f32.value ter1_LKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MaxSecureVehicleSpeed_Kph_f32.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum te_State_M_enum te_State_M_enum aPAMAxHwTrqTime_Sec_M_f32 StandstillTime_Sec_M_f32 state_State_M_enum ur_Cnt_M_u16 ne_Sec_M_f32 usSV_HwNm_M_Str.SV_UIs_f32 tt_Cnt_M_lgc	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.1299999995 22.5 0 Actual Value 4 4 0 0 0 0 1 8470 0 3.25 0	4 4 0±1 0±1 0 1 8470±1 0±1 3.25±0.000009 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_P target_TrqOvI	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_GMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_LKAMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MaxSecureVehicleSpeed_Kph_f32.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum te_State_M_enum APAMaxHwTrqTime_Sec_M_f32 ShiftLevRevTime_Sec_M_f32 StandstillTime_Sec_M_f32 tate_State_M_enum ur_Cnt_M_u16 ne_Sec_M_f32 useV_HwNm_M_Str.SV_UIs_f32 tt_Cnt_M_lgc mFault_Cnt_M_lgc	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0 0 0 0 1 8470 0 3.25 0 0	4 4 0±1 0±1 0 1 8470±1 0±1 3.25±0.000009 0±1 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_P target_TrqOvISta_Elapsed. TrqOvISta_Elapsed. TrqOvISta_Hapticsi TrqOvISta_Hapticsi TrqOvISta_Hapticsi TrqOvISta_Hapticsi TrqOvISta_Hapticsi TrqOvISta_LKAPer TrqOvISta_LKAPer TrqOvISta_LKAPer TrqOvISta_LKAPer	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_EGMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HATorque_HwNm_f32.value ter1_LKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MaxSecureVehicleSpeed_Kph_f32.value ter1_MinSecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum APAMAxHwTrqTime_Sec_M_f32 ShiftLevRevTime_Sec_M_f32 ShiftlevRevTime_Sec_M_f32 standstillTime_Sec_M_f32 tate_State_M_enum ur_Cnt_M_u16 ne_Sec_M_f32 ueSV_HwNm_M_Str.SV_UIs_f32 tt_Cnt_M_lgc mFault_Cnt_M_lgc mFault_Cnt_M_u08	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0 0 0 0 1 8470 0 3.25 0 0 2	4 4 0±1 0±1 0 1 8470±1 0±1 3.25±0.000009 0±1 0±1 2±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_P target_TrqOvISta_Elapsed TrqOvISta_Elapsed TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_LKAFau TrqOvISta_LKAFau TrqOvISta_LKAPer TrqOvISta_LKAPer TrqOvISta_LKAStat	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_EGMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HwTorque_HwNm_f32.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MaxSecureVehicleSpeed_Kph_f32.value ter1_MosecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_ShiftLeverIsInReverse_Cnt_lgc.value ter3_ShiftLeverIsInReverse_Cnt_lgc.value ter4_State_M_enum te_State_M_enum te_State_M_enum te_State_M_enum tr_Cnt_M_u16 tn_e_Sec_M_f32 tate_State_M_enum ur_Cnt_M_u16 tn_e_Sec_M_f32 tate_State_M_enum ur_Cnt_M_u16 tn_e_Sec_M_f32 tate_State_M_enum tr_Cnt_M_lgc tn_Fault_Cnt_M_lgc tn_Fault_Cnt_M_lgc tn_Fault_Cnt_M_u08 te_State_M_enum	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0 0 0 0 1 8470 0 3.25 0 0 0 2	4 4 0±1 0±1 0 1 8470±1 0±1 3.25±0.000009 0±1 0±1 2±1 4	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_P target_TrqOvISta_Elapsed TrqOvISta_Elapsed TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_Hapticoti TrqOvISta_LKAFau TrqOvISta_LKAFau TrqOvISta_LKAPer TrqOvISta_LKAPer TrqOvISta_LKAStat	ter1_CCWPosition_HwDeg_f32.value ter1_CWFound_Cnt_lgc.value ter1_CWFound_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCFault_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_ESCRequest_Cnt_lgc.value ter1_EGMOSH_APAMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_GMOSH_ESCMfgEnable_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HapticRequest_Cnt_lgc.value ter1_HwTorque_HwNm_f32.value ter1_LKAFault_Cnt_lgc.value ter1_LKAFault_Cnt_lgc.value ter1_LKARequest_Cnt_lgc.value ter1_MaxSecureVehicleSpeed_Kph_f32.value ter1_MosecureVehicleSpeed_Kph_f32.value ter1_PosTrajEnable_Cnt_lgc.value ter1_PosTrajHwAngle_HwDeg_f32.value ter1_SWARTrgtAngRequest_HwDeg_f32.value ter1_ShiftLeverIsInReverse_Cnt_lgc.value ter2_ShiftLeverIsInReverse_Cnt_lgc.value ter3_ShiftLeverIsInReverse_Cnt_lgc.value ter2_State_M_enum te_State_M_enum te_State_M_enum te_State_M_enum ur_Cnt_M_u16 ne_Sec_M_f32 ueSV_HwNm_M_Str.SV_Uis_f32 tt_Cnt_M_lgc mFault_Cnt_M_lgc mFault_Cnt_M_u08 te_State_M_enum HwAngle_HwDeg_M_f32	-625.01001 0 997.26001 0 0 0 0 0 0 0 0 0 0 0 0.100000001 0 0 0.5 355.399994 0 0.129999995 22.5 0 Actual Value 4 4 0 0 0 0 1 8470 0 3.25 0 0 2	4 4 0±1 0±1 0 1 8470±1 0±1 3.25±0.000009 0±1 0±1 2±1	· · · · · · · · · · · · · · · · · · ·





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	30.2000008	30.2000008 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.560000002	0.560000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	18.8999996	18.8999996 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.131 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	3
rqOvlSta_ESCState_State_M_enum	1
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12012.96
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12012.96
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	12012.96
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	9452
rqOvlSta_Haptictime_Sec_M_f32	12012.96
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.5999999
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.704999983
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	40.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.651000023
APAMaxHwTrg HwNm f32	1.84000003
APAMaxVehSpd Kph f32	19.7999992
ESCMaxVehSpd Kph f32	148.800003
_ESCIVIAXVERSPU_Kpit_IS2 _HapticAmplitude_MtrNm_f32	0.57999983
HapticDuration Sec f32	0.50999999
_HapticFreq_Hz_f32	19.6000004
_HapticReacttime_Sec_f32	0.368000001
_LKAMaxVehSpd_Kph_f32	43.700008
_LKAMinVehSpd_Kph_f32	40.25
_MaxHapticReqTime_Sec_f32	0.50999999
_SWARLimiter_HwDeg_f32	31
_ShiftLevRevTime_Sec_f32	0.0450000018
_StandstillThresh_Kph_f32	0.360000014
_StandstillTime_Sec_f32	0.0370000005
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target TrqOvISta Per1 ESCRequest Cnt Igc
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enu	
rget_Rte_inst_Ap_rriqovista.rriqovista_rerr_giviosnosciliatestate_state_end rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
rget_Rte_Inst_Ap_TrqOviSta_TrqOviSta_PerT_GMOSH_APAMilgEnable_Crit_igo rget_Rte_Inst_Ap_TrqOviSta_TrqOviSta_PerT_GMOSH_ESCMfgEnable_Crit_igo	
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	ium	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3	2 target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_h	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3:	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-614.909973		
target TrqOvISta Per1 CWFound Cnt Igc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	1002.20001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target TrgOvlSta Per1 ESCRequest Cnt lqc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	396.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	365.700012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	Y
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12012.96	12012.96 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	2	0	· ·
TrqOvlOta_HapticState_State_M_enum		2	J
TrqOvISta_Hapticdur_Cnt_M_u16	9452	9452 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrgOvlSta_Haptictime_Sec_M_f32	12012.96 3.5999999	12012.96 ± 1 3.5999999 ± 0.000009	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	
TrqOvISta_EKAPermFault_Cnt_M_lgc	1	1±1	
TrqOvISta LKAPermFault Cnt M u08	3	3 ± 1	
TrqOvISta_LKAState_State_M_enum		3	
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	3		
	3 0.140000001	0.140000001 ± 0.0009765625	
	0.140000001	0.140000001 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target TrqOvlSta Per1 APADrvrInterventionDetected Cnt lgc.value			~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0.140000001 1	0.140000001 ± 0.0009765625 1 ± 1	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	0.140000001 1 0	0.140000001 ± 0.0009765625 1 ± 1 0 ± 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	0.140000001 1 0 3	0.140000001 ± 0.0009765625 1 ± 1 0 ± 1 3	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0.140000001 1 0 3 3	0.140000001 ± 0.0009765625 1 ± 1 0 ± 1 3	
TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	0.140000001 1 0 3 3 2	0.140000001 ± 0.0009765625 1 ± 1 0 ± 1 3 3	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0.140000001 1 0 3 3 2 3	0.140000001 ± 0.0009765625 1 ± 1 0 ± 1 3 3 2	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0.140000001 1 0 3 3 2 3 1	0.140000001 ± 0.0009765625 1 ± 1 0 ± 1 3 3 2 3 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.140000001 1 0 3 3 2 3 1 0.140000001	0.140000001 ± 0.0009765625 1 ± 1 0 ± 1 3 3 2 3 1 ± 1 0.140000001 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.132 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12497.79
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12497.79
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	12497.79
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	10434
TrqOvlSta_Haptictime_Sec_M_f32	12497.79
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	3.95000005 0.00200000009
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.662
k_APAMaxHwTrq_HwNm_f32	1.91999996
k_APAMaxVehSpd_Kph_f32	21.6000004
k_ESCMaxVehSpd_Kph_f32	152.5 0.600000024
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0.0149999997
k_HapticFreq_Hz_f32	20.2999992
k HapticReacttime Sec f32	0.384000003
k_LKAMaxVehSpd_Kph_f32	45.5999985
k_LKAMinVehSpd_Kph_f32	42
k_MaxHapticReqTime_Sec_f32	0.524999976
k_SWARLimiter_HwDeg_f32	31.2999992
k_ShiftLevRevTime_Sec_f32	0.048999987
k_StandstillThresh_Kph_f32	0.40000006
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0410000011 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	V - 1
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_irqOvlSta_Per1_minSecurevenicleSpeed_kpn_t32 target_trqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvIsta_Per1_APARecoverablePaulis_Crit_Igc.value target_TrqOvIsta_Per1_APARequest_Cnt_Igc.value	0
target_TrqOvlota_rer1_crWFound_Cnt_lgc.value	0
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Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-604.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1007.14001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.89999976		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	406.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	376		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-32.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12497.79	12497.79 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	10434	10434 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.94390011	3.94390011 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.2000008	50.2000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	50.2000008	50.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.600000024	0.600000024 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	20.2999992	20.2999992 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.133 (Repeat Count = 1) ✓	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12982.6201
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12982.6201
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	12982.6201
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	11416
TrqOvlSta_Haptictime_Sec_M_f32	12982.6201
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.30000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	60.2000008

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.672999978
k_APAMaxHwTrq_HwNm_f32	2
k_APAMaxVehSpd_Kph_f32	23.3999996
k_ESCMaxVehSpd_Kph_f32	156.199997
k_HapticAmplitude_MtrNm_f32	0.620000005
k_HapticDuration_Sec_f32	0.0299999993
k_HapticFreq_Hz_f32 k HapticReacttime Sec f32	0.40000006
k_LKAMaxVehSpd_Kph_f32	47.5
k_LKAMinVehSpd_Kph_f32	43.75
k_MaxHapticReqTime_Sec_f32	0.540000021
k_SWARLimiter_HwDeg_f32	31.6000004
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	0.43999998
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt Igc	target_TrqOviSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target TrqOvlSta Per1 LKARequest Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-594.710022
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 1012 00002
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	1012.08002
target TrqOvISta_Per1_ESCFault_Cnt_igc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.29999995
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	417.200012
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	386.299988
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12982.6201	12982.6201 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11416	11416 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	12982.6201	12982.6201 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.30000019	4.30000019 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.620000005	0.620000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	21	21 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.134 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13467.4502
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	13467.4502
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13467.4502
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	12398
TrqOvlSta_Haptictime_Sec_M_f32	13467.4502
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.6500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.684000015
k_APAMaxHwTrq_HwNm_f32	2.07999992
k_APAMaxVehSpd_Kph_f32	25.2000008
k_ESCMaxVehSpd_Kph_f32	159.899994
k_HapticAmplitude_MtrNm_f32	0.639999986
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	21.7000008
k_HapticReacttime_Sec_f32	0.416000009
k_LKAMaxVehSpd_Kph_f32	49.4000015
k_LKAMinVehSpd_Kph_f32	45.5
k_MaxHapticReqTime_Sec_f32	0.555000007
k_SWARLimiter_HwDeg_f32	31.8999996
k_ShiftLevRevTime_Sec_f32	0.057
k_StandstillThresh_Kph_f32	0.479999989
k_StandstillTime_Sec_f32	0.0489999987
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lge	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>j_</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_l	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEi	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_LKAState_State_en		
	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt target TrqOvlSta Per1 PosSrvoHwAngle F		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosSrvonwArigie_nwDeg_isz			
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_Hr target_TrqOvlSta_Per1_SWARTrgtAngRequ		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscErtable_Ont_igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvISta_Per1_TrqOscFreq_Hz_f32		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0	.	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1140.10999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	360		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	0		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrgOvlSta Per1 HwTorque HwNm f32.value	1.70000005		
target TrqOvlSta Per1 LKAFault Cnt lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	427.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	396.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	12398	12398 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.6500001	4.6500001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc		0 ± 1	✓
	0		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
		0 ± 1 1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0		
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0	1 ± 1	~

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TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	70.1999969	70.1999969 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.639999986	0.639999986 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	21.7000008	21.7000008 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.135 (Repeat Count = 1)	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrqOvlSta
	2
TrgOvISta_APAState_State_M_enum	
rqOvlSta_ESCState_State_M_enum	0
rqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13952.2803
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	13952.2803
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	13952.2803
rqOvISta_HapticState_State_M_enum	
rqOvISta_Hapticdur_Cnt_M_u16	13380
rqOvISta_Haptictime_Sec_M_f32	13952.2803
rqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
rqOvISta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvISta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	80.1999969
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.694999993
_APAMaxHwTrq_HwNm_f32	2.16000009
_APAMaxVehSpd_Kph_f32	27
_ESCMaxVehSpd_Kph_f32	163.600006
_HapticAmplitude_MtrNm_f32	0.660000026
_HapticDuration_Sec_f32	0.0599999987
_HapticFreq_Hz_f32	22.3999996
_HapticReacttime_Sec_f32	0.432000011
_LKAMaxVehSpd_Kph_f32	51.2999992
_LKAMinVehSpd_Kph_f32	47.25
_MaxHapticReqTime_Sec_f32	0.569999993
_SWARLimiter_HwDeg_f32	32.2000008
_ShiftLevRevTime_Sec_f32	0.0610000007
_StandstillThresh_Kph_f32	0.519999981
_StandstillTime_Sec_f32	0.0529999994
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg	target_TrqOvlSta_Per1_PosTrajHwAngle_H f3 target_TrqOvlSta_Per1_SWARTrgtAngReq		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1100.20996		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1440.10999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	406.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13952.2803	13952.2803 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	13952.2803	13952.2803 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13952.2803	13952.2803	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	13380	13380 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	13952.2803	13952.2803 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.97505999	4.97505999 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	*
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	✓
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	V
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	3 0.180000007	3 0.180000007 ± 0.0009765625	
TrqOvlSta ReadytoPulse Cnt M lgc	1	1 ± 1	
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	1	1±1	
target TrqOvlSta Per1 APAState State enum.value	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	_
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	_
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	22.3999996	22.3999996 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 ChecknointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.136 (Repeat Count = 1)	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrgOvISta_APAState_State_M_enum	3
FrqOvISta_ESCState_State_M_enum	1
FrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	14437.1104
FrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	14437.1104
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	14437.1104
TrqOvISta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	14362
FrqOvISta_Haptictime_Sec_M_f32	14437.1104
FrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	5.3499999
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.70599997
_APAMaxHwTrq_HwNm_f32	2.24000001
_APAMaxVehSpd_Kph_f32	28.7999992
_ESCMaxVehSpd_Kph_f32	167.300003
_HapticAmplitude_MtrNm_f32	0.680000007
_HapticDuration_Sec_f32	0.075000003
_HapticFreq_Hz_f32	23.1000004
_HapticReacttime_Sec_f32	0.448000014
_LKAMaxVehSpd_Kph_f32	53.2000008
_LKAMinVehSpd_Kph_f32	49
_MaxHapticReqTime_Sec_f32	0.584999979
_SWARLimiter_HwDeg_f32	32.5
_ShiftLevRevTime_Sec_f32	0.0649999976
_StandstillThresh_Kph_f32	0.560000002
_StandstillTime_Sec_f32	0.057
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lo	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	2 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	a target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
arget_frqoviota_r err_oowr oand_ont_ige.value	





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1060.31006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	750		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	448.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	417.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-24.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	Y
TrqOvlSta_Hapticdur_Cnt_M_u16	14362	14362 ± 1	Y
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	*
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.3499999	5.3499999 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	<i>y</i>
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	
TrqOvISta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004	11.1000004 ± 0.0009765625 0 ± 1	J
TO104 D			
TrqOvISta_ReadytoPulse_Cnt_M_lgc			
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	*
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	0 4	0 ± 1 4	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	0 4 4	0 ± 1 4 4	*
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value	0 4 4 1	0 ± 1 4 4 1	· ·
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0 4 4 1 4	0 ± 1 4 4 1 4	· · ·
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 4 4 1 4 0	0 ± 1 4 4 1 4 0 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 4 4 1 4 0 11.1000004	0 ± 1 4 4 1 4 0 ± 1 11.1000004 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 4 4 1 4 0	0 ± 1 4 4 1 4 0 ± 1	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.137 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15891.5996
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	15891.5996
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	15891.5996
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	17308
TrqOvlSta_Haptictime_Sec_M_f32	15891.5996
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996





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APAMasel-Trail Test 96: 02: 0.75000022 APAMasel-Trail Test 96: 02: 0.75000022 APAMasel-Trail Test 96: 02: 0.75000002 APAMasel-Trail Test 96: 02: 0.75000002 APAMasel-Trail Test 96: 0.7500000000000000000000000000000000000	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
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A APAMarbinity of Jun 1920 A ESCHARCOSTON (Jun 1920) A ESCHARCOSTON (Jun	k_APAMaxHwTrqTime_Sec_f32	0.739000022
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Lipschicharing Sept. 22	k APAMaxVehSpd Kph f32	34.2000008
Lipschicharing Sept. 22	k ESCMaxVehSpd Kph f32	19.2000008
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target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. HapticRequest, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. HviTorque, HwNm, f32 target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. HviTorque, HwNm, f32 target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. KAFantt, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. KAFantt, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. KAFantt, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. KAFantt, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. KAFaste, State, enum target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. MasSecureVehicleSpeed, Kph, f32 target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. MasSecureVehicleSpeed, Kph, f32 target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosServEnable, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosServEnable, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTrajEnable, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTrajEnable, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTrajEnable, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTrajEnable, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTrajEnable, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. TrqOvista, Pert J. PosTrajEnable, Cnt. Igo: target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. Trq	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target, Rie, Inst, Ap. TrqOviSta TrqOviSta Perf _ IMrTorque _ Hwihm _ 132 target, Rie _ Inst, Ap. TrqOviSta TrqOviSta _ Perf _ I.KAFault_Cnt_ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ I.KAFault_Cnt_ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ I.KAFault_Cnt_ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ I.KAFault_Cnt_ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ I.KAFault_Cnt_ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ MacSecureVehicleSpeed _ Kph_132 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ MacSecureVehicleSpeed _ Kph_132 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Postaviontal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Postaviontal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Postaviontal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Postaviontal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Postaviontal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Postaviontal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Straintal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Straintal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Straintal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Straintal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ Straintal _ gc target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ TrqOscAmp_ MrNm_32 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ TrqOscAmp_ MrNm_32 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ TrqOscAmp_ MrNm_32 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ TrqOscAmp_ MrNm_182 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ TrqOscAmp_ MrNm_182 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ TrqOscAmp_ MrNm_182 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ TrqOscAmp_ MrNm_182 target, Rie_ Inst, Ap. TrqOviSta _ TrqOviSta _ Perf _ TrqOviSta _ Pe	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target, Rie, Inst. Ap. TrqOv/Sta TrqCV/Sta Pert_LKA/Rault_Cnt_lgc target, Rie_Inst. Ap. TrqOv/Sta TrqOv/Sta Pert_LKA/State_State_enum target, Rie_Inst. Ap. TrqOv/Sta TrqOv/Sta. Pert_LKA/State_State_enum target, Rie_Inst. Ap. TrqOv/Sta TrqOv/Sta. Pert_LKA/State_State_enum target, Rie_Inst. Ap. TrqOv/Sta. TrqOv/Sta. Pert_MINSecure/VehicleSpeed_Kph_G2 target, Rie_Inst. Ap. TrqOv/Sta. TrqOv/Sta. Pert_PosServ/Enable_Cnt_lgc target, Rie_Inst. Ap. TrqOv/Sta. TrqOv/Sta. Pert_PosServ/Enable_Cnt_lgc target, Rie_Inst. Ap. TrqOv/Sta. TrqOv/Sta. Pert_PosTrajEnable_Cnt_lgc target, Rie_Inst. Ap. TrqOv/Sta. TrqOv/Sta. Pert_PosTrajEnable_Cnt_lgc target, Rie_Inst. Ap. TrqOv/Sta. TrqOv/Sta. Pert_PosTrajEnable_Cnt_lgc target, Rie_Inst. Ap. TrqOv/Sta. TrqOv/Sta. Pert_State_PkA/Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Rault_Raul	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target, Rie, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKARquest, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKARquest, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKAState, State, enum target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKAState, State, enum target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. JMASSecureVehicleSpeed, Kph. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. JMASSecureVehicleSpeed, Kph. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. JMASSecureVehicleSpeed, Kph. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. PosservEnable, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. PosservEnable, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. PosservEnable, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. PossTraji-Mangle, HwDeg, 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. SWARTrgtAngRequest, HwDeg, 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. SWARTrgtAngRequest, HwDeg, 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. Sibitt. EverdisinReverse, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. Sibitt. EverdisinReverse, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minhm. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minhm. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minhm. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minhm. 132 target, TrqOviSta. Perf. LAPARcoverableFaults, Cnt. Igo: value target, TrqOviSta. Perf. LAPARcoverableFaults, Cnt. Igo: value 11 target, TrqOviSta. Perf. LAPARcoverableFaults, Cnt. Igo: value 12 target, TrqOviSta. Perf. CoVPFound, Cnt. Igo: value 13 target, TrqOviSta. Perf. CoVPFound, Cnt. Igo: value 14 target, TrqOviSta. Perf. LAPARcquest, Cnt. Igo: value 15 target, TrqOviSta. Perf. GMOSH, LAMMgEnable, Cnt. Igo: value 16 target, TrqOviSta. Perf. LARARcquest, Cnt. Igo: value 17 target, TrqOviSta. Perf. LARARcquest, Cnt. Igo: value 18 target, Trq	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target, Rie, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKARquest, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKARquest, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKAState, State, enum target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKAState, State, enum target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. JMASSecureVehicleSpeed, Kph. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. JMASSecureVehicleSpeed, Kph. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. JMASSecureVehicleSpeed, Kph. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. PosservEnable, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. PosservEnable, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. PosservEnable, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. PossTraji-Mangle, HwDeg, 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. SWARTrgtAngRequest, HwDeg, 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. SWARTrgtAngRequest, HwDeg, 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. Sibitt. EverdisinReverse, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. Sibitt. EverdisinReverse, Cnt. Igo: target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minhm. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minhm. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minhm. 132 target, Rei, Inst. Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minhm. 132 target, TrqOviSta. Perf. LAPARcoverableFaults, Cnt. Igo: value target, TrqOviSta. Perf. LAPARcoverableFaults, Cnt. Igo: value 11 target, TrqOviSta. Perf. LAPARcoverableFaults, Cnt. Igo: value 12 target, TrqOviSta. Perf. CoVPFound, Cnt. Igo: value 13 target, TrqOviSta. Perf. CoVPFound, Cnt. Igo: value 14 target, TrqOviSta. Perf. LAPARcquest, Cnt. Igo: value 15 target, TrqOviSta. Perf. GMOSH, LAMMgEnable, Cnt. Igo: value 16 target, TrqOviSta. Perf. LARARcquest, Cnt. Igo: value 17 target, TrqOviSta. Perf. LARARcquest, Cnt. Igo: value 18 target, Trq	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target TrqOvlSta Per1 LKAFault Cnt lgc
target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _LKARequest_Cnt_lgc target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _LKAState. State_enum target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _LKAState. State_enum target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _LMASscurevekhicleSpeed_Kph_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _NasscurevekhicleSpeed_Kph_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _NasscurevekhicleSpeed_Kph_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _PossrovibwAngle_HWDeg_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _Straipi+Wangle_HWDeg_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _TrqOscAmp_Mirth, f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _TrqOscAmp_Mirth, f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Perf _TrqOscAmp_Mirth, f32 target, TrqOviSta. Perf _Straipi+Mirth, f32 target, TrqOviSta. Perf _APRAcquest, Cnt_lgc_value 1 target_TrqOviSta. Perf _APRAcquest, Cnt_lgc_value 1 target_TrqOviSta. Perf _APRAcquest, Cnt_lgc_value 1 target_TrqOviSta. Perf _CWPostion. HwDeg_f32 value 1 target_TrqOviSta. Perf _CWPostion. HwDeg_f32 value 1 target_TrqOviSta. Perf _CWPostion. HwDeg_f32 value 1 target_TrqOviSta. Perf _Straipi-Revised. Cnt_lgc_value 1 target_TrqOviSta. Perf _GMOSH_APAMigFanable_Cnt_lgc_value 1 target_TrqOviSta. Perf _GMOSH_APAMigFanable_Cnt_lgc_value 1 target_TrqOviSta. Perf _LKARcquest_Cnt_lgc_value 1 target_TrqOviSta. Perf _LKAR		
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target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Under_Sta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SMRTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SMRTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCenap_MtrNn_f2c target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCenap_MtrNn_f2c target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc-value target_TrqOvlSta_Per1_APARequest_Cnt_lgc_value 11 target_TrqOvlSta_Per1_APARequest_Cnt_lgc_value 12 target_TrqOvlSta_Per1_CWPoond_Cnt_lgc_value 13 target_TrqOvlSta_Per1_CWPoond_Cnt_lgc_value 14 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc_value 15 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value 16 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value 17 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value 18 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value 19 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value 10 target_TrqOvlSta_Per1_GMOSH_APAM[genable_Cnt_lgc_value 11 target_TrqOvlSta_Per1_GMOSH_ESCMgEnable_Cnt_lgc_value 11 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 12 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 13 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 14 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 15 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 16 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 17 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 18 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 19 target_TrqOvlSta_Per1_Hyproquest_Cnt_lgc_value 11 target_TrqOvlSta_Per1_Hyproquest_Cn		
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target_Rie_Inst_Ap_TrqOviSta_Per1_SVARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_SVARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_SVARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_SVIRtreverisnReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_SVIRtreverisnReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_Cnt_Index target_TrqOviSta_Per1_Cnt_Index target_TrqOviSta_Per1_Cnt_Index target_TrqOviSta_Per1_Enable_Cnt_Index target_TrqOviSta_Per1_Enable_Cnt_Index target_TrqOviSta_Per1_Enable_Cnt_Index target_TrqOviSta_Per1_Enable_Cnt_Index target_TrqOviSta_Per1_Historque_Histor_Cnt_Index target_TrqOviSta_Per1_Historque_Histor_Cnt_Index target_TrqOviSta_Per1_Historque_Histor_Cnt_Index target_TrqOviSta_Per1_Historque_Histor_Cnt_Index target_TrqOviSta_Per1_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApproxet_LindApprox		
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target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvISta_Perl_APARecoverableFaults_Cnt_lgc.value target_TrqOvISta_Perl_APARecoverableFaults_Cnt_lgc.value target_TrqOvISta_Perl_APARecoverableFaults_Cnt_lgc.value target_TrqOvISta_Perl_CWFound_Cnt_lgc.value target_TrqOvISta_Perl_CWFound_Cnt_lgc.value target_TrqOvISta_Perl_CWFound_Cnt_lgc.value target_TrqOvISta_Perl_CWFound_Cnt_lgc.value target_TrqOvISta_Perl_CWFound_Cnt_lgc.value target_TrqOvISta_Perl_CWFound_Cnt_lgc.value target_TrqOvISta_Perl_ExcRequest_Cnt_lgc.value target_TrqOvISta_Perl_ExcRequest_Cnt_lgc.value target_TrqOvISta_Perl_ExcRequest_Cnt_lgc.value target_TrqOvISta_Perl_ExcRequest_Cnt_lgc.value target_TrqOvISta_Perl_ExcRequest_Cnt_lgc.value target_TrqOvISta_Perl_ExcRequest_Cnt_lgc.value target_TrqOvISta_Perl_GMOSH_ExcMigEnable_Cnt_lgc.value target_TrqOvISta_Perl_GMOSH_ExcMigEnable_Cnt_lgc.value target_TrqOvISta_Perl_HapticRequest_Cnt_lgc.value target_TrqOvISta_Perl_HapticRequest_Cnt_lgc.value target_TrqOvISta_Perl_HapticRequest_Cnt_lgc.value target_TrqOvISta_Perl_HapticRequest_Cnt_lgc.value target_TrqOvISta_Perl_HapticRequest_Cnt_lgc.value target_TrqOvISta_Perl_Marecover_VehicleSpeed_Kph_f3z.value target_TrqOvISta_Perl_Marecover_VehicleSpeed_Kph_f3z.value target_TrqOvISta_Perl_Marecover_VehicleSpeed_Kph_f3z.value target_TrqOvISta_Perl_Marecover_VehicleSpeed_Kph_f3z.value target_TrqOvISta_Perl_Marecover_VehicleSpeed_Kph_f3z.value target_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOscFreable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOscFreable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Pert_TrqOscFrea_Hz_f32 target_TrqOvISta_Pert_ApARnoRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ApAMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_ApAMfgEnable_Cnt_Igc.value target_TrqOvISta_P		
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target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	
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target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 3.70000005 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1007.14001
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 3.70000005 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 3.70000005 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 3.70000005 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 3.70000005 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 3.70000005 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 3.70000005 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvISta_Per1_HwTorque_HwNm_32.value 3.70000005 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 479 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.70000005
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 479 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	
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target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 448.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	15891.5996	15891.5996 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	17308	17308 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	15891.5996	15891.5996 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.4000001	6.4000001 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.119999997	0.119999997 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.119999997	0.119999997 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.2000008	25.2000008 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.138 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16376.4297
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	16376.4297
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	16376.4297
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	18290
TrqOvlSta_Haptictime_Sec_M_f32	16376.4297
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.75
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0196000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	15.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.75
k_APAMaxHwTrq_HwNm_f32	2.55999994
k_APAMaxVehSpd_Kph_f32	36
k_ESCMaxVehSpd_Kph_f32	24.7000008
k_HapticAmplitude_MtrNm_f32	0.150000006
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	25.8999996
k_HapticReacttime_Sec_f32	0.512000024
k_LKAMaxVehSpd_Kph_f32	60.7999992
k_LKAMinVehSpd_Kph_f32	56
k_MaxHapticReqTime_Sec_f32	0.644999981
k_SWARLimiter_HwDeg_f32	33.7000008
k_ShiftLevRevTime_Sec_f32	0.0810000002
k_StandstillThresh_Kph_f32	0.720000029
k_StandstillTime_Sec_f32	0.0729999989
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlOta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRevers target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target TrqOvISta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-360		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1012.08002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target TrqOvlSta_Per1_HapticRequest Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	4.0999999		
target TrgOvlSta Per1 LKAFault Cnt Igc.value	0		
target TrgOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	489.299988		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	458.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	*
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HanticState_State_M_enum	0 2	0 2	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	18290	18290 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	_
TrqOvlSta_Haptictime_Sec_M_i52 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.75	6.75 ± 0.000009	
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0±1	
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	15.5	15.5 ± 0.0009765625	~

0

0 ± 1

TrqOvlSta_ReadytoPulse_Cnt_M_lgc

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TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	15.5	15.5 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.8999996	25.8999996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.139 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	16861.2598
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	16861.2598
TrqOvlSta ElapsedStandstillTime Sec M f32	16861.2598
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta Hapticdur Cnt M u16	19272
TrqOvlSta_Haptictime_Sec_M_f32	16861.2598
TrgOvISta HwTorqueSV HwNm M Str.SV UIs f32	7.0999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta LKAFault Cnt M lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k APAMaxHwTrqTime Sec f32	0.760999978
k APAMaxHwTrq HwNm f32	2.6400001
k APAMaxVehSpd Kph f32	37.7999992
k ESCMaxVehSpd Kph f32	30.2000008
k_HapticAmplitude_MtrNm_f32	0.170000002
k_HapticDuration_Sec_f32	0.150000006
k_HapticFreq_Hz_f32	26.6000004
k_HapticReacttime_Sec_f32	0.527999997
k LKAMaxVehSpd Kph f32	62.7000008
k_LKAMinVehSpd_Kph_f32	57.75
k_MaxHapticReqTime_Sec_f32	0.660000026
k SWARLimiter HwDeg f32	34
k_ShiftLevRevTime_Sec_f32	0.0850000009
k_StandstillThresh_Kph_f32	0.75999999
k_StandstillTime_Sec_f32	0.0769999996
<pre>target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_</pre>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_Fe11_AFARequest_Ci1t_igc target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_Per1_APAState_State_enum	target TrqOvISta Per1 APAState State enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_FeT1_AFAState_state_entilit target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 ESCFault Cnt Igc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_Fe11_ESCFault_Crit_igc target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Crit_igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_Igc	target TrqOvISta Per1 ESCRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Crit_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_linst_Ap_mqovista.mqovista_ren_escstate_state_entilin target Rte Inst Ap TrqovIsta.TrqovIsta Per1 GMOSHOscillateState State enun	
target_Rte_inst_Ap_1rq0viSta.1rq0viSta_Pe11_GiviOSHOSciliateState_State_enun target Rte Inst Ap Trq0viSta.Trq0viSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMilgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMigEnable_Cnt_igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_ESCMigEnable_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GiviOSH_ExAlvingEriable_Crit_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Crit_igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Nte_mst_Ap_11qOviota.11qOviota_Fel1_fiw10lque_fiwlviii_i32	target_Trqoviota_reli_flwiorque_flwiviii_toz

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_F		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	- ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	<u>'</u>	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target TrqOvlSta Per1 CCWFound Cnt lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-750		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1017.02002		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrqOvISta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvISta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	499.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	468.700012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
targot_rrqOviota_reri_orintLevensini\everse_Ont_lyc.value			
Name	Actual Value	Expected Value	Result
	Actual Value	Expected Value	Result ~
Name		· ·	Result
Name TrqOvISta_APAState_State_M_enum	3	3	· ·
Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	3 3	3	~
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32	3 3 16861.2598 16861.2598 0	3 3 16861.2598 ± 1 16861.2598 ± 1 0	· ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum	3 3 16861.2598 16861.2598 0	3 3 16861.2598 ± 1 16861.2598 ± 1 0	· ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16	3 3 16861.2598 16861.2598 0 0	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32	3 3 16861.2598 16861.2598 0 0 19272 16861.2598	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticUTc_IM_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticUstate_State_M_enum TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticUTcnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticUTcnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1 1 ± 1 3 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticUtime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001 1 1 3 3	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1 1 ± 1 3 3	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_Enum.value	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001 1 1 1 3 3 0	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1 1 ± 1 3 3 0	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001 1 1 1 3 3 0	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1 1 ± 1 3 3 0 3 0 3	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001 1 1 1 3 3 1 0 3	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1 1 ± 1 3 3 0 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_CNServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001 1 1 1 3 3 1 0.140000001	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1 1 ± 1 3 ± 1 3 0 1.140000001 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	3 3 16861.2598 16861.2598 0 0 19272 16861.2598 7.0433197 1 1 3 3 0.140000001 1 1 1 3 3 1 0 3	3 3 16861.2598 ± 1 16861.2598 ± 1 0 0 19272 ± 1 16861.2598 ± 1 7.04332018 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.140000001 ± 0.0009765625 1 ± 1 1 ± 1 3 3 0 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.140 (Repeat Count = 1)	→
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvISta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18460.6309
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	22218
TrqOvlSta_Haptictime_Sec_M_f32	18460.6309
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.14999962
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0284000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	19.8999996
TrqOvIsta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.79400003
k_APAMaxHwTrq_HwNm_f32	2.88000011
k_APAMaxVehSpd_Kph_f32	43.2000008
k_ESCMaxVehSpd_Kph_f32	46.7000008
k_HapticAmplitude_MtrNm_f32	0.230000004
k_HapticDuration_Sec_f32	0.194999993
k_HapticFreq_Hz_f32	28.7000008
k_HapticReacttime_Sec_f32	0.575999975
k_LKAMaxVehSpd_Kph_f32	68.4000015
k_LKAMinVehSpd_Kph_f32	63
k_MaxHapticReqTime_Sec_f32	0.704999983 34.9000015
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	0.0970000029
k_StandstillThresh_Kph_f32	0.879999995
k StandstillTime Sec f32	0.0890000015
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviota.TrqOviota_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target Rte Inst Ap TrqOvIsta.TrqOvIsta Per I_minisecureveniciespeed_kpn_isz	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0
target_TrqOvISta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	U

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1020.40997		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1021.96002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	499.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.69999981		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta ESCState State M enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309	18460.6309 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18460.6309	18460.6309	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	22218	22218 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.08041954	8.08041954 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	19.8999996	19.8999996 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
tornet Transition Dark Dark Dark Dark Live Dark 1900 value		40 0000000 : 0 0000705005	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	19.8999996	19.8999996 ± 0.0009765625	
target_IrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	19.8999996 0.230000004	0.230000004 ± 0.0009765625	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.141 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21465
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	21465
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	21465
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	23200
TrqOvlSta_Haptictime_Sec_M_f32	21465
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	21

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Taylorida passignature Cort. 15	Name	Input Value
A. APAMachishiptic, Scott. 125		·

\$ \$2500006 **Liegotic-Architock, Minhing, 102 **SWARMARIMOCK, Mi	. –	0.805000007
CSC/March/Stage No. 192	k_APAMaxHwTrq_HwNm_f32	2.96000004
L. Epspecharines (Sec. 102) 20 200000000000000000000000000000	k_APAMaxVehSpd_Kph_f32	45
** Inspectional Part 12		
NationPrinceDime Sec. 102 0.50000000000000000000000000000000000		
S. Hasberfelder Ser. Dec. 2 0.50000008		
A. Markeverlogic Liph 125		
A. J. A. J		
S. SWAREIMENT Prices, Sec. 122 S. Sandauli Tree Sec. 122 D. Sandauli Tree	k_LKAMinVehSpd_Kph_f32	
S. Sanderstäffnee, Sec., T22	k_MaxHapticReqTime_Sec_f32	0.720000029
S. Standart Trees, Ed. 12 January R. Finand J. TryOMSta TryOMSta Perl APADovinterventionDetected. Crit. January R. Finand J. TryOMSta TryOMSta Perl APADovinterventionDetected. Crit. January R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand J. TryOMSta TryOMSta Perl APARoscoverable auto. Crit. Journal R. Finand R. TryOMSta Perl APARoscoverable auto. Crit.	k_SWARLimiter_HwDeg_f32	35.2000008
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target_Rie_Inst_Ap_TrqOviSta_Per1_LKAInhibit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_r32 target_Rie_Inst_Ap_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_r32 target_Rie_Inst_Ap_TrqOviSta_Per1_Posservehibe_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_Posservehibe_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_Posservehibe_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_Posservehibe_Nepe_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_Posservehibe_Nepe_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajhArnagle_HwDeg_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajhArnagle_HwDeg_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajhArnagle_HwDeg_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajhArnagle_HwDeg_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajhArnagle_HwDeg_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajhArnagle_HwDeg_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_SoviSta_Per1_PosTrajhArnagle_HwDeg_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_PosTrajhArnagle_HwDeg_gr32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviS		
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target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTgAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTgAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTgAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTgAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 1 target_TrqOvISta_Per1_HxTorque_HwNm_f32.value 1 target_TrqOvISta_Per1_MGADEADARACAARAC	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_I32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SWARTgtAngRequest_HwDeg_I32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SMartgtAngRequest_HwDeg_I32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_I32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_I32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_I32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFreq_Hz_I32 target_TrqOviSta_Per1_TrqOscAmp_MtrNm_I32 target_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscFreq_Hz_I32 target_TrqOviSta_Per1_TrqOscFreq_Hz_I32 target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc_value target_TrqOviSta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOviSta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOviSta_Per1_CWPosition_HwDeg_I32_value 1 target_TrqOviSta_Per1_CWPosition_HwDeg_I32_value 1 target_TrqOviSta_Per1_CWPosition_HwDeg_I32_value 1 target_TrqOviSta_Per1_ESCFault_Cnt_Igc_value 1 target_TrqOviSta_Per1_ESCFault_Cnt_Igc_value 1 target_TrqOviSta_Per1_ESCFault_Cnt_Igc_value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOviSta_Per1_EGCRequest_Cnt_Igc_value 1 target_TrqOviSta_Per1_EGMOSH_APAMfgEnable_Cnt_Igc_value 1 target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc_value 1 target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc_value 1 target_TrqOviSta_Per1_HapticRequest_Cnt_Igc_value 1 target_TrqOviS	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShifftLevertsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgenable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgenable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_ApANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRault_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvlSta_Per1_Morrque_HwNm_f32.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value		
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target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -980.51001 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1026.90002 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value -980.51001 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1026.90002 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -980.51001 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1026.90002 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1	target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1026.90002 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1026.90002 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 1026.90002 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_ESCFault_Ont_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1	·	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_igc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_igc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_igc.value 1	V = 1 = = = = = = = = = = = = = = = = =	
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1		
target IrgUviSta Peri LKARequest Cnt Igc.value 1	target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031		
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.159999996	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.80000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	21465	21465 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	23200	23200 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	21465	21465 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.5	8.5 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.25	0.25 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	29.3999996	29.3999996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.142 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	3	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	24470	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	24470	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	24470	
TrqOvlSta_HapticState_State_M_enum	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	24182	
TrqOvlSta_Haptictime_Sec_M_f32	24470	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.85000038	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0328000002	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	
TrqOvlSta_LKAState_State_M_enum	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAIncludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.815999985	
k_APAMaxHwTrq_HwNm_f32	3.03999996	
k_APAMaxVehSpd_Kph_f32	46.7999992	
k_ESCMaxVehSpd_Kph_f32	57.7000008	
k_HapticAmplitude_MtrNm_f32	0.270000011	
k_HapticDuration_Sec_f32	0.224999994	
k_HapticFreq_Hz_f32	30.1000004	
k_HapticReacttime_Sec_f32	0.60799998	
k_LKAMaxVehSpd_Kph_f32	72.1999969	
k_LKAMinVehSpd_Kph_f32	66.5	
k_MaxHapticReqTime_Sec_f32	0.735000014	
k_SWARLimiter_HwDeg_f32	35.5	
k_ShiftLevRevTime_Sec_f32	0.104999997	
k_StandstillThresh_Kph_f32	0.959999979	
k_StandstillTime_Sec_f32	0.0970000029	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	•	Faults Cnt lgc	
	target TrqOvlSta Per1 APARecoverableFau		
	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
	target TrqOvISta Per1 APAState State enu		
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
	target TrqOvlSta Per1 CCWPosition HwDe		
·	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	0 _ ·	
	target_TrqOvlSta_Per1_CWPosition_HwDeg	f32	
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	-	
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	qc	
	target TrqOvlSta Per1 ESCRequest Cnt Ig		
	target_TrqOvISta_Per1_ESCState_State_enu		
	target_TrqOvlSta_Per1_GMOSHOscillateStar		
	target TrqOvlSta Per1 GMOSH APAMfgEn		
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn	able Cnt lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f3	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lge	3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw	/Deg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	e_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-940.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1031.83997		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
. 3.2 1	0		
0 = 1 = = 1 = 1 = 1	118.400002		
	103.699997		
0 = 1 = = 7 = = 5	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
1	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	24182	24182 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.85000038	8.85000038 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
	4	4	~
, _ , _ , _ , _ , _ , _ , _ ,	22.1000004	22.1000004 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	22.1000004	22.1000004 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.270000011	0.270000011 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	30.1000004	30.1000004 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

est Step 2.143 (Repeat Count = 1)	Innut Value
lame	Input Value
tte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	4
qOvlSta_ESCState_State_M_enum	2
qOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	27475.5293
qOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	27475.5293
qOvlSta_ElapsedStandstillTime_Sec_M_f32	27475.5293
qOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	25164
qOvlSta_Haptictime_Sec_M_f32	27475.5293
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.19999981
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0350000001
rqOvlSta_LKAFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_u08	4
qOvlSta_LKAState_State_M_enum	4
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	23.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
APAMaxHwTrqTime_Sec_f32	0.827000022
APAMaxHwTrq HwNm f32	3.11999989
APAMaxVehSpd Kph f32	48.5999985
ESCMaxVehSpd Kph f32	63.2000008
_LapticAmplitude_MtrNm_f32	0.289999992
HapticDuration Sec f32	0.239999995
HapticFreq Hz f32	30.7999992
HapticReacttime Sec f32	0.624000013
_HapticReactiffle_Sec_132 LKAMaxVehSpd Kph f32	74.0999985
_LKAMinVehSpd_Kph_f32	68.25
_MaxHapticReqTime_Sec_f32	0.75
_SWARLimiter_HwDeg_f32	35.7999992
_ShiftLevRevTime_Sec_f32	0.108999997
_StandstillThresh_Kph_f32	1
_StandstillTime_Sec_f32	0.101000004
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lq	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur	
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-900.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1036.78003		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target TrqOvlSta_Per1_ESCRequest Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Crit_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrgOvlSta Per1 HwTorque HwNm f32.value	6.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	123.300003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	108.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	27475.5293	27475.5293 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	27475.5293	27475.5293 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	25164	25164 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293	27475.5293 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.11950016	9.11950016 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc			
TrgOvlSta LKAPermFault Cnt M lgc	1	1±1	~
	1	1±1 1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08		1 ± 1 4 ± 1	· ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	1 4 4	1±1 4±1 4	*
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	1 4 4 0.180000007	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625	~
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1 4 4 0.180000007	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1	\rightarrow \right
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1 4 4 0.180000007 1	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1	*
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	1 4 4 0.180000007 1 1	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	1 4 4 0.180000007 1 1 4 3	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 4 3	\rightarrow \right
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	1 4 4 0.180000007 1 1 4 3	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 4 3 0	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	1 4 4 0.180000007 1 1 4 3 0	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 4 3 0 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value	1 4 4 0.180000007 1 1 4 3 0	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 4 3 0 4 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 4 4 0.180000007 1 1 4 3 0 4 1 0.180000007	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 4 3 0 4 1 ± 1 0.180000007 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value	1 4 4 0.180000007 1 1 4 3 0 4 1 0.180000007 0.289999992	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 4 3 0 4 1 ± 1 0.180000007 ± 0.0009765625 0.289999992 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 4 4 0.180000007 1 1 4 3 0 4 1 0.180000007	1 ± 1 4 ± 1 4 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 4 3 0 4 1 ± 1 0.180000007 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-





Test Step 2.144 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	30480
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	30480
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	30480
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	26146
TrqOvlSta_Haptictime_Sec_M_f32	30480
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.55000019 0.0372000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	0.0372000001
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrgOvlSta LKAPermFault Cnt M u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.838
k_APAMaxHwTrq_HwNm_f32	3.20000005
k_APAMaxVehSpd_Kph_f32	50.4000015
k_ESCMaxVehSpd_Kph_f32	68.699969
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0.310000002 0.254999995
k_HapticFreq_Hz_f32	31.5
k HapticReacttime Sec f32	0.671999991
k_LKAMaxVehSpd_Kph_f32	79.8000031
k_LKAMinVehSpd_Kph_f32	73.5
k_MaxHapticReqTime_Sec_f32	0.764999986
k_SWARLimiter_HwDeg_f32	36.0999985
k_ShiftLevRevTime_Sec_f32	0.112999998
k_StandstillThresh_Kph_f32	1.03999996
k_StandstillTime_Sec_f32	0.104999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARcoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_enum
target_Rte_inst_Ap_irqovista.irqovista_Peri_GivioShosciliateState_state_enun target Rte Inst Ap TrqoviSta.TrqoviSta Peri GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Ont_igc target_Rte_Inst_Ap_TrqOvIsta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Crit_gc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
targot_rrqOviota_reri_oovvrouniu_ont_igc.value	U

TrqOvlSta_Per1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-860.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1041.71997		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	113.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.0999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvISta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	26146	26146 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.55000019	9.55000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992	24.2999992 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
1	4	4	~
			✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 24.2999992	24.2999992 ± 0.0009765625	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0 24.2999992 0.310000002	24.2999992 ± 0.0009765625 0.310000002 ± 0.0009765625	· ·
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	0 24.2999992	24.2999992 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.145 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	33485
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	33485
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	33485
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	27128
TrqOvlSta_Haptictime_Sec_M_f32	33485
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.89999962
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0394000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.3999996

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TrgOvISta Per1 Input Value TrgOvISta ReadytoPulse Cnt M lgc 0 k APAIncludeHaptic Cnt lqc k APAMaxHwTrgTime Sec f32 0.848999977 k_APAMaxHwTrq_HwNm_f32 3.27999997 k APAMaxVehSpd Kph f32 52.2000008 k_ESCMaxVehSpd_Kph_f32 74.1999969 k_HapticAmplitude_MtrNm_f32 0.330000013 k_HapticDuration_Sec_f32 0.270000011 k_HapticFreq_Hz_f32 32.2000008 k HapticReacttime Sec f32 0.688000023 k_LKAMaxVehSpd_Kph_f32 81.6999969 k_LKAMinVehSpd_Kph_f32 75 25 k MaxHapticReqTime Sec f32 0.779999971 k SWARLimiter HwDeg f32 36 4000015 k ShiftLevRevTime Sec f32 0.116999999 k_StandstillThresh_Kph_f32 1.08000004 k_StandstillTime_Sec_f32 0.108999997 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APAD target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig| target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc$ target_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_APAState_State_enum $target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc$ target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target TrgOvISta Per1 CWFound Cnt Igc $target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosition_HwDeg_f32$ target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target TrgOvISta Per1 ESCFault Cnt lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCRequest Cnt lgc target TrqOvISta Per1 ESCRequest Cnt Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_ESCState_State_enum target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enum target TrqOvlSta Per1 GMOSHOscillateState State enum $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$ target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc $target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$ target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc$ target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32$ target TrgOvlSta Per1 HwTorque HwNm f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc$ $target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc$ target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKARequest Cnt Igc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target TrgOvlSta Per1 LKAState State enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc$ target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$ target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreg Hz f32 target TrqOvlSta Per1 TrqOscFreq Hz f32 $target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value$ target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -820.909973 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 1046.66003 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target TrqOvlSta Per1 HwTorque HwNm f32.value 7.69999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target TrqOvlSta Per1 LKAInhibit Cnt Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value $target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value$ target TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32.value 118.400002 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value

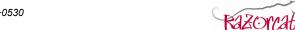
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target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value

TrqOvlSta_Per1

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Input Value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value -0.200000003 $target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value$ Name **Actual Value Expected Value** Result TrqOvlSta_APAState_State_M_enum 3 3 TrqOvlSta_ESCState_State_M_enum 4 TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32 0 0 ± 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 33485 33485 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 33485 33485 TrqOvlSta_HapticState_State_M_enum 0 0 TrqOvlSta_Hapticdur_Cnt_M_u16 27128 27128 ± 1 TrqOvlSta_Haptictime_Sec_M_f32 33485 33485 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 9.89999962 9.89999962 ± 0.000009 TrqOvlSta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvlSta_LKAPermFault_Cnt_M_lgc 1 ± 1 TrqOvlSta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqOvlSta_LKAState_State_M_enum 3 3 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.100000001 0.100000001 ± 0.0009765625 TrqOvlSta_ReadytoPulse_Cnt_M_lgc 1 ± 1 $target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$ 0 0 ± 1 $target_TrqOvlSta_Per1_APAState_State_enum.value$ 3 3 target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 $target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value$ 0 0 target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 $target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value$ 1 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.100000001 0.100000001 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.330000013 0.330000013 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value 32.2000008 32.2000008 ± 0.0009765625

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.146 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36490.3516
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	36490.3516
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	36490.3516
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	28110
TrqOvlSta_Haptictime_Sec_M_f32	36490.3516
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.10000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0416000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	26.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.860000014
k_APAMaxHwTrq_HwNm_f32	3.3599999
k_APAMaxVehSpd_Kph_f32	54
k_ESCMaxVehSpd_Kph_f32	79.6999969
k_HapticAmplitude_MtrNm_f32	0.34999994
k_HapticDuration_Sec_f32	0.284999996
k_HapticFreq_Hz_f32	32.9000015
k_HapticReacttime_Sec_f32	0.703999996
k_LKAMaxVehSpd_Kph_f32	83.5999985
k_LKAMinVehSpd_Kph_f32	77
k_MaxHapticReqTime_Sec_f32	0.795000017
k_SWARLimiter_HwDeg_f32	36.7000008
k_ShiftLevRevTime_Sec_f32	0.120999999
k_StandstillThresh_Kph_f32	1.12
k_StandstillTime_Sec_f32	0.112999998
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lge	3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	j_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgErtarget_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleS		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	IwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-781.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1051.59998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 HapticRequest Cnt lgc.value	0		
target_TrqOvlSta_ren_HapticRequest_Cnt_gc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	138		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.300003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f32.value	1.70000005		
target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	1	1	/ /
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	28110	28110 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	20110		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 1	~
		0 ± 1 1.39119995 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0		~
	0 1.39120007 0	1.39119995 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0 1.39120007 0	1.39119995 ± 0.000009 0 ± 1	*
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	0 1.39120007 0 0 2 4	1.39119995 ± 0.000009 0 ± 1 0 ± 1 2 ± 1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	0 1.39120007 0 0	1.39119995 ± 0.000009 0 ± 1 0 ± 1 2 ± 1	· ·

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TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	26.5	26.5 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.349999994	0.349999994 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.9000015	32.9000015 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	•
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.147 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	39495
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	39495
TrgOvlSta ElapsedStandstillTime Sec M f32	39495
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta Hapticdur Cnt M u16	29092
TrqOvlSta_Haptictime_Sec_M_f32	39495
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	1.20000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrgOvISta LKAFault Cnt M lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k APAMaxHwTrqTime Sec f32	0.870999992
k APAMaxHwTrq HwNm f32	3.4400006
k APAMaxVehSpd Kph f32	55,7999992
k ESCMaxVehSpd Kph f32	85.1999969
k HapticAmplitude MtrNm f32	0.370000005
k_HapticDuration_Sec_f32	0.300000012
k_HapticFreq_Hz_f32	33.599985
k_HapticReacttime_Sec_f32	0.720000029
k LKAMaxVehSpd Kph f32	85.5
k_LKAMinVehSpd_Kph_f32	78.75
k_MaxHapticReqTime_Sec_f32	0.810000002
k SWARLimiter HwDeg f32	37
k_ShiftLevRevTime_Sec_f32	0.125
k_StandstillThresh_Kph_f32	1.15999997
k_StandstillTime_Sec_f32	0.116999999
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Irist_Ap_TrqOvIsta.TrqOvIsta_Fer1_AFARequest_Crit_igc target_Rte_Irist_Ap_TrqOvIsta.TrqOvIsta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APARequest_Ont_gc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_Entirin target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_lgc	target_TrqOvIsta_Per1_APAstate_state_enum target_TrqOvIsta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWFound_Cnt_Igc	target_TrqOvIsta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWPosition_HwDeg_f32	target TrgOvlSta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target_TrqOvIsta_Per1_ESCFault_Cnt_lgc
target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_reiT_ESCFatit_Crit_igc target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Crit_igc	target_TrqOvlSta_Per1_ESCFault_Ont_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCRequest_Cnt_lgc	target TrgOvISta Per1 ESCRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enur	
target_Rte_Inst_Ap_mqovista.mqovista_ren_Gwioshiosciliatestate_state_entri target Rte Inst Ap Trqovista.Trqovista Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_GMOSH_APAMIIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvIsta_Per1_GMOSH_APAWigenable_Crit_igc target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Crit_igc
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCWigEriable_Crit_lgc
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GiviOSH_ExAwiigEnable_Crit_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target TrqOvlSta Per1 APANonRecoverableFaults Cnt lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-741.109985		
target TrgOvlSta Per1 CWFound Cnt lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1056.54004		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.20000005		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.5999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	39495	39495 ± 1	V
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	Y
TrqOvlSta_HapticState_State_M_enum	1	1	Y
TrqOvISta_Hapticdur_Cnt_M_u16	29092	29092 ± 1	· ·
TrqOvlSta_Haptictime_Sec_M_f32	39495	39495 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.20000005	1.20000005 ± 0.000009 1 ± 1	-
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrgOvlSta LKAPermFault Cnt M u08			
TraOvISta LKAState State M. enum	3	3 ± 1	•
TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	3	3 ± 1 3	· ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3 3 0.119999997	3 ± 1 3 0.119999997 ± 0.0009765625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	3 3 0.119999997 1	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	3 3 0.119999997	3 ± 1 3 0.119999997 ± 0.0009765625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	3 0.119999997 1 0 3	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	3 0.119999997 1 0	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3 3 0.119999997 1 0 3 3	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	3 3 0.119999997 1 0 3 3	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 3 0.119999997 1 0 3 3 1	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3 3 0.119999997 1 0 3 3 1 1 3	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3 3 0.119999997 1 0 3 3 1 1 3 1 0.119999997	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3 1 1 0.119999997 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.148 (Repeat Count = 1)	J.
Name	Input Value
Rte Inst Ap TrgOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	4
TrqOvISta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	42500
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	42500
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	42500
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	30074
TrqOvlSta_Haptictime_Sec_M_f32	42500
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.29999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0 4
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	4
TrgOvISta PosSrvoHwAngle HwDeg M f32	28.7000008
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.882000029
k_APAMaxHwTrq_HwNm_f32	3.51999998
k_APAMaxVehSpd_Kph_f32	57.5999985
k_ESCMaxVehSpd_Kph_f32	90.6999969
k_HapticAmplitude_MtrNm_f32	0.38999986
k_HapticDuration_Sec_f32	0.314999998
k_HapticFreq_Hz_f32	34.2999992
k_HapticReacttime_Sec_f32	0.736000001 87.4000015
k_LKAMaxVehSpd_Kph_f32	80.5
k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32	0.824999988
k_SWARLimiter_HwDeg_f32	37.2999992
k ShiftLevRevTime Sec f32	0.128999993
k_StandstillThresh_Kph_f32	1.20000005
k_StandstillTime_Sec_f32	0.120999999
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{A}) and the superscript{A} and the$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgauges and the property of the prop$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enumerator and the properties of the pro$	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target TrqOvlSta Per1 HwTorque HwNm f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_TrqOscEriable_Cnt_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-701.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1061.47998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	133.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	30074	30074 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.29999995	1.29999995 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008	28.7000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	28.7000008	28.7000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.389999986	0.389999986 ± 0.0009765625	5 ~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	34.2999992	34.2999992 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.149 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	45505
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45505
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	45505
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	31056
TrqOvlSta_Haptictime_Sec_M_f32	45505
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.3999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0482000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992





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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.893000007
k_APAMaxHwTrq_HwNm_f32	3.5999999
k APAMaxVehSpd Kph f32	59.4000015
k_ESCMaxVehSpd_Kph_f32	96.1999969
k HapticAmplitude MtrNm f32	0.409999996
k_HapticDuration_Sec_f32	0.330000013
k_HapticFreq_Hz_f32	35
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k LKAMinVehSpd Kph f32	82.25
k_MaxHapticReqTime_Sec_f32	0.839999974
k SWARLimiter HwDeg f32	37.599985
k_ShiftLevRevTime_Sec_f32	0.133000001
k StandstillThresh Kph f32	1.24000001
k_StandstillTime_Sec_f32	0.125
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target TrgOvlSta Per1 APADrvrInterventionDetected Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrgOvlSta Per1 APAState State enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreg Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1
target TrqOvlSta Per1 APARecoverableFaults Cnt Igc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-661.309998
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1066.42004
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.79999995
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	152.699997
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	138
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	7.4000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45505	45505 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	31056	31056 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	45505	45505 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.41927993	1.41928005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.140000001	0.140000001 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.140000001	0.140000001 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.409999996	0.409999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	35	35 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.150 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	48510.3398
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	48510.3398
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	48510.3398
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	32038
TrqOvlSta_Haptictime_Sec_M_f32	48510.3398
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0504000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1.
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	30.8999996
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.903999984
k_APAMaxHwTrq_HwNm_f32	3.68000007
k_APAMaxVehSpd_Kph_f32	61.2000008
k_ESCMaxVehSpd_Kph_f32	101.699997
k_HapticAmplitude_MtrNm_f32	0.430000007
k_HapticDuration_Sec_f32	0.344999999
k_HapticFreq_Hz_f32	35.7000008
k_HapticReacttime_Sec_f32	0.768000007
k_LKAMaxVehSpd_Kph_f32	91.1999969
k_LKAMinVehSpd_Kph_f32	84
k_MaxHapticReqTime_Sec_f32	0.855000019
k_SWARLimiter_HwDeg_f32	37.9000015
k_ShiftLevRevTime_Sec_f32	0.136999995
k_StandstillThresh_Kph_f32	1.27999997
k_StandstillTime_Sec_f32	0.128999993
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ılts_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_102	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_le	qc	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrqOvlSta Per1 ESCRequest Cnt Ig		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_enu		
	target_TrqOvISta_Per1_KAState_State_eric target_TrqOvISta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosServEnable Cnt Igc	target TrqOvISta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-621.409973		
target_TrqOviSta_Per1_CWFound_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1071.35999		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0 0.5		
target_TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	9.30000019		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	-
TrqOvlSta_ESCState_State_M_enum	4	4	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	48510.3398	48510.3398	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	32038	32038 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
	1.5	1.5 ± 0.000009	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32		0.1.1	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 0 1	0 ± 1 1 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	30.8999996	30.8999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.430000007	0.430000007 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	35.7000008	35.7000008 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	51515
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51515
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	51515
TrqOvlSta_HapticState_State_M_enum	2
FrqOvISta_Hapticdur_Cnt_M_u16	33020
rqOvlSta_Haptictime_Sec_M_f32	51515
FrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.60000002
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
FrqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	32
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
<pre>c_APAIncludeHaptic_Cnt_lgc</pre>	0
<pre><_APAMaxHwTrqTime_Sec_f32</pre>	0.915000021
APAMaxHwTrq HwNm f32	3.75999999
<_APAMaxVehSpd_Kph_f32	63
C ESCMaxVehSpd Kph f32	0
C_EGOMAXVERISPU_REPI_IS2 C_HapticAmplitude_MtrNm_f32	0.449999988
C_HapticDuration_Sec_f32	0.360000014
	36.4000015
(_HapticFreq_Hz_f32	
C_HapticReacttime_Sec_f32 C_HAMQUACES and Keb f32	0.783999979
<_LKAMaxVehSpd_Kph_f32	93.099985
<_LKAMinVehSpd_Kph_f32	85.75
«_MaxHapticReqTime_Sec_f32	0.870000005
C_SWARLimiter_HwDeg_f32	38.2000008
x_ShiftLevRevTime_Sec_f32	0.141000003
c_StandstillThresh_Kph_f32	1.32000005
StandstillTime_Sec_f32	0.133000001
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_C	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_en	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lg	
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Ic	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lg	
target_Rte_Inst_Ap_TrqOviota.TrqOviota_r en_Giviotin_traviligenable_ont_g target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target TrqOvISta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HapticRequest_Clit_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Fer1_HapticRequest_Crit_igc
argot_rtto_inst_rtp_rrqoviota.rrqoviota_r eri_riwitorque_riwitiin_l32	Larger_rigoviola_r or r_riw rorque_riwitin_roz

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvISta_Per1_LKARequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph	target_TrqOvISta_Per1_LKAState_State_enum		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph	_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_IviiiiSecureVeriicie target_TrqOviSta_Per1_PosServEnable_0		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_C		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajHwAngle_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDe		-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igo			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrN	lm_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_	f32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-581.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1076.30005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target TrqOvlSta_Per1_ESCRequest Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.4000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		- I
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum	3	3	
TrqOvISta_ElosedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	51515	51515 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	33020	33020 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	51515	51515 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002	1.60000002 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	*
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	· ·
target_TrqOvISta_Per1_ESCState_State_enum.value	0	0	Ž
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	
target TrqOvlSta Per1 LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	
target_frqOvlSta_refr_PosServErlable_Crit_igc.value target_frqOvlSta_refr_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.44999988	0.449999988 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	36.4000015	36.4000015 ± 0.0009765625	~
<u> </u>	1		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.152 (Repeat Count = 1)	<u></u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	54520
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	54520
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	54520
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	34002
TrqOvlSta_Haptictime_Sec_M_f32	54520
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	3
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	3
TrqOvISta PosSrvoHwAngle HwDeg M f32	33.0999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.925999999
k_APAMaxHwTrq_HwNm_f32	3.83999991
k_APAMaxVehSpd_Kph_f32	64.8000031
k_ESCMaxVehSpd_Kph_f32	180
k_HapticAmplitude_MtrNm_f32	0.469999999
k_HapticDuration_Sec_f32	0.375
k_HapticFreq_Hz_f32	37.099985
k_HapticReacttime_Sec_f32	0.800000012
k_LKAMaxVehSpd_Kph_f32	95
k_LKAMinVehSpd_Kph_f32	87.5
k_MaxHapticReqTime_Sec_f32	0.88499999
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	38.5 0.144999996
k_StandstillThresh_Kph_f32	1.36000001
k_StandstillTime_Sec_f32	0.136999995
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvISta Per1 GMOSH APAMfgEnable Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
	target TrqOvISta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrgOvISta TrgOvISta Per1 TrgOscAmp MtrNm t32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lqc	target irgovista peri irgoscenable ont igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0

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Input Value -541.609985 0 1081.23999 0		
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	Expected Value	Result
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	1 0 0 0 2.70000005 0 0 0 167.399994 152.699997 0 0.170000002 13.1000004 0 Actual Value 4 0 0 0 0 34002 0 1.75480008 0 0 1.75480008 0 0 3.3 4 33.0999985 0 0 4 0 0 33.0999985 0 0 4 0 0 33.0999985 0 0 4 0 0 37.0999985	0 2.7000005 0 2.7000005 0 0 167.399994 152.699997 0 0.17000002 13.100004 0 Actual Value 4 4 0 0 0 0 0 10 0 0 10 10 10 10 10 10 10 1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.153 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	57525.3203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	57525.3203
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	57525.3203
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	34984
TrqOvlSta_Haptictime_Sec_M_f32	57525.3203
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.936999977
k_APAMaxHwTrq_HwNm_f32	3.92000008
k_APAMaxVehSpd_Kph_f32	66.5999985
k_ESCMaxVehSpd_Kph_f32	100.5
k_HapticAmplitude_MtrNm_f32	0.49000001
k_HapticDuration_Sec_f32	0.38999986 37.799992
k_HapticFreq_Hz_f32 k HapticReacttime Sec f32	0.815999985
k_LKAMaxVehSpd_Kph_f32	96.9000015
k_LKAMinVehSpd_Kph_f32	89.25
k_MaxHapticReqTime_Sec_f32	0.89999976
k_SWARLimiter_HwDeg_f32	38.7999992
k_ShiftLevRevTime_Sec_f32	0.149000004
k_StandstillThresh_Kph_f32	1.3999998
k_StandstillTime_Sec_f32	0.141000003
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target TrqOvlSta_Per1_APANotiRecoverableFaults_Crit_igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-501.709991
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 4006 40005
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	1086.18005 0
target_IrqOviSta_Peri_ESCFault_Cnt_igc.value target TrqOviSta_Peri_ESCIsLimited Cnt_igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	172.300003
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	0	0	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	57525.3203	57525.3203 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	34984	34984 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	57525.3203	57525.3203 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79999995	1.79999995 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.180000007	0.180000007 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.49000001	0.49000001 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	37.7999992	37.7999992 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.154 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	60530
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	60530
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	60530
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	35966
TrqOvlSta_Haptictime_Sec_M_f32	60530
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.89999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	35.2999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.948000014
k_APAMaxHwTrq_HwNm_f32	4
k_APAMaxVehSpd_Kph_f32	68.4000015
k_ESCMaxVehSpd_Kph_f32	110.199997
k_HapticAmplitude_MtrNm_f32	0.50999999
k_HapticDuration_Sec_f32	0.405000001
k_HapticFreq_Hz_f32	38.5
k_HapticReacttime_Sec_f32	0.832000017
k_LKAMaxVehSpd_Kph_f32	98.8000031
k_LKAMinVehSpd_Kph_f32	91
k_MaxHapticReqTime_Sec_f32	0.915000021
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0.152999997
k_StandstillThresh_Kph_f32	1.44000006
k_StandstillTime_Sec_f32	0.144999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lq	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lge	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleS		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cni		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0	-	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-461.809998		
target TrqOvlSta Per1 CWFound Cnt Igc.value	0		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	1091.12		
	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta_Per1_ESCIsLimited Cnt_lgc.value	0		
	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	3,2999995		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	177.199997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	35966	35966 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.8999998	1.89999998 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
	0		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
		0 ± 1 0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	0		~
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	35.2999992	35.2999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.50999999	0.50999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	38.5	38.5 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Nama	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	63535
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63535
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	63535
FrqOvlSta_HapticState_State_M_enum	0
FrqOvlSta_Hapticdur_Cnt_M_u16	36948
FrqOvlSta_Haptictime_Sec_M_f32	63535
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0614
FrqOvlSta_LKAFault_Cnt_M_lgc	1
FrqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	1
FrqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	36.4000015
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
	0
APAMaxHwTrqTime_Sec_f32	0.958999991
APAMaxHwTrq HwNm f32	4.07999992
<pre><_ i / iii /</pre>	70.1999969
C ESCMaxVehSpd Kph f32	115.599998
C_Egoniaxvenopa_rpn_igz C_HapticAmplitude_MtrNm_f32	0.529999971
:hapticDuration_Sec_f32	0.419999987
	39.200008
(_HapticPreq_Hz_f32	
(_HapticReacttime_Sec_f32	0.84799999
<pre>(_LKAMaxVehSpd_Kph_f32</pre>	100.699997
x_LKAMinVehSpd_Kph_f32 x_LKAMinVehSpd_Kph_f32	92.75

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<u> </u>			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEriable_Cit_igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target TrgOvlSta Per1 CCWFound Cnt Igc.value	1		
target TrgOvlSta Per1 CCWPosition HwDeg f32.value	-421.910004		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1096.06006		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.5999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63535	63535 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	63535	63535	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	36948	36948 ± 1	Y
TrqOvlSta_Haptictime_Sec_M_f32	63535	63535 ± 1	Y
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.0982399	2.0982399 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	Y
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	V
TrqOvISta_LKAPermFault_Cnt_M_u08	1	1 ± 1	V
TrqOvISta_LKAState_State_M_enum	3 0.100000001	3 0.100000001 ± 0.0009765625	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	
target_TrqOviSta_Per1_APADivfinterventionDetected_Crit_igc.value target_TrqOviSta_Per1_APAState_State_enum.value	3	3	
target_TrqOvISta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvISta_Per1_LKAState_State_enum.value	3	3	-
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1±1	
target TrqOvlSta Per1 PosServEhable_Ont_igc.value	0.100000001	0.100000001 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.529999971	0.529999971 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	·
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	39.2000008	39.2000008 ± 0.0009765625	~
<u> </u>	1 11111		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 ChecknointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Name	Test Step 2.156 (Repeat Count = 1)	<u></u>
March Marc		Input Value
Timpolities Basers May May 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 170 1		·
Timpolities Sectioner State M. enum		
TimpOrtion Expression Management Time Sec. M. 12		
TopOrSize Desiration State Amount 1		66540.6328
TopOrdis Payerican Payer 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970 1970	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	66540.6328
TopOrdSt Haptorise Port PS	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	66540.6328
TigO/Signa, Hardrang, Sev., U. N2	TrqOvlSta_HapticState_State_M_enum	1
TROMOSIS LAPTOWIS LAPTOWIS TOTAL SIZE	TrqOvlSta_Hapticdur_Cnt_M_u16	37930
TOCOSES LAPACHERIST CM Mg PS LA	TrqOvlSta_Haptictime_Sec_M_f32	66540.6328
TROMSS LAPAMENTAL CHI M, US		
TriChOSBS_LIAPemiratia_Crim_M_ ubs 2 TriChOSBS_LIAPemiratia_Crim_M_ ubs 2 TriChOSBS_LIAPemiratia_Crim_M_ ubs 2 TriChOSBS_Index_Mempe_Hubbag_M_122 TriChOSBS_ReadyPhines_Crim_M_ ubs 2 TriChOSBS_ReadyPhines_Crim_M_ ubs 4 A.PARAcadusHave_Mape_Hubbag_M_122 TriChOSBS_ReadyPhines_Crim_M_ ubs 4 A.PARAcadusHave_Mape_Hubbag_M_122 A.PARAcadusHave_Mape_Hub		
TrigO/Sis LASA State State M. Armon 2		
TO/OSSE JASANES Site M. Brum 7. TO/OSSE RestynOvise Crit M. Jop 8. APAMANIATOR TIME Sec. 12 8. APAMA		
TripOrdiss Parishort-Monte, Dr. M. pt. i. APAIncadestein, Cel. jps ii. APAIncadestein, Cel. jps ii. APAIncadestein, Cel. jps iii. APAIncadestein, Cel.	·	
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K. APAMAWNISQ Kgh. 12 K. PESCAMAWNISQ Kgh. 12 K. Hardschriftling Minn. 132 K. Mardschriftling Minn. 132 K. Sandschriftling Minn. 132 K. Sandschriftlin		
K. PARMAVerSiped, Krin. 12 K. Harisch-Amplitice, Mirkm., 132 K. Lick-Mirkw-Prisky Left, 132 K. Lick-Mirkw-Prisky Left, 132 K. Lick-Mirkw-Prisky Left, 132 K. John-Amplitice, 132 K. John-Amplitice, 133 K. Sandastillare, 134 K. Sandastillare,		
k. Haptichardinol. psc. [12		
k. Haptichardinol. psc. [12	k_ESCMaxVehSpd_Kph_f32	121
K. Haptiche Cartier Sec 12		0.550000012
KLHAMINAYENGOL (Np. 132 102.59998 102.59998 102.59998 102.59998 102.59998 102.59998 102.59998 102.59998 102.599998 102.599998 102.599998 102.599998 102.599998 102.599999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.5999999 102.599999	k_HapticDuration_Sec_f32	0.435000002
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target_Rte_Inst_Ap_TrqOviSta_Prd_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Prd_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Prd_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Prd_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Prd_ESCIStatte_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Prd_ESCIState_State_enum target_Rte_Inst_Ap_TrqOviSta_Prd_ESCIState_State_enum target_Rte_Inst_Ap_TrqOviSta_Prd_GMOSHO_State_State_enum target_Rte_Inst_Ap_TrqOviSta_Prd_GMOSHO_State_State_enum target_Rte_Inst_Ap_TrqOviSta_Prd_GMOSHO_State_State_enum target_Rte_Inst_Ap_TrqOviSta_Prd_GMOSHO_State_State_enum target_Rte_Inst_Ap_TrqOviSta_Prd_CMOSH_ESCMIgenable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Prd_CMOSH_ESCMIgenable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LKARGette_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_LRARGette_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_RosseverPenible_Speed_Kph_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_RosseverPenible_Speed_Kph_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_PosseverEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_PossereEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prd_PossereEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Prd_SVARTrqtAngetquest_twobg_132 target_Rte_Inst_Ap_TrqOviSta_Prd_SVARTrqtAnget_Ln	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Per1_ESCISLImited_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCISLImited_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCISLImited_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Per1_ESCISLImited_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOScillateState_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MasSecureVehicleSpeed_Kph_32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SorvAmp_Mrthm_32 target_TrqOviSta_Per1_SorvAmp_Mrthm_32 target_TrqOviSta_Per1_SorvAmp_Mrthm_32 target_TrqOviSta_Per1_SorvAmp_Mrthm_32 target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc t	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_ESCState_State_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_MapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_MapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_DestrajtEvanle_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOscFreq_Rt_fg2 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOscFreq_Rt_fg2 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOscFreq_Rt_fg2 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOscFreq_Rt_fg	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_HaptGrequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAfault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAfault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAfault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAfault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAfault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAfault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAfault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAfault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_MinSecureVerhicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_MinSecureVerhicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_Pert_PosSevinable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosSevinable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TosSevnehwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TosSevnehwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TosSevnehwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TosSevnehwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOscAmp_Mrtnm_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOscAmp_Mrtnm		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOscillateState_state_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HarpticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HarpticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_DesServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SvARTrgdAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_StriftLeverIsinReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_APARequest_Cnt_lgc.value target_TrqOviSta_Per1_APARequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOscillateState_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrviEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MitNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MitNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MitNm_f32 target_TrqOviSta_Per1_TrqOscAmp_MitNm_f32 target_TrqOviSta_Per1_TrqOscAmp_MitNm_f32 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTrqAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTrqAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTrqAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTrqAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTrqAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCmp_MtNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCmp_MtNm_f32 target_TrqOviSta_Perl_SorTrqOviSta_Perl_SorTrqOviSta_Perl_TrqOsCmp_MtNm_f32 target_TrqOviSta_Perl_SortrqOviSta_Perl_TrqOsCmp_MtNm_f32 target_TrqOviSta_Perl_APARnonRecoverableFaults_Cnt_lgc_value 0 target_TrqOviSta_Perl_APARequest_Cnt_lgc_value 0 target_TrqOviSta_Perl_APARequest_Cnt_lgc_value 0 target_TrqOviSta_Perl		
target_Rte_Inst_Ap_TrqOvlSta_Prer1_GMOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prer1_GMOSH_LKAMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LARCatate_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_TrqOvlSta_Per1_SorTajEnable_Cnt_lgc target_TrqOvlSta_Per1_SorTajEnabl		
target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCAmp_MtrNm_f32 target_TrqOviSta_Per1_TrqOsCAmp_MtrNm_f32 target_Trq		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SorTajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SorTajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SorTajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SorTajEnable_Cnt_Igc target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvISta_Per1_SorTajEnable_Cnt_Igc target_TrqOvISta_Per1_SorTajEnable_Cnt_Igc target_TrqOvISta_Per1_SorTajEnable_Cnt_Igc target_TrqOvISta_Per1_SorTajEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCaEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCaEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_APARequest_Cnt_Igc value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.Value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.Value 0		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
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target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANgequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARgequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARgequest_Cnt_lgc.value 0		
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target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_SWISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_TrqOvISta_Per1_T		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 tar		
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target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq		
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target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_inqOviota_rei1_CCVVround_CntLigd.value 0	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1101		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	187		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	172.300003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	1	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	37930	37930 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.0999999	2.0999999 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	37.5	37.5 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	37.5	37.5 ± 0.0009765625	•
toward Torondote Dark Torong Aven Making 600 celus	0.550000012	0.550000012 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.00000012		
target_IrqOviSta_Peri_IrqOscAmp_MtrNm_r3z.value target_TrqOviSta_Peri_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.157 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18460.6309
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	38912
TrqOvlSta_Haptictime_Sec_M_f32	18460.6309
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0658000037
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0

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TayAndreid selection (Co. 19)	Name	Input Value
A.PAPASANICHIPS, 50; 122 0.000000000000000000000000000000000		·
APAMAMetric Former See: DE		
\$4,000000000000000000000000000000000000		0.981000006
SECTION Sect	k_APAMaxHwTrq_HwNm_f32	4.23999977
Lightpring Lig	k_APAMaxVehSpd_Kph_f32	193.800003
Appendix	_ , _ , _ ,	
Liphophic Approximation Sec. 12		
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SandardTimes Ref. 192 15, SandardTimes 192 15, SandardTim	k_MaxHapticReqTime_Sec_f32	0.959999979
SubmiditThree Kip 122	k_SWARLimiter_HwDeg_f32	45.5999985
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isinget Riss Inst. Ap. Trapolisis Pert APAsias State enum target Risp Inst. Ap. Trapolisis Pert COMPosition (Art Light State (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (1994) (19		
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target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Pert_ShifftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_ShifftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOscEnable_Cnt_Igc target_TrqOvIsta_Pert_ApANonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Pert_ApANonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Pert_ApANonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Pert_ApANonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Pert_ApANonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Pert_ApANonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Pert_CWFound_Cnt_Igc_value target_TrqOvIsta_Pert_CWFound_Cnt_Igc_value target_TrqOvIsta_Pert_CWFound_Cnt_Igc_value target_TrqOvIsta_Pert_CWFound_Cnt_Igc_value target_TrqOvIsta_Pert_CWFound_Cnt_Igc_value target_TrqOvIsta_Pert_CWFostino_HwDeg_f32_value target_TrqOvIsta_Pert_CWFostino_HwDeg_f32_value target_TrqOvIsta_Pert_CWFostino_HwDeg_f32_value target_TrqOvIsta_Pert_CWFostino_HwDeg_f32_value target_TrqOvIsta_Pert_GNGSH_PAMMgEnable_Cnt_Igc_value target_TrqOvIsta_Pert_GNGSH_PAMMgEnable_Cnt_Igc_value target_TrqOvIsta_Pert_GNGSH_ENGMgEnable_Cnt_Igc_value target_TrqOvIsta_Pert_GNGSH_ENGMgEnable_Cnt_Igc_value target_TrqOvIsta_Pert_HwTorque_HwNm_f32_value target_TrqOvIsta_Pert_HwTorque_HwNm_f32_value target_TrqOvIsta_Pert_LKARAequest_Cnt_Igc_value target_TrqOvIsta_Pert_LKARAequest_Cnt_Igc_value target_TrqOvIsta_Pert_I_KARAequest_Cnt_Igc_value target_TrqOvIsta_Pert_I_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOvIsta_Pert_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOvIsta_Pert_PosTrajEnable_Cnt_Igc_value target_TrqOvIsta_Pert_PosTrajEnable_Cnt_Igc_value	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StriftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_ApAtonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_ApARecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_ApARecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_ApARecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_ApARecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_CowFound_Cnt_Igc_value target_TrqOviSta_Pert_CowFound_Cnt_Igc_value target_TrqOviSta_Pert_CowFound_Cnt_Igc_value target_TrqOviSta_Pert_CowFound_Cnt_Igc_value target_TrqOviSta_Pert_EscFault_Cnt_Igc_value target_TrqOviSta_Pert_EscFault_Cnt_Igc_value target_TrqOviSta_Pert_EscRequest_Cnt_Igc_value target_TrqOviSta_Pert_EscRequest_Cnt_Igc_value target_TrqOviSta_Pert_GMOSH_ApAMfgEnable_Cnt_Igc_value target_TrqOviSta_Pert_GMOSH_EscMfgEnable_Cnt_Igc_value target_TrqOviSta_Pert_GMOSH_EscMfgEnable_Cnt_Igc_value target_TrqOviSta_Pert_HapticRequest_Cnt_Igc_value target_TrqOviSta_Pert_HapticRequest_Cnt_Igc_value target_TrqOviSta_Pert_HapticRequest_Cnt_Igc_value target_TrqOviSta_Pert_HapticRequest_Cnt_Igc_value target_TrqOviSta_Pert_HapticRequest_Cnt_Igc_value target_TrqOviSta_Pert_HapticRequest_Cnt_Igc_value target_TrqOviSta_Pert_HapticRequest_Cnt_Igc_value target_TrqOviSta_Pert_HaviArequest_Cnt_Igc_value target_TrqOviSta_Pert_HapticRequest_Cnt_Igc_value target_TrqOviSta_Pert_LKAFault_Cnt_Igc_value target_TrqOviSta_Pert_LKAFault_Cnt_Igc_value target_TrqOviSta_Pert_MarksecureVerbicleSpeed_Kph_f32.value target_TrqOviSta_Pert_PostrajEnable_Cnt_Igc_value target_TrqOviSta_Pert_PostrajEnable_Cnt_Igc_va		
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target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_Prrq_OviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_APARecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_APARequest_Cnt_igc.value target_TrqOviSta_Per1_APARequest_Cnt_igc.value target_TrqOviSta_Per1_APARequest_Cnt_igc.value target_TrqOviSta_Per1_CCWFound_Cnt_igc.value target_TrqOviSta_Per1_CCWFound_Cnt_igc.value target_TrqOviSta_Per1_CCWFound_Cnt_igc.value target_TrqOviSta_Per1_CCWFound_Cnt_igc.value target_TrqOviSta_Per1_CWFound_Cnt_igc.value target_TrqOviSta_Per1_ExcPault_Cnt_igc.value target_TrqOviSta_Per1_ExcPault_Cnt_igc.value target_TrqOviSta_Per1_ExcPault_Cnt_igc.value target_TrqOviSta_Per1_ExcRequest_Cnt_igc.value target_TrqOviSta_Per1_ExcRequest_Cnt_igc.value target_TrqOviSta_Per1_ExcRequest_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_APAMigEnable_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_ExcMigEnable_Cnt_igc.value target_TrqOviSta_Per1_Morque_HwNm_f32.value target_TrqOviSta_Per1_HwTorque_HwNm_f32.value target_TrqOviSta_Per1_HwTorque_HwNm_f32.value target_TrqOviSta_Per1_LKAFault_Cnt_igc.value target_TrqOviSta_Per1_LKAFault_Cnt_igc.value target_TrqOviSta_Per1_LKAFault_Cnt_igc.value target_TrqOviSta_Per1_LKAFault_Cnt_igc.value target_TrqOviSta_Per1_LKARequest_Ort_igc.value target_TrqOviSta_Per1_LKARequest_Ort_igc.value target_TrqOviSta_Per1_LKARequest_Ort_igc.value target_TrqOviSta_Per1_LKARequest_Ort_igc.value target_TrqOviSta_Per1_MorgecureVehicleSpeed_Kph_f32.value target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOviSta_Per1_MorgecureVehicleSpeed_Kph_f32.value target_TrqOviSta_Per1_PostrajEnable_Cnt_igc.value target_TrqOviSta_Per1_PostrajEnable_Cnt_igc.value target_TrqOviSta_Per1_PostrajEnable_Cnt_igc.value target_TrqOviSta_Per1_PostrajEnable_Cnt_igc.value target_TrqOviSta_Per1_MorgecureVehicleSpeed_Kph_f32.value target_TrqOviSta_Per1_PostrajEnable_Cnt_igc.value	· · · ·	
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target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	0 _ 1	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	· - · · · · · · · · · · · ·	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.1999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_lrqUviSta_Per1_PosTrajHwAngle_HwDeg_t32.value 0.119999997	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

TrqOvlSta_Per1

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309	18460.6309 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	38912	38912 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005	2.20000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0	0 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	0 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.569999993	0.569999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	40.5999985	40.5999985 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.158 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21465
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	21465
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	21465
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	39894
TrqOvlSta_Haptictime_Sec_M_f32	21465
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.29999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0680000037
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	90
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.150000006
k_APAMaxHwTrq_HwNm_f32	4.32000017
k_APAMaxVehSpd_Kph_f32	197.600006
k_ESCMaxVehSpd_Kph_f32	131.800003
k_HapticAmplitude_MtrNm_f32	0.589999974
k_HapticDuration_Sec_f32	0.465000004
k_HapticFreq_Hz_f32	41.2999992
k_HapticReacttime_Sec_f32	0.896000028
k_LKAMaxVehSpd_Kph_f32	106.400002
k_LKAMinVehSpd_Kph_f32	98
k_MaxHapticReqTime_Sec_f32	0.975000024
k_SWARLimiter_HwDeg_f32	50.0999985
k_ShiftLevRevTime_Sec_f32	0.169
k_StandstillThresh_Kph_f32	1.67999995
k_StandstillTime_Sec_f32	0.169
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt_ApadrvrInterventionDetected_Cnt$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	Faults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau	Its_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWPosition_HwDe target_TrqOvlSta_Per1_CWFound_Cnt_lgc	g_132	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_	f32	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	202	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	gc .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnd target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f3		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	m	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target TrqOvlSta Per1 PosTrajHwAngle Hw	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3		-	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrgOvlSta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-421.910004 0		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1110.88		
target TrgOvlSta Per1 ESCFault Cnt lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	V		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	196.800003		
	196.800003 182.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value			
	182.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	182.100006 0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	182.100006 0 0.129999995		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	182.100006 0 0.129999995 24.5	Expected Value	Result
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum	182.100006 0 0.129999995 24.5 0 Actual Value	4	Result
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	182.100006 0 0.129999995 24.5 0 Actual Value 4	4	~
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	182.100006 0 0.129999995 24.5 0 Actual Value 4	4 4 0±1	*
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	182.100006 0 0.129999995 24.5 0 Actual Value 4 4 0	4 4 0±1 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	182.100006 0 0.129999995 24.5 0 Actual Value 4 4 0 0	4 4 0±1 0±1 0	, ,
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	182.100006 0 0.129999995 24.5 0 Actual Value 4 0 0 0 0	4 4 0±1 0±1 0 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_M_enum TrqOvlSta_HapticState_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	182.100006 0 0.129999995 24.5 0 Actual Value 4 4 0 0	4 4 0±1 0±1 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	182.100006 0 0.129999995 24.5 0 Actual Value 4 0 0 0 39894	4 4 0±1 0±1 0 0 39894±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	182.100006 0 0.129999995 24.5 0 Actual Value 4 4 0 0 0 39894 0	4 4 0 ± 1 0 ± 1 0 0 39894 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32	182.100006 0 0.129999995 24.5 0 Actual Value 4 4 0 0 0 39894 0 2.44959998	4 4 0 ± 1 0 ± 1 0 0 39894 ± 1 0 ± 1 2.44959998 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	182.100006 0 0.129999995 24.5 0 Actual Value 4 4 0 0 0 0 39894 0 2.44959998 0 0 4	4 4 0±1 0±1 0 0 39894±1 0±1 2.44959998±0.000009 0±1 0±1 4±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticUState_State_M_enum TrqOvISta_	182.100006 0 0.129999995 24.5 0 Actual Value 4 4 0 0 0 0 39894 0 2.44959998 0 0	4 4 0 ± 1 0 ± 1 0 0 39894 ± 1 0 ± 1 2.44959998 ± 0.000009 0 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·

0

0 ± 1

 $TrqOvlSta_ReadytoPulse_Cnt_M_lgc$

2015-02-04, 12:34:40+0530

TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	90	90 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.589999974	0.589999974 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	41.2999992	41.2999992 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	24470
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	24470
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	24470
TrqOvlSta_HapticState_State_M_enum	1
FrqOvlSta_Hapticdur_Cnt_M_u16	40876
TrqOvlSta_Haptictime_Sec_M_f32	24470
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0702000037
FrqOvlSta_LKAFault_Cnt_M_lgc	1
FrqOvlSta_LKAPermFault_Cnt_M_lgc	1
FrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
FrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
<pre>APAIncludeHaptic_Cnt_lgc</pre>	0
<_APAMaxHwTrqTime_Sec_f32	0.159999996
k APAMaxHwTrq HwNm f32	4.4000001
k_APAMaxVehSpd_Kph_f32	207.399994
k ESCMaxVehSpd Kph f32	137.199997
k_HapticAmplitude_MtrNm_f32	0.610000014
<pre><_hapticDuration_Sec_f32</pre>	0.47999989
k_HapticFreq_Hz_f32	42
k_HapticReacttime_Sec_f32	0.912
k_LKAMaxVehSpd_Kph_f32	108.300003
k_LKAMinVehSpd_Kph_f32	99.75
k_MaxHapticReqTime_Sec_f32	0.109999999
<_SWARLimiter_HwDeg_f32	54.5999985
K_ShiftLevRevTime_Sec_f32	0.172999993
k_StandstillThresh_Kph_f32	1.72000003
k_StandstillTime_Sec_f32	0.172999993
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cr	nt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_e	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_I	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvIsta_Per1_HwTorque_HwNm_f32
argot_rito_mat_rp_riqoviota.riqoviota_reri_riwTolque_riwMill_laz	algot_riqoviota_i cri_riwTorquo_riwMiii_loz

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 12:34:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lc	ус	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3	2 target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3222222222222222222222222222222222222$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_F	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1115.81995		
target TrgOvlSta Per1 ESCFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.80000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	201.699997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	187		
target_TrqOvlSta_Per1_PosTrajEnable_Gnt_igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	2	2	✓ ×
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	24470	24470 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	Ī	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	40876	40876 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.4000001	2.4000001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.5	50.5 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	2	0 ± 1 2	*
target_TrqOvlSta_Per1_APAState_State_enum.value target TrqOvlSta Per1 ESCState State enum.value	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	-
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	0	0 ± 1	-
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	50.5	50.5 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.610000014	0.610000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	42	42 ± 0.0009765625	~
		-	_

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.160 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-10 0.00125584798
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	0 0
k HapticAmplitude MtrNm f32	0
k HapticDuration Sec f32	0
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	0
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	i e
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target TrqOvISta Per1 ESCRequest Cnt Igc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrgOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvIsta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
	

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1440.10999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-10		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-34.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrgOvISta ESCState State M enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	✓
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	~
TrgOvISta HapticState State M enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10	-10 ± 0.00009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0	0 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
		0.4	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	0 ± 1 0 ± 0.0009765625	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	0 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.161 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	65535
TrqOvlSta_Haptictime_Sec_M_f32	4.2949673e+009
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	90

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	1
k_APAMaxHwTrq_HwNm_f32	10
k_APAMaxVehSpd_Kph_f32	220
k_ESCMaxVehSpd_Kph_f32	180
k_HapticAmplitude_MtrNm_f32	0.75
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	100
k HapticReacttime Sec f32	2
k_LKAMaxVehSpd_Kph_f32	220
k_LKAMinVehSpd_Kph_f32	220
k_MaxHapticReqTime_Sec_f32	1
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.5
k_StandstillThresh_Kph_f32	5
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.5 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target TrgOvlSta Per1 APARecoverableFaults Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-360
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target TrqOvlSta Per1 CWPosition HwDeg f32.value	1 1440.10999
target TrqOvISta_Per1_CvvPosition_HwDeg_132.value target TrqOvISta_Per1_ESCFault Cnt lgc.value	1440.10999
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	10
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LnArkequest_Cnt_gc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	514.799988
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	511
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	65535	65535 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	10 ± 0.00009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	100	100 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

TrqOvlSta_Per1

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Test Case 3: Path Test

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TrqOvlSta_Per1



Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cvcles:

TS3.1 874 Cycles TS3.2 1041 Cycles TS3.3 905 Cycles TS3.4 890 Cycles TS3.5 899 Cycles TS3.6 904 Cycles TS3.7 947 Cycles TS3.8 932 Cycles TS3.9 914 Cycles TS3.10 900 Cycles
TS3.11 903 Cycles
TS3.11 903 Cycles
TS3.11 896 Cycles
TS3.12 896 Cycles
TS3.13 880 Cycles
TS3.14 891 Cycles
TS3.16 919 Cycles
TS3.16 919 Cycles
TS3.17 932 Cycles
TS3.18 911 Cycles
TS3.19 952 Cycles
TS3.20 923 Cycles
TS3.21 914 Cycles
TS3.22 900 Cycles
TS3.22 900 Cycles
TS3.22 900 Cycles
TS3.23 875 Cycles
TS3.24 938 Cycles
TS3.25 937 Cycles
TS3.26 931 Cycles
TS3.27 935 Cycles
TS3.27 935 Cycles
TS3.29 910 Cycles
TS3.29 910 Cycles
TS3.39 933 Cycles
TS3.31 913 Cycles
TS3.31 913 Cycles
TS3.31 103 Cycles
TS3.33 1008 Cycles
TS3.34 1022 Cycles
TS3.35 1030 Cycles
TS3.36 1027 Cycles
TS3.37 1019 Cycles
TS3.38 1018 Cycles
TS3.39 1019 Cycles
TS3.34 1010 Cycles
TS3.34 1017 Cycles
TS3.45 1039 Cycles
TS3.47 963 Cycles
TS3.49 966 Cycles
TS3.50 969 Cycles
TS3.51 930 Cycles
TS3.51 930 Cycles
TS3.52 936 Cycles TS3.51 930 Cycles
TS3.52 936 Cycles
TS3.52 936 Cycles
TS3.55 932 Cycles
TS3.55 923 Cycles
TS3.55 923 Cycles
TS3.56 930 Cycles
TS3.57 937 Cycles
TS3.56 930 Cycles
TS3.56 931 Cycles
TS3.60 932 Cycles
TS3.61 936 Cycles
TS3.62 922 Cycles
TS3.62 922 Cycles
TS3.65 939 Cycles
TS3.66 941 Cycles
TS3.67 909 Cycles
TS3.67 909 Cycles
TS3.68 905 Cycles
TS3.68 905 Cycles
TS3.70 909 Cycles
TS3.71 946 Cycles
TS3.72 949 Cycles
TS3.73 903 Cycles
TS3.74 904 Cycles
TS3.75 953 Cycles
TS3.76 987 Cycles
TS3.77 987 Cycles
TS3.78 908 Cycles
TS3.78 908 Cycles
TS3.80 935 Cycles
TS3.81 933 Cycles
TS3.81 933 Cycles
TS3.83 1037 Cycles
TS3.84 939 Cycles TS3.84 939 Cycles



Description Vector Description:

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Vector Description:

TS3.1"(LKARequest_Cnt_T_igc == TRUE)=False
(HapticRequest_Cnt_T_igc == TRUE)=False
(ShiftLeverIshIReverse_Cnt_T_igc == TRUE)=False
(MaxSecureVehicleSpeed_Kph_T_ig2 >= k_StandstillThresh_Kph_f32)=Flase
(TrqOvlSta_HapticState_State_M_enum_l= Haptic_Inactive)=True
(Abs_f32_m(HwTrqFilt_Hwhm_T_ig2) >= k_APAMaxHwTrq_Hwhlm_f32)=False
((LKAFauil_Cnt_T_igc == TRUE)_b& (TrqOvlSta_LKAFernFauil_Cnt_M_igc == FALSE) && (TrqOvlSta_LKAFauil_Cnt_M_igc == FALSE))=False
if(CWFound_Cnt_T_igc == TRUE)_b& (CWFound_Cnt_T_igc == TRUE))=True
(GMOSH_APAMigEnable_Cnt_T_igc == TRUE)_EFRO_ULS_F32)=True
(GMOSH_APAMigEnable_Cnt_T_igc == TRUE)_EFRO_ULS_F32)=True
(HapticRequest_Cnt_T_igc == TRUE)_EFRO_ULS_F32)=True
(TrqOvlSta_Haptictinne_Sec_M_f32 >= k_APAMaxHwTrq_Hwhlm_f32)=False
(ShiftLeverIshIReverse_Cnt_T_igc == TRUE)=True
(TrqOvlSta_HapticState_State_M_enum_l= Haptic_Inactive)=False
(MaxSecureVehicleSpeed_Kph_T_f32 >= k_StandstillThresh_Kph_f32)=True
(TrqOvlSta_HapticState_State_M_enum_l= Haptic_Inactive)=False
(Abs_f32_m(HwTrqFilt_Hwhlm_f32) >= k_APAMaxHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticState_State_M_enum_l= Haptic_Inactive)=False
(LKAFauil_Cnt_T_igc == TRUE)=True
(TrqOvlSta_HapticState_State_M_enum_l= Sec_M_f32 >= (k_APAMaxHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticState_State_M_enum_l= Sec_M_f32 >= (k_APAMaxHwTrq_Imm_sec_f32))=False
(LKAFauil_Cnt_T_igc == TRUE)=True
(TrqOvlSta_HapticInne_Sec_M_f32 >= (k_MaxHapticReqTime_Sec_f32))=True
(TrqOvlSta_HapticInne_Sec_M_f32 >= (k_MaxHapticReqTime_Sec_f32))=True
(TrqOvlSta_HapticInne_Sec_M_f32 >= k_StandstillThresh_kph_f32)=True
(TrqOvlSta_HapticInne_Sec_M_f32 >= k_Standsti
     -- FALSE)
TS3.5((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
((HapticRequest_Cnt_T_lgc==TRUE)&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)) ||(LrnEOTFound_Cnt_T_lgc==FALSE) )=True
TS3.6(TrqOvlSta_LKAPermFault_Cnt_M_u08 >= 3U)=True
TS3.7(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=False
TS3.8"((LKAFault_Cnt_T_lgc == TRUE)=True && (TrqOvlSta_LKAPermFault_Cnt_M_lgc == FALSE)=True&& (TrqOvlSta_LKAFault_Cnt_M_lgc == FALSE)=False)
== FALSE)=False)
       case(APA_TempInhibit):True
(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=False"
TS3.9((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32)=False || (MaxSecureVehicleSpeed_Kph_T_f32 >
        k_LKAMaxVehSpd_Kph_f32)=True)
     K_LAWIAXVEISPO_TOIT_102_FTIGE
TS3.10"case(APA_AvailCntrl):True
if(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=True"
TS3.11"(TrqOvlSta_LKAPermFault_Cnt_M_lgc == TRUE)=False
(LKARequest_Cnt_T_lgc == FALSE)=True"
TS3.12"case(APA_TempInhibit):True
    TS3.12"case(APA_TempInhibit):True

(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=True"

TS3.13(TrqOvlSta_LKAPemFault_Cnt_M_lgc == TRUE)=True

TS3.14(APARequest_Cnt_T_lgc == FALSE)=True

TS3.15"((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
((HapticRequest_Cnt_T_lgc == FALSE) = False"

(LKARequest_Cnt_T_lgc == FALSE) = False"

TS3.16"((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
(HapticRequest_Cnt_T_lgc==TRUE)||(APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
(HapticRequest_Cnt_T_lgc==TRUE)||(APAMaxVehSpd_Kph_f32) ||(LTREOTFound_Cnt_T_lgc == FALSE)) = False||
((TrqOvlSta_APAState_State_M_enum == APA_Active) && (PosTrajEnable_Cnt_T_lgc == TRUE)) = True"

TS3.17((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) = False||(APARecoverableFaults_Cnt_T_lgc==TRUE) = FALSE)) = FALSE)) = True!||(TREOTFOUND_Cnt_T_lgc == TRUE) = TRUE||TRUE = TRUE) = TRUE) = TRUE||TRUE = TRUE = TRUE) = TRUE||TRUE = TRUE) = TRUE||TRUE = TRUE = TRUE = TRUE) = TRUE||TRUE = TRUE = TRUE = TRUE) = TRUE||TRUE = TRUE = TRUE = TRUE = TRUE) = TRUE||TRUE = TRUE = TRUE
        ((HapticRequest_Cnt_T_lgc==TRUE)=true&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)) =True||(LmEOTFound_Cnt_T_lgc == FALSE)|
    ((HapticRequest_Cnt_T_lgc==TRUE)=true&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)) = True||(LrnEOTFound_Cnt_T_lgc == FALSE))  
TS3.18"(k_APAIncludeHaptic_Cnt_lgc == FALSE)=True  
((StandstillTime_Cnt_T_lgc==TRUE)=False && (APARequest_Cnt_T_lgc ==TRUE)=True&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))  
TS3.19((StandstillTime_Cnt_T_lgc==FALSE) = False|| (APARequest_Cnt_T_lgc ==FALSE)=True|| (ShiftLevRevTime_Cnt_T_lgc==FALSE))  
TS3.19((StandstillTime_Cnt_T_lgc==FALSE) = False|| (APARequest_Cnt_T_lgc ==FALSE)=True|| (ShiftLevRevTime_Cnt_T_lgc==FALSE))  
TS3.20((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32))|| (MaxSecureVehicleSpeed_Kph_T_f32 > k_ESCMaxVehSpd_Kph_f32)=True)  
TS3.21((ESCRequest_Cnt_T_lgc == FALSE) || (MinSecureVehicleSpeed_Kph_T_f32 > k_ESCMaxVehSpd_Kph_f32))=True  
TS3.23((CWFound_Cnt_T_lgc == FALSE) || (MinSecureVehicleSpeed_Kph_T_f32 > k_ESCMaxVehSpd_Kph_f32))=True  
TS3.24((MaxSecureVehicleSpeed_Kph_T_f32 < k_APAMaxVehSpd_Kph_f32)  
&& (APARequest_Cnt_T_lgc ==FALSE)  
&& (HapticRequest_Cnt_T_lgc==FALSE)  
&& (APARecoverableFaults_Cnt_T_lgc==FALSE)  
&& (LrnEOTFound_Cnt_T_lgc ==FALSE)  
=True  
&& (CHAPCOVERABLE)  
TRUE)=False)
```



```
((HapticRequest_Cnt_T_lgc==TRUE)&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)) ||(LrnEOTFound_Cnt_T_lgc == FALSE) )=False
    (LKARequest_Cnt_T_lgc == FALSE)=False"
TS3.39((StandstillTime_Cnt_T_lgc==FALSE)=true || (APARequest_Cnt_T_lgc ==FALSE)|| (ShiftLevRevTime_Cnt_T_lgc==FALSE))
TS3.40((StandstillTime_Cnt_T_lgc==FALSE)=False || (APARequest_Cnt_T_lgc ==FALSE)=True|| (ShiftLevRevTime_Cnt_T_lgc==FALSE))
    TS3.41((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 > k_LKAMinVehSpd_Kph_f32) || (LKARequest_Cnt_T_lgc == FALSE))=True
TS3.42((StandstillTime_Cnt_T_lgc==TRUE) && (APARequest_Cnt_T_lgc ==TRUE)&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))=False
  TS3.42((StandstillTime_Cnt_T_igc==TRUE) && (ĀPĀRequest_Cnt_T_lgc ==TRUE)&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))=False TS3.43((StandstillTime_Cnt_T_lgc==TRUE)=True && (APARequest_Cnt_T_lgc ==TRUE))
TS3.44((StandstillTime_Cnt_T_lgc==TRUE)=True && (APARequest_Cnt_T_lgc==TRUE))
TS3.44((StandstillTime_Cnt_T_lgc==TRUE)=True && (APARequest_Cnt_T_lgc ==TRUE))
TS3.45((StandstillTime_Cnt_T_lgc==TRUE)=False&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))
TS3.45((StandstillTime_Cnt_T_lgc==TRUE)=True && (APARequest_Cnt_T_lgc ==TRUE)=True&&(ShiftLevRevTime_Cnt_T_lgc==TRUE)=False)
TS3.46((StandstillTime_Cnt_T_lgc==FALSE) =False|| (APARequest_Cnt_T_lgc ==FALSE)=False||
(ShiftLevRevTime_Cnt_T_lgc==FALSE)=True)
TS3.47((StandstillTime_Cnt_T_lgc==FALSE)|| (APARequest_Cnt_T_lgc ==FALSE)|| (ShiftLevRevTime_Cnt_T_lgc==FALSE)|| (ShiftLevRevTime_Cnt_T_l
TS3.62((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 >
  TS3.62((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 > k_LKAMaxVehSpd_Kph_f32) || (LKARequest_Cnt_T_lgc == FALSE))=True
TS3.63((LKAInhibit_Cnt_T_lgc == TRUE) || (LKAFault_Cnt_T_lgc == TRUE) || (TrqOvISta_ESCState_State_M_enum == ESC_Active) || (TrqOvISta_ESCState_State_M_enum == ESC_TemporarilyLimited))=False
TS3.64((LKAInhibit_Cnt_T_lgc == TRUE)=False || (LKAFault_Cnt_T_lgc == TRUE)=False || (TrqOvISta_ESCState_State_M_enum == ESC_Active)=False || (TrqOvISta_ESCState_State_M_enum == ESC_TemporarilyLimited)=True)
TS3.65((LKAInhibit_Cnt_T_lgc == TRUE)=False || (LKAFault_Cnt_T_lgc == TRUE) = False|| (TrqOvISta_ESCState_State_M_enum == ESC_Active)=True || (TrqOvISta_ESCState_State_M_enum == ESC_Active)=True || (TrqOvISta_ESCState_State_M_enum == ESC_Active) = false || (TrqOvISta_ESCState_State_M_enum == ESC_A
   TS3.67(ESCRequest_Cnt_T_lgc == TRUE)=True
TS3.68(ESCRequest_Cnt_T_lgc == TRUE)=False
TS3.69(ESClsLimited_Cnt_T_lgc == TRUE)=False
    TS3.70(ESCRequest_Cnt_T_lgc == FALSE)=True
TS3.71"(ESCFault_Cnt_T_lgc == TRUE)=False
    (ESCIsLimited_Cnt_T_lgc == FALSE)=False"
    TS3.72(ESCIsLimited_Cnt_T_lgc == FALSE)=True
TS3.73(ESCRequest_Cnt_T_lgc == FALSE)=True
TS3.74"((TrqOvISta_APAState_State_M_enum == APA_Active) =True&& (PosTrajEnable_Cnt_T_lgc == TRUE )=False)
    ((TrqOvlSta_APAState_State_M_enum != APA_Active) && (PosTrajEnable_Cnt_T_lgc == TRUE ))=False" TS3.75(LKARequest_Cnt_T_lgc == TRUE)=False TS3.76"switch (TrqOvlSta_APAState_State_M_enum)
    Default:True"
     TS3.77"case (ESC_ManufacturingDisable):True
    case(APA_PermanentlyFailed):=True
     TS3.78"switch (TrqOvlSta_ESCState_State_M_enum)
    default:True
    switch (TrqOvISta HapticState State M enum)
    default:True"
  default: Irue*
TS3.79(TrgOvlSta_Hapticdur_Cnt_M_u16 > (k_HapticDuration_Sec_f32/D_2MS_SEC_F32))=>FALSE
TS3.80((StandstillTime_Cnt_T_lgc==FALSE)F || (APARequest_Cnt_T_lgc ==FALSE)T || (ShiftLevRevTime_Cnt_T_lgc==FALSE)F)=>TRUE
TS3.81((StandstillTime_Cnt_T_lgc==FALSE)F || (APARequest_Cnt_T_lgc ==FALSE)F || (ShiftLevRevTime_Cnt_T_lgc==FALSE)F || (APARequest_Cnt_T_lgc ==FALSE)F || (ShiftLevRevTime_Cnt_T_lgc==FALSE)F || (APARequest_Cnt_T_lgc ==FALSE)F || (ShiftLevRevTime_Cnt_T_lgc==FALSE)F || (ShiftLevRevTime_Cnt_T_lgc==FALS
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Test Step 3.1 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	4	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	
TrqOvlSta_HapticState_State_M_enum	1	

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Name	Input Value
TrqOvlSta Hapticdur Cnt M u16	5270
TrqOvlSta Haptictime Sec M f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrgOvlSta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	4
	3
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	1
	0
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	target TrgOvlSta Per1 SWARTrgtAngRequest HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviota_TrqOviota_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvlSta_Fer1_TrqOscEriable_Crit_igc
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_riqovista_reri_riqoscrieq_nz_isz
	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	200 04004
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-360.01001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360.980011
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1

target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value

target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value

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TrqOvlSta_Per1

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Name	Input Value		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	~
TrqOvISta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

1 ± 1

3.25 ± 0.0009765625

1

3.25

Test Step 3.2 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981

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Name	Input Value		
k_HapticReacttime_Sec_f32	1.23199999		
k LKAMaxVehSpd Kph f32	146.300003		
k LKAMinVehSpd Kph f32	100		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	30		
k_ShiftLevRevTime_Sec_f32	0.308999985		
k_StandstillThresh_Kph_f32	0		
k_StandstillTime_Sec_f32	0.308999985		
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventInterventionDetected_Cnt_Inst_ApadrvrInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInt$	target_TrqOvlSta_Per1_APADrvrIntervention	Detected_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe target_TrqOvlSta_Per1_CWFound_Cnt_lgc	9_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_102	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	ac	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc$	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_ target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvISta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	:	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1		
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000000 ± 1	*
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	
TraCulSta_ElapsedStandstill Time_Sec_M_f32	0	0	

0

TrqOvlSta_HapticState_State_M_enum

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Name	Actual Value	Expected Value	Result
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.850000024
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	15
TrqOvlSta_Haptictime_Sec_M_f32	75.6800003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85999966
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
<_APAIncludeHaptic_Cnt_lgc	1.
<pre>APAMaxHwTrqTime_Sec_f32</pre>	0.109999999
x_APAMaxHwTrq_HwNm_f32	0.100000001
APAMaxVehSpd Kph f32	0.200000003
K ESCMaxVehSpd Kph f32	10.1000004
K HapticAmplitude MtrNm f32	0
K_HapticDuration_Sec_f32	0.50999999
K HapticFreg Hz f32	3
K HapticReacttime Sec f32	0.159999996
C LKAMaxVehSpd Kph f32	0
K LKAMinVehSpd Kph f32	0.40000006
MaxHapticRegTime Sec f32	0.0149999997
SWARLimiter HwDeg f32	32.2000008
ShiftLevRevTime Sec f32	0.00100000005
StandstillThresh Kph f32	0
< StandstillTime Sec f32	0.00100000005
rarget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APARecoverableFaults Cnt Igc	target TrqOvISta Per1 APARecoverableFaults Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APAState State enum	target TrqOvlSta Per1 APAState State enum
arget Rte Inst Ap TrqOviSta.TrqOviSta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CCWPosition HwDeg f32	target TrqOvlSta Per1 CCWPosition HwDeg f32
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fet1_CGWFostion_Hwbeg_is2	target_TrqOvISta_Per1_CWFound_Cnt_lgc
arget_Rte_inst_Ap_rrqOviSta.frqOviSta_Per1_CwFound_Cnt_igc arget Rte_Inst_Ap_rrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
arget_Iste_mat_Ap_11q0viota.11q0viota_Fe11_0vvF0siti0i1_fiwDeg_132	target_11q0vi3ta_rei1_0vvr0sittot1_11wbeg_132

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lc	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lg	6	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAState State enum	target_TrqOvISta_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0 1		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-366.450012		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.040009		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target TrqOvlSta Per1 LKAFault Cnt lgc.value	0.209999993		
target_TrqOvlSta_Per1_LKAFatht_Ont_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	25.2999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	35.0999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-32.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008	0.460000008 ± 1	Y
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45	45 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	15	15 ± 1	
TrgOvlSta Haptictime Sec M f32	75.6800003	75.6800003 ± 1	·
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.83985996	-9.83985996 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005	1.20000005 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	*
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	Y
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	· ·
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 4	0	Y
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	
target TrqOvlSta_rer1_PosServEhable_Ont_igc.value target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	1.20000005	1.20000005 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	~

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Name	Actual Value	Expected Value	Result
target TrgOvlSta Per1 TrgOscFreg Hz f32.value	3	3 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Input Value
target_Rte_Inst_Ap_TrqOvlSta
2
1
7.11999989
132.839996
8
2
240
150
-9.25
0.00419999985
0
1
2
1
2.29999995
1
0
0.119999997
0.079999982
1.7999995
12.5
0.00600000005
0.0149999997
3.53999996
0.016000008
1.8999998
1.75
0.029999993
33.599985
0.0049999989
0.039999991
0.0049999989
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_TrqOvlSta_Per1_APAState_State_enum
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_TrqOvlSta_Per1_ESCState_State_enum
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target TrqOvlSta Per1 LKAInhibit Cnt Igc
target TrqOvISta Per1 LKARequest Cnt Igc
target_TrqOvlSta_Per1_LKAState_State_enum
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_TrqOvlSta_Per1_MiaxSecureVehicleSpeed_Kpir_i32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
t 2 1 7 1 8 2 2 1 - C C 1 2 1 C C C C 1 1 C C C C C C C C C

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Name	Input Value		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDe	eg_f3_target_TrqOvlSta_Per1_SWARTrgtAngRequest_	_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lg	c target_TrqOvlSta_Per1_ShiftLeverIsInReverse_0	Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	2	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target TrgOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target TrqOvlSta Per1 APARequest Cnt lgc.value	0		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-364.670013		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.070007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvISta_Per1_ESCNequest_Crit_gc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvISta_Per1_GMOSH_ESCMigEnable_Cnt_lgc.value	1		
	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	•		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.560000002		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.2000008		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value Ex	cpected Value	Result
TraOulSta ADAStata State M anum			ittesuit
TrqOvlSta_APAState_State_M_enum	3 3		√ V
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	3 3 3		
		± 1	~
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3		V
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3 0 0 0 1		*
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	t 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	t 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	t 1 0 ± 1 ± 1 25 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 ± 1 ± 1 25 ± 0.000009 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3 3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 109999999 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	3 3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	3 3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value	3 3 3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPstate_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 (109999999 ± 0.0009765625 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_EscState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADtate_State_enum.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1 £ 1 £ 1 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_EscState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 10000000005 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

3.53999996

Test Step 3.5 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	225.639999	

target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value

3.53999996 ± 0.0009765625

2015-02-04, 12:34:40+0530



Name	Input Value
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	684
TrqOvlSta_Haptictime_Sec_M_f32	1345
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	4.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.140000001
k_APAMaxHwTrq_HwNm_f32	0.23999995
k_APAMaxVehSpd_Kph_f32	5.4000001
k_ESCMaxVehSpd_Kph_f32	17.2999992
k_HapticAmplitude_MtrNm_f32	0.0179999992
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	4.55999994
k_HapticReacttime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.6999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.0599999987
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	36.4000015 0.0130000003
k StandstillThresh Kph f32	0.119999997
k_StandstillTime_Sec_f32	0.0130000003
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cnt	
target_Rte_Inst_Ap_TrqOviota_TrqOviota_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_state_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target TrqOvlSta Per1_APARequest Cnt_lgc.value	1
	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-362.890015
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-362.690015 1
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target TrqOvlSta Per1 CWPosition HwDeg f32.value	361.100006
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvIsta_Per1_ESCFault_Crit_lgc.value target_TrqOvIsta_Per1_ESCIsLimited_Crit_lgc.value	1
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvIsta_Per1_E3CRequest_Ont_Igc.value target_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
2 _ 12 2 2 7 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	

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Name	Input Value		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.25999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	308.519989 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	684	684 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973	-8.02999973 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0179999992	0.0179999992 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.55999994	4.55999994 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

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Test Step 3.6 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	1050
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.40000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.029999993
k_HapticDuration_Sec_f32	0.075000003

TrqOvlSta_Per1





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Name	Input Value		
k_HapticFreq_Hz_f32	5.57399988		
k_HapticReacttime_Sec_f32	0.0799999982		
k_LKAMaxVehSpd_Kph_f32	9.5		
k LKAMinVehSpd Kph f32	8.75		
k MaxHapticReqTime Sec f32	0.0900000036		
k SWARLimiter HwDeg f32	39.2000008		
k ShiftLevRevTime Sec f32	0.0209999997		
k_StandstillThresh_Kph_f32	0.200000003		
k_StandstillTime_Sec_f32	0.0209999997		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_		Detected Ont Ide	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc			
	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvISta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.130005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.96000004		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	·	Expected Value	Result
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum	Actual Value	4	Result
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	Actual Value 4 3	4 3	Result
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum	Actual Value	4	→

0

0

 $TrqOvlSta_ElapsedStandstillTime_Sec_M_f32$

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Name	Actual Value	Expected Value	Result
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991	2315.65991 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.029999993	0.029999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvISta
TrqOvISta APAState State M enum	2
TrgOvISta ESCState State M enum	2
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	2639.65991
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	659.880005
TrqOvlSta ElapsedStandstillTime Sec M f32	2670.19995
TrqOvlSta HapticState State M enum	0
TrgOvlSta Hapticdur Cnt M u16	6100
TrgOvlSta Haptictime Sec M f32	3285.03003
TrgOvlSta HwTorqueSV HwNm M Str.SV UIs f32	-5.59000015
rqOvISta HwTorqueSV HwNm M Str.K UIs f32	0.0174000002
FrgOvISta LKAFault Cnt M lgc	0
FrgOvISta LKAPermFault Cnt M Igc	0
FrgOvISta LKAPermFault Cnt M u08	2
TrgOvISta LKAState State M enum	1
TrgOvISta PosSrvoHwAngle HwDeg M f32	8.89999962
rqOvISta ReadytoPulse Cnt M Igc	0
APAIncludeHaptic Cnt Igc	1
APAMaxHwTrqTime Sec f32	0.180000007
APAMaxHwTrq HwNm f32	0.560000002
APAMaxVehSpd Kph f32	12.6000004
ESCMaxVehSpd Kph f32	26.8999996
HapticAmplitude MtrNm f32	0.041999994
HapticDuration Sec f32	0.104999997
HapticFreq Hz f32	6.60200024
HapticReacttime Sec f32	0.112000003
LKAMaxVehSpd Kph f32	13.3000002
LKAMinVehSpd Kph f32	12.25
MaxHapticReqTime Sec f32	0.119999997
SWARLimiter HwDeg f32	42
_ShiftLevRevTime_Sec_f32	0.0289999992
StandstillThresh Kph f32	0.280000001
	0.0289999992
_StandstillTime_Sec_f32 arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
<pre>larget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc larget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32</pre>	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lg</u> c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-379.329987		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.160004		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target Tranvista Part APAState State onum value			
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	0 3	3	✓
	0 3 0	3 0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	0 3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 3 0 3 1	3 0 3 1±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 3 0 3 1 0.170000002	3 0 3 1 ± 1 0.170000002 ± 0.0009765625	\rightarrow \right
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 3 0 3 1	3 0 3 1±1	\rightarrow \frac{1}{2}

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

est Step 3.8 (Repeat Count = 1)	Innut Value
	Input Value
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
	0
rqOvISta_ESCState_State_M_enum	4
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998 835.559998
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
	0 11150
rqOvlSta_Hapticdur_Cnt_M_u16	425564
rqOvlSta_Haptictime_Sec_M_f32 rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.3699989
rqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0218000002
rqOvISta_LKAFault_Cnt_M_lgc	1
	0
	2
rqOvISta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
	0
APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime_Sec_f32	0.200000003
APAMaxHwTrg HwNm f32	0.720000003
APAMaxVehSpd_Kph_f32	16.2000008
ESCMaxVehSpd_Kph_f32	31.7000008
HapticAmplitude_MtrNm_f32	0.0540000014
	0.135000005
- ,	7.63000011
HapticReacttime Sec f32	0.143999994
LKAMaxVehSpd_Kph_f32	17.1000004
LKAMinVehSpd Kph f32	15.75
MaxHapticReqTime_Sec_f32	0.150000006
SWARLimiter HwDeg f32	44.7999992
ShiftLevRevTime_Sec_f32	0.0370000005
StandstillThresh_Kph_f32	0.360000014
StandstillTime_Sec_f32	0.0370000005
riget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target TrqOvlSta Per1 APARequest Cnt lgc
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWPosition HwDeg f32	target TrgOvlSta Per1 CCWPosition HwDeg f32
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target TrqOvlSta Per1 ESCRequest Cnt Igc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
·	
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKAState State enum	target TrqOvISta Per1 LKAState State enum
riget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
5go	





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTraj	Enable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwD		rgtAngRequest HwDeg f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOsc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscl		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscI		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target TrqOvlSta Per1 APARequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-377.549988		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.190002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrgOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	0	0	Nesuit
	4	4	
TrqOvlSta_ESCState_State_M_enum			
TrqOvlOta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum			
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	· ·
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	V
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	*
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	V
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	Y
TrqOvlSta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	Y
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	Y
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
torget TraOviSta Bort TraOcoEros Hz f22 volus	7 63000011	7 63000011 ± 0 0000765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

7.63000011

Test Step 3.9 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	0	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6198.2998	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999	

 $target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value$

7.63000011 ± 0.0009765625

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Name	Input Value
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	6149.16016
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	16200
TrqOvISta_Haptictime_Sec_M_f32	5225.33984
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	13.3000002
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.219999999
k_APAMaxHwTrq_HwNm_f32	0.879999995
k APAMaxVehSpd Kph f32	19.7999992
_ ; _ ; _	36.5
k_ESCMaxVehSpd_Kph_f32	
k_HapticAmplitude_MtrNm_f32	0.0659999996
k_HapticDuration_Sec_f32	0.165000007
k_HapticFreq_Hz_f32	8.65799999
k_HapticReacttime_Sec_f32	0.175999999
k_LKAMaxVehSpd_Kph_f32	20.8999996
k_LKAMinVehSpd_Kph_f32	19.25
k_MaxHapticReqTime_Sec_f32	0.180000007
k_SWARLimiter_HwDeg_f32	47.599985
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	0.43999998
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvlSta Per1 ESCRequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target TrgOvlSta Per1 LKARequest Cnt lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAState State enum	target TrqOvlSta Per1 LKAState State enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MaxSecureVehicleSpeed Kph f32	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOviSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-375.769989
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.220001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target TrgOvlSta Per1 ESCRequest Cnt loc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1

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Name	Input Value		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.05999994		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	89		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-11.6000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999	1011.23999 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvISta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	16200	16200 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001	-3.1500001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0659999996	0.0659999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	8.65799999	8.65799999 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.10 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11536.2598
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1538.28003
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	11367.5996
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	31350
TrqOvlSta_Haptictime_Sec_M_f32	8134.31982
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.50999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0394000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	19.8999996
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.280000001
k_APAMaxHwTrq_HwNm_f32	1.36000001
k_APAMaxVehSpd_Kph_f32	30.6000004
k_ESCMaxVehSpd_Kph_f32	50.9000015
k_HapticAmplitude_MtrNm_f32	0.101999998

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TrqOvlSta_Per1

Name	Input Value		
k_HapticDuration_Sec_f32	0.254999995		
k_HapticFreq_Hz_f32	11.7419996		
k_HapticReacttime_Sec_f32	0.272000015		
k_LKAMaxVehSpd_Kph_f32	32.2999992		
k_LKAMinVehSpd_Kph_f32	29.75		
k_MaxHapticReqTime_Sec_f32	0.270000011		
k_SWARLimiter_HwDeg_f32	56		
k_ShiftLevRevTime_Sec_f32	0.0689999983		
k_StandstillThresh_Kph_f32	0.680000007		
k_StandstillTime_Sec_f32	0.0689999983		
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_RestrictionPerformance (Continuous Continuous Continuo Con$	target_TrqOvlSta_Per1_APADrvrIntervention	Detected_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	IC .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_le	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlOta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target TrqOvlSta Per1 APARecoverableFaults Cnt Igc.value	1		
target TrgOvlSta Per1 APARequest Cnt Igc.value	0		
target TrgOvlSta Per1 CCWFound Cnt Igc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-373.98999		
target TrgOvlSta Per1 CWFound Cnt lgc.value	1		
target TrgOvlSta Per1 CWPosition HwDeq f32.value	361.25		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-3.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	108.599998		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	118.400002		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-0.200000003		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TraOulSto ElancadShiftl auBouTime Sec M 522	0	0 ± 1	

0

0 ± 1

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Name	Actual Value	Expected Value	Result
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	31350	31350 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	8134.31982	8134.31982 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.50999999	0.50999999 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.101999998	0.101999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	11.7419996	11.7419996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

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Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13315.5801
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13107.0801
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	36400
TrqOvlSta_Haptictime_Sec_M_f32	9103.98047
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.73000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
<_APAIncludeHaptic_Cnt_lgc	1
<_APAMaxHwTrqTime_Sec_f32	0.300000012
<_APAMaxHwTrq_HwNm_f32	1.51999998
<_APAMaxVehSpd_Kph_f32	34.2000008
k_ESCMaxVehSpd_Kph_f32	55.7000008
<_HapticAmplitude_MtrNm_f32	0.114
K_HapticDuration_Sec_f32	0.284999996
<_HapticFreq_Hz_f32	12.7700005
x_HapticReacttime_Sec_f32	0.30399999
<pre>c_LKAMaxVehSpd_Kph_f32</pre>	36.0999985
<pre>c_LKAMinVehSpd_Kph_f32</pre>	33.25
<pre>MaxHapticReqTime_Sec_f32</pre>	0.300000012
x_SWARLimiter_HwDeg_f32	58.7999992
<pre>c_ShiftLevRevTime_Sec_f32</pre>	0.0769999996
x_StandstillThresh_Kph_f32	0.75999999
StandstillTime_Sec_f32	0.0769999996
rarget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected (Cnt target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cn	
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igo	
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt Igc	target TrqOvlSta Per1 APARequest Cnt Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWPosition HwDeg f32	target TrgOvlSta Per1 CCWPosition HwDeg f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target TrgOvlSta Per1 CWFound Cnt Igc

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Nama	Innut Value		
Name	Input Value	f22	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg	_132	
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCrault_Crit_igc target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Crit_igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lg	90	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg	-	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSHOscillateState State enun			
target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAState State enum	target TrgOvlSta Per1 LKAState State enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvISta_Per1_TrqOscFreq_Hz_f32	-	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvISta Per1 CCWPosition HwDeg f32.value	-372.209991		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.279999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.80000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvISta Per1 LKAInhibit Cnt Igc.value	0		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	118.400002		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f32.value	3.5999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	rtoduk
TrgOvISta_AFAState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996	1713.95996 ± 1	~
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrqOvISta_HapticState_State_M_enum	2	2	•
TrqOvISta_Hapticdur_Cnt_M_u16	36400	36400 ± 1	
TrqOvlSta Haptictime Sec M f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002	1.73000002 ± 0.000009	
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	0	0±1	•
TrqOvlSta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.18999998	0.189999998 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	_
tardet irdovista peri apastate state enum value			_
target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	~

0

0.189999998

0

1 ± 1

0.189999998 ± 0.0009765625

target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value

 $target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value$

target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.114	0.114 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target TrqOvlSta Per1 TrqOscFreq Hz f32.value	12.7700005	12.7700005 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

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lame	Input Value
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvISta_APAState_State_M_enum	0
rqOvlSta_ESCState_State_M_enum	3
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16874.2207
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	16586.0391
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	46500
rqOvlSta_Haptictime_Sec_M_f32	11043.2998
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
qOvlSta_LKAFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	2
qOvlSta_LKAState_State_M_enum	0
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	26.5
qOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	0.340000004
APAMaxHwTrq_HwNm_f32	1.84000003
APAMaxVehSpd_Kph_f32	41.4000015
_ESCMaxVehSpd_Kph_f32	65.3000031
_HapticAmplitude_MtrNm_f32	0.137999997
HapticDuration_Sec_f32	0.344999999
HapticFreq_Hz_f32	14.8260002
HapticReacttime_Sec_f32	0.368000001
LKAMaxVehSpd_Kph_f32	43.7000008
LKAMinVehSpd_Kph_f32	40.25
MaxHapticReqTime_Sec_f32	0.360000014
SWARLimiter_HwDeg_f32	34.4000015
ShiftLevRevTime_Sec_f32	0.0930000022
StandstillThresh_Kph_f32	0.920000017
StandstillTime Sec f32	0.0930000022
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_C	nt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target TrqOvlSta Per1 APAState State enum
get Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target TrgOvlSta Per1 CWPosition HwDeg f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_Igc	target TrqOvISta Per1 ESCIsLimited Cnt Igc
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCReguest Cnt lgc	target TrqOvlSta Per1 ESCRequest Cnt Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrqOvISta Per1 ESCState State enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_en	
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Ig	
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Ic	
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Ig	
get_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GiviOSH_ERAWingEnable_Cht_ig rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HapricRequest_Ont_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReq	uest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve	erse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-370.429993		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.309998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.4000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	138		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007	2065.32007 ± 1	•
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	

target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007	2065.32007 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	46500	46500 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008	4.17000008 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.137999997	0.137999997 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	14.8260002	14.8260002 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.13 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	0	
TrqOvlSta_ESCState_State_M_enum	0	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18653.5391	

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Name	Input Value
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2241
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18325.5195
TrqOvlSta_HapticState_State_M_enum	1
TrgOvISta Hapticdur Cnt M u16	51550
TrqOvlSta_Haptictime_Sec_M_f32	12012.96
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.057
TrqOvISta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_EKAF ettili auti_ctt_wi_ge TrqOvISta_LKAPermFault_Cnt_M_u08	1
TrgOvISta_EKAr ettili aut_ctit_iw_uob TrgOvISta_EKAr ettili aut_ctit_iw_uob	0
·	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.360000014
k_APAMaxHwTrq_HwNm_f32	2
k_APAMaxVehSpd_Kph_f32	45
k_ESCMaxVehSpd_Kph_f32	70.0999985
k_HapticAmplitude_MtrNm_f32	0.150000006
k_HapticDuration_Sec_f32	0.375
k_HapticFreq_Hz_f32	15.8540001
k_HapticReacttime_Sec_f32	0.40000006
k_LKAMaxVehSpd_Kph_f32	47.5
k_LKAMinVehSpd_Kph_f32	43.75
k_MaxHapticReqTime_Sec_f32	0.389999986
k_SWARLimiter_HwDeg_f32	37.2000008
k_ShiftLevRevTime_Sec_f32	0.101000004
k_StandstillThresh_Kph_f32	1
k_StandstillTime_Sec_f32	0.101000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target TrgOvlSta Per1 ESCFault Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFrag_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.649994
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.339996
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1

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Name	Input Value		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-2.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	51550	51550 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.38999987	5.38999987 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.8540001	15.8540001 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	-

Test Step 3.14 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	2	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31108.7793	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3470.76001	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	30501.8809	
TrqOvlSta_HapticState_State_M_enum	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	50530	
TrqOvlSta_Haptictime_Sec_M_f32	35000.5391	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	44.099985	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	
k_APAIncludeHaptic_Cnt_lgc	0	
k_APAMaxHwTrqTime_Sec_f32	0.5	
k_APAMaxHwTrq_HwNm_f32	3.11999989	
k_APAMaxVehSpd_Kph_f32	70.1999969	
k_ESCMaxVehSpd_Kph_f32	103.699997	

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Name	Input Value		
k_HapticAmplitude_MtrNm_f32	0.233999997		
k_HapticDuration_Sec_f32	0.584999979		
k_HapticFreq_Hz_f32	23.0499992		
k HapticReacttime Sec f32	0.624000013		
k LKAMaxVehSpd Kph f32	74.0999985		
k LKAMinVehSpd Kph f32	68.25		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	56.7999992		
k_ShiftLevRevTime_Sec_f32	0.157000005		
k StandstillThresh Kph f32	1.5599994		
k_StandstillTime_Sec_f32	0.157000005		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_		Detected Cnt lac	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrgOvISta Per1 APAState State enu		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_Igc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	902	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target TrgOvlSta Per1 CWPosition HwDeg	f32	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_,	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_igc	nc .	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Crit_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Crit_igc	target TrqOvISta Per1 ESCRequest Cnt Ig		
target_Rte_inst_Ap_rrqOviSta.rrqOviSta_Perr_ESCRequest_Crit_igc target_Rte_inst_Ap_rrqOviSta.rrqOviSta_Perr_ESCRequest_Crit_igc			
target_Rte_inst_Ap_irqovista.irqovista_Peri_EscState_State_enum target_Rte_inst_Ap_Trqovista.Trqovista_Peri_EscState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enutarget_TrqOvlSta_Per1_GMOSHOscillateSta		
target Rte Inst Ap Trqovista.Trqovista Peri GMOSHoscillatestate state enun target_Rte_Inst_Ap_Trqovista.Trqovista_Peri_GMosh_APAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSHOscillateSta target_TrqOviSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-366.869995		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.369995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	216.399994		
target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	226.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvISta_ESCState_State_M_enum	4	4	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	_

0

0 ± 1

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Name	Actual Value	Expected Value	Result
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	50530	50530 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	1.21000004 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.233999997	0.233999997 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	23.0499992	23.0499992 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrgOvISta APAState State M enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	220386
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	30810
TrgOvlSta ElapsedStandstillTime Sec M f32	44754
FrgOvISta HapticState State M enum	0
FrgOvISta Hapticdur Cnt M u16	20942
FrgOvISta Haptictime Sec M f32	51515
FrgOvISta HwTorqueSV HwNm M Str.SV UIs f32	0
FrgOvISta HwTorqueSV HwNm M Str.K UIs f32	0.221000001
FrqOvlSta_LKAFault_Cnt_M_lgc	0
FrgOvISta LKAPermFault Cnt M lgc	0
FrgOvISta LKAPermFault Cnt M u08	1
FrgOvISta LKAState State M enum	2
FrgOvlSta PosSrvoHwAngle HwDeg M f32	57.2999992
FrgOvISta ReadytoPulse Cnt M Igc	0
APAIncludeHaptic Cnt Igc	1
<pre></pre>	0.620000005
APAMaxHwTrq HwNm f32	4.71999979
APAMaxVehSpd Kph f32	106.199997
C ESCMaxVehSpd Kph f32	132.5
HapticAmplitude MtrNm f32	0.354000002
HapticDuration Sec f32	0.88499999
HapticFreq Hz f32	29.2180004
HapticReacttime Sec f32	0.94400006
C LKAMaxVehSpd Kph f32	112.099998
LKAMinVehSpd Kph f32	103.25
MaxHapticRegTime Sec f32	0.89999976
c_SWARLimiter_HwDeg_f32	34
ShiftLevRevTime Sec f32	0.237000003
StandstillThresh Kph f32	2.3599999
StandstillTime Sec f32	0.237000003
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APADrvrInterventionDetected Cnt	target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target TrqOvlSta Per1 CCWPosition HwDeg f32

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Input Value

TrqOvlSta_Per1

Name



Name	Input value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_le	ac	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvlSta Per1 ESCRequest Cnt Ig		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_end		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn	lable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H	wDea f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	v -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-365.089996		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.399994		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1 4.5		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1 4.5 0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1 4.5 0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1 4.5 0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1 4.5 0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1 4.5 0 0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1 4.5 0 0 1 275.200012 285		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1 4.5 0 0 1 275.200012 285		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1 4.5 0 0 1 275.200012 285 1 0.109999999		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Lnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1 4.5 0 0 1 275.200012 285 1 0.109999999		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Lnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038	Expected Value	Result
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038	Expected Value	Result
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3	· ·	~
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value	3 3	Result
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0	3 3 0±1	· · ·
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810	3 3 0±1 30810±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0	3 3 0±1 30810±1 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0	3 3 0±1 30810±1 0	***
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0	3 3 0±1 30810±1 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0	3 3 0±1 30810±1 0	***
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SHiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942	3 3 0±1 30810±1 0 0 20942±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum	1 4.5 0 0 1 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticHum_Sec_M_f32 TrqOvISta_HapticHum_Sec_M_f32 TrqOvISta_HapticHum_Sec_M_f32 TrqOvISta_HapticHum_Sec_M_f32 TrqOvISta_HapticHum_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAFault_Cnt_M_Igc	1 4.5 0 0 1 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EscState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_u16 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EscState_Menum TrqOvISta_EscState_State_Menum TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_Menum TrqOvISta_HapticState_State_Menum TrqOvISta_HapticState_State_Menum TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_Menum	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 0 30810 0 0 20942 51515 0.994499981 0 0 1	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1 2	***************************************
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EscState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_u16 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EscState_Menum TrqOvISta_EscState_State_Menum TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_Menum TrqOvISta_HapticState_State_Menum TrqOvISta_HapticState_State_Menum TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_Menum	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 0 30810 0 0 20942 51515 0.994499981 0 0 1	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1 2	***************************************
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Ont_M_Igc TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc TrqOvISta_ReadytoPulse_Cnt_M_Igc TrqOvISta_ReadytoPulse_Cnt_M_Igc	1 4.5 0 0 1 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1 2 0.1099999999±0.0009765625	***************************************
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticOtdur_Cnt_M_u16 TrqOvISta_Hapticotdur_Cnt_M_u16 TrqOvISta_Hapticotime_Sec_M_f32 TrqOvISta_Hapticotime_Sec_M_f32 TrqOvISta_Hapticotime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1 4.5 0 0 1 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999 0 0	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1 2 0.109999999±0.0009765625 0±1 0±1	***************************************
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_EsCState_State_Menum TrqOvISta_ElapsedShandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticOtdr_Cnt_M_u16 TrqOvISta_HapticOtdr_Cnt_M_u16 TrqOvISta_HapticOtdr_Cnt_M_u16 TrqOvISta_HapticOtdr_Cnt_M_u16 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAState_State_Menum TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999 0 0 1 2 0.1099999999 0 0 3	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1 2 0.109999999±0.0009765625 0±1 0±1 3	***************************************
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_Menum TrqOvISta_EsCState_State_Menum TrqOvISta_ElapsedShandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_Menum TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_LKAPault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999 0 0 1 2 0.1099999999 0 0 3 3 3	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1 2 0.1099999999±0.0009765625 0±1 0±1 3 3	***************************************
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticOtt_M_u16 TrqOvISta_HapticOtt_M_u16 TrqOvISta_HapticOtt_M_u16 TrqOvISta_HapticItime_Sec_M_f32 TrqOvISta_HapticItime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_u16 TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAState_State_M_enum TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999 0 0 3 3 3 0	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1 2 0.109999999±0.0009765625 0±1 0±1 3 3 0	***************************************
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ARAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999 0 0 1 2 2 0.1099999999 0 0 3 3 3 0 2	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 1±1 2 0.109999999±0.0009765625 0±1 0±1 3 3 0 2	***************************************
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticOtt_M_u16 TrqOvISta_HapticOtt_M_u16 TrqOvISta_HapticOtt_M_u16 TrqOvISta_HapticItime_Sec_M_f32 TrqOvISta_HapticItime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_u16 TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAState_State_M_enum TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	1 4.5 0 0 1 275.200012 285 1 0.109999999 -9.10000038 1 Actual Value 3 3 0 30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999 0 0 3 3 3 0	3 3 0±1 30810±1 0 0 20942±1 51515±1 0.994499981±0.0000009 0±1 0±1 1±1 2 0.109999999±0.0009765625 0±1 0±1 3 3 0	***************************************

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.354000002	0.354000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	29.2180004	29.2180004 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.16 (Repeat Count = 1)	Innut Value
	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
,	2
	1
	1952499
	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
FrqOvISta_LKAState_State_M_enum	0
FrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k LKAMinVehSpd Kph f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	49
k_ShiftLevRevTime_Sec_f32	0.0370000005
	4.4000001
	0.0370000005
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvlSta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target TrqOvISta Per1 CWFound Cnt Igc
· · · · ·	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
	target TrqOvISta Per1 ESCRequest Cnt Igc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target_riqovista_Per1_GMOSH_EKAMIgErlable_Cht_igc target_TrqoviSta_Per1_HapticRequest_Cht_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
	target_TrqOvISta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cr	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg	f3. target_TrqOvlSta_Per1_SWARTrgtAngRed	quest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve	erse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrN	m f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f:	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-363.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.429993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	· ·
TrqOvISta_ESCState_State_M_enum	3	3	V
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422 0	84736.2422 ± 1 0	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	1	1	-
TrqOvISta_HapticState_State_M_enum	33000	33000 ± 1	
TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta Haptictime Sec M f32	0	0 ± 1	-
	-4.80000019	-4.80000019 ± 0.000009	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOviSta_LKAFault_Cnt_M_lgc	1	1±1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	- J
TrqOvISta_LKAState_State_M_enum	0	0	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	38.2000008	38.2000008 ± 0.0009765625	
TrqOvISta ReadytoPulse Cnt M lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	·
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	
target TrgOvISta Per1 ESCState State enum.value	3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_enum.value	1	1	
target TrqOvISta Per1 LKAState State enum.value	0	0	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value		38.2000008 ± 0.0009765625	✓
	38.2000008		
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	38.2000008 0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value			· ·
	0.660000026	0.660000026 ± 0.0009765625	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.17 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3

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Name	Input Value
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2020425
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	86335.6016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	391692
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	31000
TrqOvlSta_Haptictime_Sec_M_f32	210780
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.232999995
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	174.5
k_HapticAmplitude_MtrNm_f32	0.671999991
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	55.9459991
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	220
k LKAMinVehSpd Kph f32	196
k MaxHapticReqTime Sec f32	0.135000005
k_SWARLimiter_HwDeg_f32	49.5999985
k_ShiftLevRevTime_Sec_f32	0.0329999998
k StandstillThresh Kph f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOviSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.529999
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.459991
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1

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TrqOviSta_Per i			CILAI
Name	Input Value		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	210780	210780 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.18 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997

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Name	Input Value		
k_ESCMaxVehSpd_Kph_f32	15.6000004		
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0 1.71000004		
k_HapticFreq_Hz_f32	56.973999		
k_HapticReacttime_Sec_f32	1.824		
k_LKAMaxVehSpd_Kph_f32	216.600006		
k_LKAMinVehSpd_Kph_f32	199.5		
k_MaxHapticReqTime_Sec_f32	0.20999993		
k_SWARLimiter_HwDeg_f32	50.2000008		
k_ShiftLevRevTime_Sec_f32	0.0529999994		
k_StandstillThresh_Kph_f32	4.55999994		
k_StandstillTime_Sec_f32	0.0529999994		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverable target_TrqOvlSta_Per1_APARecoverableFau		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target TrqOvISta_Per1_APARequest Cnt Ig		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEntarget_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f:		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	•	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 SWARTrgtAngRequest HwDeg f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-389.75		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_132.value	1 361.48999		
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015 1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvIsta_Per1_PosTrajnwArigie_nwDeg_i32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.10000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	V
			_

 ${\sf TrqOvlSta_ESCState_State_M_enum}$





Name	Actual Value	Expected Value	Result
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.19 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	2224203
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	91133.6797
TrqOvlSta ElapsedStandstillTime Sec M f32	430968
TrqOvlSta HapticState State M enum	2
TrqOvlSta Hapticdur Cnt M u16	45000
TrqOvlSta Haptictime Sec M f32	228811.703
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.70000005
TrqOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.507000029
TrqOvlSta LKAFault Cnt M lgc	0
TrqOvISta LKAPermFault Cnt M Igc	0
TrqOvISta LKAPermFault Cnt M u08	2
TrqOvISta LKAState State M enum	2
TrqOvlSta PosSrvoHwAngle HwDeg M f32	49.4000015
TrqOvlSta ReadytoPulse Cnt M lgc	0
k APAIncludeHaptic Cnt Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k APAMaxHwTrg HwNm f32	9.27999973
k APAMaxVehSpd Kph f32	208.800003
k ESCMaxVehSpd Kph f32	30.399996
k_HapticAmplitude_MtrNm_f32	0.460000008
k HapticDuration Sec f32	1.7400001
k_HapticFreq_Hz_f32	59.0299988
k HapticReacttime Sec f32	1.85599995
k LKAMaxVehSpd Kph f32	20
k LKAMinVehSpd Kph f32	203
k MaxHapticRegTime Sec f32	0.239999995
k SWARLimiter HwDeg f32	21.3999996
k ShiftLevRevTime Sec f32	0.0610000007
k StandstillThresh Kph f32	4.63999987
k_StandstillTime_Sec_f32	0.0610000007
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cnt	target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc

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Input Value



Name



name	input value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_F	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_Hw	/Deg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_0		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_C		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_el	nun target_TrqOvlSta_Per1_GMOSHOscillat	eState_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_Inst_Ap_TrqOvlSta_Per1_GMOSH_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_ApAMfgEnable_Cnt_A$	gc target_TrqOvlSta_Per1_GMOSH_APAN	lfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_I	gc target_TrqOvlSta_Per1_GMOSH_ESCM	fgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lg	target TrqOvISta Per1 GMOSH LKAM	fgEnable Cnt lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HwTorque HwNm f32	target_TrqOvlSta_Per1_HwTorque_HwN		
		_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_C		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State	e_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_	f32 target_TrqOvlSta_Per1_MaxSecureVehi	icleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f	32 target_TrqOvlSta_Per1_MinSecureVehic	cleSpeed_Kph_f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target_TrqOvlSta_Per1_PosServEnable		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrgOvISta Per1 PosSrvoHwAng		
	0 = 1 = = 0	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngl		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg	_f3 target_TrqOvlSta_Per1_SWARTrgtAngF	Request_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverIsInRe	everse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_Mt	rNm_f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_	Cnt lgc	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrgOvlSta Per1 TrgOscFreq Hz		
target TrgOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-387.970001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.519989		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrgOvlSta Per1 HwTorque HwNm f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	103		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value			
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	1	1	· •
TrqOvlSta_ESCState_State_M_enum	4	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	,
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	
TrqOvlOta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc			-
TIGOTION LIVE MILL OIL IVI INC		1 ± 1	
	1	1 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	1 1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 1 3	1 ± 1 3 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1 1 3 3	1 ± 1 3 ± 1 3	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 1 3	1 ± 1 3 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1 1 3 3	1 ± 1 3 ± 1 3	•

0

4

0

3

0 ± 1

4

0

3

target_TrqOvlSta_Per1_APAState_State_enum.value
target_TrqOvlSta_Per1_ESCState_State_enum.value

target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value

target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.18999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.20 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta APAState State M enum	0
TrqOvISta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
	0
TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cat_M_ut6	29000
TrqOvlSta_Hapticdur_Cnt_M_u16	198760.641
TrqOvlSta_Haptictime_Sec_M_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.5 0.419
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	1
TrqOvlSta_LKAFault_Cnt_M_lgc	
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	23.2000008
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results and the property of the property o$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
gooopqo.nota_nota_nota_nota_natorialiotopeeu_rpn_loz	

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N	Immed Walter		
Name target Dta Inst An TraQuiSta TraQuiSta Dord MinSeaura\/ahioleSecond Keh #22	Input Value target TrqOvlSta Per1 MinSecureVehicleSp	and Kah f22	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	- -	
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlOta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlOta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-386.190002		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.549988		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.6999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	83136.8828	83136.8828 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrgOvlSta HapticState State M enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	-
TrgOvlSta Haptictime Sec M f32	198760.641	198760.641 ± 1	•
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.449800014	0.449800014 ± 0.0000009	-
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrgOvlSta LKAPermFault Cnt M lgc	0	0 ± 1	
TrgOvISta LKAPermFault Cnt M u08	0	0 ± 1	
TrqOvISta_LKAState_State_M_enum	2	2	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	-
TrgOvISta ReadytoPulse Cnt M lgc	1	1 ± 1	-
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	-
target TrqOvlSta Per1 APAState State enum.value	3	3	
target TrqOvlSta Per1 ESCState State enum.value	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value	2	2	
target_IrqOviSta_Peri_LKAState_state_enum.value target_TrqOviSta_Peri_PosServEnable_Cnt_lgc.value			
	0.150000006	1 ± 1	V
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.21 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2





TroUNSE_EspectAblaseNaTerine_Sec_M_102	Name	Input Value
TigOUSSE_BisecedShuffwarFire_Sec_M_102 TigOUSSE_BisecedShuffwarFire_Sec_M_102 TigOUSSE_BisecedShuffwarFire_Sec_M_102 TigOUSSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_INSECEM_INSE_InsecedShuffwarFire_Sec_M_102 TigOUSSE_INSECEM_INSE_InsecedShuffwarFire_ShuffwarFire_Sec_M_102 TigOUSSE_INSECEM_INSE_InsecedShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_ShuffwarFire_Sh		·
TROUNDE Laborational Time See, M. 122 TROUNDE Laborational Time See, M. 122 TROUNDE Laboration Crit. Murils S200 TROUNDE Laboration Crit. Murils S200 TROUNDE Laboration See, M. 122 TROUNDE LABOration Crit. Murils TROUNDE LABOR		
Trq-OKSIa Haptochine See, M. 192 Trq-OKSIa HAPToquasiy, Hahrim M. Sri X, Us. 192 Trq-OKSIa LAXBernifaul, Cri. M. Uge Trq-OKSIA Trq-OKSIa Trq-OKSIa LAXBernifaul, Cri. M. Uge Trq-OKSIA LAXBernifaul, Cri. M. Uge Trq-OKSIA LAXBernifaul, Cri. M. Uge Trq-OKSIA Trq-OKSIA Trq-OKSIa Laxbernifaul, Cri. M. Uge Trq-OKSIA Trq-OKSIA Trq-OKSIA Trq-OKSIA Laxbernifaul, Cri. M. Uge Trq-OKSIA LAXBernifaul, Cri. M. Uge Trq-OKSIA Trq-OKSIA Trq-OKSIA Trq-OKSIA Laxbernifaul, Cri. Uge Trq-OKSIA Trq-OKSIA Trq-OKSIA Trq-OKSIA Prq I APARoviniterventionDetected, Cri. Uge Trq-OKSIA Trq-OKSIA Trq-OKSIA Trq-OKSIA Prq I APARoviniterventionDetected, Cri. Uge Trq-OKSIA Trq-OKSIA Trq-OKSIA Prq I APARoviniterventionDetected, Cri. Uge Trq-OKSIA Trq-OKSIA Trq-OKSIA Prq I APARoviniterventio		
TROUNDS Inspiration, Sec. M. 12 TROUNDS INSPIRATION, M. 12 TROUNDS INSPIRATIO	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	51515
Trackliss Ambrowers Ambrown Str. Str. Uls. 512 1,00000002 1,00000002 1,00000002 1,00000002 1,00000002 1,00000002 1,00000002 1,00000002 1,000000002 1,000000001 1,000000002 1,000000001 1,0000000001 1,0000000001 1,0000000001 1,0000000001 1,0000000000	TrqOvISta_HapticState_State_M_enum	2
TrqOSSIs InVTORUSEY J-HAVIN M. SEX ULIS 22 17QOSSIs LIAKFORM COLD M. IDS 17QOSSIS LIAKFORM COLD		
TrqONSIs LKAPemFaul Cn I. M. jos 1 TrqONSIs LKAPemFaul Cn I. M. jos 1 TrqONSIs LKAPemFaul Cn I. M. jos 1 TrqONSIs LKAPemFaul Cn I. M. jos 2 TrqONSIs LKAPemFaul Cn I. M. jos 2 TrqONSIs LKAPemFaul Cn I. M. jos 2 TrqONSIs LKAPemFaul Cn I. M. jos 3 TrqONSIs LKAPemFaul Cn I. M. jos 2 TrqONSIs LKAPEMPAUR J LKAPEMPAUR J S TrqONSIS LASHOPHAKING HAGE M S TrqONSIS LASHOPHAKING LKAPEMPAUR J S TrqONSIS L		
TroONISIa LIANFAUL COIL M. Igo		
TrQO/SIS LIA/ASISIE_MIR JNSNONHAVINGE JNSNS JNSN		
TrqOviSta_LKAState, Siate_M_enum		
TrgO/Sta_Ready/Pulse_Cnt_Mige		2
TRONSER_ReadynDrules_Cnt_Uge	TrqOvlSta_LKAState_State_M_enum	2
k_APAMashtvTrqTime_Sec_132 k_APAMashtvTrqTime_Sec_132 k_APAMashtvTrqTime_Sec_132 k_APAMashtvTrqTime_Sec_132 k_APAMashtvTrqTime_Sec_132 k_APAMashtvTrqTime_Sec_132 k_APAMashtvTrqTime_Sec_132 k_APAMashtvTrqTime_Sec_132 k_APAMashtvTrqTime_Sec_132 l_APAMashtvTrqTime_Sec_132 l_APAMashtvTrqTime_Se	TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	32
K_APAMasHwTrq_HwNm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_HwNm_I32 K_APAMasHwTrq_HwNm_I32 K_APAMasHwTrq_HwNm_I32 K_APAMasHwTrq_HwNm_I32 K_APAMasHwTrq_HwNm_I32 K_APAMasHwTrq_HwNm_I32 K_APAMasHwTrq_HwNm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_Hwm_I32 K_APAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAPAMasHwTrq_KAP	1	
K_APAMazHeNTRL Hawkin, 132 17,000000000000000000000000000000000000		
R. ESCMaxVehSpd, Kpn Ji		
R. ESCMaxVehSpd Kpn J32		
k. Haptic/brarlon. Sec. § 32	_ , _ , _	
k. HapticPountion. Sec, f32 0.380000014 k. HapticPreq. Hz, f32 0.783999979 k. LKAMinVehSpd, Kph, f32 9.30999985 k. LKAMinVehSpd, Kph, f32 85.75 k. MaxHapticReqTime, Sec, f32 0.870000005 k. SWARLImiter HwDeg, f32 38.2000008 k. ShiftLevRevTime, Sec, f32 0.141000003 k. StandstillTimes M, Kph, f32 1.32000005 k. StandstillTimes Sec, f32 0.130000001 k. StandstillTime Sec, f32 0.130000001 k. StandstillTime Sec, f32 0.130000005 k. StandstillTime Sec, f32 0.130000001 k. StandstillTime Sec, f32 0.130000001 target, Rie, Inst, Ap, TrqOVISta, TrqOVISta, Perl, APAROReverserableFaults, Cnt, Igo target, TrqOVISta, Perl, APAROreverserableFaults, Cnt, Igo target, Rie, Inst, Ap, TrqOVISta, TrqOVISta, Perl, APARGeuest, Clt, Igo target, TrqOVISta, TrqOVISta, TrqOVISta, Perl, CWFound, Cnt, Igo target, TrqOVISta, Perl, APARGeverserableFaults, Cnt, Igo target, Rie, Inst, Ap, TrqOVISta, TrqOVISta, Perl, CWFound, Cnt, Igo target, TrqOVISta, Perl, APARGeverserableFaults, Cnt, Igo target, TrqOVISta, Perl, CWFound, Cnt, Igo target, Rie, Inst, Ap, TrqOVISta, TrqOVISta, Perl, CWFound, Cnt, Igo target, TrqOVISta, Perl, CWFound, Cnt, Igo </td <td></td> <td></td>		
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k_KMAHpicReqTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_StandstillTrines_Nec_f32 k_StandstillTrines_Nec_f32 target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARoviriterventionDetected_Cnt_target_Rel_inst_Ap_TrqOvIsta_Pert_APARovi		
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target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWPound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWPound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWPound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_OSCIIIaleState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARfgenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARfgenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARfault_Cnt_lgc target_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_state_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSHoSilaleState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSHoSilaleState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARFault_Cnt_lgc target_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_TrqOvISta_Per1_LKARfault_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCISLImited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCISLImited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCISLImited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCISLImited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCISLImited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCISLImited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOSHOSHIJateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOSHIJateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_PAMMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_PAMMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOv		
target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_BSCMGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_ESCIstate_state_enum target_Rte_Inst_Ap_TrqOvlSta_Rer1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Rer1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Rer1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHO		
target_Rte_Inst_Ap_TrqOvISta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_TrqOvISta_Per1_LKARequest_Cnt_Igc target_TrqOvISta_Per1_LKARequest_Cnt_Igc target_TrqOvISta_Per1_LKARequest_Cnt_Igc target_TrqOvISta_Per1_LKARequest_Cnt_Igc target_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum		· - ·
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc \\ target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc \\ target_TrqOvlSta_Per1_ShiftLeverls$		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonPacoverableEquits_Crit_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARecoverable=raulis_Ctit_igc.value		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -384.410004		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 361.579987		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0	target_IrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0





Name	Input Value		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.4000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	3	3	•
TrqOvlSta_ESCState_State_M_enum	0	0	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51515	51515 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	33020	33020 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	51515	51515 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002	1.60000002 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.449999988	0.449999988 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target TrqOvlSta Per1 TrqOscFreq Hz f32.value	36.4000015	36.4000015 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.22 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	54520
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	54520
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	54520
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	34002
TrqOvlSta_Haptictime_Sec_M_f32	54520
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	33.099985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.925999999
k_APAMaxHwTrq_HwNm_f32	3.83999991





		(
Name	Input Value		
k_APAMaxVehSpd_Kph_f32	64.8000031		
k_ESCMaxVehSpd_Kph_f32	180		
k_HapticAmplitude_MtrNm_f32	0.469999999		
k_HapticDuration_Sec_f32	0.375		
k_HapticFreq_Hz_f32	37.0999985		
k_HapticReacttime_Sec_f32	0.800000012		
k_LKAMaxVehSpd_Kph_f32	95		
k_LKAMinVehSpd_Kph_f32	87.5		
k_MaxHapticReqTime_Sec_f32	0.88499999		
k_SWARLimiter_HwDeg_f32	38.5		
k_ShiftLevRevTime_Sec_f32	0.144999996		
k_StandstillThresh_Kph_f32	1.36000001		
k_StandstillTime_Sec_f32	0.136999995		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrIntervention	Detected_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lq	target_TrqOvlSta_Per1_APANonRecoverable	Faults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFau	lts_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	able_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	0	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	/Deg_f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.630005		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.609985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.70000005		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value			
	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0 167.399994		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0 167.399994 152.699997		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0 167.399994 152.699997 0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0 167.399994 152.699997 0 0.170000002		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0 167.399994 152.699997 0 0.170000002 13.1000004		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0 167.399994 152.699997 0 0.170000002		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0 167.399994 152.699997 0 0.170000002 13.1000004	Expected Value	Res



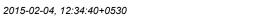


Name	Actual Value	Expected Value	Result
TrqOvlSta_ESCState_State_M_enum	0	0	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	34002	34002 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.75480008	1.75479996 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	33.0999985	33.0999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	33.0999985	33.0999985 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.469999999	0.469999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	37.0999985	37.0999985 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrqOvlSta
TrgOvlSta APAState State M enum	4
TrgOvISta ESCState State M enum	4
TrqOvlSta_E3c5tate_5tate_w_endin TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	5450
TrgOvlSta ElapsedStandstillTime Sec M f32	0
TrgOvISta HapticState State M enum	1
· - ·	
TrqOvlSta_Hapticdur_Cnt_M_u16	5270 27475.5293
TrqOvlSta_Haptictime_Sec_M_f32	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt Igc	target TrqOvlSta Per1 APARequest Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_state_enum

TrqOvlSta_Per1





<u> </u>			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_Hw	0_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwD		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_6target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_6target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State_State	target_TrqOvISta_Per1_ESCState_State_		
target Rte Inst Ap TrqOvISta.TrqOvISta Perl GMOSH APAMfgEnable Cnt			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH_AFAINIGETIABLE_CIT_			
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_C		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 HwTorque HwNm f32	target TrqOvISta Per1 HwTorque HwNn		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc	target_TrqOvISta_Per1_LKAFault_Cnt_lgo	_	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAInhibit Cnt Igc	target TrqOvISta Per1 LKAInhibit Cnt lo		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosServEnable Cnt Igc	target_TrqOvISta_Per1_PosServEnable_0		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvISta_Per1_PosTrajEnable_C		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 SWARTrgtAngRequest HwDe		·	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igo			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrN		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_C	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_t		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target TrgOvlSta Per1 APARecoverableFaults Cnt Igc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-380.850006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.640015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvISta_ESCState_State_M_enum	4	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	
TrqOvlSta_HapticState_State_M_enum	1	1	
TrqOvISta Hapticdur Cnt M u16	5270	5270 ± 1	
TrqOvISta Haptictime Sec M f32	0	0 ± 1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.69000006	5.69000006 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1±1	
TrqOvISta_LKAPermFault_Cnt_M_u08	4	4 ± 1	
TrqOvISta_LKAState_State_M_enum	3	3	
TrqOvISta PosSrvoHwAngle HwDeg M f32	1	1 ± 0.0009765625	
TraCulSta BoodutaBulas Cat M Iga		0 . 4	

0

0

4

4

0 ± 1

0 ± 1

4

4

target_TrqOvlSta_Per1_APAState_State_enum.value

target_TrqOvlSta_Per1_ESCState_State_enum.value

target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value

 $target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value$

TrqOvlSta_ReadytoPulse_Cnt_M_lgc

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.24 (Repeat Count = 1) Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
FrqOvlSta_LKAFault_Cnt_M_lgc	1
FrqOvISta_LKAPermFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
FrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
FrqOvISta_ReadytoPulse_Cnt_M_lgc	0
<pre>c_APAIncludeHaptic_Cnt_lgc</pre>	1
<pre><pre><pre></pre><pre></pre><pre><pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre><pre></pre><pre><pre><pre><pre><pre><pre><pre><pre><pre><pre><pre><pre><pre><td>0.20000003</td></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre>	0.20000003
	0.720000029
<_APAMaxHwTrq_HwNm_f32 <_APAMaxVohSnd_Knh_f32	70.1999969
<pre><_APAMaxVehSpd_Kph_f32</pre>	
k_ESCMaxVehSpd_Kph_f32	31.7000008
K_HapticAmplitude_MtrNm_f32	0.0540000014
C_HapticDuration_Sec_f32	0.135000005
C_HapticFreq_Hz_f32	7.63000011
C_HapticReacttime_Sec_f32	0.143999994
<_LKAMaxVehSpd_Kph_f32	17.1000004
<_LKAMinVehSpd_Kph_f32	15.75
x_MaxHapticReqTime_Sec_f32	0.150000006
c_SWARLimiter_HwDeg_f32	35.7000008
<_ShiftLevRevTime_Sec_f32	0.0370000005
C_StandstillThresh_Kph_f32	0.360000014
_StandstillTime_Sec_f32	0.0370000005
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target TrgOvlSta Per1 CCWFound Cnt Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWPosition HwDeg f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 CWPosition HwDeg f32	
	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f	•	Speed Kph f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_C		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReve		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNr		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target TrqOvlSta Per1 APARecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-379.070007		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.670013		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	- Nosuit
TrqOvISta_ESCState_State_M_enum	4	4	_
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	_
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvISta_LiapseuStatiuStill*Iffle_Sec_ivi_132 TrqOvISta_HapticState_State_M_enum	0	0	•
TrqOvISta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	
	0	0 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32	-4.20148611	-4.20148611 ± 0.000009	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	-
TrqOvISta_EKAFeITIFadit_Cit_w_uoo	0	0	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.18999998	0.189999998 ± 0.0009765625	~
	0.169999996		
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target TrqOvlSta Per1 APADrvrInterventionDetected Cnt lgc.value	1	0 ± 1	
	1	1±1	
target_TrqOvlSta_Per1_APAState_State_enum.value	1 4	1 4	
target_TrqOvlSta_Per1_ESCState_State_enum.value			
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	V
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.18999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	7 00000044	0 ± 1	V
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	_

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	

Test Step 3.25 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta

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Name	Input Value
rqOvlSta_APAState_State_M_enum	0
rqOvlSta_ESCState_State_M_enum	4
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	11150
rqOvlSta_Haptictime_Sec_M_f32	425564
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.200000003
_APAMaxHwTrq_HwNm_f32	0.720000029
_APAMaxVehSpd_Kph_f32	70.1999969
_ESCMaxVehSpd_Kph_f32	31.7000008
_HapticAmplitude_MtrNm_f32	0.0540000014
_HapticDuration_Sec_f32	0.135000005
_HapticFreq_Hz_f32	7.63000011
_HapticReacttime_Sec_f32	0.143999994
_LKAMaxVehSpd_Kph_f32	17.1000004
_LKAMinVehSpd_Kph_f32	15.75
_MaxHapticReqTime_Sec_f32	0.150000006
_SWARLimiter_HwDeg_f32	35.7000008
_ShiftLevRevTime_Sec_f32	0.0370000005
_StandstillThresh_Kph_f32	0.360000014
_StandstillTime_Sec_f32	0.0370000005
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
rget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
irget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
	0
arget_TrqOvISta_Per1_APARequest Cnt Igc.value	
	0
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0 -377.290009
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value arget_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	





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Name	Input Value		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	<u> </u>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V

Test Step 3.26 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003

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TrqOvlSta_Per1



Name	Input Value
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32 k_HapticDuration_Sec_f32	0.0540000014 0.135000005
k_HapticFreq_Hz_f32	7.63000003
k HapticReacttime Sec f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_ target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_Igc target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta_Per1_ESCState_State_enum
target_Nte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosServEnable Cnt Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target TrqOvlSta Per1 APARecoverableFaults Cnt lqc.value	0
target_rrqOvlSta_Per1_APARecoverableFaults_Crit_igc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOviSta_FeT_AFAIXequest_Git_igc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-375.51001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.730011
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	
arget_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value arget_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
arget_IrqOviSta_Per1_GMOSH_LKAMigEnable_Cnt_lgc.value arget_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOviSta_FeT1_TrwTorque_TrwTort_igc.value	1
target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1

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TrqOvlSta_Per1





Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.27 (Repeat Count = 1)	🗸
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1.
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc$	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlOta_Per1_ESCIsLimited_Cnt_I	-	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_ESCState_State_en target_TrqOvlSta_Per1_GMOSHOscillateSta		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecureVehicleSptarget_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServErlable_Ont_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvISta Per1 PosServeHable_Crit		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajEnable Cnt Igc	target_TrqOvISta_Per1_PosTrajEnable_Cnt_		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajHwAngle HwDeg f32	target TrqOvISta Per1 PosTrajHwAngle Hv		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3		y_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	-373.730011 1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.76001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015 79.1999969		
target_IrqOviSta_Per1_MinSecureVenicleSpeed_kpn_t32.value target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	*
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32		-4.20148611 ± 0.000009	~
TraOvISta LKAEquit Cpt M Jac	-4.20148611		
TrqOvlSta_LKAParmEault_Cnt_M_lgc	1	1 ± 1	*
TrqOvISta_LKAPermFault_Cnt_M_lgc	1 0	1 ± 1 0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	1	1 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	1 0 2	1±1 0±1 2±1	V
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	1 0 2 0	1±1 0±1 2±1 0	*
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	1 0 2 0 0.189999998	1 ± 1 0 ± 1 2 ± 1 0 0.189999998 ± 0.0009765625	•

0

target_TrqOvlSta_Per1_APAState_State_enum.value

 $target_TrqOvlSta_Per1_ESCState_State_enum.value$

0

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TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.28 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
FrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
FrqOvISta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	11150
rqOvlSta_Haptictime_Sec_M_f32	425564
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.36999989
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.200000003
c_APAMaxHwTrq_HwNm_f32	0.720000029
_APAMaxVehSpd_Kph_f32	70.1999969
_ESCMaxVehSpd_Kph_f32	31.7000008
	0.0540000014
	0.135000005
HapticFreq Hz f32	7.63000011
s_HapticReacttime_Sec_f32	0.143999994
	17.1000004
LKAMinVehSpd_Kph_f32	15.75
 :_MaxHapticReqTime_Sec_f32	0.150000006
SWARLimiter_HwDeg_f32	35.7000008
s_ShiftLevRevTime_Sec_f32	0.0370000005
StandstillThresh Kph f32	0.360000014
s_standstillTime_Sec_f32	0.0370000005
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target TrqOvlSta Per1 CCWFound Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviota_ctcty_continue_cg_toz_
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlota_ren_EscRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlota_ren_EscState_state_enum
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rerT_ESCState_State_enum arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rerr_GMOSH_APAMfgEnable_Cnt_igc	
arget_Rte_inst_Ap_friqOviSta.friqOviSta_PerT_GMOSH_APAMilgEnable_Cnt_igo arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSH_ESCMfgEnable_Cnt_igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc

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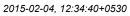


Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_e		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_t			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_C		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_	- 0_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_h		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReve		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNr	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-371.950012		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.790009		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	- 100 UII
TrqOvISta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrgOvlSta HapticState State M enum	0	0	•
TrgOvlSta Hapticdur Cnt M u16	11150	11150 ± 1	
TrgOvlSta Haptictime Sec M f32	425564	425564 ± 1	~
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.20148611	-4.20148611 ± 0.000009	
TrgOvlSta LKAFault Cnt M lgc	1	1 ± 1	~
TrgOvlSta LKAPermFault Cnt M lgc	0	0 ± 1	
TrgOvlSta LKAPermFault Cnt M u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	
TrgOvlSta PosSrvoHwAngle HwDeg M f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target TrqOvlSta Per1 LKAState State enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	v
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~
<u> </u>	1		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~



est Step 3.29 (Repeat Count = 1)	Input Value
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	0
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	41000
rqOvlSta_Haptictime_Sec_M_f32	216790.344
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	2
rqOvISta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
qOvlSta_ReadytoPulse_Cnt_M_lgc	
_APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	0.254999995
APAMaxHwTrq_HwNm_f32	9.11999989
APAMaxVehSpd_Kph_f32	205.199997
ESCMaxVehSpd_Kph_f32	15.6000004
HapticAmplitude_MtrNm_f32	1 71000004
HapticDuration_Sec_f32	1.71000004
HapticFreq_Hz_f32	56.973999 1.824
HapticReacttime_Sec_f32	216.600006
_LKAMinVehSpd_Kph_f32	199.5
_LKAMinVehSpd_Kph_f32	0.209999993
MaxHapticReqTime_Sec_f32 SWARLimiter_HwDeg_f32	15.6000004
ShiftLevRevTime_Sec_f32	0.0529999994
StandstillThresh Kph f32	4.55999994
StandstillTime_Sec_f32	0.0529999994
<pre>_ctantostarranio_oco_noz</pre> <pre>urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt</pre>	
<pre>irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_It</pre>	
lirget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrqOvlSta Per1 APAState State enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CWFound Cnt Igc	target TrqOvlSta Per1 CWFound Cnt Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt lgc	target TrqOvlSta Per1 ESCRequest Cnt Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	2 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	3 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
rget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
rrget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
	0
rget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-370.170013		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvISta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	*
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.30 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2020425
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	86335.6016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	391692
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	31000
TrqOvlSta_Haptictime_Sec_M_f32	210780
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.232999995
k_APAMaxHwTrq_HwNm_f32 k_APAMaxVehSpd_Kph_f32	8.72000027 196.199997
k_ESCMaxVehSpd_Kph_f32	174.5
k_HapticAmplitude_MtrNm_f32	0.671999991
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	55.9459991
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	220
k_LKAMinVehSpd_Kph_f32	196
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	17.7000008
k_ShiftLevRevTime_Sec_f32	0.032999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_linst_Ap_mqovista.mqovista_ren_ArARequest_cni_igc target_Rte_linst_Ap_mqovista.mqovista_ren_ArARequest_cni_igc	target TrqOvlSta Per1 APAState State enum
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_rte_inst_Ap_TrqOviSta.TrqOviSta_rer1_Haptichequest_Cnt_igc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_StilliLeverIstilReverse_Citi_gc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_linst_Ap_mqovista.mqovista_ren_mqoscAmp_ivitiviii_isz target_Rte_linst_Ap_mqovista.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_mqovista.mqovista_ren_mqoscEnable_cnt_igc	target TrqOvISta Per1 TrqOscFreq Hz f32
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.390015
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.850006
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_Haptickequest_Cnt_lgc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOviSta_Per1_Hw10rque_Hw10ri1_i52.value target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvIsta_FeT1_EKAFatilt_Cftt_lgc.value target_TrqOvIsta_Per1_EKAFatilt_Cftt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	41.2000008
target_rigoviota_r err_waxocoure veriloicopeca repri loz.value	
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	61.7999992
	61.7999992

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	✓
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	210780	210780 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.31 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	15.6000004
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Each 1997) and (See Each 1997) and (See Each 1997) are the second of the second$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	* 422	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>J_</u> 132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	lao	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l target TrqOvlSta Per1 ESCRequest Cnt_le		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en	•	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_F	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	a target_TrqOvlSta_Per1_SWARTrgtAngRequ	iest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-366.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.880005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvISta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.4000001	-3.4000001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrgOvlSta LKAPermFault Cnt M u08	3	3 ± 1	-
4	3		
TrqOvlSta_LKAState_State_M_enum	3	3	•

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Nama	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
FrqOvISta_Hapticdur_Cnt_M_u16	41000
FrqOvISta_Haptictime_Sec_M_f32	216790.344
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
FrqOvlSta_LKAFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	3
FrqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
<pre>c_APAIncludeHaptic_Cnt_lgc</pre>	0
<pre>C_APAMaxHwTrqTime_Sec_f32</pre>	0.254999995
APAMaxHwTrg HwNm f32	9.11999989
<pre><_APAMaxVehSpd_Kph_f32</pre>	205.199997
CSCMaxVehSpd Kph f32	15.6000004
- · · · · · - · · -	0
C_HapticAmplitude_MtrNm_f32	·
<_HapticDuration_Sec_f32	1.71000004
K_HapticFreq_Hz_f32	56.973999
C_HapticReacttime_Sec_f32	1.824
<pre>c_LKAMaxVehSpd_Kph_f32</pre>	216.600006
<_LKAMinVehSpd_Kph_f32	199.5
C_MaxHapticReqTime_Sec_f32	0.20999993
c_SWARLimiter_HwDeg_f32	15.6000004
ShiftLevRevTime_Sec_f32	0.0529999994
c_StandstillThresh_Kph_f32	4.55999994
_StandstillTime_Sec_f32	0.0529999994
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Ci	nt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc	target_TrqOviota_i cri_cown osition_imecg_oz
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rei1_ESCFauit_Citt_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
arget Rte Inst Ap TrqOvIsta.TrqOvISta Per1 ESCRequest Cnt Igc	target TrgOvISta Per1 ESCRequest Cnt lgc
	0 - 1 - 1 - 2
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_e	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 12:34:40+0530





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lc	дс	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3	2 target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3:	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_F	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_t	3. target_TrqOvlSta_Per1_SWARTrgtAngRequ	iest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-364.829987		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.910004		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target TrqOvlSta_Per1_HwTorque HwNm f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	61.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrqOvlSta_ESCState_M_enum	3	3	✓
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	~
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum			
TrqOvlSta_Hapticdur_Cnt_M_u16	1	1	•
	1 41000	1 41000 ± 1	V
TrqOvlSta_Haptictime_Sec_M_f32	•		· ·
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	41000	41000 ± 1	· · · · · · · · · · · · · · · · · · ·
	41000 0	41000 ± 1 0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	41000 0 -3.4000001	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	41000 0 -3.4000001 0	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	41000 0 -3.4000001 0	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1	~
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFauIt_Cnt_M_Igc TrqOvISta_LKAPermFauIt_Cnt_M_Igc TrqOvISta_LKAPermFauIt_Cnt_M_u08	41000 0 -3.4000001 0 1 3	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	41000 0 -3.4000001 0 1 3 3	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1 0	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1 0 1 3	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_SCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1 0 1 3 1	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1 1 3	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1 0 1 3	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1 1 3 1	***************************************
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1 0 1 3 1	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1 1 3 1 3 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1 0 1 3 1 3 1 0.150000006	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1 1 3 1 3 1 ± 1 0.150000006 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1 0 1 3 1 3 1 0.150000006 0	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1 1 3 1 ± 1 0.150000006 ± 0.0009765625 0 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	41000 0 -3.4000001 0 1 3 3 0.150000006 1 0 1 3 1 3 1 0.150000006	41000 ± 1 0 ± 1 -3.4000001 ± 0.000009 0 ± 1 1 ± 1 3 ± 1 3 0.150000006 ± 0.0009765625 1 ± 1 0 ± 1 1 3 1 3 1 ± 1 0.150000006 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



est Step 3.33 (Repeat Count = 1)	Input Value
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	1
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	44450
rqOvlSta_Haptictime_Sec_M_f32	0
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	1
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime_Sec_f32	0.601666689
APAMaxHwTrq_HwNm_f32	0
APAMaxVehSpd_Kph_f32	138.600006
ESCMaxVehSpd_Kph_f32	12
HapticAmplitude_MtrNm_f32	0.462000012 1.15499997
HapticDuration_Sec_f32	
_HapticFreq_Hz_f32 _HapticReacttime_Sec_f32	32.3019981 1.23199999
_haptickeactiffie_sec_is2 _LKAMaxVehSpd_Kph_f32	146.300003
LKAMinVehSpd_Kph_f32	100
MaxHapticReqTime_Sec_f32	0.600000024
SWARLimiter_HwDeg_f32	13
ShiftLevRevTime_Sec_f32	0.308999985
StandstillThresh Kph f32	0
StandstillTime_Sec_f32	0.308999985
<pre>irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt</pre>	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
riget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
rget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
rget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-363.049988		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.940002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.20000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrqOvlSta_ESCState_State_M_enum	2	2	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	-
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	-
TrqOvlSta_LKAState_State_M_enum	0	0	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	→
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	→
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.34 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1.
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k APAMaxHwTrq HwNm f32	0
k APAMaxVehSpd Kph f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k HapticAmplitude MtrNm f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k LKAMinVehSpd Kph f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k SWARLimiter HwDeg f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k StandstillThresh Kph f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.269989
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.970001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target TrgOvlSta Per1 HwTorque HwNm f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.20000003
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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.35 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1.	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	
TrqOvlSta_Haptictime_Sec_M_f32	0	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	
TrqOvlSta_LKAState_State_M_enum	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAIncludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.601666689	
k_APAMaxHwTrq_HwNm_f32	0	
k_APAMaxVehSpd_Kph_f32	138.600006	
k_ESCMaxVehSpd_Kph_f32	12	
k_HapticAmplitude_MtrNm_f32	0.462000012	
k_HapticDuration_Sec_f32	1.15499997	
k_HapticFreq_Hz_f32	32.3019981	
k_HapticReacttime_Sec_f32	1.23199999	
k_LKAMaxVehSpd_Kph_f32	146.300003	
k_LKAMinVehSpd_Kph_f32	100	
k_MaxHapticReqTime_Sec_f32	0.600000024	
k_SWARLimiter_HwDeg_f32	13	
k_ShiftLevRevTime_Sec_f32	0.308999985	
k_StandstillThresh_Kph_f32	0	
k_StandstillTime_Sec_f32	0.308999985	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	:	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enunceters$	target_TrqOvlSta_Per1_GMOSHOscillateSta	ite_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-390.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
		0 ± 1	· ·
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1		
	1 0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 0 2	0 ± 1 2 ± 1	· ·

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 3.36 (Repeat Count = 1)	Innut Value
lame	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	1
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	1
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	44450
rqOvlSta_Haptictime_Sec_M_f32	0
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	1.
rqOvlSta_LKAState_State_M_enum	0
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime Sec f32	0.601666689
APAMaxHwTrq HwNm f32	0
APAMaxVehSpd Kph f32	138.600006
ESCMaxVehSpd Kph f32	12
_HapticAmplitude_MtrNm_f32	0.462000012
HapticDuration Sec f32	1.15499997
HapticFreq Hz f32	32.3019981
_ '	
_HapticReacttime_Sec_f32	1.23199999
_LKAMaxVehSpd_Kph_f32	146.300003
_LKAMinVehSpd_Kph_f32	100
_MaxHapticReqTime_Sec_f32	0.600000024
_SWARLimiter_HwDeg_f32	13
_ShiftLevRevTime_Sec_f32	0.308999985
_StandstillThresh_Kph_f32	0
_StandstillTime_Sec_f32	0.308999985
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enui	
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	
rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_inst_Ap_frqOviSta.frqOviSta_Feff_GMOSH_ESCWigEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H target TrqOvlSta Per1 SWARTrgtAngRequ		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvIsta_Per1_ShiftLeverIsInRever		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-388.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.029999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvISta_ESCState_State_M_enum	2	2	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1 1 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_LiapsedStatidStill*Time_Sec_iw_i32 TrqOvISta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	
TrqOvlSta Haptictime Sec M f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.43524981	2.43525004 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.200000003 0.462000012	0.200000003 ± 0.0009765625 0.462000012 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0.462000012	1±1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	
gq- 10.ta_1 0.11.q-001 10q_112_102.10100			

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.37 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1
TrqOvlSta_HapticState_State_M_enum TrqOvlSta Hapticdur Cnt M u16	1 44450
TrqOvISta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	4 2000000
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1.3999998
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32 k_HapticReacttime_Sec_f32	32.3019981 1.23199999
k LKAMaxVehSpd Kph f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvISta Per1 ESCRequest Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target TrgOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target TrqOvISta Per1 LKAInhibit Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
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TrqOvlSta_Per1

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-386.450012		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.20000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvISta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.38 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998

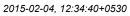
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A APANdederlong A-Holm 192		
L. APARAdeside Spec. Crt. Lige L. APARAdeside Spec. Rep. 12 1880,0000	Name	Input Value
A APARAMENTATINE Sec. 12 A APARAMENTATION SERVICES A APARAMENTATION SERVICES A APARAMENTATION SERVICES A RESOLVANCHINGS (Sp. 12) Linguistic Fire Linguist	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
A.	k_APAIncludeHaptic_Cnt_lgc	1
A. APAMAWAYES, Chr. 12	k_APAMaxHwTrqTime_Sec_f32	0.601666689
ESCHARVENSE Pai 12	k APAMaxHwTrq HwNm f32	0
A	k APAMaxVehSpd Kph f32	138.600006
A	k ESCMaxVehSpd Kph f32	12
1,1940/07		
1. Inspirite pt. 1/12 12 1399999 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 14 1300003 1 13000000000000000000000		
1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
K. JAMAWANSPE, KPh. 172		
A LANAMONNESSE (Pp. 132 K. J. WARHSPORTER) See, 132 S. SEMENTER (PP. 132 K. SEMENTER) SEE, 132 K. SEMENTER (PP. 132 K. SEMENTER) SEE, 133 K. SEMENTER (PP. 132 K. SEMENTER) SEE, 134 K. SEMENTER (PP. 132 K. SEMENTER) SEE, 134 K. SEMENTER (PP. 134 K. SEMENTER) SEE		
K. MANTENDER MARCHE SEC. 12 K. SIMELANDER MARCHE SEC. 12 K. SIME		
K. SMILLEN,		
K. Shadeullimer, Sec. 12 k. Shadeullimer, Sec. 13 L. Shadeullimer, Sec.		
k, Sandsdiffmen, Koft, 12 k, Sandsdiffmen, Koft, 12 target, Ric, Jinst, Ap, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. Indoces, Drock, Indoces,		
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target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _ESCRequest_Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _ESCState_State_enum target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _GMOSH-OscillateState_State_enum target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _GMOSH-OscillateState_State_enum target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _GMOSH-ASMIGEnable_Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _GMOSH-ASMIGEnable_Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _GMOSH LKAMIGEnable_Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _HapticRequest, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _LKARequest, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _LKARequest, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _MKARequest, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _MKARequest, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _MKARequest, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _MKARequest, Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _PosServEnable_Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _PosTrajethwange_HwDeg132 target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _NocAmp_Mirth, 132 target, TrqOviSta, Perl _Ap., TrqOviSta,	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ERCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvehindeSpeed_Kph_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvehindeSpeed_Kph_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTraijEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTraijEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SoxNehMangle_HvDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SoxNehMangle_HvDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SoxNehMangle_HvDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SoxNehMangle_HvDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SoxNehMangle_HvDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SoxNehMangle_HvDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SoxNehMangle_HvDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SoxNehMangle_HvDeg_132 target_Rt	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH-DSOILaleState, State, enum target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH-ApAMIgEnable Cnt. I.go target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH-LESCMIgEnable Cnt. I.go target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH, LESCMIgEnable Cnt. I.go target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH, LESCMIgEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. JAMDISta. Pert. JAMDIST	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
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target_Rie_Inst_Ap_TrqOviSta_Prer1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Prer1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_AptiOviSta_Prer1_	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enun$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_NimSccureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrorhwangle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrorhwangle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Shift_everisInReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Shift_everisInReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchaple_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchaple_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchaple_Cnt_Igc target_TrqOviSta_Pert_Swartgrager_Cnt_Igc target_TrqOviSta_Pert_ApARonRecoverableFaults_Cnt_Igc-value 0 target_TrqOviSta_Pert_ApARonRecoverableFaults_Cnt_Igc-value 1 target_TrqOviSta_Pert_CWPound_Cnt_Igc-value 1 target_TrqOviSta_Pert_CWPound_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCSCRquest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCSCRquest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCSCRquest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCSCRquest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCSCRquest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCSCRquest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCSCRquest_Cnt_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HarpticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HwTorque_HwNn_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARstae_Unt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARstae_Unt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_state_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiPwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiPwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiPwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiPwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SiriIt_verrisinReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SiriIt_verrisinReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOviSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOviSta_Pert_CoviFound_Cnt_Igc.value 1 target_TrqOviSta_Pert_CoviFound_Cnt_Igc.value 1 target_TrqOviSta_Pert_EoCReques_Cnt_Igc.value 1 target_TrqOviSta_Pert_EoCReques_Cnt_Igc.value 1 target_TrqOviSta_Pert_GoMOSH_PSMfigenable_Cnt_Igc.value 1 target_TrqOviSta_Pert_GoMOSH_PSMfigenable_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARaequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARaequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecurevehicleSpeed_Kph_gr32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SorIfficaverIsInReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_Mirthm_gr32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_Mirthm_gr32 target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscAmp_Mirthm_gr32 target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscAmp_Mirthm_gr32 target_TrqOviSta_Pert_ApartecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc target_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc target_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc target_TrqOviSta_Pert_CVCVPosition_HvDeg_gr32_value 1 target_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc_value 1 target_TrqOviSta_Pert_CVCVPosition_HvDeg_gr32_value 1 target_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc_value 1 target_TrqOviSta_Pert_CVCVPosition_HvDeg_gr32_value 1 target_TrqOviSta_Pert_CVCVPosition_HvDeg_gr32_value 1 target_TrqOviSta_Pert_ESCRautle_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCRautle_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCRautle_Cnt_lgc_value 1 target_TrqOviSta_Pert_GCNAPAME_PSDA_Mirthm_GRAUTLE_Cnt_lgc_value 1 target_TrqOviSta_Pert_GCNAPAME_PSDA_Mirth	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
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target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKASRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKASRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt Igc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_KARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_RtqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSevnehable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovhWangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TosScAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCFreq_Hz_f32 target_TrqOvISta_Per1_APANecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRault_Cnt_Igc.value 1 target_TrqOvISta_Per1_MASEcureVerbicleSpeed_Kph_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta.Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_SVMARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_SVMARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_SVMARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_SVMARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_TrqOsCenable_Cnt_Igc target_TrqOviSta.Per1_APARcoverableFaults_Cnt_Igc.value target_TrqOviSta.Per1_APARcoverableFaults_Cnt_Igc.value 1 target_TrqOviSta.Per1_APARcoverableFaults_Cnt_Igc.value 1 target_TrqOviSta.Per1_CWPound_Cnt_Igc.value 1 target_TrqOviSta.Per1_CWPound_Cnt_Igc.value 1 target_TrqOviSta.Per1_CWPosition_HwDeg_f32_value 362_089996 target_TrqOviSta.Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta.Per1_GMOSH_APAMfigenable_Cnt_		
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target_Rte_Inst_Ap_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvISta_Per1_APARCeoverableFaults_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARCeoverableFaults_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWPosition_HwDeg_f32_value 2 6 6 6 6 6 6 6 6 6 6 6 6 6 6 7 7 7 7 7		
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target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_ApraceoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARequest_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARcoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARcoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_igc.value target_TrqOvlSta_Per1_APARequest_Cnt_igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_igc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1	0 = = = 1 = 1 = = = = = = = = = = = = =	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_SCMfgEnable_Cnt_lgc.value 1	0 = = = 1	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	· - ·	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value -384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_SECMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.089996
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
1 T O 191 B 4 OMOSULIVANI 5 11 O 11 O	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_IrqOviSta_Per1_GMOSH_LKAMigEnable_Cnt_lgc.value	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1	· - ·	
	target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1	· - ,	
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 1		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.200000003		

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	1 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.39 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.6499998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	Faults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau	Its_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	~ 400	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWPosition_HwDe target_TrqOvlSta_Per1_CWFound_Cnt_lgc	g_132	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_	f32	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	502	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	JC .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnatarget_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f3		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc	,_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgd	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_	- •	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequetarget TrqOvlSta Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	.	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
$target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value$	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.890015		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	362.119995 0		
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	a		
target TrqOvlSta Per1 HwTorque HwNm f32.value	1		
	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	8.10000038 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	8.10000038 1 0		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	8.10000038 1 0		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	8.10000038 1 0 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	8.10000038 1 0 1 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	8.10000038 1 0 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	8.10000038 1 0 1 1 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_HwDeg_f32.value	8.10000038 1 0 1 1 1 1 1 0.200000003		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajEnable_HwDeg_f32.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	8.10000038 1 0 1 1 1 1 0.200000003 0	Expected Value	Result
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	8.10000038 1 0 1 1 1 1 1 0.200000003 0 1 Actual Value 1	1	Result ~
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Lnt_lgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum	8.10000038 1 0 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2	1 2	~
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_HwDeg_f32.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8.10000038 1 0 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009	1 2 0.002000000009 ± 1	· ·
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	8.10000038 1 0 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0.00200000009	1 2 0.00200000009 ± 1 0.00200000009 ± 1	, , , , , , , , , , , , , , , , , , ,
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32	8.10000038 1 0 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0.00200000009 0	1 2 0.00200000009 ± 1 0.00200000009 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum	8.10000038 1 0 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0.00200000009 0	1 2 0.00200000009 ± 1 0.00200000009 ± 1 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_HwDeg_f32.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticOtur_Cnt_M_u16	8.10000038 1 0 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0.00200000009 0 0	1 2 0.00200000009 ± 1 0.00200000009 ± 1 0 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticOtime_State_Menum TrqOvISta_HapticOtime_Sec_M_f32 TrqOvISta_HapticOtime_Sec_M_f32	8.10000038 1 0 1 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0 0 0 0 0 0 0.00200000009	1 2 0.00200000009 ± 1 0.00200000009 ± 1 0 0 0 ± 1 0.00200000009 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32	8.10000038 1 0 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0.0020000009 0 0 0 0 0 0.00200000009 -1.64999998	1 2 0.00200000009 ± 1 0.00200000009 ± 1 0 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticotur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32	8.10000038 1 0 1 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0 0 0 0 0 0 0.00200000009	1 2 0.00200000009 ± 1 0.00200000009 ± 1 0 0 ± 1 0.00200000009 ± 1 -1.64999998 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_Menum TrqOvISta_HapticState_State_State_State_State_State_State_State_State_Stat	8.10000038 1 0 1 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0.00200000009 0 0 0 0 0 0 0 0 1.64999998 1	1 2 0.00200000009 ± 1 0.00200000009 ± 1 0 0 0 ± 1 0.00200000009 ± 1 -1.64999998 ± 0.000009 1 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SHiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_EsCState_State_M_enum TrqOvISta_EsCState_State_Menum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticot_nt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAFault_Cnt_M_lgc	8.10000038 1 0 1 1 1 1 1 1 0.200000003 0 1 Actual Value 1 2 0.00200000009 0.00200000009 0 0 0 0 0 0.002000000009 -1.64999998 1 0	1 2 0.00200000009 ± 1 0.00200000009 ± 1 0 0 0 ± 1 0.00200000009 ± 1 -1.64999998 ± 0.000009 1 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·

 $TrqOvlSta_ReadytoPulse_Cnt_M_lgc$

1 ± 1

2015-02-04, 12:34:40+0530

TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Jamo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvISta_APAState_State_M_enum	2
FrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	33000
rqOvlSta_Haptictime_Sec_M_f32	204770
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.210999995
APAMaxHwTrq HwNm f32	8.8000019
_APAMaxVehSpd_Kph_f32	198
ESCMaxVehSpd Kph f32	164.300003
	0.660000026
_HapticAmplitude_MtrNm_f32	
t_HapticDuration_Sec_f32	1.6499998
_HapticFreq_Hz_f32	54.9179993
_HapticReacttime_Sec_f32	1.75999999
_LKAMaxVehSpd_Kph_f32	209
_LKAMinVehSpd_Kph_f32	120.220001
_MaxHapticReqTime_Sec_f32	0.150000006
_SWARLimiter_HwDeg_f32	58.2999992
_ShiftLevRevTime_Sec_f32	0.0370000005
_StandstillThresh_Kph_f32	4.4000001
_StandstillTime_Sec_f32	0.0370000005
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetect	ted_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults	s_Cnt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cr	nt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_rrqoviota_r_crcools_mined_ont_rgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvISta_Per1_ESCState_State_enum
arget_Rte_inst_Ap_frq0vi3ta.frq0vi3ta_Fer1_E3C3tate_state_entim arget_Rte_inst_Ap_frq0viSta.frq0viSta_Per1_GM0SH0scillateState_Sta	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_0	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_t	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_0	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.529999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.480011		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	84736.2422	84736.2422 ± 1	_
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	1	1	_
TrgOvlSta Hapticdur Cnt M u16	33000	33000 ± 1	
TrgOvlSta Haptictime Sec M f32	0	0 ± 1	V
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-4.80000019	-4.80000019 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrgOvlSta LKAPermFault Cnt M lgc	0	0 ± 1	
TrqOvISta LKAPermFault Cnt M u08	3	3±1	-
TrgOvISta_LKAState State M enum	0	0	
TrqOvISta_ExActate_State_ivi_entitiff TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	- 4
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	V
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 3.41 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_equip	3 0
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	38.2000008
TrqOvIsta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	59.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.400001
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0370000005 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target TrqOvlSta Per1 APAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HapticRequest Cnt lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviota.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKARequest Cnt Igc	target TrqOvlSta Per1 LKARequest Cnt Igc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 LKAState State enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
targot_ridoviota_r or i_oover ourid_ont_igo.value	<u> '</u>

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TrqOviSta_Peri		IMA	Citab
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-389.75		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.51001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	•
TrqOvISta ESCState State M enum	3	3	•
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	1952499	1952499 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	•
target TrqOvlSta Per1 TrqOscEnable Cnt lgc.value	1	1 ± 1	✓
target_Trqoviota_r err_TrqoseEnable_Ont_ige.value	I I	! = !	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.42 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	1.64999998 54.9179993
k HapticReacttime Sec f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	59.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0370000005 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target TrgOvlSta Per1 APARecoverableFaults Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-387.970001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target TrqOvlSta Per1 CWPosition HwDeg f32.value	1 362.540009
target TrqOvISta_Per1_CvvPosition_HwDeg_132.value target TrqOvISta_Per1_ESCFault Cnt lgc.value	1
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LnArkequest_Cnt_gc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	204770	204770 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.43 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	59.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	•	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFat	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	ic .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg	f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_102	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
	<u> </u>		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc	<u></u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvISta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvISta_Per1_PosSrvoHwAngle_H	- -	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Peri_PosTrajEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Peri_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlOta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvISta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-386.190002		
target_TrqOvISta_Per1_CWFound_Cnt_Igc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	362.570007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	3.70000005		
Name	Actual Value	Expected Value	Posul
TrqOvlSta_APAState_State_M_enum	0	0	Result
TrqOvISta_ESCState_State_M_enum	3	3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019	-4.80000019 ± 0.000009	•
TrqOvISta_LKAPault_Cnt_M_lgc	1 0	1 ± 1 0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3	0 ± 1 3 ± 1	
TrqOvISta_LKAPermFauit_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	0	0	
	· -		
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	-

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 3.44 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	0
rqOvlSta_ESCState_State_M_enum	0
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	29000
rqOvlSta_Haptictime_Sec_M_f32	198760.641
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	0
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.36500001
APAMaxHwTrg HwNm f32	8.64000034
APAMaxVehSpd Kph f32	194.399994
ESCMaxVehSpd Kph f32	52.599985
_LapticAmplitude_MtrNm_f32	0.648000002
HapticDuration Sec f32	1.62
HapticFreq Hz f32	3
HapticReacttime Sec f32	1.72800004
_HapticReactiffle_Sec_132 LKAMaxVehSpd Kph f32	205.199997
_LKAMinVehSpd_Kph_f32	0
_MaxHapticReqTime_Sec_f32	0.119999997
_SWARLimiter_HwDeg_f32	0.119999997
_ShiftLevRevTime_Sec_f32	0.0289999992
_StandstillThresh_Kph_f32	4.32000017
_StandstillTime_Sec_f32	0.0289999992
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lq	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur	
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
<pre>ingct_ntc_inst_Ap_TrqOviota:rrqoviota_rer1_eMOSH_ESCMfgEnable_Cnt_lgc</pre>	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOviSta_TrqOviSta_FeT1_HapticRequest_Crit_igc	target_TrqOvISta_Per1_HwTorque_HwNm_f32
igot_isto_inst_Ap_fiqOviota.fiqOviota_Feli_HwTofque_Hwiviii_i32	talget_rideviola_reli_liwioidae_riwidii_loz

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequitarget_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvlSta Per1 TrqOscFreq Hz f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-384.410004		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.600006		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.6999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0±1	· ·
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	V
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0	0	
TrgOvISta_Hapticdur Cnt M u16	29000	29000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	-
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	0.449800014	0.449800014 ± 0.0000009	-
TrgOvISta LKAFault Cnt M lgc	0	0 ± 1	~
TrgOvISta LKAPermFault Cnt M Igc	0	0 ± 1	-
TrgOvISta LKAPermFault Cnt M u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	2	2	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	V
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625 0 ± 1	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	
targot_rigoviota_reri_rigosorreq_riz_laz.value	<u> </u>	0 ± 0.0008/00023	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 ChecknointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-





Test Step 3.45 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-5.5 0.419
TrqOvISta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	194.399994 52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k HapticDuration Sec f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	0.119999997
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017 0.0289999992
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	V - 1
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_inst_Ap_irqOviSta.irqOviSta_Peri_LKAState_state_enum target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Peri_MaxSecureVehicleSpeed_Kph_f32	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. \\$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlota_rer1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
	

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IrqOviSta_Peri		IMA	CILAB
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.630005		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.630005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	•
TrqOvlSta ESCState State M enum	3	3	~
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	83136.8828	83136.8828 ± 1	•
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrqOvlSta HapticState State M enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	2	2	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target TrqOvlSta Per1 GMOSHOscillateState State enum.value	3		~
target_TrqOvi3ta_Feri_GiviO3HOScillate3tate_State_entini.value	0	0	
target_TrqOvISta_Per1_LKAState_State_enum.value		0 2	✓
	0	1	· ·
target_TrqOvlSta_Per1_LKAState_State_enum.value	0 2	2	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 2 1	2 1±1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 2 1 0.150000006	2 1 ± 1 0.150000006 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

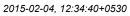
Test Step 3.46 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992

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	(14 17 10 10
Name	Input Value
	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
	1.62
- ' '	3
	1.72800004
_ , _ , _	205.199997
_	0
	0.119999997
	52.5999985 0.028999992
	4.32000017
	0.0289999992
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target TrqOvlSta Per1 APARecoverableFaults Cnt Igc
	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
	0
312 11 112 1 21 11 12 23	1
rarget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-380.850006
312 41 112 1 21 11 12 23 11 1	1
· - · · · -	362.660004
312 11 112 1 2 1 1 2 1 2 1 1 1 1 1 1 1 1	1
· - ·	1
0 = 1 = 2 = 0	1
	1
0 = 1 = 2 0 = 20	1
	1
	8.6999981
* - '	0
target TrgOvlSta Per1 LKAFault Cnt lgc value	
	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1 164.800003





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.449800014	0.449800014 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	1	1	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	1884573
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	83136.8828
TrqOvlSta ElapsedStandstillTime Sec M f32	365508
TrgOvlSta HapticState State M enum	0
TrgOvlSta Hapticdur Cnt M u16	29000
TrgOvlSta Haptictime Sec M f32	198760.641
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-5.5
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.419
TrgOvlSta LKAFault Cnt M lgc	1
TrgOvISta LKAPermFault Cnt M Igc	0
TrgOvISta LKAPermFault Cnt M u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta PosSrvoHwAngle HwDeg M f32	57.7999992
TrqOvlSta ReadytoPulse Cnt M lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k HapticAmplitude MtrNm f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	53.5999985
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	52	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	rc	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	·	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-379.070007		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.690002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target TrqOvlSta Per1 HwTorque HwNm f32.value	8.6999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	164.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.39994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	3	3	~
TrqOvISta_ESCState_State_M_enum	3	3	•
TrgOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	83136.8828	83136.8828 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	2	2	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓

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TrqOvlSta_Per1



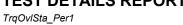
Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
FrqOvlSta_Hapticdur_Cnt_M_u16	29000
FrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
FrqOvlSta_LKAFault_Cnt_M_lgc	1
FrqOvISta_LKAPermFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_u08	0
FrqOvlSta_LKAState_State_M_enum	0
FrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
<_APAMaxHwTrqTime_Sec_f32	0.36500001
_APAMaxHwTrq_HwNm_f32	8.64000034
<pre><_APAMaxVehSpd_Kph_f32</pre>	194.399994
c_ESCMaxVehSpd_Kph_f32	52.5999985
	0.648000002
_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
_HapticReacttime_Sec_f32	1.72800004
K LKAMaxVehSpd Kph f32	205.199997
c_LKAMinVehSpd_Kph_f32	0
MaxHapticRegTime Sec f32	0.119999997
 <_SWARLimiter_HwDeg_f32	54.5999985
shiftLevRevTime_Sec_f32	0.0289999992
StandstillThresh_Kph_f32	4.32000017
StandstillTime Sec f32	0.0289999992
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APADrvrInterventionDetected_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_C	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_l	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvista_Fe11_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fer1_CGVFosition_Hwbeg_is2 arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc target TrgOvISta Per1 ESCRequest Cnt Iqc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	0 - 1 1
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

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Name	Input Value				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_I	-			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc		target_TrqOvlSta_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph		target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 MinSecureVehicleSpeed Kph					
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosServEnable Cnt Igc					
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32		target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_0				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle				
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg		target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc		target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_Mtrl	Nm_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_C	cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_	<u>f</u> 32			
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-377.290009				
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.720001				
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.69999981				
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1				
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	164.800003				
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994				
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006				
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004				
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1				
Name	Actual Value	Expected Value	Result		
TrqOvlSta_APAState_State_M_enum	3	3	-		
TrqOvlSta_ESCState_State_M_enum	3	3	•		
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-		
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	-		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-		
TrqOvlSta_HapticState_State_M_enum	0	0	-		
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	-		
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	•		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	-		
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~		
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•		
TrqOvlSta_LKAState_State_M_enum	2	2			
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	•		
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	•		
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•		
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3 3	-		
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	Ĭ		
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	-		
target TrqOvlSta Per1 LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1			
target_IrqOvlSta_Per1_PosServEnable_Cnt_igc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625			
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000000 ± 0.0009765625			
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0.545050502 ± 0.5003763523	•		
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	-		
0 1	<u>'</u>	, = 1			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~



Test Step 3.49 (Repeat Count = 1)	▼
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.3699989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 0
TrqOvISta_LKAPermFault_Cnt_W_gc	2
TrqOvISta_LKAState_State_M_enum	1
TrgOvlSta PosSrvoHwAngle HwDeg M f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.099985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32	0.150000006 35.7000008
k ShiftLevRevTime Sec f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_lgc$	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_rer1_ESGRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvISta_Per1_GMOSHOscillateState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfqEnable Cnt lqc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_kte_inst_Ap_irqOviSta.irqOviSta_Peri_PosSrvoHwAngie_HwDeg_t32 target_kte_inst_Ap_trqOviSta.trqOviSta_Peri_PosTrajEnable_Cnt_igc	target_IrqOvlSta_Per1_PosSrv6HwAngle_HwDeg_132 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosTrajErlable_Off_ige target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajErlable_Crit_igc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target Rie inst Ap Tryovisia.Truovisia Ferr Swak TruiAnukeunesi Hwisen ist	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOvsAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0 0
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-375.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.75		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.50 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.20000003
k_APAMaxHwTrq_HwNm_f32	0.720000029 16.2000008
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0370000005
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	1 - 1 - 1 - 1 - 1 - 1 - 1
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvISta Per1 PosTrajHwAngle HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-373.730011 4
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1 362 770000
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	362.779999
target_rrqOvlSta_Per1_ESCFauit_Crit_igc.value target_rrqOvlSta_Per1_ESCIsLimited_Crit_igc.value	1
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	1 0.18999998
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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	2	2	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.51 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cndetected_Cnd$	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ılts_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	:g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_102	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_li	ac	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCRequest Cnt Igc	target TrqOvlSta Per1 ESCRequest Cnt lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	able_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlOta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvISta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	_*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-371.950012		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.809998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	3.3599999		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	14		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	835.559998	835.559998 ± 1	Ĭ
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0	0	-
TrqOvISta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	
	425564	425564 ± 1	
rigovista naptictime sec ivi isz		-4.20148611 ± 0.000009	
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-4.20148611	-4.20140011 ± 0.000009	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc	-4.20148611 1	1±1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32			
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1 0 2 0	1±1 0±1 2±1 0	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 0 2	1±1 0±1 2±1	•

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
	·
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.20000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k LKAMaxVehSpd Kph f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k MaxHapticRegTime Sec f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.037000005
 k_StandstillThresh_Kph_f32	0.360000014
k StandstillTime Sec f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_C	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverabler adits_Crit_igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Fe1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_Hwbeg_i32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPound_Cnt_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target TrgOvlSta Per1_ESCRequest Cnt Iqc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	0 - 1 1 0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_er	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Iq	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Iq	· · - · · ·
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target TrqOvlSta Per1 PosTrajHwAngle Hv		
		,_	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrgOvlSta Per1 ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-370.170013		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.839996		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		l
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	Ž
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_i32	835.559998	835.559998 ± 1	~
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	-
TrgOvlSta HapticState State M enum	0	0	~
TrgOvlSta Hapticdur Cnt M u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	1	1	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	V
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	V
target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	1	1	*
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.189999998	1 ± 1 0.189999998 ± 0.0009765625	~
target_IrqUviSta_Peri_PosSrvoHwAngie_HwDeg_rsz.value target_TrqUviSta_Peri_TrqUscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	
gqda oq-oooqioz.valao			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	_



Test Step 3.53 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	425564 -4.3699989
TrqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0218000002
TrgOvlSta LKAFault Cnt M lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.20000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008 31.7000008
k_ESCMaxVehSpd_Kph_f32 k_HapticAmplitude_MtrNm_f32	0.0540000014
k HapticDuration Sec f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	32.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0370000005 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	V - 1
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_IrqOviSta.IrqOviSta_Peri_MaxSecurevenicleSpeed_Kpn_132 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Peri_MinSecureVehicleSpeed_Kph_132	target_rqovista_Peri_maxsecureveniciespeed_kpn_rs2 target_rqovista_Peri_maxsecureveniciespeed_kpn_rs2
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
0 1=	1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.390015		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.869995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	-
TrqOvlSta_ESCState_State_M_enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	- -
TrqOvlSta_HapticState_State_M_enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	- •
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	2	2	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.54 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004

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TapONISTE_Pack_Pack_Pack_Pack_Pack_Pack_Pack_Pack	Name	Input Value
A. APARAmetering telestry (1985 - 1987)		·
A APASsatisforTraille See, 122 A STEROSOM A		
	. –	0.200000003
\$1,700,000	k_APAMaxHwTrq_HwNm_f32	0.720000029
Captor Company (1) Captor	k_APAMaxVehSpd_Kph_f32	16.2000008
Lipspering Lips 12	- · - · -	
Lipsopie		
Liganicianima Sec. 202		
L_MANNI-NES-R_M_R_M_2 L_Manni-Nes-R_M_2 L_Manni-N		
LipAdarphotoping Sept 20 150000006		
S. SMARHEIME PHORE, 19.2 S. Sharkeithmen, Yell, 19.2 S. Sardellitmen, See, 19.2 S.	k_LKAMinVehSpd_Kph_f32	
S. Smidstiffnes Sec. 192 S. Smidstiffnes (July 1975) S. S	k_MaxHapticReqTime_Sec_f32	0.150000006
A Samedill Time, Spt. 122 Jarget, Rin, Ind. A., Trock/Sta. Trock/Sta. Pert. APADominian entotoleted cot. trapet. Trock/Sta. Pert. APADominian entotoleted c	k_SWARLimiter_HwDeg_f32	33.7000008
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target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value farget_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 16		
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target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 3.3599999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	·	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 3.3599999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 3.3599999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	· - ·	
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 3.3599999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 3.3599999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 3.3599999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
		16
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.189999998	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	2	2	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.55 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>j_</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_t		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	IC	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-364.829987		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.929993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	3.3599999		
target TrgOvlSta Per1 LKAFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target TrgOvlSta Per1 LKARequest Cnt lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	0	0	~
TrqOvISta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	_
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc		0 ± 1	✓
	1	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
	0 2	0 ± 1 2 ± 1	*
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	0 2 2	0 ± 1 2 ± 1 2	· · · · ·
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	0 ± 1 2 ± 1	*

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Namo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOvlSta_HapticState_State_M_enum	2
FrqOvISta_Hapticdur_Cnt_M_u16	1050
FrqOvISta_Haptictime_Sec_M_f32	2315.65991
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOvlSta_LKAFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_lgc	0
FrqOvISta_LKAPermFault_Cnt_M_u08	2
FrqOvlSta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
<pre>c_APAIncludeHaptic_Cnt_lgc</pre>	0
<pre>(_APAMaxHwTrqTime_Sec_f32</pre>	0.159999996
APAMaxHwTrq HwNm f32	0.40000006
CAPAMaxVehSpd_Kph_f32	9
CSCMaxVehSpd Kph f32	22.1000004
_	0.029999993
C_HapticAmplitude_MtrNm_f32	
<_HapticDuration_Sec_f32	0.075000003
K_HapticFreq_Hz_f32	5.57399988
C_HapticReacttime_Sec_f32	0.0799999982
<pre>c_LKAMaxVehSpd_Kph_f32</pre>	9.5
<_LKAMinVehSpd_Kph_f32	8.75
C_MaxHapticReqTime_Sec_f32	0.090000036
c_SWARLimiter_HwDeg_f32	22.1000004
ShiftLevRevTime_Sec_f32	0.0209999997
C_StandstillThresh_Kph_f32	0.20000003
C_StandstillTime_Sec_f32	0.0209999997
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cr	nt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target TrqOvlSta Per1 CCWFound Cnt lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc	target_TrqOviSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_inist_Ap_inqovista.inqovista_reii_E3Crauit_Git_igc arget_Rte_inist_Ap_inqovista.inqovista_reii_E3Crauit_Git_igc	target_TrqOvlSta_Per1_ESGIsLimited_Cnt_lgc
· · · · · ·	target_TrqOviSta_Per1_ESCIsLimited_Crit_igc target TrqOviSta_Per1_ESCRequest Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	0 - 1 1
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_6	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-363.049988		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	362.959991		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrgOvlSta Per1 HwTorque HwNm f32.value	1.96000004		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrgOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	1	1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_M_enum	2	2	V
TrqOvlSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	V
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991	2315.65991 ± 1	*
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1 1±1	
TrgOvISta_LKAPermFault_Cnt_M_igc TrgOvISta_LKAPermFault_Cnt_M_u08	3	3±1	
TrgOvISta_LKAState State M enum	3	3	
TrqOvISta_ErAState_state_w_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	-
TrqOvISta ReadytoPulse Cnt M Igc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	1	1	_
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	J
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	-
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	V
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0299999993	0.029999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	✓
	-		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.57 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	1050
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.8099994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	2
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	3
TrqOvISta_ENAState_State_W_entitin	6.6999981
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.40000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.029999993
k_HapticDuration_Sec_f32	0.075000003
k_HapticFreq_Hz_f32	5.57399988
k_HapticReacttime_Sec_f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k_LKAMinVehSpd_Kph_f32	8.75
k_MaxHapticReqTime_Sec_f32	0.090000036
k_SWARLimiter_HwDeg_f32	22.1000004
k_ShiftLevRevTime_Sec_f32	0.0209999997 0.200000003
k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
	target_IrqOviSta_Peri_SwakTrgtangkequest_Hwbeg_t32 target_TrqOviSta_Peri_ShiftLeverIsInReverse_Cnt_lgc
target Rte Inst An TrgOvISta TrgOvISta Per1 Shiftl everleinDeverse Cot Igo	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrgOvlSta_Per1_TrgOscAmp_MtrNm_f32	target TrgOvlSta Per1 TrgOscAmp MtrNm f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrgOvlSta_Per1_TrgOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 1

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.269989		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.98999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.96000004		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	4	4	
TrgOvISta ESCState State M enum	0	0	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	,
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	,
TrgOvlSta HapticState State M enum	2	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	
TrgOvlSta Haptictime Sec M f32	2315.65991	2315.65991 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	•
TrgOvlSta LKAFault Cnt M lgc	1	1 ± 1	
TrgOvISta LKAPermFault Cnt M Igc	1	1 ± 1	,
TrgOvlSta LKAPermFault Cnt M u08	3	3 ± 1	
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrgOvlSta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	
TrgOvlSta ReadytoPulse Cnt M lgc	0	0 ± 1	•
target TrgOvISta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	
	_	2	,
target TrqOvlSta Per1 GMOSHOscillateState State enum.value	2		
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target TrqOvlSta Per1 LKAState State enum.value	3	3	•
target_TrqOvlSta_Per1_LKAState_State_enum.value		3 1±1	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3	·	•
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3	1 ± 1	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3 1 0.150000006	1 ± 1 0.150000006 ± 0.0009765625	

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.58 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	
TrqOvlSta_Haptictime_Sec_M_f32	0	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	
TrqOvlSta_LKAState_State_M_enum	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998	

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticPreq_Hz_f32	1.15499997 32.3019981
k HapticReacttime Sec f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target TrqOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiffLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target TrqOvISta Per1 TrqOscEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-390.01001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.019989 0
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

TrqOvlSta_Per1





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	1	1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.59 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	6100
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	26.8999996
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReacttime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	28.8999996
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_In$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg	_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	99	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSHOscillateState State enun	target TrgOvlSta Per1 GMOSHOscillateSta		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target TrqOvISta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-388.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.049988		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.599985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	0	0	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	6100	6100 ± 1	
TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta Haptictime Sec M f32	3285.03003	3285.03003 ± 1	
TrqOvISta_Haptictime_Sec_Iw_T32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.44645023	-5.44645023 ± 0.000009	
Indonoral Introductor International Control	0.77070060		
TrgOvISta LKAFault Cnt M lgc	1	1 + 1	
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1±1 1+1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 3		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1 3 ± 1	•





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 3.60 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	2
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	6100
rqOvlSta_Haptictime_Sec_M_f32	3285.03003
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.180000007
APAMaxHwTrq HwNm f32	0.560000002
APAMaxVehSpd Kph f32	12.6000004
	12.6000004 26.8999996
_ESCMaxVehSpd_Kph_f32	
_HapticAmplitude_MtrNm_f32	0.041999994
_HapticDuration_Sec_f32	0.104999997
_HapticFreq_Hz_f32	6.60200024
_HapticReacttime_Sec_f32	0.112000003
_LKAMaxVehSpd_Kph_f32	13.3000002
_LKAMinVehSpd_Kph_f32	12.25
_MaxHapticReqTime_Sec_f32	0.119999997
_SWARLimiter_HwDeg_f32	28.8999996
_ShiftLevRevTime_Sec_f32	0.0289999992
_StandstillThresh_Kph_f32	0.280000001
_StandstillTime_Sec_f32	0.0289999992
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
urget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
rget_Rte_inst_Ap_rrqOviSta_rrqOviSta_Perr_ESCState_State_enum rget_Rte_inst_Ap_rrqOviSta_TrqOviSta_Perr_ESCState_State_enum	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32



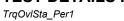


Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_	enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph	_f32 target_TrqOvlSta_Per1_MaxSecureVehic	cleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph	_f32 target_TrqOvlSta_Per1_MinSecureVehic	eSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_	Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle	e_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_0	Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosTrajHwAngle	_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDe	g_f3 target_TrqOvlSta_Per1_SWARTrgtAngR	equest_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInReverse_Cnt_Ignum_ShiftLeverlsInRe$	target_TrqOvlSta_Per1_ShiftLeverIsInRe	verse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_Mtr	Nm_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_0	Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz	_f32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-386.450012		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.079987		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	2.66000009		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	59.5999985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	2639.65991	2639.65991 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	659.880005	659.880005 ± 1	~
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrgOvlSta Hapticdur Cnt M u16	6100	6100 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.61 (Repeat Count = 1)	▼
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	6100
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	2
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1
TrqOvISta PosSrvoHwAngle HwDeg M f32	8.89999962
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	26.8999996
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReacttime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	28.8999996 0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvISta Per1 GMOSH APAMfgEnable Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc$	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
	target TrqOvISta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ab TrgOvista Frontista Peri TrgOscAmb Mirnm 132	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lqc	target rigoviola rei rigosceriable ont igo
target_Rte_Inst_Ap_IrqOvIsta.IrqOvIsta_Per1_IrqOscAmp_wtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-384.670013		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	363.109985		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	0	0	•
TrqOvlSta_ESCState_State_M_enum	1	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	
0 = 1 = = 0 = 0 =	1	0.0419999994 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 I 0.0009703023	
	0.0419999994	0.0419999994 ± 0.0009763623	•

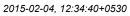
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.62 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	225.639999
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	684
TrqOvlSta_Haptictime_Sec_M_f32	1345
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	4.5





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.140000001
k_APAMaxHwTrq_HwNm_f32	0.239999995
k_APAMaxVehSpd_Kph_f32	5.4000001
k_ESCMaxVehSpd_Kph_f32	17.2999992
k_HapticAmplitude_MtrNm_f32	0.0179999992
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	4.55999994
k_HapticReacttime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.69999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.0599999987
k_SWARLimiter_HwDeg_f32	20.2999992
k_ShiftLevRevTime_Sec_f32	0.0130000003
k_StandstillThresh_Kph_f32	0.119999997
k_StandstillTime_Sec_f32	0.0130000003
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApA$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lqc	
target Rte Inst Ap TrqOvISta.TrqOvISta Per I_StillLevensinReverse_Crit_igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per I TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscEnable_Cnt_gc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target TrqOvISta Per1 APARcoverableFaults Cnt Igc.value	1
target_TrqOvISta_Per1_APARecoverablePaulis_Crit_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	1
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
target TrgOvlSta Per1 CCWPosition HwDeg f32.value	-382.890015
target TrqOvISta Per1 CWFound Cnt Igc.value	-362.090015
target_TrqOvISta_Per1_CWPoultio_Cnt_igc.value target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	363.140015
target TrqOvISta Per1 ESCFault Cnt Igc.value	1
target TrqOvISta_Per1_ESCFault_Cnt_igc.value	1
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_ESGRequest_Ont_igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value	0
target TrqOvISta Per1 HwTorque HwNm f32.value	1.25999999
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value	1.2393999
target TrqOvISta Per1 LKAInhibit Cnt Igc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_Ervnkequest_Onl_gc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	40
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995
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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	0	0	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	308.519989 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	684	684 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973	-8.02999973 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0179999992	0.0179999992 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.55999994	4.55999994 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.63 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	g target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwD	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	~ 400	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>g_</u> 132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	lao	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_ target_TrqOvlSta_Per1_ESCRequest_Cnt_I	-	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_er		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enur			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	V - '		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	3 target_TrqOvlSta_Per1_SWARTrgtAngRequ	uest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	1_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-381.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.170013		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1		
target TrgOvlSta Per1 HapticRequest Cnt Iqc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	-
TrqOvISta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOviota_Etv>otate_ivi_etiatii	U	0	
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	3.70000005	3.70000005 ± 0.0009765625	•

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3.70000005	3.70000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Namo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k APAMaxHwTrq HwNm f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k ESCMaxVehSpd Kph f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.66000026
k_HapticDuration_Sec_f32	1.6499998
	54.9179993
k_HapticFreq_Hz_f32	
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected	d_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_0	Cnt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSH APAMfgEnable Cn	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fer1_GMOSH_ESCMfgEnable_Cn	
target_Rte_linst_Ap_TrqOviSta.TrqOviSta_Fe11_GMOSH_LKAMfgEnable_Cn	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_GMOSH_ERAMIgETiable_Cri target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target TrqOvlSta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32



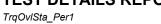


Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngReque target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_l		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreg Hz f32	target TrqOvlSta Per1 TrqOscFreq Hz f32	3-	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-379.329987		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.200012		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1 0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	121		
toward TaxOulOta Dard DarTariFrankla Out law unive	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value			
target_frqOvista_Peri_PosTrajEnable_Cht_igc.value target_TrqOviSta_Peri_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
	0.109999999 3.70000005		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005	Expected Value	Result
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	3.70000005 1	Expected Value	Result
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name	3.70000005 1 Actual Value	· ·	Result
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3.70000005 1 Actual Value 2 3 0	2 3 0±1	**************************************
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3.70000005 1	2 3 0±1 84736.2422±1	~
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3.70000005 1	2 3 0±1 84736.2422±1	**************************************
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	3.70000005 1	2 3 0±1 84736.2422±1 0	**************************************
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	3.70000005 1	2 3 0±1 84736.2422±1 0 1 33000±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	3.70000005 1	2 3 0±1 84736.2422±1 0 1 33000±1 0±1	**************************************
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 3.70000005±0.0009765625	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 3.70000005	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EscState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc TrqOvlSta_ReadytoPulse_Cnt_M_lgc	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 3.70000005 1	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 3.70000005±0.0009765625 1±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticUstate_State_M_enum TrqOvlSta_HapticUstate_State_M_enum TrqOvlSta_HapticUstate_Sec_M_f32 TrqOvlSta_HapticUstate_Sec_M_f32 TrqOvlSta_HapticUstate_Sec_M_f32 TrqOvlSta_HapticUstate_Sec_M_f32 TrqOvlSta_LKAFault_Cnt_M_u16 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 3.70000005 1 0	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 3.70000005±0.0009765625 1±1 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotlar_Cnt_M_u16 TrqOvlSta_Hapticitime_Sec_M_f32 TrqOvlSta_Hapticitime_Sec_M_f32 TrqOvlSta_Hapticitime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APAState_State_enum.value	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 3.70000005 1 0 2	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 3.70000005±0.0009765625 1±1 0±1 2	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 3.70000005 1 0 2	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 0±1 3±1 0 3.70000005±0.0009765625 1±1 0±1 2 3	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 3.70000005 1 0 2 3 1	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 3.70000005±0.0009765625 1±1 0±1 2	
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_PerFault_Cnt_M_lgc TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_Pesc_Ort_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 3 0 3.70000005 1 0 2 3 1	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 0±1 3±1 0 3.70000005±0.0009765625 1±1 0±1 2 3 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EscState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotl_Cnt_M_u16 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_gc TrqOvlSta_LKAPermFault_Cnt_M_gc TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_CosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 3 0 3.70000005 1 0 1 3.70000005 0.660000026	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 3.70000005±0.0009765625 1±1 0±1 2 3 1 0 1±1 3.70000005±0.0009765625 0.660000026±0.0009765625	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EsCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticotur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Harbrictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_gc TrqOvlSta_LKAPermFault_Cnt_M_gc TrqOvlSta_Per1_LKAState_State_Menum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3.70000005 1 Actual Value 2 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 3 0 3.70000005 1 0 2 3 1 0 1 3.70000005	2 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 3.70000005±0.0009765625 1±1 0±1 2 3 1 0 1±1 3.70000005±0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 3.65 (Repeat Count = 1)	J. Committee of the com
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	5
TrqOvISta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	0
TrqOvISta PosSrvoHwAngle HwDeg M f32	1.3999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003 100
k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	100.599998
k ShiftLevRevTime Sec f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Each 1998) and (See Each 1998) and (See Each 1998) are also as a function of the context of the$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgauge and the property of the prope$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target TrqOvlSta Per1 ESCFault Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enumeration and the properties of the properties$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target TrqOvlSta Per1 HwTorque HwNm f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_rrqOvlSta_Per1_tw10rque_nwnin_isz
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOviSta_Per1_LKARequest_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAState State enum	target TrqOvlSta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1



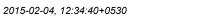


Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-377.549988		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.230011		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	5	5	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	5	5	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.66 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	5270
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1

TrqOvlSta_Per1





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k APAMaxVehSpd Kph f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k HapticAmplitude MtrNm f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k LKAMaxVehSpd Kph f32	89.3000031
k LKAMinVehSpd Kph f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k SWARLimiter HwDeg f32	0
k_ShiftLevRevTime_Sec_f32	0.188999996
k StandstillThresh Kph f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target TrgOvlSta Per1 APADrvrInterventionDetected Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrgOvlSta Per1 APAState State enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target TrgOvlSta Per1 ESCFault Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAState State enum	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ShiftLeverlsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreg Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-375.769989
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.26001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	•
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.67 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	5
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	3
TrqOvlSta_Hapticdur_Cnt_M_u16	5270
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	·g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 _ 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	0 <u>2</u>	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State end		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	.lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-373.98999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360.200012		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrqOvISta Per1 HwTorque HwNm f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	5	5	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	~
TrqOvlSta_HapticState_State_M_enum	3	3	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	V
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	5	5	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
	1
TrqOvISta_APAState_State_M_enum	4
FrgOvISta_ESCState_State_M_enum	
FrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8867.28027 1274.76001
FrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	8758.37988
FrqOvISta_ElapsedStandstillTime_Sec_M_f32	1
FrqOvISta_HapticState_State_M_enum	23775
FrqOvISta_Hapticdur_Cnt_M_u16	
FrqOvISta_Haptictime_Sec_M_f32	6679.83008
FrgOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.32000005 0.0328000002
FrqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	
FrqOvISta_LKAFault_Cnt_M_lgc	1
FrqOvISta_LKAPermFault_Cnt_M_lgc	1 2
FrqOvISta_LKAPermFault_Cnt_M_u08	2
FrgOvISta_LKAState_State_M_enum	
FrgOvISta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
x_APAIncludeHaptic_Cnt_lgc	
<_APAMaxHwTrqTime_Sec_f32 <_APAMaxHwTrq_HwNm_f32	0.25
(_APAMaxHwTrq_HwNm_f32	1.12
<_APAMaxVehSpd_Kph_f32	25.2000008
C_ESCMaxVehSpd_Kph_f32	43.700008
(_HapticAmplitude_MtrNm_f32 (_HapticPuration_Soc_f32	0.083999989
(_HapticDuration_Sec_f32	0.20999993
K_HapticFreq_Hz_f32	10.199998
C_HapticReacttime_Sec_f32 HAMS: \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \(\text{AMS: } \t	0.224000007
<pre>(_LKAMaxVehSpd_Kph_f32</pre>	26.6000004
C_LKAMinVehSpd_Kph_f32	24.5
<_MaxHapticReqTime_Sec_f32	0.224999994
C_SWARLimiter_HwDeg_f32	21.7999992
C_ShiftLevRevTime_Sec_f32	0.057
x_StandstillThresh_Kph_f32	0.560000002
StandstillTime_Sec_f32 StandstillTime_Sec_f33 StandstillTime_Sec_f34 StandstillTime_Sec_f44 StandstillTim	0.057
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-998.710022		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	429.160004		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	5.11000013		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1274.76001	1274.76001 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	8758.37988	8758.37988	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	23775	23775 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	6679.83008	6679.83008 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005	-1.32000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004	16.6000004 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	V
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	16.6000004	16.6000004 ± 0.0009765625	
target TraOvISta Per1 TraOscAmp MtrNm f32 value	I N N830000080		
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0839999989	0.0839999989 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	0.0839999989 1 10.1999998	0.0839999989 ± 0.0009765625 1 ± 1 10.1999998 ± 0.0009765625	7

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~	
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V	



Test Step 3.69 (Repeat Count = 1)	la constant de la con
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1 0
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 3
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	2
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvISta ReadytoPulse Cnt M Igc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32 k_HapticReacttime_Sec_f32	56.973999 1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target TrqOvlSta Per1 CCWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fei1_GMOSH_AFAMilgEnable_Cit_gc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvonwArigle_Hwbeg_isz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
	10
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	0

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	849.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrgOvlSta ESCState State M enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	87934.9609	87934.9609 ± 1	•
TrgOvlSta ElapsedStandstillTime Sec M f32	404784	404784	-
TrqOvlSta HapticState State M enum	1	1	-
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta LKAPermFault Cnt M Igc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	-
TrqOvlSta ReadytoPulse Cnt M lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	-
target TrqOvlSta Per1 ESCState State enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target TrqOvlSta Per1 LKAState State enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	-
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0	0 ± 0.0009765625	-
○ - · 1 1 1 1 1 1 - 1 1 1 1 1 1 1 1 1	1.5		
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.70 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993 20.2000008
k_SWARLimiter_HwDeg_f32 k ShiftLevRevTime Sec f32	0.0529999994
k StandstillThresh Kph f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	849.059998
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1
target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.60000024
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1 0.150000006
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784	404784	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992	43.7999992 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	43.7999992	43.7999992 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.71 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	52	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	rc.	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	849.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999 0		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.400015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	1	1	- Ttoount
TrqOvISta_ESCState_State_M_enum	3	3	✓
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	✓
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	404784	404784	
TrqOvISta_HapticState_State_M_enum	1	1	•
TrqOvISta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	_
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.4000001	-3.4000001 ± 0.000009	-
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.72 (Repeat Count = 1) ✓	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOviSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta ElapsedStandstillTime Sec M f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvISta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k APAMaxVehSpd Kph f32	138.600006
k ESCMaxVehSpd Kph f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k HapticReacttime Sec f32	1.23199999
k LKAMaxVehSpd Kph f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k SWARLimiter HwDeg f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target TrqOvISta Per1 APAState State enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_reri_ArAState_State_entime target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target TrqOvISta Per1 CCWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target TrqOvISta Per1 CWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCFault Cnt Igc	target TrqOvISta Per1 ESCFault Cnt Igc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvIsta Per1 ESCRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_mqoviota.mqoviota_ren_sostate_state_state enuit	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlota_ren_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvIsta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
g	





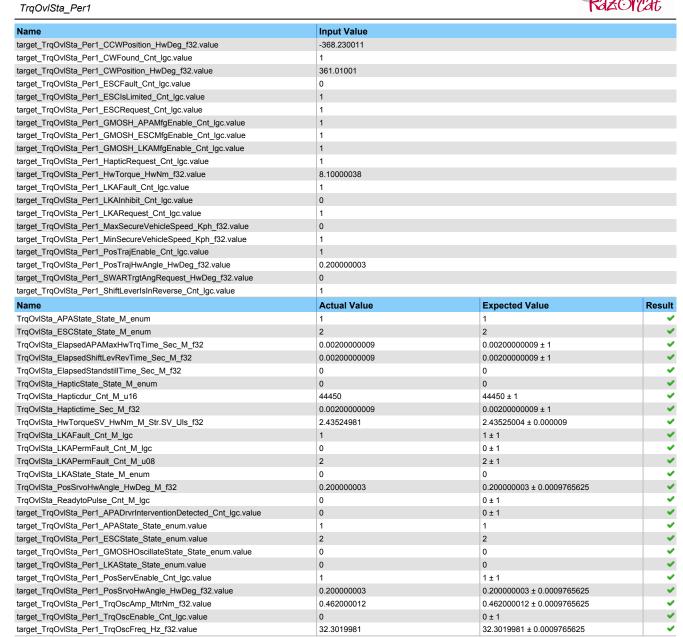
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-368.230011		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	361.01001		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta_Per1_ESCIsLimited Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_E3CRequest_Crit_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	V
TrqOvlSta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	- 4
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	1 2	2	-
target_TrqOvlSta_Per1_ESCState_State_enum.value			
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	
target_rqoviSta_Peri_PosServEnable_Cnt_igc.value target_TrqOviSta_Peri_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	-
target_TrqOviSta_Per1_PosStvonwArigie_nwbeg_isz.value target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0.462000012	0.462000012 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	-
0q==.= =q=sor.rod_r.e_roz.rondo	1	1. 52.222. 2 5.0000. 00020	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Name	Test Step 3.73 (Repeat Count = 1)	▼
No. Impure Impu	Name	Input Value
Tropolities Section		·
TropOstes Bispeed Name Interface Set. M. 192 0 TropOstes Bispeed Name Interface Set. M. 192 0 TropOstes Bispeed Name Interface Set. M. 192 0 TropOstes Industrian Conf. M. 192 0 TropOst	TrqOvlSta_APAState_State_M_enum	
TROMOSE Bispectabilities See, M. 20 TropOSE Bispectabilities See,	TrqOvlSta_ESCState_State_M_enum	1
Topockas Supposed Service Service Supposed Service Servi	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
Topicida Sparticus State March	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TROUBLE SPRINGER CH. M. 151 TROUBLE SPRINGER SE M. 152 TROUBLE SPRINGER SE	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	
TROMBIS Informacy Hole Min SISY US 102 TROMBIS INFORMACY Hole Min SISY US 102 TROMBIS INFORMACY Hole Min SISY US 10 TROMBIS INFORMACY HOLE MIN SIS		i e
TEROMES INFORMED/ LINKING MS 05 Mg 102 TEROMOS INFORMED/ LINKING MS 105 Mg 102 TEROMOS INFORMED (CPI Mg 10) TEROMOS INFORME		
TROWSE JAKSHER MATERIAL CH M. 196		-
TROWING JAKPent LOT M. 190 TROWING JAKPent JC M. 190 TROWING		
TECOMOSIS LIA/Particul Cott M		
TODO/Sile J NOSHINE SINE M, M you A PARAMENHORIS COLLING A PARAMENHORIS COLL	TrqOvlSta_LKAPermFault_Cnt_M_lgc	
1,000/058 Perside Crit My De	TrqOvlSta_LKAPermFault_Cnt_M_u08	1
Track/instance Trac	TrqOvlSta_LKAState_State_M_enum	0
A.P.A.MacHort True, Dec. 12	TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	
A. PAMAMAN Trailing Sec., 152 0.00160088	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	
A.PAMAWAYISID KN. 122		
K. JOANAW-NISPEL (Apr.) 12 138.000006		
Language Main 12		
k. Hapscharden, 1924 k. Hapschren, 192, 192 k. Hapschren, 192, 192 k. Hapschren, 192, 192 k. Hapschren, 192, 192 k. Hapschren, 192, 193 k. Hapschren, 192, 193 k. Hapschren, 192, 193 k. LAMANIVERS, 193 k. Sandatillines, 193 k. Sandatill	_ :=:=	
k, Hapsticheactime, Sec. 132 k, Hapsticheactime, Sec. 132 k, Hapsticheactime, Sec. 132 k, LARAMARVSB, Kyh. 132 k, Simitar, Hapsticheactime, Sec. 132 k, Si	k_HapticAmplitude_MtrNm_f32	
k, Lykondevelscher Kph. 122 k, Likondarvelscher Kph. 123 k, Likondarvelscher Kph. 124 k, Lykondevelscher Kph. 125 k, Shinteurker Time, Sec. 122 k, Shinteurker Kph. 125 k, Shi	k_HapticDuration_Sec_f32	
L KAMAINVerSigd Koft, 132 L KAMAINVerSigd Koft, 132 L KAMAINVerSigd Koft, 132 L L KAMAINVERSigd Koft, 132 L SIMARAILIMITE, HACRES, 132 S O 0.800000024 L SIMARAILIMITE, HACRES, 132 O 0.8000000024 L SIMARAILIMITE, HACRES, 132 O 0.8000000024 L SIMARAILIMITE, HACRES, 132 O 0.800000000000000000000000000000000000	k_HapticFreq_Hz_f32	32.3019981
k, LMAMPHONEOR (pt.) 12 k, MarthaplioRea_Time, Sec. (132 k, Shintikur-New Time, Sec. (132 k, Shinti	k_HapticReacttime_Sec_f32	1.23199999
k, Marklamiter I-Mong, 152 k, Shintlamer I-M	k_LKAMaxVehSpd_Kph_f32	146.300003
k, SindstellThesh, Kpn, 132 k, SlandstellThesh, Kpn, 132 logo Relationsh, TrqOviSta, Perl APADrivinterventionDetected, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APADrivinterventionDetected, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APADrivinterventionDetected, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARomecoverableFaults, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARomecoverableFaults, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARoguest, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARoguest, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARoguest, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. CoWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CMSOrd, Prof. Inst. Ap. TrqOviSta, Prof. OMSOrd, Prof. MSOrd, Prof. Inst. Ap. TrqOviSta, Prof. OMSOrd, Prof. MSOrd, Prof. Inst. Ap. TrqOviSta, Prof. OMSOrd, Prof. Inst. Inst. Ap. TrqOviSta, Prof. OMSOrd, Prof. Inst. Inst. Ap. TrqOviSta, Prof. OMSOrd, Prof. Inst. Inst. Ap. TrqOviSta, Prof. Inst. Ap. TrqOviSta, Prof. Inst. Ap. TrqOviSta, Prof. Inst. Ap. TrqOviSta, Prof. Inst.	k_LKAMinVehSpd_Kph_f32	
k, Sindhardiffresh, Kyn, 132 k, Slandstiffresh, Kyn, 132 k, Slandstiffresh, Kyn, 132 c, Slandstiffresh, 132 c, Slandstiffre		
SandstillTries_Sec_f32 Inspect Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARceuvest_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARceuvest_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARceuvest_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ExcStarle_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ExcStarle_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ExcStarle_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOVSta_Per1_ExcStarle_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOVSta_Per1_CMOVSta_Per1_CMOVSta_Per1_ExcStarle_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOVSta_Per1_CMOVSta_Per1_CMOVSta_Per1_ExcStarle_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOVSta_Per1_CMOVSta_Per1_CMOVSta_Per1_ExcStarle_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOVSta_Per1_CMOVSta_Per1_CMOVSta_Per1_ExcStarle_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Per1_LMOVSta_Pe		
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target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Ent_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCReques_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCReques_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DAVISta_Enable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DAVISta_Enable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DAVISta_Enable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MSTA_Enable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPantit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPantit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPantit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPantit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPantit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPantit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPantit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DxStroviSta_DxStroviStroviSta_Perl_LKAPantit_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DxStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStroviStrovi	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
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target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEAULT_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MoSH_ESCMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapitcRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Describable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Describable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqO	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	
target_Rte_Inst_Ap_TrqOv/Sta_Perf_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_Perf_ESCIsalmited_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCIsalmited_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCStalmited_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCStale_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCSMSMS_Bate_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCMSMS_BESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_MaxSecure/VehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_DasSev/Enable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_DasSev/Enable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_PosSev/Enable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_PosSev/Enable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_Perf_PosSev/Enable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_Perf_PosSev/Enable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_Perf_PosSev/Enable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_Perf_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Perf_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Perf_ShiffLeverIsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ShiffLeverIsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ShiffLeverIsinRevers		
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target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARAIIt_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARsecureVehicleSpeed_Kpf_32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kpf_32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_APMifgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_APMifgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMifgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMifgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_State_enum target_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_State_enum target_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SosRomP_MtNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SosRomP_MtNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsSca_Pert_MtnPage_trqost_TrqOvlSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SosRomP_MtNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCaPaple_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCaPaple_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCaPaple_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCaPaple_Cnt_Igc target_TrqOvlSta_Pert_TrqOsCaPaple_Cnt_Igc target_TrqOvlSta_Pert_APARecoverableFaults_Cnt		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_APAMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvoftwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SiffLeverIsinReverse_Cnt_lgc target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOvScan_DwTkTym_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOvScan_DwTkTym_f32 target_TrqOviSta_Per1_ShiffLeverIsinReverse_Cnt_gc target_TrqOviSta_Per1_TrqOvScan_DwTkTym_f32 target_TrqOviSta_Per1_APARequest_Cnt_Igc_value 0 target_TrqOviSta_Per1_APARequest_Cnt_Igc_value		
target_Rte_Inst_Ap_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Perl_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOsCamp_MtrNm_f32 target_TrqOvISta_Perl_TrqOsCamp_MtrNm_f32 target_TrqOvISta_Perl_TrqOsCamp_MtrNm_f32 target_TrqOvISta_Perl_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Perl_APARequest_Cnt_Igc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_Hwhm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASInte_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASite_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SorAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_DosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_state_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Perl_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Perl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_SvARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_SvARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Perl_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Perl_APARequest_Cnt_lgc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	
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target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 11	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
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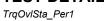
Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.74 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998

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Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	1.15499997 32.3019981
k HapticReacttime Sec f32	1.23199999
k LKAMaxVehSpd Kph f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0 target TraCviSta Per1 APADayInterventionDetected Cet Inc.
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 261 01001
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	361.01001 0
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.20000003





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.75 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target TrqOvlSta Per1 CCWPosition HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	59_102	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvISta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target TrqOvISta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlOta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKARequest Cnt Igc	target TrqOvISta Per1 LKARequest Cnt Ig	IC .	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosSrvoHwAngle_H target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajErlable_Cff_igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvISta Per1 PosTrajHwAngle Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3		-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	361.01001		
target_TrqOvlSta_Fer1_ESCFatit_Crit_tgc.value target TrqOvlSta_Per1_ESCIsLimited Cnt_tgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.20000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvISta_ESCState_State_M_enum	2	2	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1 1 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.002000000009 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	1 ± 1 0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2±1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	0 ± 1	✓





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

lame	Input Value
te_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
qOvISta_APAState_State_M_enum	1
qOvlSta_ESCState_M_enum	0
qOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
qOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
qOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
qOvlSta_HapticState_State_M_enum	1
qOvlSta_Hapticdur_Cnt_M_u16	25475
qOvlSta_Haptictime_Sec_M_f32	186740.953
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.6499998
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
qOvlSta LKAFault Cnt M lgc	0
qOvlSta LKAPermFault Cnt M lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	2
qOvlSta_LKAState_State_M_enum	3
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
qOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	1
APAMaxHwTrqTime_Sec_f32	0.144999996
APAMaxHwTrq_HwNm_f32	6.15999985
APAMaxVehSpd Kph f32	138.600006
ESCMaxVehSpd Kph f32	133.699997
HapticAmplitude_MtrNm_f32	0.462000012
HapticDuration_Sec_f32	1.55999994
HapticFreq_Hz_f32	51.8339996
HapticReacttime_Sec_f32	1.23199999
LKAMaxVehSpd_Kph_f32	146.300003
LKAMinVehSpd_Kph_f32	134.75
MaxHapticReqTime_Sec_f32	0.600000024
SWARLimiter_HwDeg_f32	17.2000008
ShiftLevRevTime_Sec_f32	0.308999985
StandstillThresh_Kph_f32	3.07999992
StandstillTime_Sec_f32	0.308999985
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_C	int_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_l	gc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_	enun target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt	_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Nome	Innut Value		
Name target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	Input Value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_I	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	lwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_fraction{ $	3 target_TrqOvlSta_Per1_SWARTrgtAngReq	uest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReve	rse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn	n_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	500.600006		
target_TrqOvIsta_Per1_PosTrajErlable_Crit_rgc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	Result
TrqOvISta_ESCState_State_M_enum	3	3	
TrgOvISta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	79938.1563	79938.1563 ± 1	*
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	
TrgOvISta HapticState State M enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-1.64999998	-1.64999998 ± 0.000009	-
TrqOvlSta LKAFault Cnt M lgc	0	0 ± 1	✓
TrqOvISta LKAPermFault Cnt M Igc	0	0 ± 1	✓
TrqOvISta LKAPermFault Cnt M u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32		0.450000000 - 0.0000705005	
	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0.150000006	0.150000006 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value			~
	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1 0	1 ± 1 0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	1 0 1	1 ± 1 0 ± 1 1	0
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	1 0 1 3	1±1 0±1 1 3	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1 0 1 3 2	1±1 0±1 1 3 2	• • • • • • • • • • • • • • • • • • •
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	1 0 1 3 2 3	1±1 0±1 1 3 2 3	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	1 0 1 3 2 3 1 0.150000006 0.462000012	1±1 0±1 1 3 2 3 1±1 0.150000006±0.0009765625 0.462000012±0.0009765625	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 0 1 3 2 3 1 0.150000006	1 ± 1 0 ± 1 1 3 2 3 1 ± 1 0.150000006 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 3.77 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	25475
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	186740.953 -1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrgOvlSta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006 133.699997
k_ESCMaxVehSpd_Kph_f32 k_HapticAmplitude_MtrNm_f32	0.462000012
k HapticDuration Sec f32	1.55999994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.308999985 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target TrqOvlSta Per1 ESCRequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	V - 1
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_IrqOviSta.IrqOviSta_Peri_MaxSecurevenicleSpeed_Kpn_132 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Peri_MinSecureVehicleSpeed_Kph_132	target_rqovista_Peri_maxsecureveniciespeed_kpn_rs2 target_rqovista_Peri_maxsecureveniciespeed_kpn_rs2
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
	1

TrqOvlSta_Per1

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Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrqOvlSta_ESCState_State_M_enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	✓
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.78 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	25475
TrqOvlSta_Haptictime_Sec_M_f32	186740.953
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992

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TrgOvISta Per1 Input Value TrgOvISta ReadytoPulse Cnt M lgc k APAIncludeHaptic Cnt lqc k APAMaxHwTrgTime Sec f32 0.144999996 k_APAMaxHwTrq_HwNm_f32 6.15999985 k APAMaxVehSpd Kph f32 138.600006 k_ESCMaxVehSpd_Kph_f32 133.699997 k_HapticAmplitude_MtrNm_f32 0.462000012 k_HapticDuration_Sec_f32 1.55999994 k_HapticFreq_Hz_f32 51.8339996 k HapticReacttime Sec f32 1 23199999 k_LKAMaxVehSpd_Kph_f32 146.300003 k_LKAMinVehSpd_Kph_f32 134 75 k MaxHapticReqTime Sec f32 0.600000024 k SWARLimiter HwDeg f32 17 2000008 k ShiftLevRevTime Sec f32 0.308999985 k_StandstillThresh_Kph_f32 3.07999992 k_StandstillTime_Sec_f32 0.308999985 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APAD target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig| target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc$ target_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_APAState_State_enum $target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc$ target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc$ target TrgOvISta Per1 CWFound Cnt Igc $target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosition_HwDeg_f32$ target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target TrgOvISta Per1 ESCFault Cnt lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCRequest Cnt lgc target TrqOvISta Per1 ESCRequest Cnt Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_ESCState_State_enum target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enum target TrqOvlSta Per1 GMOSHOscillateState State enum $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$ target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$ target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc$ target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32$ target TrgOvlSta Per1 HwTorque HwNm f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc$ $target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc$ target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKARequest Cnt Igc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target TrgOvlSta Per1 LKAState State enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 $target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc$ target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc $target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$ target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreg Hz f32 target TrqOvlSta Per1 TrqOscFreq Hz f32 $target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value$ 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -1029.01001 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 799.659973 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value n target TrqOvlSta Per1 HwTorque HwNm f32.value 8.10000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value n target TrqOvlSta Per1 LKAInhibit Cnt Igc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value $target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value$ target TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value

0.150000006

target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.79 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	186740.953
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1.
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.5599994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	IC	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.		·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvISta_Per1_CWFound_Cnt_Igc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	799.659973		
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target TrqOvlSta_Per1_HwTorque HwNm f32.value	8.10000038		
0 = 1 = 1 = =			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	~
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	1	1 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08		0 ± 1 2 ± 1	
	0		~
TrqOvISta_LKAPermFault_Cnt_M_u08	0 2	2 ± 1	

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TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Nama	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
FrqOvlSta_HapticState_State_M_enum	2
FrqOvlSta_Hapticdur_Cnt_M_u16	45000
FrqOvlSta_Haptictime_Sec_M_f32	228811.703
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
FrqOvlSta_LKAFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	1
<pre>C_APAMaxHwTrqTime_Sec_f32</pre>	0.298999995
APAMaxHwTrg HwNm f32	9.2799973
CAPAMaxVehSpd_Kph_f32	208.800003
CSCMaxVehSpd Kph f32	30.399996
	0.46000008
_HapticAmplitude_MtrNm_f32	
x_HapticDuration_Sec_f32	1.74000001
:_HapticFreq_Hz_f32	59.0299988
C_HapticReacttime_Sec_f32	1.85599995
x_LKAMaxVehSpd_Kph_f32	20
c_LKAMinVehSpd_Kph_f32	203
C_MaxHapticReqTime_Sec_f32	0.239999995
C_SWARLimiter_HwDeg_f32	21.3999996
s_ShiftLevRevTime_Sec_f32	0.0610000007
_StandstillThresh_Kph_f32	4.63999987
_StandstillTime_Sec_f32	0
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetec	cted_Cnt_ target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFault	ts_Cnt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_C	int_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvISta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_inst_Ap_mqovista.mqovista_Pen_EscRequest_cnt_igc arget_Rte_inst_Ap_mqovista.TrqoviSta_Pen1_EscRequest_cnt_igc	target_TrqOvISta_Per1_ESCState_State_enum
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arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_Sta	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrgOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	1	1	- 100 u.i.
TrqOvISta_ESCState_State_M_enum	4	4	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	
TrqOvlSta_HapticState_State_M_enum	0	0	-
TrgOvlSta Hapticdur Cnt M u16	0	0 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	
TrgOvISta_LKAPermFault Cnt M lgc	1	1±1	
TrgOvISta LKAPermFault Cnt M u08	3	3±1	-
TrgOvISta LKAState State M enum	3	3	
TrqOvISta_ExAState_M_entitin TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.18999998	0.189999998 ± 0.0009765625	-
	0.169999996		
TrqOvISta_ReadytoPulse_Cnt_M_lgc		0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	V
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 3.81 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	45000
TrqOvlSta_Haptictime_Sec_M_f32	228811.703
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-2.70000005 0.507000029
TrqOvIsta_HW10rqueSv_HW10rlm_M_Str.N_Dis_IS2 TrqOvISta_LKAFault_Cnt_M_lgc	0.507000029
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta LKAPermFault Cnt M u08	2
TrqOviSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996 0.460000008
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	1.74000001
k HapticFreg Hz f32	59.0299988
k HapticReacttime Sec f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0 target TraCyliSta Part ADADaylatanyantianDetected Cat Iga
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	V - 1
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_NiinSecurevenicieSpeed_Rpn_isz target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
goqo.nota_r or r_oom ound_ont_igo.value	<u> </u>





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	✓
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target TrgOvlSta Per1 TrgOscEnable Cnt Igc.value	0	0 ± 1	✓
mile - independent - independent - independent			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.82 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	45000
TrqOvlSta_Haptictime_Sec_M_f32	228811.703
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973 208.800003
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	30,399996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0 torrest TraCulSta Part ADADau(lateauantianDetected Cat Inc.
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	2 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 SWARTrgtAngRequest HwDeg f3	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	1
target_TrqOvISta_Per1_ESCISLIMIted_Cnt_igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvIsta_reT_ESCRequest_Crit_igc.value target_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1 0.18999998
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	

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Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797	91133.6797 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015	49.4000015 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	49.4000015	49.4000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.83 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	228811.703
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventInterventI$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

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Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFal	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	IC	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvISta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.		·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvISta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	1		
	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-887.609985		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797	91133.6797 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	•
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	1	1 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	-
		1±1	•
TrqOvlSta LKAFault Cnt M lgc	1		
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	1		
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08			•
TrqOvISta_LKAPermFault_Cnt_M_lgc	1 3	1 ± 1 3 ± 1	

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Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
ΓrgOvlSta APAState State M enum	
rqOvISta_ESCState_State_M_enum	1
rqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	0
	0
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
rqOvISta_HapticState_State_M_enum	
rqOvISta_Hapticdur_Cnt_M_u16	44450
rqOvISta_Haptictime_Sec_M_f32	0
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	1
rqOvISta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.601666689
_APAMaxHwTrq_HwNm_f32	0
_APAMaxVehSpd_Kph_f32	138.600006
_ESCMaxVehSpd_Kph_f32	12
_HapticAmplitude_MtrNm_f32	0.462000012
_HapticDuration_Sec_f32	1.15499997
_HapticFreq_Hz_f32	32.3019981
_HapticReacttime_Sec_f32	1.23199999
_LKAMaxVehSpd_Kph_f32	146.300003
_LKAMinVehSpd_Kph_f32	100
_MaxHapticReqTime_Sec_f32	0.60000024
_SWARLimiter_HwDeg_f32	30
_ShiftLevRevTime_Sec_f32	0.308999985
_StandstillThresh_Kph_f32	0
_StandstillTime_Sec_f32	0.308999985
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_state_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
urget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
irget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target TrqOvlSta Per1 HapticRequest Cnt Igc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HwTorque_HwNm_f32	target_TrqOviota_r cri_riapicricquest_On_ge target_TrqOviota_Per1_HwTorque_HwNm_f32
.goop_riqoviota.riqoviota_r or i_riw roiquo_riwinii_i02	





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	361.01001		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target TrqOvISta_rer1_ESCISEIIIIIted_Off_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	V
TrqOvlSta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	2	0 2	-
target_TrqOvlSta_Per1_ESCState_State_enum.value			
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	-
target_TrqOvlSta_Per1_PosStvortwAngre_nwbeg_i3z.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.462000012	0.402000012 ± 0.0009763623	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	-
3	1	1. 32.222. 2 3.0000. 00020	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	V

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