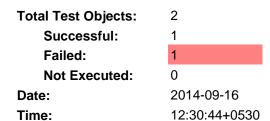
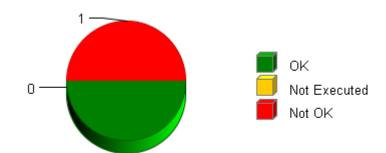


Summary

Overall Test Object Results (including Coverage)





Selected Project Items

Test Object "CBD_UnitTest/TrqArblim_FltInj/TrqArblim_Init" Test Object "CBD_UnitTest/TrqArblim_FltInj/TrqArblim_Per1"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

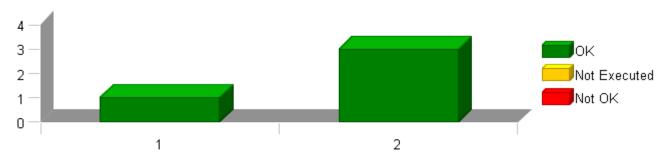
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Modified Condition / Decision Coverage,

Multiple Condition Coverage

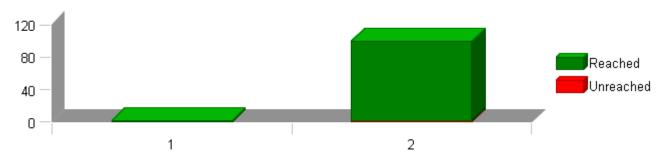
Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

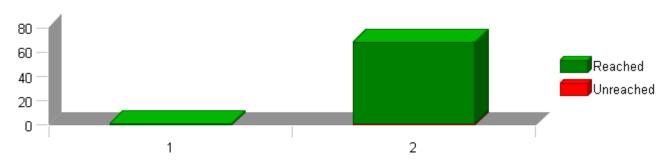


Statement (C0) Coverage: Total Statements for Each Test Object



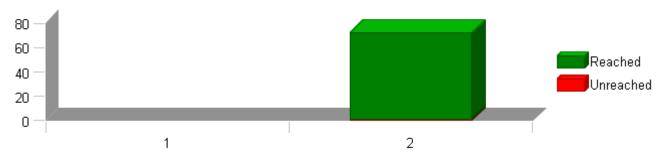
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

MC/DC Coverage: Total Condition Combinations for Each Test Object

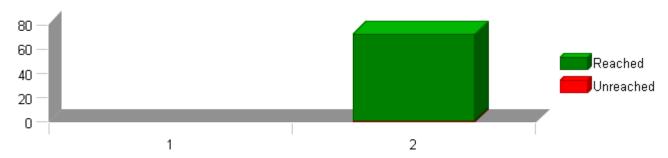


The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2014-09-16, 12:30:44+0530



Test Object List

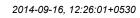
Project TrqArblim

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	MC/DC	MCC	Test Cases Result
	TrqArblim	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed
	CBD_UnitTest	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed
	TrqArblim_FltInj	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed
1	<u>TrqArblim_Init</u>	100 %	100 %	-	-	1 of 1 passed
2	<u>TrqArblim_Per1</u>	99 %	98.52 %	98.61 %	98.61 %	3 of 3 passed

© Report created by TESSY V3.1.7, report template V2.0





 Project
 TrqArblim

 Module
 TrqArblim_FltInj

 Test Object
 TrqArblim_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

TrqArblim_Init

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_TrqArblim
Configuration File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqArblim\src\Ap_TrqArblim.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_ON -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utp\contract \Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_ON -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utp\contract \Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path)\include

Comments/Description	on/Specification
Name	Text
Name Module 'TrqArblim_FitInj'	Text
	Total RAM Used (Bytes):33 Total CALS Used (Bytes):104 Special Test Requirements: Test Date:9/16/2014 Comments: Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested. Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 3:In function ""TrqArblim_Per1"" path ""(D_ONE_ULS_F32 <= ((D_2MS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" cannot be covered as ""((D_2MS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" always gets updated with a value less than 1."

Attributes			
Name	Value		
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>		
Float Precision	9		
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>		
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>		
Library	libc.a		

2014-09-16, 12:26:01+0530

TrqArblim_Init



Attributes				
Name	Value			
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl			
Time Enabled	false			
Time Unit	Cycles			
Timer Prescale	0			
Timer Resolution				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP			

TS1.13 ALL MAX



Test Case 1: Boundary Test Performance Metrics: (With "None" Instrumentation and "WithPS" Environment) Specification CPU Cycles: TS1.1 515.00 Cycles 515.00 514.00 475.00 461.00 447.00 432.00 475.00 475.00 457.00 457.00 457.00 Cycles TS1.2 TS1.3 TS1.4 TS1.5 TS1.6 TS1.7 TS1.8 TS1.9 TS1.10 TS1.11 TS1.12 Description Vector Description: TS1.1 k_APASmoothHwTrqLPFKn_Hz_f32 = MIN TS1.2 k_APASmoothHwTrqLPFKn_Hz_f32 = MAX TS1.3 k_APASmoothHwTrqLPFKn_Hz_f32 = POS TS1.4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = MIN TS1.5 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = MAX TS1.6 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = ZERO TS1.7 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = NEG TS1.8 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = POS TS1.9 TrqArblim_HwTorqueSV_HwNm_M_Str.K = MIN TS1.10 TrqArblim_HwTorqueSV_HwNm_M_Str.K = MAX TS1.11 TrqArblim_HwTorqueSV_HwNm_M_Str.K = POS TS1.12 ALL MIN TS1.13 ALL MIN

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.63		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.023		
k_APASmoothHwTrqLPFKn_Hz_f32	0.1		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.001255848 ± 0.03125	✓

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.087		
k_APASmoothHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.715390457 ± 0.03125	✓

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.151		
k_APASmoothHwTrqLPFKn_Hz_f32	50.65		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470851779	0.470851766 ± 0.03125	✓

Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.215		
k_APASmoothHwTrqLPFKn_Hz_f32	5.41		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0657246709	0.065724639 ± 0.03125	✓



TrqArblim_Init

Test Step 1.5 (Repeat Count = 1)				
Name	Input Value			
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10			
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.279			
k_APASmoothHwTrqLPFKn_Hz_f32	11.42			
Name	Actual Value	Expected Value	Result	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	0 ± 0.03125	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133686125	0.133686089 ± 0.03125	✓	

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.343		
k_APASmoothHwTrqLPFKn_Hz_f32	15.85		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.18059516	0.180595133 ± 0.03125	✓

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407		
k_APASmoothHwTrqLPFKn_Hz_f32	20		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.222232342	0.222232321 ± 0.03125	✓

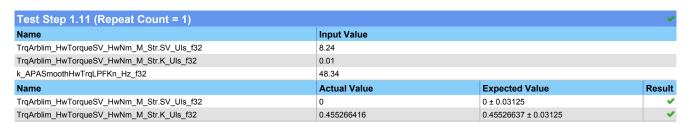
Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.68		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.471		
k_APASmoothHwTrqLPFKn_Hz_f32	28.61		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim HwTorqueSV HwNm M Str.K Uls f32	0.301991463	0.301991406 ± 0.03125	✓

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.001255848		
k_APASmoothHwTrqLPFKn_Hz_f32	36.86		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.370730996	0.370730981 ± 0.03125	~

Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.715390457		
k_APASmoothHwTrqLPFKn_Hz_f32	42		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.4100914	0.410091339 ± 0.03125	✓



TrqArblim_Init



Test Step 1.12 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.001255848		
k_APASmoothHwTrqLPFKn_Hz_f32	0.1		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.001255848 ± 0.03125	✓

Test Step 1.13 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390457		
k_APASmoothHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.715390457 ± 0.03125	✓

2014-09-16, 12:30:22+0530





 Project
 TrqArblim

 Module
 TrqArblim_FltInj

 Test Object
 TrqArblim_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	99 %
Branch (C1) Coverage	98.52 %
MCC Coverage	98.61 %
MC/DC Coverage	98.61 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_TrqArblim
Configuration File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqArblim\src\Ap_TrqArblim.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_ON -D_DATA_ACCESS= -Dconst= -l\$(PROJECTROOT)\TrqArblim\utp\contract \Ap_TrqArblim -l\$(PROJECTROOT)\TrqArblim\utp\contract -l\$(PROJECTROOT)\NxtrLib\include -l\$(PROJECTROOT)\StdDef\include -l\$ (Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_ON -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utp\contract \Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path)\include

pecification
Text
Report Unit Test
Name of Tester:Priti Mangalekar Code File(s) Under Test: Ap_TrqArblim.c Code File(s) Version:5 Module Design Document:TrqArblim_MDD.docx Module Design Document Version:6 Data Dictionary Version:5 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):1456 Total RAM Used (Bytes):33 Total CALS Used (Bytes):33 Total CALS Used (Bytes):104 Special Test Requirements: Test Date:9/16/2014 Comments: Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested. Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 3:In function ""TrqArblim_Per1"" path ""(D_ONE_ULS_F32 <= ((D_2MS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" cannot be covered as ""((D_2MS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))""

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2014-09-16, 12:30:22+0530



Attributes			
Name	Value		
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj		
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src		
Library	libc.a		
Linker File	(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd		
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>		
Time Enabled	false		
Time Unit	Cycles		
Timer Prescale	0		
Timer Resolution	1		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg		
Workspace File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP		



Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 2714.00 Cycles TS1.2 2698.00 Cycles

Description

Vector Description:

TS 1.1Shortest execution Path: (APAlowDisableRate_pSec_T_f32 >= -0.2F)=True,(PosServEnable_Cnt_T_lgc == TRUE)=False, (GMOSHOscillate_State_T_enum == Haptic_Active)=False,(k_APAUseAsstScale_lgc == TRUE)=True,(k_APAUseRetScale_lgc == TRUE)=True, (k_APAUseRetScale_lgc == TRUE)=True, (k_APAUseApdmpScale_lgc == TRUE)))=False, (k_LKAUseSlewCal_lgc == FALSE)=False, (FinalSlew_NmpSec_T_f32 < D_FINALSLEWLMT_NMPSec_F32)=False, (Abs_f32_m(ESCCmd_HwNm_T_j32) > k_ESCMax_HwNm_f32)=False, (Track_problem_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=False, (Track_problem_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=False, (GMOSHOscillate_State_T_enum == Haptic_Active)=True, (k_APAUseAsstScale_lgc == TRUE)=False, (k_APAUseAptise_State_T_enum == Haptic_Active)=True, (k_APAUseApdmpScale_lgc == TRUE)=False, (LKA_State_T_enum != LKA_Active) || ((GMOSHOscillate_State_T_enum != Haptic_Inactive) || ((GMOSHOscillate_State_T_enum != Haptic_Inactive) || ((GMOSHOscillate_State_T_enum != Haptic_Inactive) || ((FosServEnable_Cnt_T_lgc == TRUE)))=True, (k_LKAUseSlewCal_lgc == FALSE)=True, (FinalSlew_NmpSec_T_f32 < D_FINALSLEWLMT_NMPSec_F32)=False, ((ESC_State_T_enum != ESC_Active)&&(ESC_State_T_enum != ESC_Active)|| ((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active))|| ((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active)|| ((TrqArblim_ESC_S

Test Case 2: Path Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 2392.00 Cycles
TS2.2 2725.00 Cycles
TS2.3 2732.00 Cycles
TS2.4 2423.00 Cycles
TS2.5 2459.00 Cycles
TS2.6 2309.00 Cycles
TS2.7 2608.00 Cycles
TS2.8 2472.00 Cycles
TS2.9 2301.00 Cycles
TS2.10 2425.00 Cycles
TS2.11 2457.00 Cycles
TS2.12 2239.00 Cycles
TS2.12 2374.00 Cycles
TS2.13 2374.00 Cycles
TS2.14 2425.00 Cycles

Description

Vector Description:

TS2.1""(APAlowDisableRate_pSec_T_f32 >= -0.2F)=False,((LKA_State_T_enum != LKA_Active)=True || ((GMOSHOscillate_State_T_enum != Haptic_Inactive) || (PosServEnable_Cnt_T_lgc == TRUE))),(k_LKAUseSlewCal_lgc == FALSE)=True""
TS2.2(k_APAUseAstScale_lgc == TRUE)=True,(k_APAUseRetScale_lgc == TRUE)=True, (k_APAUseADmpScale_lgc == TRUE)=True
TS2.3(PosServEnable_Cnt_T_lgc == TRUE)=True,(GMOSHOscillate_State_T_enum == Haptic_Active)=True
TS2.3("(LKA_State_T_enum != LKA_Active) = False|| ((GMOSHOScillate_State_T_enum == Haptic_Active)=True
TS2.4""((LKA_State_T_enum != LKA_Active) = False|| ((PosServEnable_Cnt_T_lgc == TRUE))=True"
TS2.50pTrqOv_MtrNm_T_f32 = Limit_m(DpTrqov_MtrNm_T_f32,-D_OPTRQOVLMT_MTRNM_F32,D_OPTRQOVLMT_MTRNM_F32)=False
TS2.6LKATrqReq_HwNm_T_f32 = Limit_m(LKADesired_HwNm_T_f32, (D_2MS_SEC_F32 * -FinalSlew_NmpSec_T_f32) + TrqArblim_PreLKATrqReq_HwNm_M_f32), ((D_2MS_SEC_F32 * FinalSlew_NmpSec_T_f32)+ TrqArblim_PreLKATrqReq_HwNm_M_f32). ((D_2MS_SEC_F32 * FinalSlew_NmpSec_T_f32)+ TrqArblim_PreLKATrqReq_HwNm_M_f32)=False
TS2.8(APAlowDisableRate_pSec_T_f32 >= -0.2F)=True
TS2.9((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active))||(TrqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=True
TS2.10((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active))||(TrqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=False
TS2.11((ESC_State_T_enum != ESC_Active)&&(ESC_State_T_enum != ESC_TemporarilyLimited))=False
TS2.11((ESC_State_T_enum != ESC_Active)&&(ESC_State_T_enum != ESC_TemporarilyLimited))=False
TS2.12(1CPADSiableRate_Uls_T_f32 = Limit_m(D_ZERO_ULS_F32, (D_ZMS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))=False
TS2.13APADisableRate_Uls_T_f32 = Limit_m(D_ZERO_ULS_F32, (D_ZMS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))=False
TS2.13APADisableRate_Uls_M_f32, ((D_ZMS_SEC_F32 * k_APAEnableRate_pSec_f32) + TrqArblim_APADisableRate_Uls_M_f32))=False
TS2.14(Abs_f32_m(ESCCmd_HwNm_T_f32) > k_ESCMax_HwNm_f32)=False

Test Step 2.1 (Repeat Count = 1)		
Name	Input Value	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.00999999978	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.85999966	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00200000009	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.60000038	
k_APAEnableRate_pSec_f32	1.5	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	2.24000001	
k_LKAUseSlewCal_lgc	0	

2014-09-16, 12:30:22+0530



TrqArblim_Per1	MACICAL
Name	Input Value
_APADisableRateX_HwNm_u4p12[0]	0
_APADisableRateX_HwNm_u4p12[1]	410
_APADisableRateX_HwNm_u4p12[2]	819
_APADisableRateX_HwNm_u4p12[3]	1229
_APADisableRateX_HwNm_u4p12[4]	1638
_APADisableRateX_HwNm_u4p12[5]	2048
_APADisableRateY_pSec_u7p9[0]	0
_APADisableRateY_pSec_u7p9[1]	512
_APADisableRateY_pSec_u7p9[2]	1024
_APADisableRateY_pSec_u7p9[3]	1536
_APADisableRateY_pSec_u7p9[4]	2048
_APADisableRateY_pSec_u7p9[5]	2560
_APASmoothX_Uls_u2p14[0]	0
_APASmoothX_Uls_u2p14[1]	164
_APASmoothX_Uls_u2p14[2]	328
_APASmoothX_Uls_u2p14[3]	492
_APASmoothX_UIs_u2p14[4]	655
_APASmoothX_Uls_u2p14[5]	819
_APASmoothX_UIs_u2p14[6]	983
_APASmoothX_Uls_u2p14[7]	1147
_APASmoothX_Uls_u2p14[8]	1311
_APASmoothX_Uls_u2p14[9]	1475
_APASmoothY_UIs_u2p14[0]	12780
_APASmoothY_Uls_u2p14[1]	12943
_APASmoothY_Uls_u2p14[2]	13107
_APASmoothY_UIs_u2p14[3]	13271
_APASmoothY_UIs_u2p14[4]	13435
_APASmoothY_UIs_u2p14[5]	13599
_APASmoothY_Uls_u2p14[6]	13763
_APASmoothY_Uls_u2p14[7]	13926
_APASmoothY_Uls_u2p14[8]	14090
_APASmoothY_Uls_u2p14[9]	14254
_LKASlewX_Kph_u8p8[0]	0
_LKASlewX_Kph_u8p8[1]	512
:_LKASlewX_Kph_u8p8[2]	1024
:_LKASlewX_Kph_u8p8[3]	1536
LKASlewX_Kph_u8p8[4]	2048
_LKASlewX_Kph_u8p8[5]	2560
_LKASlewY_NmpSec_u4p12[0]	0
:_LKASlewY_NmpSec_u4p12[1]	410
LKASlewY_NmpSec_u4p12[2]	819
_LKASlewY_NmpSec_u4p12[3]	1229
LKASlewY_NmpSec_u4p12[4]	1638
_LKASlewY_NmpSec_u4p12[5]	2048
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
arget_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.82000017
arget_TrqArblim_Per1_ESCState_State_enum.value	0
arget_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.64000034
arget_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998
arget_TrqArblim_Per1_LKAState_State_enum.value	0
arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	2
arget_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
arget_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.20999993
arget_114Arbiin1_1 et 1_1 0301400ma_iviti14m_i32.valde	

2014-09-16, 12:30:22+0530



Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.85956001	-9.85956001 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003	-10.2200003 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.5732336	-8.57322979 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Nama	Innut Value
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f3
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.023
FrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038
<pre><_APAEnableRate_pSec_f32</pre>	6
<pre>x_APAUseADmpScale_lgc</pre>	1
<_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
c_ESCMax_HwNm_f32	4.8499999
c_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	410
_APADisableRateX_HwNm_u4p12[1]	819
_APADisableRateX_HwNm_u4p12[2]	1229
_APADisableRateX_HwNm_u4p12[3]	1638
_APADisableRateX_HwNm_u4p12[4]	2048
_APADisableRateX_HwNm_u4p12[5]	2458
_APADisableRateY_pSec_u7p9[0]	512
_APADisableRateY_pSec_u7p9[1]	1024
_APADisableRateY_pSec_u7p9[2]	1536
_APADisableRateY_pSec_u7p9[3]	2048
_APADisableRateY_pSec_u7p9[4]	2560
_APADisableRateY_pSec_u7p9[5]	3072
_APASmoothX_Uls_u2p14[0]	164
_APASmoothX_UIs_u2p14[1]	328
_APASmoothX_Uls_u2p14[2]	492
_APASmoothX_UIs_u2p14[3]	655
APASmoothX Uls u2p14[4]	819
APASmoothX UIs u2p14[5]	983
_APASmoothX_Uls_u2p14[6]	1147
_APASmoothX_Uls_u2p14[7]	1311
_APASmoothX_UIs_u2p14[8]	1475
_APASmoothX_Uls_u2p14[9]	1638
_APASmoothY_Uls_u2p14[0]	12943
APASmoothY Uls u2p14[1]	13107
_APASmoothY_Uls_u2p14[2]	13271
_APASmoothY_Uls_u2p14[3]	13435
_APASmoothY_Uls_u2p14[4]	13599
_APASmoothY_Uls_u2p14[4]	13763
	13926
_APASmoothY_Uls_u2p14[6]	14090
_APASmoothY_UIs_u2p14[7]	14254
_APASmoothY_Uls_u2p14[8]	
_APASmoothY_Uls_u2p14[9]	14418
_LKASlewX_Kph_u8p8[0]	512

2014-09-16, 12:30:22+0530



TraArblim Per1

TrqArblim_Per1			MAZUICAL
Name	Input Value		
t_LKASlewX_Kph_u8p8[1]	1024		
t_LKASlewX_Kph_u8p8[2]	1536		
t_LKASlewX_Kph_u8p8[3]	2048		
t_LKASlewX_Kph_u8p8[4]	2560		
t_LKASlewX_Kph_u8p8[5]	3072		
t_LKASlewY_NmpSec_u4p12[0]	410		
t_LKASlewY_NmpSec_u4p12[1]	819		
t_LKASlewY_NmpSec_u4p12[2]	1229		
LKASlewY_NmpSec_u4p12[3]	1638		
t LKASlewY NmpSec u4p12[4]	2048		
t_LKASlewY_NmpSec_u4p12[5]	2458		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	-9.5		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_U	Jls f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cn	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelive		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_HwTorque_HwNn	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_t		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target_TrqArblim_Per1_LKACmd_HwNm_		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target_TrqArblim_Per1_LKAState_State_o		
target Rte Inst Ap TrgArblim.TrgArblim Per1 LKATorqueDelivered HwNm f32	target_TrqArblim_Per1_LKATorqueDelive		
target Rte Inst Ap TrgArblim.TrgArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicle		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target TrqArblim Per1 PosServEnable (
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_rrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989	<u>.</u>	
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.22999954		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	5.86000013		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.560000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.0999999		
	Actual Value	Expected Value	Beau
Name		Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.0109999999	0.0109999999 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	1	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.24571609	-9.24571991 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046	-9.02000046 ± 0.00024414062	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.09400082	-8.09399986 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4.8499999	-4.8499999 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0784785151	-0.0784785002 ± 0000390625	
erget Translim Dorf DeturnDDEcetor IIIe f22 value	0.00120051	0.001200007 ± 0000200625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	-

0.99129951

Test Step 2.3 (Repeat Count = 1)		
Name	Input Value	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.0359999985	
TrqArblim_ESC_State_Cnt_M_enum	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.64000034	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

0.991299987 ± 0000390625

TrqArblim_Per1

2014-09-16, 12:30:22+0530



Name	Input Value
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.82000017
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.5999999
k_APAEnableRate_pSec_f32	10.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1 3.30999994
k_ESCMax_HwNm_f32 k_LKAUseSlewCal_lgc	3.50999994
t_APADisableRateX_HwNm_u4p12[0]	819
t_APADisableRateX_HwNm_u4p12[1]	1229
t_APADisableRateX_HwNm_u4p12[2]	1638
t_APADisableRateX_HwNm_u4p12[3]	2048
t_APADisableRateX_HwNm_u4p12[4]	2458
t_APADisableRateX_HwNm_u4p12[5]	2867
t_APADisableRateY_pSec_u7p9[0]	1024
t_APADisableRateY_pSec_u7p9[1]	1536
t_APADisableRateY_pSec_u7p9[2]	2048
t_APADisableRateY_pSec_u7p9[3] t_APADisableRateY_pSec_u7p9[4]	2560 3072
t_APADisableRateY_pSec_u7p9[4]	3584
t_APASmoothX_UIs_u2p14[0]	328
t_APASmoothX_Uls_u2p14[1]	492
t_APASmoothX_Uls_u2p14[2]	655
t_APASmoothX_Uls_u2p14[3]	819
t_APASmoothX_Uls_u2p14[4]	983
t_APASmoothX_Uls_u2p14[5]	1147
t_APASmoothX_Uls_u2p14[6]	1311
t_APASmoothX_Uls_u2p14[7]	1475
t_APASmoothX_Uls_u2p14[8]	1638
t_APASmoothX_UIs_u2p14[9] t_APASmoothY_UIs_u2p14[0]	1802 13107
t_APASmoothY_Uis_u2p14[1]	13271
t_APASmoothY_Uls_u2p14[2]	13435
t_APASmoothY_Uls_u2p14[3]	13599
t_APASmoothY_Uls_u2p14[4]	13763
t_APASmoothY_Uls_u2p14[5]	13926
t_APASmoothY_Uls_u2p14[6]	14090
t_APASmoothY_Uls_u2p14[7]	14254
t_APASmoothY_Uls_u2p14[8]	14418
t_APASmoothY_Uls_u2p14[9]	14582
t_LKASlewX_Kph_u8p8[0] t LKASlewX Kph u8p8[1]	1024 1536
t LKASlewX Kph u8p8[2]	2048
t_LKASlewX_Kph_u8p8[3]	2560
t_LKASlewX_Kph_u8p8[4]	3072
t_LKASlewX_Kph_u8p8[5]	3584
t_LKASlewY_NmpSec_u4p12[0]	819
t_LKASlewY_NmpSec_u4p12[1]	1229
t_LKASlewY_NmpSec_u4p12[2]	1638
t_LKASlewY_NmpSec_u4p12[3]	2048
t_LKASlewY_NmpSec_u4p12[4]	2458
t_LKASlewY_NmpSec_u4p12[5] target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f:	2867 -8.80000019
target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor UIs f32	target TrqArblim Per1 AssistDDFactor UIs f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_IrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target TrqArblim Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.42000008

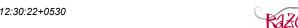
© Report created by TESSY V3.1.7, report template V2.1

 $target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value$

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$ target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

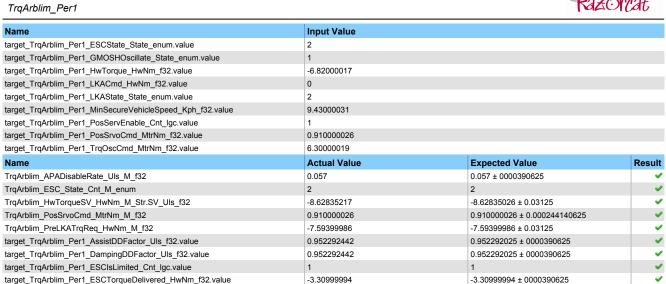
2014-09-16, 12:30:22+0530



-3 ± 0.0000390625 0 ± 0.0000390625

6.34341002 ± 0000390625

0.952292025 ± 0000390625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

6.34341431

0.952292442

-3

Test Step 2.4 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.101000004
TrqArblim_ESC_State_Cnt_M_enum	1.
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.59000015
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0174000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.82000005
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.0999999
k_APAEnableRate_pSec_f32	33
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.337000012
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	2867
t_APADisableRateX_HwNm_u4p12[1]	3277
t_APADisableRateX_HwNm_u4p12[2]	3686
t_APADisableRateX_HwNm_u4p12[3]	4096
t_APADisableRateX_HwNm_u4p12[4]	4506
t_APADisableRateX_HwNm_u4p12[5]	4915
t_APADisableRateY_pSec_u7p9[0]	3584
t_APADisableRateY_pSec_u7p9[1]	4096
t_APADisableRateY_pSec_u7p9[2]	4608
t_APADisableRateY_pSec_u7p9[3]	5120
t_APADisableRateY_pSec_u7p9[4]	5632
t_APADisableRateY_pSec_u7p9[5]	6144
t_APASmoothX_Uls_u2p14[0]	1147
t_APASmoothX_UIs_u2p14[1]	1311
t_APASmoothX_UIs_u2p14[2]	1475
t_APASmoothX_Uls_u2p14[3]	1638
t_APASmoothX_Uls_u2p14[4]	1802
t_APASmoothX_Uls_u2p14[5]	1966
t_APASmoothX_Uls_u2p14[6]	2130
t_APASmoothX_Uls_u2p14[7]	2294
t_APASmoothX_Uls_u2p14[8]	2458
t_APASmoothX_Uls_u2p14[9]	2621

© Report created by TESSY V3.1.7, report template V2.1

8

2014-09-16, 12:30:22+0530



TrqArblim_Per1

· -			
Name	Input Value		
t_APASmoothY_Uls_u2p14[0]	13926		
t_APASmoothY_Uls_u2p14[1]	14090		
t_APASmoothY_Uls_u2p14[2]	14254		
t_APASmoothY_Uls_u2p14[3]	14418		
t_APASmoothY_Uls_u2p14[4]	14582		
t_APASmoothY_Uls_u2p14[5]	14746		
t_APASmoothY_Uls_u2p14[6]	14909		
t_APASmoothY_Uls_u2p14[7]	15073		
t APASmoothY Uls u2p14[8]	15237		
t_APASmoothY_Uls_u2p14[9]	15401		
t_LKASlewX_Kph_u8p8[0]	3584		
t LKASlewX Kph u8p8[1]	4096		
t_LKASlewX_Kph_u8p8[2]	4608		
t_LKASlewX_Kph_u8p8[3]	5120		
t_LKASlewX_Kph_u8p8[4]	5632		
t_LKASlewX_Kph_u8p8[5]	6144		
t_LKASlewY_NmpSec_u4p12[0]	2867		
t_LKASlewY_NmpSec_u4p12[1]	3277		
t LKASlewY NmpSec u4p12[2]	3686		
t_LKASlewY_NmpSec_u4p12[3]	4096		
t_LKASlewY_NmpSec_u4p12[4]	4506		
t LKASlewY NmpSec u4p12[5]	4915		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_		. (22	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.230000004		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.11999989		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	256.339996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.66000009		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.30000019		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.166999996	0.166999996 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	2	2	
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.48873234	-5.48872995 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	2.66000009	2.66000009 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.09399986	-5.09399986 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000330025	
target TrgArblim Per1 ESCIsLimited Cnt Igc.value	1	1	
target_TrqArblim_For1_ESCTorqueDelivered_HuAlm_f22_value	0.337000013	0.337000013 ± 0000300635	

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 0		0.417567909 0.417567998 ± 000039	0625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value		1 1 ± 0000390625	1 ± 0000390625	
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call Ap TrgArblim FaultInjection SCom FltInjection	1	Rte Call Ap TrqArblim FaultInjection SCom FltInjection	1	✓

Rte_Call_TrqArblim_Per1_CP1_CheckpointReached

0.337000012

0.337000012

 $Rte_Call_TrqArblim_Per1_CP1_CheckpointReached$

target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value$

0.337000012 ± 0000390625

0 ± 0.0000390625

0.337000012 ± 0.0000390625



Test Step 2.5 (Repeat Count = 1) Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.152999997
TrqArblim_ESC_State_Cnt_M_enum	0
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.1500001
FrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002
FrqArblim_PosSrvoCmd_MtrNm_M_f32	2.98000002
FrqArblim_PreLKATrqReq_HwNm_M_f32	-3.0999999
c_APAEnableRate_pSec_f32	51
<pre>c_APAUseADmpScale_lgc</pre>	1
<pre>c_APAUseAsstScale_lgc</pre>	1
<pre>x_APAUseRetScale_lgc</pre>	1
C_ESCMax_HwNm_f32	1.20000005
<u>CLKAUseSlewCal_lgc</u>	1
_APADisableRateX_HwNm_u4p12[0]	4506
_APADisableRateX_HwNm_u4p12[1]	4915
_APADisableRateX_HwNm_u4p12[2]	5325
_APADisableRateX_HwNm_u4p12[3]	5734
_APADisableRateX_HwNm_u4p12[4]	6144
_APADisableRateX_HwNm_u4p12[5]	6554
_APADisableRateY_pSec_u7p9[0]	5632
_APADisableRateY_pSec_u7p9[1]	6144
_APADisableRateY_pSec_u7p9[2]	6656
_APADisableRateY_pSec_u7p9[3]	7168
_APADisableRateY_pSec_u7p9[4]	7680
_APADisableRateY_pSec_u7p9[5]	8192
_APASmoothX_UIs_u2p14[0]	1802
_APASmoothX_UIs_u2p14[1]	1966
_APASmoothX_Uls_u2p14[2]	2130
_APASmoothX_Uls_u2p14[3]	2294
_APASmoothX_UIs_u2p14[4]	2458
_APASmoothX_UIs_u2p14[5]	2621
_APASmoothX_UIs_u2p14[6]	2785
_APASmoothX_UIs_u2p14[7]	2949
_APASmoothX_UIs_u2p14[8]	3113
_APASmoothX_UIs_u2p14[9]	3277
_APASmoothY_Uls_u2p14[0]	14582
_APASmoothY_Uls_u2p14[1]	14746 14909
_APASmoothY_Uls_u2p14[2] _APASmoothY_Uls_u2p14[3]	15073
_APASmoothY_Uls_u2p14[4]	15237
_APASmoothY_Uls_u2p14[4]	15401
APASmoothY Uls u2p14[6]	15565
: ::	
_APASmoothY_Uls_u2p14[7] _APASmoothY_Uls_u2p14[8]	15729 15892
	16056
_APASmoothY_Uls_u2p14[9]	5632
_LKASlewX_Kph_u8p8[0] _LKASlewX_Kph_u8p8[1]	6144
KASIewX_Kph_u8p8[2]	6656
_LKASlewX_Kph_u8p8[3]	7168
_LKASIewX_kpii_uopo[3] _LKASIewX_Kph_u8p8[4]	7680
_txasiewx_xpii_uopo[4] _txasiewx_xpii_uopo[4]	8192
	4506
_LKASlewY_NmpSec_u4p12[1]	4915
_LKASlewY_NmpSec_u4p12[1] LKASlewY_NmpSec_u4p12[2]	5325
_LKASlewY_NmpSec_u4p12[3]	5734
_LKASlewY_NmpSec_u4p12[4]	6144
_LKASlewY_NmpSec_u4p12[5]	6554
_rxasiew+_nmpsec_u4p12[5] arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_UIs_	
arget_Rte_lost_Ap_TrqArblim_Faultinjection_Scom_Fitinjection_SignalFatit_bis_ arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_bampingbbFactor_ois_is2 arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCCIId_HwNIII_I32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCCfild_HWNffi_152 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_inst_Ap_rtqArbiim.rtqArbiim_Perr_ESCisLimited_Cht_igc arget_Rte_Inst_Ap_rtqArbiim.rtqArblim_Perr_ESCState_State_enum	target_frqArbiim_Peri_ESCisLiffilled_Crit_igc target_frqArbiim_Peri_ESCisLiffilled_Crit_igc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_ESC10IqueDelivered_nwiviii_132 target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_InvToIque_nwini_i32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
2.920_110(_,10_114,10.1111.1114).11111111111111111111111111	target_TrqArblim_Per1_LKACmd_HwNm_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	arget_TrqArblim_Per1_LKAState_State_enum		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32			
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	arget_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtrl	arget_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	Jls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	m_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011			
target_TrqArblim_Per1_ESCState_State_enum.value	1			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.86999989			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.15999985			
target_TrqArblim_Per1_LKAState_State_enum.value	0			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	38.2933006			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.10000038			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.254999995	0.254999995 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	1	Ī	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.91367602	-2.91368008 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	~	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	~	
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	~	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.20000005	1.20000005 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.20000005	1.20000005 ± 0.0000390625	✓	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	•

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.244000003
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.12
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0416000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.40000006
k_APAEnableRate_pSec_f32	82.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	7373
t_APADisableRateX_HwNm_u4p12[1]	7782
t_APADisableRateX_HwNm_u4p12[2]	8192
t_APADisableRateX_HwNm_u4p12[3]	8602
t_APADisableRateX_HwNm_u4p12[4]	9011
t_APADisableRateX_HwNm_u4p12[5]	9421
t_APADisableRateY_pSec_u7p9[0]	9216
t_APADisableRateY_pSec_u7p9[1]	9728
t_APADisableRateY_pSec_u7p9[2]	10240
t_APADisableRateY_pSec_u7p9[3]	10752
t_APADisableRateY_pSec_u7p9[4]	11264
t_APADisableRateY_pSec_u7p9[5]	11776
t_APASmoothX_Uls_u2p14[0]	2949

2014-09-16, 12:30:22+0530





Name	Input Value		
t_APASmoothX_Uls_u2p14[1]	3113		
t_APASmoothX_Uls_u2p14[2]	3277		
t_APASmoothX_Uls_u2p14[3]	3441		
t_APASmoothX_Uls_u2p14[4]	3604		
t_APASmoothX_Uls_u2p14[5]	3768		
t_APASmoothX_Uls_u2p14[6]	3932		
t_APASmoothX_Uls_u2p14[7]	4096		
t_APASmoothX_Uls_u2p14[8]	4260		
t_APASmoothX_Uls_u2p14[9]	4424		
t_APASmoothY_Uls_u2p14[0]	0		
t_APASmoothY_Uls_u2p14[1]	164		
t_APASmoothY_Uls_u2p14[2]	328		
t_APASmoothY_Uls_u2p14[3]	492		
t_APASmoothY_Uls_u2p14[4]	655		
t_APASmoothY_Uls_u2p14[5]	819		
t_APASmoothY_Uls_u2p14[6]	983		
t_APASmoothY_Uls_u2p14[7]	1147		
t_APASmoothY_Uls_u2p14[8]	1311		
t_APASmoothY_Uls_u2p14[9]	1475		
t_LKASlewX_Kph_u8p8[0]	9216		
t_LKASlewX_Kph_u8p8[1]	9728		
t_LKASlewX_Kph_u8p8[2]	10240		
t_LKASlewX_Kph_u8p8[3]	10752		
t_LKASlewX_Kph_u8p8[4]	11264		
t LKASlewX Kph u8p8[5]	11776		
t_LKASiewY_NmpSec_u4p12[0]	7373		
t_LKASiewY_NmpSec_u4p12[t]	7782		
t_LKASlewY_NmpSec_u4p12[1]	8192		
	8602		
t_LKASlewY_NmpSec_u4p12[3]	9011		
t_LKASlewY_NmpSec_u4p12[4]	9421		
t_LKASlewY_NmpSec_u4p12[5]			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f		f00	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_U		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	_ : _	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.34000015		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-2.73000002		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	83.4432983		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32	0.409000009	0.409000009 ± 0000390625	_
TrqArblim_ESC_State_Cnt_M_enum	3	3	·
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.29555202	1.29554999 ± 0.03125	
TrqArblim PosSrvoCmd MtrNm M f32	-3.70000005	-3.70000005 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.382734507	0.382735014 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	-
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Peri_bampingbbFactor_ois_isz.value target_TrqArblim_Peri_ESCIsLimited_Cnt_lgc.value	0	0	
	0	0 ± 0000390625	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value target TrqArblim Per1_lqTrqOv HwNm_f32.value			-
target_frqArblim_Per1_tKATorqueDelivered_HwNm_f32.value	0.382734507 0.382734507	0.382734507 ± 0.0000390625 0.382734507 ± 0.0000390625	
			-
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.86376238	1.86375999 ± 0000390625	

2014-09-16, 12:30:22+0530



Name	Actual Value	Expected Value	Result
Name	Actual value	Expected value	Kesuit
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Fest Step 2.7 (Repeat Count = 1)	Innut Value
lame	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
tte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.34799999
rqArblim_ESC_State_Cnt_M_enum	0
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6
rqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0592
rqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985
rqArblim_PreLKATrqReq_HwNm_M_f32	4.4000001
_APAEnableRate_pSec_f32	118.5
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
_ESCMax_HwNm_f32	1.27999997
_LKAUseSlewCal_lgc	1.
_APADisableRateX_HwNm_u4p12[0]	10650
_APADisableRateX_HwNm_u4p12[1]	11059
_APADisableRateX_HwNm_u4p12[2]	11469
_APADisableRateX_HwNm_u4p12[3]	11878
_APADisableRateX_HwNm_u4p12[4]	12288
APADisableRateX HwNm u4p12[5]	12698
APADisableRateY_pSec_u7p9[0]	13312
_APADisableRateY_pSec_u7p9[1]	13824
_APADisableRateY_pSec_u7p9[2]	14336
_APADisableRateY_pSec_u7p9[3]	14848
_APADisableRateY_pSec_u7p9[4]	15360
_APADisableRateY_pSec_u7p9[5]	15872
_APASmoothX_Uls_u2p14[0]	4260
_APASmoothX_Uls_u2p14[1]	4424
_APASmoothX_Uls_u2p14[2]	4588
_APASmoothX_Uls_u2p14[3]	4751
_APASmoothX_Uls_u2p14[4]	4915
_APASmoothX_Uls_u2p14[5]	5079
_APASmoothX_Uls_u2p14[6]	5243
_APASmoothX_Uls_u2p14[7]	5407
_APASmoothX_Uls_u2p14[8]	5571
APASmoothX_Uls_u2p14[9]	5734
_APASmoothY_Uls_u2p14[0]	1311
APASmoothY Uls u2p14[1]	1475
_APASmoothY_Uls_u2p14[2]	1638
_APASmoothY_Uls_u2p14[3]	1802
APASmoothY Uls u2p14[4]	1966
_APASmoothY_Uls_u2p14[4]	2130
	2294
_APASmoothY_Uls_u2p14[6]	
_APASmoothY_UIs_u2p14[7]	2458
_APASmoothY_UIs_u2p14[8]	2621
_APASmoothY_UIs_u2p14[9]	2785
_LKASlewX_Kph_u8p8[0]	13312
LKASlewX_Kph_u8p8[1]	13824
_LKASlewX_Kph_u8p8[2]	14336
_LKASlewX_Kph_u8p8[3]	14848
_LKASlewX_Kph_u8p8[4]	15360
_LKASlewX_Kph_u8p8[5]	15872
_LKASlewY_NmpSec_u4p12[0]	10650
LKASlewY_NmpSec_u4p12[1]	11059
LKASlewY_NmpSec_u4p12[2]	11469
LKASlewY_NmpSec_u4p12[3]	11878
_LKASlewY_NmpSec_u4p12[4]	12288
LKASlewY_NmpSec_u4p12[5]	12698
	5_f. 8

TrqArblim_Per1

2014-09-16, 12:30:22+0530





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_1	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.19999981		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.5999999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	135.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.560000002		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.285999984	0.286000013 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.27776003	5.27776003 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985	-6.15999985 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.39379978	4.39379978 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.186563313	-0.186563 ± 0000390625	✓
towart Tradeline Days Date and Decrease IIIa 522 value	0.000740740	0.000743000 0000300035	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

0.969713748

0.969713986 ± 0000390625

Test Step 2.8 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.774999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.70000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.386000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.5999999
k_APAEnableRate_pSec_f32	411.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	29491
t_APADisableRateX_HwNm_u4p12[1]	29901
t_APADisableRateX_HwNm_u4p12[2]	30310

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

2014-09-16, 12:30:22+0530





,			
Name	Input Value		
t_APADisableRateX_HwNm_u4p12[3]	30720		
t_APADisableRateX_HwNm_u4p12[4]	31130		
t_APADisableRateX_HwNm_u4p12[5]	31539		
t_APADisableRateY_pSec_u7p9[0]	0		
t_APADisableRateY_pSec_u7p9[1]	0		
t_APADisableRateY_pSec_u7p9[2]	0		
t_APADisableRateY_pSec_u7p9[3]	0		
t_APADisableRateY_pSec_u7p9[4]	0		
t_APADisableRateY_pSec_u7p9[5]	0		
t_APASmoothX_UIs_u2p14[0]	12124		
t_APASmoothX_UIs_u2p14[1]	12288 12452		
t_APASmoothX_Uls_u2p14[2]	12452		
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	12780		
t_APASmoothX_Uls_u2p14[5]	12943		
t_APASmoothX_Uls_u2p14[6]	13107		
t_APASmoothX_Uls_u2p14[7]	13271		
t_APASmoothX_Uls_u2p14[8]	13435		
t_APASmoothX_Uls_u2p14[9]	13599		
t APASmoothY Uls u2p14[0]	9175		
t_APASmoothY_Uls_u2p14[1]	9339		
t_APASmoothY_Uls_u2p14[2]	9503		
t APASmoothY Uls u2p14[3]	9667		
t_APASmoothY_Uls_u2p14[4]	9830		
t_APASmoothY_Uls_u2p14[5]	9994		
t_APASmoothY_Uls_u2p14[6]	10158		
t_APASmoothY_Uls_u2p14[7]	10322		
t_APASmoothY_Uls_u2p14[8]	10486		
t_APASmoothY_Uls_u2p14[9]	10650		
t_LKASlewX_Kph_u8p8[0]	36352		
t_LKASlewX_Kph_u8p8[1]	36864		
t_LKASlewX_Kph_u8p8[2]	37376		
t_LKASlewX_Kph_u8p8[3]	37888		
t_LKASlewX_Kph_u8p8[4]	38400		
t_LKASlewX_Kph_u8p8[5]	38912		
t_LKASlewY_NmpSec_u4p12[0]	29082		
t_LKASlewY_NmpSec_u4p12[1]	29491		
t_LKASlewY_NmpSec_u4p12[2]	29901		
t_LKASlewY_NmpSec_u4p12[3]	30310		
t_LKASlewY_NmpSec_u4p12[4]	30720		
t_LKASlewY_NmpSec_u4p12[5]	31130		
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_enutarget_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArbilm.TrqArbilm_Per1_KATorqueDelivered_HwNm_132 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_LKA1orqueDelivered target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_inst_Ap_frqArbiim.frqArbiim_Perf_minSecureverlideSpeed_Rph_isz target_Rte_inst_Ap_frqArbiim.frqArbiim_Perf_opTrqOv_MtrNm_f32	target TrgArblim Per1 OpTrgOv MtrNm f3		
target_Rte_inst_Ap_1rqArbilm.1rqArbilm_Per1_Op1rqOv_mtrNm_132 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	0 - 1 1		
target_Rte_Inst_Ap_TrqArbilm.TrqArbilm_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServEnable_Cnt_ target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_inst_Ap_TrqArbilm.TrqArbilm_Per1_PosSrvoCmd_httr\tim_ts2 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn target_TrqArblim_Per1_ReturnDDFactor_Uls		
target_Rte_inst_Ap_trqArbiim.trqArbiim_Pert_ReturnDDFactor_0is_ts2 target_Rte_inst_Ap_trqArbiim.TrqArbiim_Pert_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_Uis target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_Rte_inst_Ap_11qArbiiin.11qArbiiin_Pe1_11qOsconid_MitNin_132 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954	_10_	
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.94999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	444.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	7.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.2999995		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.774599969	0.774600029 ± 0000390625	Resu
TrqArblim ESC State Cnt M enum	4	4	
<u></u>			

2014-09-16, 12:30:22+0530



Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.72650003	-4.72650003 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59399986	2.5940001 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.47177887	4.47177982 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value
	· ·
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32) Rte_Inst_Ap_TrqArblim	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32 target_Rte_Inst_Ap_TrqArblim
: _ :	0.20000003
FrqArblim_APADisableRate_Uls_M_f32	0.20000000
FrqArblim_ESC_State_Cnt_M_enum	7.92999983
FrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.133000001
FrqArblim_PosSrvoCmd_MtrNm_M_f32	3.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	0
<_APAEnableRate_pSec_f32	231
x_APAUseADmpScale_lgc	0
<_APAUseAsstScale_lgc	0
x_APAUseRetScale_lgc	0
c_ESCMax_HwNm_f32	0.75
<pre>C_LKAUseSlewCal_lgc</pre>	0
_APADisableRateX_HwNm_u4p12[0]	20890
_APADisableRateX_HwNm_u4p12[1]	21299
_APADisableRateX_HwNm_u4p12[2]	21709
_APADisableRateX_HwNm_u4p12[3]	22118
_APADisableRateX_HwNm_u4p12[4]	22528
_APADisableRateX_HwNm_u4p12[5]	22938
_APADisableRateY_pSec_u7p9[0]	26112
_APADisableRateY_pSec_u7p9[1]	26624
_APADisableRateY_pSec_u7p9[2]	27136
_APADisableRateY_pSec_u7p9[3]	27648
_APADisableRateY_pSec_u7p9[4]	28160
_APADisableRateY_pSec_u7p9[5]	28672
_APASmoothX_Uls_u2p14[0]	8356
_APASmoothX_Uls_u2p14[1]	8520
_APASmoothX_Uls_u2p14[2]	8684
_APASmoothX_Uls_u2p14[3]	8847
_APASmoothX_Uls_u2p14[4]	9011
_APASmoothX_Uls_u2p14[5]	9175
_APASmoothX_Uls_u2p14[6]	9339
_APASmoothX_Uls_u2p14[7]	9503
_APASmoothX_Uls_u2p14[8]	9667
_APASmoothX_Uls_u2p14[9]	9830
_APASmoothY_Uls_u2p14[0]	5407
_APASmoothY_Uls_u2p14[1]	5571
_APASmoothY_Uls_u2p14[2]	5734
_APASmoothY_Uls_u2p14[3]	5898
_APASmoothY_Uls_u2p14[4]	6062
_APASmoothY_Uls_u2p14[5]	6226
_APASmoothY_Uls_u2p14[6]	6390
_APASmoothY_Uls_u2p14[7]	6554
_APASmoothY_Uls_u2p14[8]	6717
_APASmoothY_Uls_u2p14[9]	6881
_LKASlewX_Kph_u8p8[0]	26112
_LKASlewX_Kph_u8p8[1]	26624
_LKASlewX_Kph_u8p8[2]	27136

2014-09-16, 12:30:22+0530





Name	Input Value		
t LKASlewX Kph u8p8[3]	27648		
t_LKASlewX_Kph_u8p8[4]	28160		
t_LKASlewX_Kph_u8p8[5]	28672		
t LKASlewY NmpSec u4p12[0]	20890		
t_LKASlewY_NmpSec_u4p12[1]	21299		
t LKASlewY NmpSec u4p12[2]	21709		
t_LKASlewY_NmpSec_u4p12[3]	22118 22528		
t_LKASlewY_NmpSec_u4p12[4]			
t_LKASlewY_NmpSec_u4p12[5]	22938		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelive		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate	_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNr	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_	enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelive	ered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicl	eSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_	Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mt	rNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_Mtrl	Nm f32	
target TrqArblim Per1 ESCCmd HwNm f32.value	-7.5999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target TrgArblim Per1 HwTorque HwNm f32.value	9.05000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.12		
target TrgArblim Per1 LKAState State enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	296.292999		
target TrgArblim Per1 PosServEnable Cnt Igc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.29999995		
		l=	-
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.662	0.662 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	1	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.07895947	8.07896042 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.9000001	2.9000001 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	0	0 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.75	-0.75 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.75	-0.75 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.493716717	-0.493717015 ± 0000390625	
Assert Tradeline Death Determined the 600 celus	4	4 . 000000000	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

1

1 ± 0000390625

Test Step 2.10 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.375
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.30000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.209999993
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.0999999

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

2014-09-16, 12:30:22+0530



Name Input Value k_APAEnableRate_pSec_f32 255.75	
k APAEnableRate pSec f32 255.75	
k_APAUseADmpScale_lgc 1	
k_APAUseAsstScale_lgc 1	
k_APAUseRetScale_lgc 1	
k_ESCMax_HwNm_f32	
k_LKAUseSlewCal_lgc	
t_APADisableRateX_HwNm_u4p12[1] 24166	
t_APADisableRateX_HwNm_u4p12[2] 24576	
t_APADisableRateX_HwNm_u4p12[3] 24986	
t_APADisableRateX_HwNm_u4p12[4] 25395	
t_APADisableRateX_HwNm_u4p12[5] 25805	
t_APADisableRateY_pSec_u7p9[0] 29696	
t_APADisableRateY_pSec_u7p9[1] 30208	
t_APADisableRateY_pSec_u7p9[2] 30720 t_APADisableRateY_pSec_u7p9[3] 31232	
t_APADisableRateY_pSec_u7p9[4] 31744	
t_APADisableRateY_pSec_u7p9[5] 32256	
t_APASmoothX_UIs_u2p14[0] 9503	
t_APASmoothX_Uls_u2p14[1] 9667	
t_APASmoothX_Uls_u2p14[2] 9830	
t_APASmoothX_Uls_u2p14[3] 9994	
t_APASmoothX_Uls_u2p14[4] 10158	
t_APASmoothX_UIs_u2p14[5] 10322	
t_APASmoothX_UIs_u2p14[6] 10486 t_APASmoothX_UIs_u2p14[7] 10650	
t_APASmoothX_Uls_u2p14[8] 10813	
t APASmoothX UIs u2p14[9] 10977	
t_APASmoothY_Uls_u2p14[0] 6554	
t_APASmoothY_Uls_u2p14[1] 6717	
t_APASmoothY_Uls_u2p14[2] 6881	
t_APASmoothY_Uls_u2p14[3] 7045	
t_APASmoothY_Uls_u2p14[4] 7209	
t_APASmoothY_Uls_u2p14[5] 7373 t_APASmoothY_Uls_u2p14[6] 7537	
t_APASmoothY_Uls_u2p14[6] 7537 t_APASmoothY_Uls_u2p14[7] 7700	
t_APASmoothY_Uls_u2p14[8] 7864	
t_APASmoothY_UIs_u2p14[9] 8028	
t_LKASlewX_Kph_u8p8[0] 29696	
t_LKASlewX_Kph_u8p8[1] 30208	
t_LKASlewX_Kph_u8p8[2] 30720	
t_LKASlewX_Kph_u8p8[3] 31232	
t_LKASlewX_Kph_u8p8[4] 31744 t_LKASlewX_Kph_u8p8[5] 32256	
t_LKASlewY_NmpSec_u4p12[0] 23757	
t LKASlewY NmpSec u4p12[1] 24166	
t_LKASlewY_NmpSec_u4p12[2] 24576	
t_LKASlewY_NmpSec_u4p12[3] 24986	
t_LKASlewY_NmpSec_u4p12[4] 25395	
t_LKASlewY_NmpSec_u4p12[5] 25805	
target Rte Call Ap TrqArblim FaultInjection SCom FitInjection SignalPath Uls ft -1.79999995	
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_UIs_f32 target_TrqArblim_Per1_AssistDDFactor_UIs_f32 target_TrqArblim_Per1_AssistDDFactor_UIs_f32 target_TrqArblim_Per1_DampingDDFactor_UIs_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_DampingDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_Per1_ESCState_state_enum	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum target_TrqArblim_Per1_GMOSHOscillate_State_enum	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_HwTorque_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_TrqArblim_Per1_IqTrqOv_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKAState_State_enum target_State_enum target_State	
target_Rte_Inst_Ap_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_opTrqOv_MtrNm_f32 target_TrqArblim_Per1_opTrqOv_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_TrqArblim_Per1_PosServEnable_Cnt_Igc	
$target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_lgc \\ target_TrqArblim_Per1_PosServEnable_Cnt_lgc \\ target_T$	
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 4.23999977	
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 4.23999977 target_TrqArblim_Per1_ESCState_enum.value 3 target_TrqArblim_Per1_PosServEnable_Cnt_Igc target_TrqArblim_Per1_PosSe	
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 4.23999977	



Name	Input Value		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.3499999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	341.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.100000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.258355469	0.248999998 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.83250046	-5.83249998 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017	5.07000017 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.08839989	-4.08739996 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.523976862	0.505002975 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte Call Ap TrgArblim FaultInjection SCom FltInjection(SignalPath Uls f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte Inst Ap TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrgArblim ESC State Cnt M enum	4
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	-0.600000024
TrgArblim HwTorqueSV HwNm M Str.K UIs f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
FrgArblim PreLKATrgReg HwNm M f32	5.5999999
<_APAEnableRate_pSec_f32	470.25
APAUseADmpScale lgc	1
APAUseAsstScale lgc	1
<pre>s</pre> <pre>APAUseRetScale_lgc</pre>	1
<pre>C_ESCMax_HwNm_f32</pre>	2
K LKAUseSlewCal Igc	1
	22938
APADisableRateX_HwNm_u4p12[1]	23347
APADisableRateX HwNm u4p12[2]	23757
: APADisableRateX HwNm u4p12[3]	24166
: APADisableRateX HwNm u4p12[4]	24576
 _APADisableRateX_HwNm_u4p12[5]	24986
 _APADisableRateY_pSec_u7p9[0]	29696
_APADisableRateY_pSec_u7p9[1]	30208
 _APADisableRateY_pSec_u7p9[2]	30720
_APADisableRateY_pSec_u7p9[3]	31232
_APADisableRateY_pSec_u7p9[4]	31744
_APADisableRateY_pSec_u7p9[5]	32256
APASmoothX_Uls_u2p14[0]	10813
	10977
	11141
_APASmoothX_Uls_u2p14[3]	11305
_APASmoothX_Uls_u2p14[4]	11469
_APASmoothX_Uls_u2p14[5]	11633
_APASmoothX_Uls_u2p14[6]	11796
_APASmoothX_Uls_u2p14[7]	11960
_APASmoothX_Uls_u2p14[8]	12124
_APASmoothX_Uls_u2p14[9]	12288
	6881
:_APASmoothY_Uls_u2p14[1]	7045
APASmoothY_Uls_u2p14[2]	7209

2014-09-16, 12:30:22+0530





Name	Input Value		
t_APASmoothY_Uls_u2p14[3]	7373		
t_APASmoothY_Uls_u2p14[4]	7537		
t_APASmoothY_Uls_u2p14[5]	7700		
t_APASmoothY_Uls_u2p14[6]	7864		
t_APASmoothY_Uls_u2p14[7]	8028		
t_APASmoothY_Uls_u2p14[8]	8192		
t_APASmoothY_Uls_u2p14[9]	8356		
t_LKASlewX_Kph_u8p8[0]	35328		
t_LKASlewX_Kph_u8p8[1]	35840		
t_LKASlewX_Kph_u8p8[2]	36352		
t_LKASlewX_Kph_u8p8[3]	36864		
t_LKASlewX_Kph_u8p8[4]	37376		
t_LKASlewX_Kph_u8p8[5]	37888		
t_LKASlewY_NmpSec_u4p12[0]	28262		
t_LKASlewY_NmpSec_u4p12[1]	28672		
t_LKASlewY_NmpSec_u4p12[2]	29082		
t_LKASlewY_NmpSec_u4p12[3]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901		
t_LKASlewY_NmpSec_u4p12[5]	30310		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_fltInje$	-3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_el	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	ed_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNr	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.6999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.809000015	0.809000015 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.97300005	-2.97300005 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58520031	5.58519983 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.618896842	0.618897021 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~



Test Step 2.12 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_UIs_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0
FrqArblim_ESC_State_Cnt_M_enum	0
FrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10
FrgArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996 -10.2399998
FrqArblim_PosSrvoCmd_MtrNm_M_f32 FrqArblim_PreLKATrqReq_HwNm_M_f32	-8.8000019
	-0.00000019
<pre><_APAEnableRate_pSec_f32 <_APAUseADmpScale_lgc</pre>	0
<pre><_Ar AddeAbilipocale_lgc</pre> <pre><_APAUseAsstScale_lgc</pre>	0
<pre>CAPAUseRetScale_lgc</pre>	0
C_ESCMax_HwNm_f32	0
C LKAUseSlewCal lgc	0
_APADisableRateX_HwNm_u4p12[0]	0
APADisableRateX HwNm u4p12[1]	0
_APADisableRateX_HwNm_u4p12[2]	0
_APADisableRateX_HwNm_u4p12[3]	0
_APADisableRateX_HwNm_u4p12[4]	0
_APADisableRateX_HwNm_u4p12[5]	0
_APADisableRateY_pSec_u7p9[0]	0
_APADisableRateY_pSec_u7p9[1]	0
APADisableRateY_pSec_u7p9[2]	0
_APADisableRateY_pSec_u7p9[3]	0
_APADisableRateY_pSec_u7p9[4]	0
APADisableRateY_pSec_u7p9[5]	0
_APASmoothX_Uls_u2p14[0]	0
_APASmoothX_Uls_u2p14[1]	0
_APASmoothX_Uls_u2p14[2]	0
_APASmoothX_Uls_u2p14[3]	0
_APASmoothX_Uls_u2p14[4]	0
_APASmoothX_Uls_u2p14[5]	0
_APASmoothX_Uls_u2p14[6]	0
APASmoothX_Uls_u2p14[7]	0
APASmoothX_Uls_u2p14[8]	0
:_APASmoothX_Uls_u2p14[9]	0
:_APASmoothY_Uls_u2p14[0]	0
:_APASmoothY_Uls_u2p14[1]	0
:_APASmoothY_Uls_u2p14[2]	0
:_APASmoothY_Uls_u2p14[3]	0
:_APASmoothY_Uls_u2p14[4]	0
:_APASmoothY_Uls_u2p14[5]	0
t_APASmoothY_Uls_u2p14[6]	0
_APASmoothY_Uls_u2p14[7]	0
t_APASmoothY_Uls_u2p14[8]	0
_APASmoothY_Uls_u2p14[9]	0
_LKASlewX_Kph_u8p8[0]	0
_LKASlewX_Kph_u8p8[1]	0
_LKASlewX_Kph_u8p8[2]	0
_LKASlewX_Kph_u8p8[3]	0
_LKASlewX_Kph_u8p8[4]	0
_LKASlewX_Kph_u8p8[5]	0
_LKASlewY_NmpSec_u4p12[0]	0
_LKASlewY_NmpSec_u4p12[1]	0
_LKASlewY_NmpSec_u4p12[2]	0 0
_LKASlewY_NmpSec_u4p12[3]	0
_LKASlewY_NmpSec_u4p12[4] LKASlewY_NmpSec_u4p12[5]	0
_LKASiewY_NmpSec_u4p12[5] arget Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArbiim.TrqArbiim_Per1_AssistDDFactor_Uis_132 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uis_f32	target_TrqArblim_Per1_AssistDDPactor_Uis_f32 target_TrqArblim_Per1_DampingDDFactor_Uis_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCCind_mwnin_is2 arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESClsLimited_Cnt_lgc	target TrqArblim Per1 ESCIsLimited Cnt Igc
arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCist.imited_Cnt_igc arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIstainited_Crit_igc target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESC5tate_5tate_enum arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_nwivin_isz arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_ESCTorqueDelivered_nwiNfi_132 target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_inst_Ap_rtqArbiim.rtqArbiim_Perr_GMOSHOscillate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim_Perr_GMOSHOscillate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim_Perr_GMOSHOScillate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim_Perr_GMOSHOScillate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim_Perr_GMOSHOScillate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.Perr_GMOSHOSCIllate_State_enum arget_Rte_inst_Ap_rtqArbiim.TrqArbiim.T	target_TrqArblim_Per1_HwTorque_HwNm_f32
argor_rao_mar_np_rrqnramm.rrqnramm_rerr_mw.rorque_mwixiii_isz	
arget Rte Inst An TraArhlim TraArhlim Per1 InTraOv HuNhm f32	target Irgarblim Peri Igirg()v Hwbim t32
	target_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_TrqArblim_Per1_I_KACmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_IrqArblim_Per1_iqIrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum





Name	Input Value			
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f$	32 target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	rget_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	1_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10.2299995			
target_TrqArblim_Per1_ESCState_State_enum.value	0			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998			
target_TrqArblim_Per1_LKAState_State_enum.value	0			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.80000019			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	0	0	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10	-10 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998	-10.2399998 ± 0.000244140625	✓	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.77299976	-8.77299976 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	-	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	1
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390027
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.2299995
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019
k_APAEnableRate_pSec_f32	500
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	5
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	40960
t_APADisableRateX_HwNm_u4p12[1]	40960
t_APADisableRateX_HwNm_u4p12[2]	40960
t_APADisableRateX_HwNm_u4p12[3]	40960
t_APADisableRateX_HwNm_u4p12[4]	40960
t_APADisableRateX_HwNm_u4p12[5]	40960
t_APADisableRateY_pSec_u7p9[0]	65535
t_APADisableRateY_pSec_u7p9[1]	65535
t_APADisableRateY_pSec_u7p9[2]	65535
t_APADisableRateY_pSec_u7p9[3]	65535
t_APADisableRateY_pSec_u7p9[4]	65535
t_APADisableRateY_pSec_u7p9[5]	65535
t_APASmoothX_Uls_u2p14[0]	16384
t_APASmoothX_Uls_u2p14[1]	16384
t_APASmoothX_Uls_u2p14[2]	16384
t_APASmoothX_Uls_u2p14[3]	16384

2014-09-16, 12:30:22+0530





APASSONIANIA U.S. 420140 10056	Nome	Innut Value		
APASONSHIPMON, U.S., 1991491 16984	Name	•		
APAShemore V. Us. 12949				
JAMSBORDON, Use, 1907419				
APASomeDiv. Us. 1209418 18384				
APAShemore/U.B. 1201419 16584				
JAMPSONOMY J.B. 1,2014 1984 1				
APASonodiv Us. Lg54(0)				
ARASmoon' Us up3415 16384				
JAMASHORDY UB, Up0144 1938				
APAStronomy Use 1,2614(8) 16384				
ARASmonth' Usu upsique 19384	t_APASmoothY_Uls_u2p14[3]	16384		
APASancaint' Us u_02449	t_APASmoothY_Uls_u2p14[4]	16384		
JAPASmoothy Use 201410 16984	t_APASmoothY_Uls_u2p14[5]	16384		
LARS-Smooth V, Us, 2014 8 1638 LARS-Smooth V, Lay 1, 2014 8 1638 LARS-Smooth V, Lay 1, 2014 8 1632	t_APASmoothY_Uls_u2p14[6]	16384		
LARS-back V.Sp. up8912	t_APASmoothY_Uls_u2p14[7]	16384		
	t_APASmoothY_Uls_u2p14[8]	16384		
LIAS-Bew Kin 1988 1	t_APASmoothY_Uls_u2p14[9]	16384		
LLASSew X, Kpr. 19882 LLASSew X, Kpr. 19894 LLASSew X, Kpr. 198	t_LKASlewX_Kph_u8p8[0]	56320		
LIASBOWN KN 1988	t_LKASlewX_Kph_u8p8[1]	56320		
LASABEW X, Kon Jusp83		56320		
LASABeay K, Pol. 18684 LASABeay K, Pol. 18684 LASABeay K, Pol. 18695 LASABeay K, Pol.				
LAKASIEWA Xon_Jep8[6]				
LLAKSBEW / NimpSec_Lip12 1				
LUASBewY NmpSec_Jeh1213				
LLASSee/W. NimpSec., July 122 LLASSee/W. NimpSec., July 123 LLASSee/W. NimpSec., July 123 LLASSee/W. NimpSec., July 124 LLASSee/W. NimpSec., July 124 LLASSee/W. NimpSec., July 124 LLASSEE/W. NimpSec., July 125 Lapter, Rev. Leaf, July 125 Lapter, Rev. Le				
LUASBEWN_Norsec_u4e12[4]				
LUASIBEWY NmpSec_u4pt2[6]				
LLAAS Bew NmpSec_usin 2 5	t_LKASlewY_NmpSec_u4p12[3]	55296		
Image_Ric_Dat_An_Traphtim_Faultinjection_SCom_Fithrjection_SignalParh_Uis_Fitarget_Ric_Inst_An_Traphtim_Faultinjection_Scom_Fithrjection_SignalParh_Uis_Fitarget_Ric_Inst_An_Traphtim_Fault_BiscDom_Fault_BiscDom_Fault_BiscDom_Fa	t_LKASlewY_NmpSec_u4p12[4]	55296		
	t_LKASlewY_NmpSec_u4p12[5]	55296		
	$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_flows$	10.2299995		
	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_L	Jls_f32	
	target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target TrqArblim Per1 ESCCmd HwNm f3	2	
target_Rte_Inst_Ap_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_Inst_Ap_TrqArblim_Inst_Ap_TrqArblim_Per1_Inst_Ap_TrqArblim_Inst_Ap_TrqArblim_Per1_Inst_Ap_TrqArblim_Inst_Ap_TrqArblim_Per1_Inst_Ap_TrqArblim_Ins				
target_Re_Inst_Ap_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_Re_Inst_Ap_TrqArblim_Per1_LkACmd_HwNm_f32 target_Re_Inst_Ap_TrqArblim_Per1_LkACmd_HwNm_f32 target_Re_Inst_Ap_TrqArblim_Per1_LkAState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_LkAState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_LkAState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_Per1_LkAState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_Per1_LkAState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_Per1_Per1_Per1_Per1_Per1_Per1_Per1				
target_Rie_Inst_Ap_TrqArblim_Pert_LKACmd_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_LKACstate_State_enum target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_LKACstate_State_enum target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_LKACstate_State_enum target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_AbtracyueDelivered_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_AbtracyueDelivered_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_AbtracyueDelivered_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_AbtracyueDelivered_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ReturnDDFactor_Uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ReturnDDFactor_Uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ReturnDDFactor_Uis_f32 target_TrqArblim_Pert_ESCSCdd_HwNm_f32_value target_TrqArblim_Pert_ESCSCdd_HwNm_f32_value target_TrqArblim_Pert_LKACState_state_enum_value target_TrqArblim_Pert_LKACState_State_enum_value target_TrqArblim_Pert_LKACState_State_enum_value target_TrqArblim_Pert_LKAState_State_enum_value target_TrqArblim_Pert_LKAState_State_enum_value target_TrqArblim_Pert_LKAState_State_enum_value target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32_value target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32_value target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32_value target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32_value target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32_value target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32_value target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32_value target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32_value target_TrqArblim_Pert_Delivered_HwNm_f32_value 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0				
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DFrqCv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OFTrqCv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OFTrqCv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uis_f32 target_TrqArblim_Per1_ESCSCtate_State_enum_value target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_LKACmd_HwNm_f32_value target_TrqArblim_Per1_LKACmd_HwNm_f32_value target_TrqArblim_Per1_LKACmd_HwNm_f32_value target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value target_TrqArblim_Per1_PosServEnable_Uis_M_f32 1 1 1 1 0000390625 **VarqArblim_Per1_TrqArblim_M_f32_value **VarqArblim_Per1_TrqArblim_M_f32_value **Actual Value **Expected Value **Result **TrqArblim_Per1_TrqArblim_M_f32_value **Actual Value **Actual				
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSrvoCmd_Mithm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSrvoCmd_Mithm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSrvoCmd_Mithm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSrvoCmd_Mithm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSrvoCmd_Mithm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSrvoCmd_Mithm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Refl_RtelmnDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_RtelmnDDFactor_Uls_f32 target_TrqArblim_Perl_Ltelmn_Rte				
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_pTrqOv_Mrthm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_pDrsavCy_Mrthm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSevCend_Mrthm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSevCend_Mrthm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSevCend_Mrthm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uis_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32 target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_HwTorque_HwNm_f32_value target_TrqArblim_Per1_HwTorque_HwNm_f32_value target_TrqArblim_Per1_HwTorque_HwNm_f32_value target_TrqArblim_Per1_MrthSecureVehicleSpeed_Kph_f32_value target_TrqArblim_Per1_PosSevCend_Mrthm_f32_value target_TrqArblim_Per1_PosSevCend_Mrthm_f32_value target_TrqArblim_Per1_PosSevCend_Mrthm_f32_value target_TrqArblim_Per1_PosSevCend_Mrthm_f32_value target_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32_value target_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32_value target_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32_value target_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32_value target_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32_value target_TrqArblim_Per1_TrqOsCCmd_Mrthm_f32_value target_TrqArblim_Per1_TrqOsCCmd_Mrthm_Mf32_value target_TrqArblim_Per1_DmpingDDFactor_Uis_f32_value 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	· · · · · ·			
target_Rite_Inst_Ap_TrqArblim_TrqArblim_Pert_OpTrqOv_MtrNm_f32 target_Rite_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSeveEnable_Cnt_lgc target_Rite_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSeveEnable_Cnt_lgc target_Rite_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSeveEnable_Cnt_lgc target_Rite_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSeveEnable_Cnt_lgc target_Rite_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSeveEnable_Cnt_lgc target_Rite_Inst_Ap_TrqArblim_Pert_PosSeveEnable_Cnt_lgc target_Rite_Inst_Ap_TrqArblim_Pert_ReturnDDFactor_Uis_f32 target_Rite_Inst_Ap_TrqArblim_Pert_TrqArblim_Pert_TrqOsCend_MtrNm_f32 target_TrqArblim_Pert_ESCCmd_MtrNm_f32.value target_TrqArblim_Pert_ESCCState_State_enum_value target_TrqArblim_Pert_BOMOSHOScillate_State_enum_value target_TrqArblim_Pert_HVTorque_HvNM_f32.value 10 target_TrqArblim_Pert_HVTorque_HvNM_f32.value 11 target_TrqArblim_Pert_LKAState_State_enum_value 4 target_TrqArblim_Pert_LKAState_State_enum_value 4 target_TrqArblim_Pert_PosSeveEnable_Cnt_lgc_value 51 target_TrqArblim_Pert_PosSeveEnable_Cnt_lgc_value 4 target_TrqArblim_Pert_PosSeveEnable_Cnt_lgc_value 51 target_TrqArblim_Pert_PosSeveEnable_Cnt_lgc_value 58 880000019 **Rand** **Actual Value** **Expected Value** **Result TrqArblim_Pert_TrqOscCmd_MtrNm_f32.value 880000019 **Actual Value** **Expected Value** **Result TrqArblim_HVTorqueSV_HvNm_M_Str.SV_Uls_f32 10 10 10 ± 0.03125 10 10 ± 0.03125 10 10 ± 0.03030625 10 10 ± 0.000390625 10 ± 0.000390625 10 ± 0.000390625 10 ± 0.				
target_Rte_Inst_Ap_TrqArbilm_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArbilm_Per1_PosServCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_PosServCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_PosServCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_TrqArbilm_Per1_PosServCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_TrqArbilm_Per1_TrqOsCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_TrqArbilm_Per1_TrqOsCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_TrqArbilm_Per1_TrqOsCmd_MtrNm_f32 target_TrqArbilm_Per1_ESCCtate_State_enum_value target_TrqArbilm_Per1_ESCCtate_State_enum_value target_TrqArbilm_Per1_ESCCState_State_enum_value target_TrqArbilm_Per1_HwTorque_HwNm_f32_value target_TrqArbilm_Per1_HwTorque_HwNm_f32_value target_TrqArbilm_Per1_LKAState_State_enum_value target_TrqArbilm_Per1_LKAState_State_enum_value target_TrqArbilm_Per1_LKAState_State_enum_value target_TrqArbilm_Per1_ProSServEnable_Cnt_Igc_value target_TrqArbilm_Per1_PosServEnable_Cnt_Igc_value target_TrqArbilm_Per1_PosServEnable_Cnt_Igc_value target_TrqArbilm_Per1_PosServEnable_Cnt_Igc_value target_TrqArbilm_Per1_PosServEnable_Cnt_Igc_value target_TrqArbilm_Per1_PosServEnable_Cnt_Igc_value target_TrqArbilm_Per1_PosServEnable_Cnt_Igc_value target_TrqArbilm_Per1_PosServEnable_Cnt_Igc_value target_TrqArbilm_Per1_PosServEnable_Cnt_Igc_value trqArbilm_PasServEnable_Cnt_MtrNm_f32_value trqArbilm_PasServEnable_Cnt_MtrNm_f32_value 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4				
target_Rie_Inst_Ap_TrqArbilm_Per1_PosSrvoCmd_MitNm_f32 target_Rie_Inst_Ap_TrqArbilm_Per1_ReturnDDFactor_Uis_f32 target_Rie_Inst_Ap_TrqArbilm_Per1_TrqArbilm_Per1_TrqArbilm_Per1_TrqArbilm_Per1_ReturnDDFactor_Uis_f32 target_Rie_Inst_Ap_TrqArbilm_Per1_TrqArbilm_Rig_Value target_TrqArbilm_Per1_LKACMd_HwNm_f32_value target_TrqArbilm_Per1_LKAState_State_enum_value target_TrqArbilm_Per1_LKAState_State_enum_value target_TrqArbilm_Per1_DrqArbilm_Per1_LKAState_State_enum_value target_TrqArbilm_Per1_PrqArbilm_Per1_PrqArbilm_Per1_State_Enum_value target_TrqArbilm_Per1_PrqArbilm_Per1_PrqArbilm_Frq_PrqArbilm_Frq_PrqArbilm_Prq_Prq_DrqArbilm_Prq_Prq_DrqArbilm_Prq_Prq_DrqArbilm_Prq_Prq_DrqArbilm_Prq_Prq_DrqArbilm_Prq_Prq_DrqArbilm_Prq_Prq_DrqArbilm_Pr	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Re_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Re_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCMd_HwNm_f32. target_TrqArblim_Per1_ESCCMd_HwNm_f32. target_TrqArblim_Per1_ESCSCMd_HwNm_f32. target_TrqArblim_Per1_ESCSCMd_HwNm_f32. target_TrqArblim_Per1_ESCSCMd_HwNm_f32. value target_TrqArblim_Per1_ESCSCMd_HwNm_f32. value target_TrqArblim_Per1_HwTorque_HwNm_f32. value target_TrqArblim_Per1_HwTorque_HwNm_f32. value target_TrqArblim_Per1_HwTorque_HwNm_f32. value target_TrqArblim_Per1_HwTorque_HwNm_f32. value target_TrqArblim_Per1_HwTorque_HwNm_f32. value target_TrqArblim_Per1_HwTorque_HwNm_f32. value target_TrqArblim_Per1_DosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_DosServEnable_Cnt_lgc_value target_TrqArblim_Per1_DosServEnable_Cnt_lgc_value target_TrqArblim_Per1_DosServEnable_Cnt_lgc_value target_TrqArblim_Per1_Arget_HwNm_M_f32 target_TrqArblim_Per1_DosServEnable_Cnt_lgc_value target_TrqArblim_Per1_DosServEnable_Cnt_lgc_value target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc_value target_TrqArblim_Per1_ESCISLi	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim_Pert_TrqOscCmd_MtrNm_f32 target_TrqArblim_Pert_ESCCmd_HwNm_f32.value target_TrqArblim_Pert_ESCState_State_enum.value target_TrqArblim_Pert_ESCState_State_enum.value target_TrqArblim_Pert_ESCState_State_enum.value target_TrqArblim_Pert_HwTorque_HwNm_f32.value target_TrqArblim_Pert_HwTorque_HwNm_f32.value target_TrqArblim_Pert_LKACState_State_enum.value target_TrqArblim_Pert_LKACState_State_enum.value target_TrqArblim_Pert_LKACState_State_enum.value target_TrqArblim_Pert_LKACState_State_enum.value target_TrqArblim_Pert_LKACState_State_enum.value target_TrqArblim_Pert_PosServEnable_Cnt_Igc.value target_TrqArblim_Pert_PosServEnable_Cnt_Igc.value target_TrqArblim_Pert_PosServEnable_Cnt_Igc.value target_TrqArblim_Pert_PosServEnable_Cnt_Igc.value target_TrqArblim_Pert_PosServEnable_Cnt_Igc.value target_TrqArblim_Pert_PosServEnable_Cnt_Igc.value target_TrqArblim_Pert_PosServEnable_Cnt_Igc.value target_TrqArblim_Pert_PosServEnable_Cnt_Igc.value trqArblim_APADisableRate_Uis_M_f32 trqArblim_APADisableRate_Uis_M_f32 trqArblim_PosServEnable_Cnt_Igc.value trqArblim_PosServEnable_Cnt_Igc.value trqArblim_PosServEnable_Cnt_Igc.value target_TrqArblim_Pert_AssistDFactor_Uis_f32_value 0 0 0 10 0 0 10 0 0 0 10 0 0 0 10 0 0 0 0	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs	_f32	
target_TrqArblim_Perf_ESCState_State_enum.value	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_f32	
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32_value 10.2299995 target_TrqArblim_Per1_LKACmd_HwNm_f32_value 10.2299995 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_LKAState_State_enum.value 511 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 11 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 11 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 12 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 13 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 14 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 15 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 15 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 11 target_TrqArblim_PADDisableRate_Uls_M_f32 11 12 target_TrqArblim_ESC_State_Cnt_M_enum 14 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32_value 10.2299995 target_TrqArblim_Per1_LKACmd_HwNm_f32_value 10.2299995 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_LKAState_State_enum.value 511 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 11 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 11 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 12 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 13 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 14 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 15 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 15 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 11 target_TrqArblim_PADDisableRate_Uls_M_f32 11 12 target_TrqArblim_ESC_State_Cnt_M_enum 14 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	target TrgArblim Per1 ESCState State enum.value	4		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value				
target_TrqArblim_Per1_LKACmd_HwNm_f32.value				
target_TrqArblim_Per1_LKAState_State_enum.value				
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value				
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value				
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 8.80000019 Name Actual Value Expected Value Result TrqArblim_APADisableRate_Uls_M_f32 1 1±0000390625 ✓ TrqArblim_ESC_State_Cnt_M_enum 4 4 ✓ TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 10 10±0.03125 ✓ TrqArblim_PosSrvoCmd_MtrNm_M_f32 8.80000019 8.80000019±0.000244140625 ✓ TrqArblim_PreLKATrqReq_HwNm_M132 8.77299976 8.77299976±0.03125 ✓ target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0±0000390625 ✓ target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0±0000390625 ✓ target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0±0000390625 ✓ target_TrqArblim_Per1_IgtrqOv_HwNm_f32.value 3 3±0.0000390625 ✓ target_TrqArblim_Per1_IgtrqOv_HwNm_f32.value 3 3±0.0000390625 ✓ target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3±0.0000390625 ✓ target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019±0000390625 ✓ <td></td> <td></td> <td></td> <td></td>				
Result R				
Name Actual Value Expected Value Result TrqArblim_APADisableRate_Uls_M_f32 1 1±0000390625 ✓ TrqArblim_ESC_State_Cnt_M_enum 4 4 ✓ TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 10 10±0.03125 ✓ TrqArblim_PosSrvoCmd_MtrNm_M_f32 8.80000019 8.80000019±0.000244140625 ✓ TrqArblim_PreLKATrqReq_HwNm_M132 8.77299976 8.77299976±0.03125 ✓ target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0±0000390625 ✓ target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0±0000390625 ✓ target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0±0000390625 ✓ target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3±0.0000390625 ✓ target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3±0.0000390625 ✓ target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019±0000390625 ✓				
TrqArblim_APADisableRate_Uls_M_f32 1 1±0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 10 10±0.03125 TrqArblim_PosSrvoCmd_MtrNm_M_f32 8.80000019 8.80000019±0.000244140625 TrqArblim_PreLKATrqReq_HwNm_M_f32 8.77299976 8.77299976±0.03125 target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0±0000390625 target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0±0000390625 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0±0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3±0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3±0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019±0000390625	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019		
TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 10 10 ± 0.03125 TrqArblim_PosSrvoCmd_MtrNm_M_f32 8.80000019 8.80000019 ± 0.000244140625 TrqArblim_PreLKATrqReq_HwNm_M_f32 8.77299976 8.77299976 ± 0.03125 target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625	Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 10 10 ± 0.03125 TrqArblim_PosSrvoCmd_MtrNm_M_f32 8.80000019 8.80000019 ± 0.000244140625 TrqArblim_PreLKATrqReq_HwNm_M_f32 8.77299976 8.77299976 ± 0.03125 target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625	TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32 8.80000019 8.80000019 ± 0.000244140625 TrqArblim_PreLKATrqReq_HwNm_M_f32 8.77299976 8.77299976 ± 0.03125 target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625	TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32 8.80000019 8.80000019 ± 0.000244140625 TrqArblim_PreLKATrqReq_HwNm_M_f32 8.77299976 8.77299976 ± 0.03125 target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625	TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10	10 ± 0.03125	-
TrqArblim_PreLKATrqReq_HwNm_M_f32 8.77299976 8.77299976 ± 0.03125 target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625	TrqArblim_PosSrvoCmd_MtrNm_M_f32			✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625				_
target_TrqArblim_Per1_DampingDDFactor_Uls_132.value 0 0 ± 0000390625 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_132.value 0 0 ± 0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625				·
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value 0				
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0 ± 0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625				
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 3 3 ± 0.0000390625				
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value 3 3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625				V
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 8.80000019 8.80000019 ± 0000390625				*
				~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value 0 0 ± 0000390625 ✓	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019		~
	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0	0 ± 0000390625	<u> </u>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Fest Step 2.14 (Repeat Count = 1)	Input Value
	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.270000011
rqArblim_ESC_State_Cnt_M_enum	4
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.33999991
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
rqArblim_PosSrvoCmd_MtrNm_M_f32	-9.03999996
rqArblim_PreLKATrqReq_HwNm_M_f32	1.3999998
_APAEnableRate_pSec_f32	91.5
:_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
C_ESCMax_HwNm_f32	3.25
_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	8192
_APADisableRateX_HwNm_u4p12[1]	8602
_APADisableRateX_HwNm_u4p12[2]	9011
_APADisableRateX_HwNm_u4p12[3]	9421
_APADisableRateX_HwNm_u4p12[4]	9830
_APADisableRateX_HwNm_u4p12[5]	10240
APADisableRateY pSec u7p9[0]	10240
_APADisableRateY_pSec_u7p9[1]	10752
_APADisableNate1_psec_u1ps[1] _APADisableRateY_psec_u7ps[2]	11264
	11776
_APADisableRateY_pSec_u7p9[3]	
_APADisableRateY_pSec_u7p9[4]	12288
_APADisableRateY_pSec_u7p9[5]	12800
_APASmoothX_Uls_u2p14[0]	3277
_APASmoothX_Uls_u2p14[1]	3441
_APASmoothX_Uls_u2p14[2]	3604
_APASmoothX_Uls_u2p14[3]	3768
_APASmoothX_Uls_u2p14[4]	3932
_APASmoothX_Uls_u2p14[5]	4096
_APASmoothX_Uls_u2p14[6]	4260
_APASmoothX_Uls_u2p14[7]	4424
_APASmoothX_Uls_u2p14[8]	4588
_APASmoothX_Uls_u2p14[9]	4751
_APASmoothY_Uls_u2p14[0]	328
_APASmoothY_Uls_u2p14[1]	492
_APASmoothY_Uls_u2p14[2]	655
APASmoothY Uls u2p14[3]	819
_APASmoothY_Uls_u2p14[4]	983
_APASmoothY_Uls_u2p14[5]	1147
_APASmoothY_Uls_u2p14[6]	1311
_APASmoothY_UIs_u2p14[7]	1475
_APASmoothY_Uls_u2p14[8]	1638
_APASmoothY_UIs_u2p14[9]	1802
_LKASlewX_Kph_u8p8[0]	10240
_LKASlewX_Kph_u8p8[1]	10752
_LKASlewX_Kph_u8p8[2]	11264
_LKASlewX_Kph_u8p8[3]	11776
_LKASlewX_Kph_u8p8[4]	12288
_LKASlewX_Kph_u8p8[5]	12800
_LKASlewY_NmpSec_u4p12[0]	8192
_LKASlewY_NmpSec_u4p12[1]	8602
_LKASlewY_NmpSec_u4p12[2]	9011
LKASlewY_NmpSec_u4p12[3]	9421
_LKASlewY_NmpSec_u4p12[4]	9830
_LKASlewY_NmpSec_u4p12[5]	10240
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls	
goo_oan_/.prrq/.ibinn_r.aaningodion_oooni_r.itingodion_oignair.atil_ois	

2014-09-16, 12:30:22+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.85999966		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.5		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	96.3432999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.2999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.230000004	0.223203003 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.77880001	1.77880001 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.03999996	-9.03999996 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.39399993	1.39400005 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0000390625	✓
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.39399993	1.39400005 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.39399993	1.39400005 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0000390625	~
target TrqArblim Per1 ReturnDDFactor Uls f32.value	0.9885028	0.990368009 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

2014-09-16, 12:30:22+0530



Test Case 3: Boundary Test

TrqArblim_Per1

© Report created by TESSY V3.1.7, report template V2.1

2014-09-16, 12:30:22+0530



Specification

TrqArblim_Per1

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 2392.00 Cycles TS3.2 2725.00 Cycles TS3.3 2732.00 Cycles TS3.4 2550.00 Cycles TS3.5 2793.00 Cycles TS3.6 2391.00 Cycles TS3.7 2509.00 Cycles TS3.8 2361.00 Cycles TS3.9 2674.00 Cycles TS3.9 2674.00 Cycles TS3.10 2325.00 Cycles TS3.11 2345.00 Cycles TS3.12 2402.00 Cycles TS3.13 2267.00 Cycles TS3.14 2504.00 Cycles TS3.15 2256.00 Cycles TS3.16 2503.00 Cycles TS3.17 2342.00 Cycles TS3.18 2554.00 Cycles TS3.18 2564.00 Cycles TS3.19 2264.00 Cycles TS3.20 2344.00 Cycles TS3.20 2394.00 Cycles TS3.20 2344.00 Cycles TS3.21 2399.00 Cycles TS3.22 2390.00 Cycles TS3.23 2439.00 Cycles TS3.24 2389.00 Cycles TS3.25 2290.00 Cycles TS3.26 2246.00 Cycles TS3.27 2550.00 Cycles TS3.28 2350.00 Cycles TS3.29 2444.00 Cycles TS3.30 2290.00 Cycles TS3.31 2514.00 Cycles TS3.32 2302.00 Cycles TS3.33 2469.00 Cycles TS3.33 2469.00 Cycles TS3.32 2302.00 Cycles
TS3.33 2469.00 Cycles
TS3.35 2336.00 Cycles
TS3.35 2336.00 Cycles
TS3.37 2457.00 Cycles
TS3.37 2457.00 Cycles
TS3.39 2412.00 Cycles
TS3.39 2308.00 Cycles
TS3.40 2426.00 Cycles
TS3.41 2308.00 Cycles
TS3.42 2224.00 Cycles
TS3.42 2224.00 Cycles
TS3.44 2254.00 Cycles
TS3.45 2248.00 Cycles
TS3.46 2456.00 Cycles
TS3.46 2456.00 Cycles
TS3.48 2506.00 Cycles
TS3.49 2317.00 Cycles
TS3.50 2324.00 Cycles
TS3.51 2293.00 Cycles
TS3.51 2293.00 Cycles
TS3.52 2281.00 Cycles
TS3.53 2221.00 Cycles TS3.52 2281.00 Cycles TS3.53 2241.00 Cycles TS3.55 2217.00 Cycles TS3.55 2207.00 Cycles TS3.56 2307.00 Cycles TS3.57 2343.00 Cycles TS3.58 2333.00 Cycles TS3.60 2236.00 Cycles TS3.60 2236.00 Cycles TS3.61 2238.00 Cycles TS3.62 2245.00 Cycles TS3.63 2238.00 Cycles TS3.63 2238.00 Cycles TS3.63 2238.00 Cycles TS3.64 2170.00 Cycles TS3.65 2266.00 Cycles TS3.66 2202.00 Cycles TS3.67 2305.00 Cycles TS3.68 2599.00 Cycles TS3.69 2292.00 Cycles TS3.70 2235.00 Cycles TS3.71 2264.00 Cycles TS3.72 2390.00 Cycles TS3.72 2390.00 Cycles TS3.73 2187.00 Cycles TS3.74 2449.00 Cycles TS3.75 2409.00 Cycles TS3.76 2229.00 Cycles TS3.77 2228.00 Cycles TS3.77 2207.00 Cycles TS3.79 2207.00 Cycles TS3.80 2307.00 Cycles TS3.81 2373.00 Cycles TS3.82 2339.00 Cycles TS3.83 2193.00 Cycles TS3.83 2193.00 Cycles TS3.84 2198.00 Cycles TS3.85 2347.00 Cycles TS3.86 2216.00 Cycles TS3.87 2498.00 Cycles TS3.88 2218.00 Cycles TS3.89 2375.00 Cycles TS3.90 2255.00 Cycles TS3.91 2245.00 Cycles TS3.92 2256.00 Cycles TS3.93 2191.00 Cycles TS3.94 2255.00 Cycles TS3.95 2306.00 Cycles TS3.95 2306.00 Cycles TS3.96 2177.00 Cycles TS3.97 2215.00 Cycles TS3.99 2274.00 Cycles TS3.99 2231.00 Cycles TS3.100 2184.00 Cycles TS3.101 2338.00 Cycles TS3.102 2215.00 Cycles TS3.103 2318.00 Cycles TS3.104 2523.00 Cycles TS3.105 2236.00 Cycles TS3.106 2255.00 Cycles TS3.107 2192.00 Cycles

TS3.108 2268.00 Cycles





Description Vector Description:

```
TS3.1LKACmd_HwNm_f32 = Min
   TS3.2LKACmd_HwNm_f32 = Max
   TS3.3LKACmd_HwNm_f32 = Zero
TS3.4LKACmd_HwNm_f32 = Pos
TS3.5LKACmd_HwNm_f32 = Neg
   TS3.6SecureVehicleSpeed_Kph_f32 = Min
TS3.7SecureVehicleSpeed_Kph_f32 = Max
TS3.8SecureVehicleSpeed_Kph_f32 = Pos
   TS3.9PosServEnable_Cnt_lgc = False
TS3.10PosServEnable_Cnt_lgc = True
TS3.11PosSrvoCmd_MtrNm_f32 = Min
   TS3.12PosSrvoCmd_MtrNm_f32 = Max
TS3.13PosSrvoCmd_MtrNm_f32 = Zero
TS3.14PosSrvoCmd_MtrNm_f32 = Pos
    TS3.15PosSrvoCmd_MtrNm_f32= Neg
   TS3.16HwTorque_HwNm_f32 = Min
TS3.17HwTorque_HwNm_f32 = Max
TS3.18HwTorque_HwNm_f32 = Zero
 IS3.18Hw|orque_HwNm_f32 = Zero
TS3.19HwTorque_HwNm_f32 = Pos
TS3.20HwTorque_HwNm_f32 = Neg
TS3.21TrqOscCmd_MtrNm_f32 = Min
TS3.22TrqOscCmd_MtrNm_f32 = Max
TS3.23TrqOscCmd_MtrNm_f32 = Zero
TS3.24TrqOscCmd_MtrNm_f32 = Pos
TS3.25TrqOscCmd_MtrNm_f32 = Neg
TS3.26LKAState_State_enum = LKA_Inactive
TS3.27LKAState_State_enum = LKA_Active
TS3.27LKAState_State_enum = LKA_Active
TS3.28LKAState_State_enum = LKA_TemporarilyLimited
TS3.29LKAState_State_enum = LKA_PermanentlyFailed
TS3.39LKAState_State_enum = LKA_ManufacturingDisable
TS3.31GMOSHOscillate_State_enum = Haptic_Inactive
TS3.31GMOSHOscillate_State_enum = Haptic_Active
TS3.33GMOSHOscillate_State_enum = Haptic_Vaiting
TS3.34TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Min
TS3.35TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Zero
TS3.37TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Pos
TS3.38TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Neg
TS3.38TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Neg
TS3.39TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Ros
TS3.40TrqArblim_HwTorqueSV_HwNm_M_Str.K = Min
TS3.40TrqArblim_HwTorqueSV_HwNm_M_Str.K = Pos
TS3.42TrqArblim_APADisableRate_Uls_M_f32 = Min
TS3.43TrqArblim_APADisableRate_Uls_M_f32 = Pos
   TS3.44TrqArblim_APADisableRate_Uls_M_f32 = Pos
   TS3.45TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Min
TS3.46TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Max
    TS3.47TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Zero
   TS3.48TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Pos
TS3.49TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Neg
   TS3.50TrqArblim_PreLKATrqDel_HwNm_M_f32 = Min
TS3.51TrqArblim_PreLKATrqDel_HwNm_M_f32 = Min
TS3.52TrqArblim_PreLKATrqDel_HwNm_M_f32 = Zero
    TS3.53TrgArblim PreLKATrgDel HwNm M f32 = Pos
   TS3.54TrqArblim_PreLKATrqDel_HwNm_M_f32 = Neg
TS3.55k_APAEnableRate_pSec_f32 = Min
TS3.56k_APAEnableRate_pSec_f32 = Max
   TS3.57k_APAEnableRate_pSec_f32 = Pos
TS3.58k_APAUseAsstScale_lgc = False
TS3.59k_APAUseAsstScale_lgc = True
   TS3.60k_APAUseRetScale_lgc = False
   TS3.61k_APAUseRetScale_lgc = True
TS3.62k_APAUseADmpScale_lgc = False
TS3.63k_APAUseADmpScale_lgc = True
  TS3.68k_LKAUSeSlewCal_lgc = False
TS3.64k_LKAUseSlewCal_lgc = False
TS3.65k_LKAUSeSlewCal_lgc = True
TS3.66t_LKASlewX_Kph_u8p8[6] = Min
TS3.67t_LKASlewX_Kph_u8p8[6] = Max
TS3.68t_LKASlewX_Kph_u8p8[6] = Pos
TS3.69t_LKASlewY_NmpSec_u4p12[6] = Min
  IS3.69f_LKASlewY_NmpSec_u4p12[6] = Min
TS3.70t_LKASlewY_NmpSec_u4p12[6] = Max
TS3.71t_LKASlewY_NmpSec_u4p12[6] = Pos
TS3.72t_APADisableRateX_HwNm_u4p12[6] = Min
TS3.73t_APADisableRateX_HwNm_u4p12[6] = Max
TS3.74t_APADisableRateX_HwNm_u4p12[6] = Pos
TS3.75t_APADisableRateY_pSec_u7p9[6] = Min
TS3.76t_APADisableRateY_pSec_u7p9[6] = Max
TS3.77t_APADisableRateY_pSec_u7p9[6] = Pos
TS3.78t_APASmoothX_UIs_u2p14[10] = Min
TS3.79t_APASMOOTHX_UIS_u2p14[10] = Min
 TS3.781_APASmoothX_UIs_u2p14[10] = Min TS3.79t_APASmoothX_UIs_u2p14[10] = Max TS3.801_APASmoothX_UIs_u2p14[10] = Pos TS3.81t_APASmoothY_UIs_u2p14[10] = Min TS3.82t_APASmoothY_UIs_u2p14[10] = Max TS3.83t_APASmoothY_UIs_u2p14[10] = Pos TS3.84ESCCmd_HwNm_f32=Min TS3.85ESCCmd_HwNm_f32 = Max TS3.85ESCCmd_Hwnm_f32 
   TS3.86ESCCmd_HwNm_f32 = Zero
TS3.87ESCCmd_HwNm_f32 = Pos
TS3.88ESCCmd_HwNm_f32 = Neg
    TS3.89ESCState_State_enum=ESC_Inactive
   TS3.90ESCState_State_enum=ESC_Active
TS3.91ESCState_State_enum=ESC_TemporarilyLimited
TS3.92ESCState_State_enum=ESC_PermanentlyFailed
   TS3.93ESCState_State_enum=ESC_ManufacturingDisable TS3.94k_ESCMax_HwNm_f32=Min
```



TS3.95k_ESCMax_HwNm_f32=Max
TS3.96k_ESCMax_HwNm_f32=Pos
TS3.97TrqArblim_ESC_State_Cnt_M_enum=ESC_Inactive
TS3.98TrqArblim_ESC_State_Cnt_M_enum=ESC_Active
TS3.99TrqArblim_ESC_State_Cnt_M_enum=ESC_TemporarilyLimited
TS3.100TrqArblim_ESC_State_Cnt_M_enum=ESC_PermanentlyFailed
TS3.101TrqArblim_ESC_State_Cnt_M_enum=ESC_ManufacturingDisable
TS3.102Rte_Call_FaultInjection_SCom_FitInjection=Min
TS3.103Rte_Call_FaultInjection_SCom_FitInjection=Max
TS3.104Rte_Call_FaultInjection_SCom_FitInjection=Neg
TS3.105Rte_Call_FaultInjection_SCom_FitInjection=Pos
TS3.106Rte_Call_FaultInjection_SCom_FitInjection=Pos
TS3.107All Min
TS3.108All Max

Test Step 3.1 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.0099999978
FrqArblim_ESC_State_Cnt_M_enum	0
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.85999966
rqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00200000009
FrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003
rqArblim_PreLKATrqReq_HwNm_M_f32	-8.60000038
APAEnableRate pSec f32	1.5
	0
APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
ESCMax_HwNm_f32	2.24000001
:_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	0
_APADisableRateX_HwNm_u4p12[1]	410
_APADisableRateX_HwNm_u4p12[2]	819
_APADisableRateX_HwNm_u4p12[3]	1229
_APADisableRateX_HwNm_u4p12[4]	1638
_APADisableRateX_HwNm_u4p12[5]	2048
_APADisableRateY_pSec_u7p9[0]	0
_APADisableRateY_pSec_u7p9[1]	512
_APADisableRateY_pSec_u7p9[2]	1024
_APADisableRateY_pSec_u7p9[3]	1536
APADisableRateY_pSec_u7p9[4]	2048
APADisableRateY pSec u7p9[5]	2560
_APASmoothX_Uls_u2p14[0]	0
_APASmoothX_Uls_u2p14[1]	164
	328
_APASmoothX_Uls_u2p14[2]	
_APASmoothX_UIs_u2p14[3]	492
_APASmoothX_UIs_u2p14[4]	655
_APASmoothX_Uls_u2p14[5]	819
_APASmoothX_Uls_u2p14[6]	983
_APASmoothX_Uls_u2p14[7]	1147
_APASmoothX_Uls_u2p14[8]	1311
_APASmoothX_Uls_u2p14[9]	1475
_APASmoothY_Uls_u2p14[0]	12780
_APASmoothY_Uls_u2p14[1]	12943
_APASmoothY_Uls_u2p14[2]	13107
_APASmoothY_Uls_u2p14[3]	13271
_APASmoothY_Uls_u2p14[4]	13435
_APASmoothY_Uls_u2p14[5]	13599
_APASmoothY_Uls_u2p14[6]	13763
APASmoothY Uls u2p14[7]	13926
	14090
_APASmoothY_UIs_u2p14[8]	
_APASmoothY_Uls_u2p14[9]	14254
_LKASlewX_Kph_u8p8[0]	0
_LKASlewX_Kph_u8p8[1]	512
_LKASlewX_Kph_u8p8[2]	1024
_LKASlewX_Kph_u8p8[3]	1536
_LKASlewX_Kph_u8p8[4]	2048
_LKASlewX_Kph_u8p8[5]	2560
_LKASlewY_NmpSec_u4p12[0]	0
LKASlewY_NmpSec_u4p12[1]	410
_LKASlewY_NmpSec_u4p12[2]	819
_LKASlewY_NmpSec_u4p12[3]	1229
_LKASlewY_NmpSec_u4p12[4]	1638
	2048
_LKASIewY_NmpSec_u4p12[5] arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t	732	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.82000017		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.64000034		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	2		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.20999993		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	5.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.85956001	-9.85956001 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003	-10.2200003 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.5732336	-8.57322979 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

-0

1

0 ± 0000390625

1 ± 0000390625

Test Step 3.2 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.023
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00419999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038
k_APAEnableRate_pSec_f32	6
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.8499999
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	410
t_APADisableRateX_HwNm_u4p12[1]	819
t_APADisableRateX_HwNm_u4p12[2]	1229
t_APADisableRateX_HwNm_u4p12[3]	1638
t_APADisableRateX_HwNm_u4p12[4]	2048

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	2458		
t_APADisableRateY_pSec_u7p9[0]	512		
t_APADisableRateY_pSec_u7p9[1]	1024		
t_APADisableRateY_pSec_u7p9[2]	1536		
t_APADisableRateY_pSec_u7p9[3]	2048		
	2560		
t_APADisableRateY_pSec_u7p9[4]			
t_APADisableRateY_pSec_u7p9[5]	3072		
t_APASmoothX_Uls_u2p14[0]	164		
t_APASmoothX_Uls_u2p14[1]	328		
t_APASmoothX_Uls_u2p14[2]	492		
t_APASmoothX_Uls_u2p14[3]	655		
t_APASmoothX_Uls_u2p14[4]	819		
t_APASmoothX_Uls_u2p14[5]	983		
t_APASmoothX_Uls_u2p14[6]	1147		
t_APASmoothX_Uls_u2p14[7]	1311		
t_APASmoothX_Uls_u2p14[8]	1475		
t_APASmoothX_Uls_u2p14[9]	1638		
t_APASmoothY_Uls_u2p14[0]	12943		
t_APASmoothY_Uls_u2p14[1]	13107		
t_APASmoothY_Uls_u2p14[2]	13271		
t_APASmoothY_Uls_u2p14[3]	13435		
t_APASmoothY_Uls_u2p14[4]	13599		
t_APASmoothY_Uls_u2p14[5]	13763		
t_APASmoothY_Uls_u2p14[6]	13926		
t APASmoothY Uls u2p14[7]	14090		
t_APASmoothY_Uls_u2p14[8]	14254		
t_APASmoothY_Uls_u2p14[9]	14418		
t LKASlewX Kph u8p8[0]	512		
t_LKASlewX_Kph_u8p8[1]	1024		
t_LKASlewX_Kph_u8p8[2]	1536		
	2048		
t_LKASlewX_Kph_u8p8[3]	2560		
t_LKASlewX_Kph_u8p8[4]			
t_LKASlewX_Kph_u8p8[5]	3072		
t_LKASlewY_NmpSec_u4p12[0]	410		
t_LKASlewY_NmpSec_u4p12[1]	819		
t_LKASlewY_NmpSec_u4p12[2]	1229		
t_LKASlewY_NmpSec_u4p12[3]	1638		
t_LKASlewY_NmpSec_u4p12[4]	2048		
t_LKASlewY_NmpSec_u4p12[5]	2458		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	-9.5		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_I	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_l	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target TrqArblim Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target TrqArblim Per1 PosSrvoCmd MtrNn		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor Uls f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target TrqArblim Per1 TrqOscCmd MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989		
	1		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.22999954		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	5.86000013		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.560000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0109999999	0.0109999999 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.24571609	-9.24571991 ± 0.03125	-
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046	-9.02000046 ± 0.000244140625	-
,		3.0020020	





Name	Actual Value	Expected Value	Result
Hame	Actual Value	Expected value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.09400082	-8.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_UIs_f32.value	0.99129951	0.991299987 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4.8499999	-4.8499999 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0784785151	-0.0784785002 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0359999985
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.64000034
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00639999984
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.82000017
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.5999999
k_APAEnableRate_pSec_f32	10.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.30999994
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	819
t_APADisableRateX_HwNm_u4p12[1]	1229
t_APADisableRateX_HwNm_u4p12[2]	1638
t_APADisableRateX_HwNm_u4p12[3]	2048
t_APADisableRateX_HwNm_u4p12[4]	2458
t_APADisableRateX_HwNm_u4p12[5]	2867
t_APADisableRateY_pSec_u7p9[0]	1024
t_APADisableRateY_pSec_u7p9[1]	1536
t_APADisableRateY_pSec_u7p9[2]	2048
t_APADisableRateY_pSec_u7p9[3]	2560
t_APADisableRateY_pSec_u7p9[4]	3072
t_APADisableRateY_pSec_u7p9[5]	3584
t_APASmoothX_Uls_u2p14[0]	328
t APASmoothX Uls u2p14[1]	492
t_APASmoothX_Uls_u2p14[2]	655
t_APASmoothX_Uls_u2p14[3]	819
t_APASmoothX_Uls_u2p14[4]	983
t_APASmoothX_Uls_u2p14[5]	1147
t APASmoothX Uls u2p14[6]	1311
t_APASmoothX_Uls_u2p14[7]	1475
t_APASmoothX_Uls_u2p14[8]	1638
t_APASmoothX_UIs_u2p14[9]	1802
t_APASmoothY_Uls_u2p14[0]	13107
t_APASmoothY_Uls_u2p14[1]	13271
t_APASmoothY_Uls_u2p14[2]	13435
t_APASmoothY_Uls_u2p14[3]	13599
t_APASmoothY_Uls_u2p14[4]	13763
t APASmoothY Uls u2p14[5]	13926
t_APASmoothY_Uls_u2p14[6]	14090
t_APASmoothY_UIs_u2p14[7]	14254
t_APASmoothY_Uls_u2p14[8]	14418
t_APASmoothY_Uls_u2p14[9]	14582
t_LKASlewX_Kph_u8p8[0]	1024
t_LKASlewX_Kph_u8p8[1]	1536
t LKASlewX Kph u8p8[2]	2048
t_LKASlewX_Kph_u8p8[3]	2560

2014-09-16, 12:30:22+0530



TrqArblim_Per1

TrqArblim_Per1			MACICAL
Name	Input Value		
t LKASlewX Kph u8p8[4]	3072		
t LKASlewX Kph u8p8[5]	3584		
t_LKASlewY_NmpSec_u4p12[0]	819		
t_LKASlewY_NmpSec_u4p12[1]	1229		
t LKASlewY NmpSec u4p12[2]	1638		
t LKASlewY NmpSec u4p12[3]	2048		
t LKASlewY NmpSec u4p12[4]	2458		
t LKASlewY NmpSec u4p12[5]	2867		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	-8.80000019		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	; f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target TrqArblim Per1 DampingDDFactor	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target TrqArblim Per1 ESCCmd HwNm f3	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State er		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target TrqArblim Per1 ESCTorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate S		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State en		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target TrqArblim Per1 LKATorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleS		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target TrqArblim Per1 OpTrqOv MtrNm f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor Uls f32	target_TrqArblim_Per1_ReturnDDFactor_UI		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	-5.42000008	_	
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.82000017		
target TrqArblim Per1 LKACmd HwNm f32.value	0		
target TrqArblim Per1 LKAState State enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	9.43000031		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.910000026		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.30000019		
Name	Actual Value	Expected Value	Result
TrgArblim APADisableRate Uls M f32	0.057	0.057 ± 0000390625	✓
TrgArblim ESC State Cnt M enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.62835217	-8.62835026 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.910000026	0.910000026 ± 0.0002441406	25
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.59399986	-7.59399986 ± 0.03125	
target TrqArblim Per1 AssistDDFactor Uls f32.value	0.952292442	0.952292025 ± 0000390625	✓
target TrqArblim Per1 DampingDDFactor Uls f32.value	0.952292442	0.952292025 ± 0000390625	_
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.30999994	-3.30999994 ± 0000390625	_
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target TrqArblim Per1 LKATorqueDelivered HwNm f32.value	0	0 ± 0.0000390625	_
target TrqArblim Per1 OpTrqOv MtrNm f32.value	6.34341431	6.34341002 ± 0000390625	✓
		1.1.10.1.002 2.0000000020	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0.952292442

0.952292025 ± 0000390625

Test Step 3.4 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0489999987
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.02999973
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00860000029
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.61999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.0999999

2014-09-16, 12:30:22+0530



KAPAEnableTate pSec 132 15	
K.APALesARDingScale_lgc	
K_APALiseAssiScale_lgc 0 K_APALiseRelScale_lgc 0 K_ESCMat_HAWn_{22} 1.20000005 K_LKAUSSEWCAL_lgc 0 L_APADassiDeCall_lgc 0 L_APADassiDeCall_lgc 1229 L_APADassiDeCall_HAWn_u4p12[] 2048 L_APADassiDeCall_HAWn_u4p12[] 2048 L_APADassiDeCall_HAWn_u4p12[] 2867 L_APADassiDeCall_HAWn_u4p12[] 2867 L_APADassiDeCall_HAWn_u4p12[] 2867 L_APADassiDeCall_HAWn_u4p12[] 286 L_APADassiDeCall_HAWn_u4p12[] 286 L_APADassiDeCall_HAWn_u4p12[] 286 L_APADassiDeCall_HAWn_u4p12[] 288 L_APADassiDeCall_HAWn_u4p12[] 298 L_APADassiDeCall_HAWn_u4p12[] 298 L_APADassiDeCall_HAWn_u4p12[] 296 L_APADassiDeCall_HAWn_u4p12[] 354 L_APADassiDeCall_HAWn_u4p12[] 356 L_APADassiDeCall_HAWn_u4p12[] 492 L_APADassiDeCall_HAWn_u4p12[] 492 L_APASimochY_u5_u2p14[] 492 L_APASimochY_u5_u2p14[] 1147	
K_RANJERISCIALE_195 0	
KESCMA: Hwhm. 152	
K_LKAUseSiewCalgc	
LAPADisableRateX_Hwhm_u4p12[0]	
LAPAIsableRateX, Hwhm_usp12(1)	
LAPASinableRateX, HwNm_u4p12[3]	
APADisableRateX, Hwhm_u4p12 3 2458 L.PADisableRateX, Hwhm_u4p12 4 2667 L.PADisableRateX, Hwhm_u4p12 5 3277 L.PADisableRateY_pSec_u7p6 0 1536 L.PADisableRateY_pSec_u7p6 1 2048 L.PADisableRateY_pSec_u7p6 2 2560 L.PADisableRateY_pSec_u7p6 2 2560 L.PADisableRateY_pSec_u7p6 3 3072 L.PADisableRateY_pSec_u7p6 4 3584 L.PADisableRateY_pSec_u7p6 4 3584 L.PADisableRateY_pSec_u7p6 4 3684 L.PADisableRateY_pSec_u7p6 4 492 L.PADISableRateY_pSec_u7p6 4 492 L.PADISableRateY_pSec_u7p6 4 492 L.PASmoothX_UIs_u2p14 0 492 L.PASmoothX_UIs_u2p14 2 819 L.PASmoothX_UIs_u2p14 3 983 L.PASmoothX_UIs_u2p14 4 1147 L.PASmoothX_UIs_u2p14 4 1147 L.PASmoothX_UIs_u2p14 6 1475 L.PASmoothX_UIs_u2p14 6 1475 L.PASmoothX_UIs_u2p14 6 1475 L.PASmoothX_UIs_u2p14 6 1802 L.PASmoothY_UIs_u2p14 6 1802 L.PASmoothY_UIs_u2p14 6 1475 L.PASmoothY_UIs_u2p14 6 1494 L.PASmoothY_UIs_u2p14 6 1494 L.PASmoothY_UIs_u2p14 6 1495 L.PASmoothY_UIs_u2p14 6 1494 L.PASmoothY_UIs_u2p14 6 1495 L.PASmoothY_UIs_u2p14 6 1495 L.PASmoothY_UIs_u2p14 6 1496 L.PASMoothY_UIS_u2p14 6 1496	
APADisableRateX, HwNm_u4p12[4] 2867 LPADisableRateX, HwNm_u4p12[4] 3277 1536	
LAPADisableRateX_HwNm_u4p12[5] 3277 LAPADisableRateY_pSec_u7p6[1] 1536 LAPADisableRateY_pSec_u7p6[1] 2048 LAPADisableRateY_pSec_u7p6[2] 2560 LAPADisableRateY_pSec_u7p6[2] 2560 LAPADisableRateY_pSec_u7p6[3] 3072 LAPADisableRateY_pSec_u7p6[3] 3072 LAPADisableRateY_pSec_u7p6[4] 3584 LAPADisableRateY_pSec_u7p6[5] 4096 LAPASmothX_UIs_u2p14[0] 492 LAPASmothX_UIs_u2p14[1] 655 LAPASmothX_UIs_u2p14[2] 819 LAPASmothX_UIs_u2p14[2] 819 LAPASmothX_UIs_u2p14[3] 983 LAPASmothX_UIs_u2p14[4] 1147 LAPASmothX_UIs_u2p14[6] 1311 LAPASmothX_UIs_u2p14[6] 1475 LAPASmothX_UIs_u2p14[7] 1638 LAPASmothX_UIs_u2p14[8] 1802 LAPASmothX_UIs_u2p14[8] 1802 LAPASmothX_UIs_u2p14[9] 1966 LAPASmothX_UIs_u2p14[9] 1966 LAPASmothY_UIS_u2p14[9] 1966 LAPASmothY_UIS_u2p14[9] 13435 LAPASmothY_UIS_u2p14[9] 13435 LAPASmothY_UIS_u2p14[9] 13599 LAPASmothY_UIS_u2p14[9] 13699 LAPASmothY_UIS_u2p14[9] 13929 LAPASmothY_UIS_u2p14[9] 13929 LAPASmothY_UIS_u2p14[9] 14448 LLKASlewX_Kpt_uBp8[1] 2048 LLKASlewX_Kpt_uBp8[1] 2048 LLKASlewX_Kpt_uBp8[2] 2560 LLKASlewX_Kpt_uBp8[3] 3072 LLKASlewX_Kpt_uBp8[4] 3584 LLKASlewX_Kpt_uBp8[4] 3684 LLKASlewX_Kpt_uBp8[4] 3684 LLKASlewX_Kpt_uBp8[4] 3684 LLKASlewX_MpnSec_u4p12[9] 3248	
LAPADisableRateY_pSec_u7p9(0)	
LAPADisableRateY_pSec_u7p8[1] 2048 LAPADisableRateY_pSec_u7p8[2] 2550 LAPADisableRateY_pSec_u7p8[2] 3072 LAPADisableRateY_pSec_u7p8[3] 3072 LAPADisableRateY_pSec_u7p8[4] 3584 LAPADisableRateY_pSec_u7p8[5] 4096 LAPASmothX_UIs_u2p14[0] 492 LAPASmothX_UIs_u2p14[1] 655 LAPASmothX_UIs_u2p14[2] 819 LAPASmothX_UIs_u2p14[3] 983 LAPASmothX_UIs_u2p14[4] 1147 LAPASmothX_UIs_u2p14[6] 1147 LAPASmothX_UIs_u2p14[6] 1147 LAPASmothX_UIs_u2p14[6] 1475 LAPASmothX_UIs_u2p14[6] 1475 LAPASmothX_UIs_u2p14[8] 1802 LAPASmothX_UIs_u2p14[8] 1802 LAPASmothX_UIs_u2p14[8] 1802 LAPASmothY_UIs_u2p14[9] 1966 LAPASmothY_UIs_u2p14[1] 13379 LAPASmothY_UIs_u2p14[1] 13399 LAPASmothY_UIs_u2p14[1] 13435 LAPASmothY_UIs_u2p14[1] 13926 LAPASmothY_UIs_u2p14[1] 13926 LAPASmothY_UIs_u2p14[1] 13926 LAPASmothY_UIs_u2p14[1] 13926 LAPASmothY_UIs_u2p14[1] 13926 LAPASmothY_UIs_u2p14[1] 14418 LAPASmothY_UIs_u2p14[6] 14254 LAPASmothY_UIs_u2p14[6] 14254 LAPASmothY_UIs_u2p14[6] 14254 LAPASmothY_UIs_u2p14[6] 14264 LAPASmothY_UIs_u2p14[6] 14418 LAPASMOthY_UIS_u2p	
LAPADisableRatery_PSec_u7p8[2] 2560 LAPADisableRatery_PSec_u7p8[3] 3072 LAPADisableRatery_PSec_u7p8[4] 3584 LAPADisableRatery_PSec_u7p9[5] 4096 LAPASmoothX_UIs_u2p14[0] 492 LAPASmoothX_UIs_u2p14[7] 655 LAPASmoothX_UIs_u2p14[3] 819 LAPASmoothX_UIs_u2p14[3] 983 LAPASmoothX_UIs_u2p14[4] 1147 LAPASmoothX_UIs_u2p14[6] 1311 LAPASmoothX_UIs_u2p14[7] 1638 LAPASmoothX_UIs_u2p14[8] 1802 LAPASmoothX_UIs_u2p14[9] 1966 LAPASmoothY_UIs_u2p14[9] 1966 LAPASmoothY_UIs_u2p14[0] 13271 LAPASmoothY_UIs_u2p14[0] 13271 LAPASmoothY_UIs_u2p14[2] 13599 LAPASmoothY_UIs_u2p14[3] 13763 LAPASmoothY_UIs_u2p14[4] 13926 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[8] 14582 LAPASmoothY_UIs_u2p14[8] 14582 LAPASmoothY_UIs_u2p14[8] 14582 LAPASmoothY_UIs_u2p14[8] 14582 LAPASmoot	
LAPADisableRateY_pSec_u7p8[3] 3072 LAPADisableRateY_pSec_u7p8[4] 3584 LAPADisableRateY_pSec_u7p8[4] 4096 LAPASmoothX_UIs_u2p14[0] 492 LAPASmoothX_UIs_u2p14[0] 492 LAPASmoothX_UIs_u2p14[2] 819 LAPASmoothX_UIs_u2p14[3] 983 LAPASmoothX_UIs_u2p14[3] 983 LAPASmoothX_UIs_u2p14[6] 1147 LAPASmoothX_UIs_u2p14[6] 1311 LAPASmoothX_UIs_u2p14[6] 1475 LAPASmoothX_UIs_u2p14[6] 1475 LAPASmoothX_UIs_u2p14[7] 1638 LAPASmoothX_UIs_u2p14[8] 1802 LAPASmoothX_UIs_u2p14[8] 1996 LAPASmoothX_UIs_u2p14[9] 1996 LAPASmoothY_UIs_u2p14[9] 1996 LAPASmoothY_UIs_u2p14[1] 13435 LAPASmoothY_UIs_u2p14[1] 13435 LAPASmoothY_UIs_u2p14[3] 13763 LAPASmoothY_UIs_u2p14[4] 13926 LAPASmoothY_UIs_u2p14[4] 13926 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[6] 14476 LAPASmoothY_UIs_u2p14[6] 14582 LAPASMo	
LAPADisableRatery_pSec_urp9[4] 3584 LAPADisableRatery_pSec_urp9[5] 4096 LAPASmoothX_Uls_u2p14[9] 492 LAPASmoothX_Uls_u2p14[1] 655 LAPASmoothX_Uls_u2p14[3] 819 LAPASmoothX_Uls_u2p14[3] 983 LAPASmoothX_Uls_u2p14[4] 1147 LAPASmoothX_Uls_u2p14[6] 1311 LAPASmoothX_Uls_u2p14[6] 1475 LAPASmoothX_Uls_u2p14[7] 1638 LAPASmoothX_Uls_u2p14[8] 1802 LAPASmoothX_Uls_u2p14[9] 1966 LAPASmoothY_Uls_u2p14[9] 1966 LAPASmoothY_Uls_u2p14[1] 13435 LAPASmoothY_Uls_u2p14[1] 13435 LAPASmoothY_Uls_u2p14[3] 13763 LAPASmoothY_Uls_u2p14[4] 13926 LAPASmoothY_Uls_u2p14[6] 14090 LAPASmoothY_Uls_u2p14[6] 14254 LAPASmoothY_Uls_u2p14[7] 14418 LAPASmoothY_Uls_u2p14[8] 14582 LAPASmoothY_Uls_u2p14[8] 14582 LAPASmoothY_Uls_u2p14[8] 14582 LAPASmoothY_Uls_u2p14[8] 14582 LAPASmoothY_Uls_u2p14[8] 14746 LKASIewX_Kph_u8p8[0]	
LAPASmoothX_Uls_uzp14[0] 492 LAPASmoothX_Uls_uzp14[1] 655 LAPASmoothX_Uls_uzp14[2] 819 LAPASmoothX_Uls_uzp14[2] 983 LAPASmoothX_Uls_uzp14[3] 983 LAPASmoothX_Uls_uzp14[4] 1147 LAPASmoothX_Uls_uzp14[4] 1147 LAPASmoothX_Uls_uzp14[5] 1311 LAPASmoothX_Uls_uzp14[6] 1475 LAPASmoothX_Uls_uzp14[7] 1638 LAPASmoothX_Uls_uzp14[7] 1638 LAPASmoothX_Uls_uzp14[8] 1906 LAPASmoothX_Uls_uzp14[9] 1906 LAPASmoothY_Uls_uzp14[9] 1906 LAPASmoothY_Uls_uzp14[9] 13435 LAPASmoothY_Uls_uzp14[9] 13435 LAPASmoothY_Uls_uzp14[9] 13696 LAPASmoothY_Uls_uzp14[9] 13699 LAPASmoothY_Uls_uzp14[9] 13926 LAPASmoothY_Uls_uzp14[9] 13926 LAPASmoothY_Uls_uzp14[9] 14926 LAPASmoothY_Uls_uzp14[9] 14926 LAPASmoothY_Uls_uzp14[9] 14926 LAPASmoothY_Uls_uzp14[9] 14926 LAPASmoothY_Uls_uzp14[9] 14926 LAPASmoothY_Uls_uzp14[9] 14926 LAPASmoothY_Uls_uzp14[9] 14418 LAPASmoothY_Uls_uzp14[9] 14418 LAPASmoothY_Uls_uzp14[9] 14418 LAPASmoothY_Uls_uzp14[9] 14418 LAPASmoothY_Uls_uzp14[9] 14746 LLKASlewX_Kph_usp8[0] 1536 LLKASlewX_Kph_usp8[1] 2048 LLKASlewX_Kph_usp8[3] 3072 LLKASlewX_Kph_usp8[4] 3564 LLKASlewX_Kph_usp8[5] 4096 LLKASlewX_Kph_usp8[6] 4096 LLKASlewX_Kph_usp8[6] 4096 LLKASlewX_Kph_usp8[6] 4096 LLKASlewX_Nph_usp8[6] 4096	
t_APASmoothX_Uis_u2p14[0] 492 t_APASmoothX_Uis_u2p14[1] 655 t_APASmoothX_Uis_u2p14[2] 819 t_APASmoothX_Uis_u2p14[3] 983 t_APASmoothX_Uis_u2p14[4] 1147 t_APASmoothX_Uis_u2p14[5] 1311 t_APASmoothX_Uis_u2p14[6] 1475 t_APASmoothX_Uis_u2p14[8] 1802 t_APASmoothX_Uis_u2p14[9] 1966 t_APASmoothY_Uis_u2p14[9] 1966 t_APASmoothY_Uis_u2p14[1] 13271 t_APASmoothY_Uis_u2p14[1] 13435 t_APASmoothY_Uis_u2p14[2] 13599 t_APASmoothY_Uis_u2p14[3] 13763 t_APASmoothY_Uis_u2p14[6] 14990 t_APASmoothY_Uis_u2p14[6] 14254 t_APASmoothY_Uis_u2p14[8] 14582 t_APASmoothY_Uis_u2p14[9] 14418 t_APASmoothY_Uis_u2p14[9] 14748 t_LKASiewX_Kph_u8p8[0] 1538 t_LKASiewX_Kph_u8p8[3] 3072 t_LKASiewX_Kph_u8p8[5] 4096 t_LKASiewY_NmpSec_u4p12[0] 1229 t_LKASiewY_NmpSec_u4p12[1] 1638 t_LKASiewY_NmpSec_u4p12[2] 2048 t_LKASiewY_NmpSe	
L APASmoothX_UIs_u2p14[2] 819 LAPASmoothX_UIs_u2p14[3] 983 LAPASmoothX_UIs_u2p14[5] 1147 LAPASmoothX_UIs_u2p14[5] 1311 LAPASmoothX_UIs_u2p14[6] 1475 LAPASmoothX_UIs_u2p14[7] 1638 LAPASmoothX_UIs_u2p14[8] 1802 LAPASmoothY_UIs_u2p14[9] 1966 LAPASmoothY_UIs_u2p14[0] 13271 LAPASmoothY_UIs_u2p14[1] 13435 LAPASmoothY_UIs_u2p14[2] 13599 LAPASmoothY_UIs_u2p14[3] 13763 LAPASmoothY_UIs_u2p14[4] 13926 LAPASmoothY_UIs_u2p14[5] 14090 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[7] 14418 LAPASmoothY_UIs_u2p14[8] 14582 LAPASmoothY_UIs_u2p14[9] 14746 LLKASiewX_Kph_u8p8[0] 1536 LLKASiewX_Kph_u8p8[2] 2048 LLKASiewX_Kph_u8p8[3] 3072 LLKASiewX_Kph_u8p8[4] 3584 LLKASiewX_Kph_u8p8[5] 4096 LLKASiewX_Kph_u8p8[6] 4096 LLKASiewX_Kph_u8p8[6] 4096 LLKASiewX_Kph_u8p8[6] 4096 <	
LAPASmoothY_UIs_u2p14[3] 983 LAPASmoothX_UIs_u2p14[4] 1147 LAPASmoothX_UIs_u2p14[5] 1311 LAPASmoothX_UIs_u2p14[6] 1475 LAPASmoothX_UIs_u2p14[7] 1638 LAPASmoothX_UIs_u2p14[7] 1638 LAPASmoothY_UIs_u2p14[8] 1802 LAPASmoothY_UIs_u2p14[9] 1966 LAPASmoothY_UIs_u2p14[9] 13271 LAPASmoothY_UIs_u2p14[1] 13435 LAPASmoothY_UIs_u2p14[2] 1399 LAPASmoothY_UIs_u2p14[3] 13763 LAPASmoothY_UIs_u2p14[3] 13763 LAPASmoothY_UIs_u2p14[4] 1992 LAPASmoothY_UIs_u2p14[6] 14090 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[7] 14418 LAPASmoothY_UIs_u2p14[8] 14582 LAPASmoothY_UIs_u2p14[8] 14582 LAPASmoothY_UIs_u2p14[8] 14746 LLKASlewX_Kph_u8p8[0] 1536 LLKASlewX_Kph_u8p8[1] 2048 LLKASlewX_Kph_u8p8[2] 2560 LLKASlewX_Kph_u8p8[3] 3072 LLKASlewX_Kph_u8p8[4] 3584 LLKASlewY_Kph_u8p8[4] 3584 LLKASlewY_NphSec_u4p12[0] 1229 LLKASlewY_NmpSec_u4p12[1] 1638 LLKASlewY_NmpSec_u4p12[2] 2048 LLKASlewY_NmpSec_u4p12[2] 2048 LLKASlewY_NmpSec_u4p12[2] 2048 LLKASlewY_NmpSec_u4p12[3] 2458	
LAPASmoothX_UIs_u2p14[4] 1147 LAPASmoothX_UIs_u2p14[5] 1311 LAPASmoothX_UIs_u2p14[6] 1475 LAPASmoothX_UIs_u2p14[7] 1638 LAPASmoothX_UIs_u2p14[8] 1802 LAPASmoothX_UIs_u2p14[9] 1966 LAPASmoothY_UIs_u2p14[9] 1966 LAPASmoothY_UIs_u2p14[1] 13435 LAPASmoothY_UIs_u2p14[1] 13435 LAPASmoothY_UIs_u2p14[2] 13599 LAPASmoothY_UIs_u2p14[3] 13763 LAPASmoothY_UIs_u2p14[4] 13926 LAPASmoothY_UIs_u2p14[5] 14090 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[6] 14254 LAPASmoothY_UIs_u2p14[7] 14418 LAPASmoothY_UIs_u2p14[8] 14582 LAPASmoothY_UIs_u2p14[9] 14746 LLAPASmoothY_UIs_u2p14[9] 14746 LLAPASmoothY_UIs_u2p14[9] 1536 LAPASMOOthY_UIS_u2p14[9] 1536 LAPAS	
LAPASmoothX, Uls_uzp14[5] 1311 LAPASmoothX, Uls_uzp14[6] 1475 LAPASmoothX, Uls_uzp14[7] 1638 LAPASmoothX, Uls_uzp14[8] 1802 LAPASmoothY, Uls_uzp14[9] 1966 LAPASmoothY, Uls_uzp14[0] 13271 LAPASmoothY, Uls_uzp14[1] 13435 LAPASmoothY, Uls_uzp14[2] 13599 LAPASmoothY, Uls_uzp14[3] 13763 LAPASmoothY, Uls_uzp14[6] 14900 LAPASmoothY, Uls_uzp14[6] 14254 LAPASmoothY, Uls_uzp14[7] 14418 LAPASmoothY, Uls_uzp14[8] 14582 LAPASmoothY, Uls_uzp14[9] 14746 LLKASlewX, Kph_u8p8[0] 1536 LLKASlewX, Kph_u8p8[3] 2048 LLKASlewX, Kph_u8p8[3] 3072 LLKASlewX, Kph_u8p8[4] 3564 LLKASlewY, NmpSec_u4p12[0] 1229 LLKASlewY, NmpSec_u4p12[1] 1638 LLKASlewY, NmpSec_u4p12[2] 2048 LLKASlewY, NmpSec_u4p12[3] 2048 LLKASlewY, NmpSec_u4p12[3] 2048 LLKASlewY, NmpSec_u4p12[3] 2048	
t_APASmoothX_Uls_u2p14[6] 1475 t_APASmoothX_Uls_u2p14[7] 1638 t_APASmoothX_Uls_u2p14[8] 1802 t_APASmoothX_Uls_u2p14[9] 1966 t_APASmoothY_Uls_u2p14[0] 13271 t_APASmoothY_Uls_u2p14[1] 13435 t_APASmoothY_Uls_u2p14[2] 13599 t_APASmoothY_Uls_u2p14[3] 13763 t_APASmoothY_Uls_u2p14[4] 13926 t_APASmoothY_Uls_u2p14[5] 14090 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[3] 3584 t_LKASlewY_NmpSec_u4p12[0] 129 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2048	
t_APASmoothX_Uls_u2p14[7] 1638 t_APASmoothX_Uls_u2p14[8] 1802 t_APASmoothX_Uls_u2p14[9] 1966 t_APASmoothY_Uls_u2p14[0] 13271 t_APASmoothY_Uls_u2p14[1] 13435 t_APASmoothY_Uls_u2p14[2] 13599 t_APASmoothY_Uls_u2p14[3] 13763 t_APASmoothY_Uls_u2p14[4] 13926 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[6] 14418 t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[8] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[4] 1229 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothX_UIs_u2p14[8] 1966 t_APASmoothY_UIs_u2p14[9] 1966 t_APASmoothY_UIs_u2p14[1] 13271 t_APASmoothY_UIs_u2p14[1] 13435 t_APASmoothY_UIs_u2p14[2] 13599 t_APASmoothY_UIs_u2p14[3] 13763 t_APASmoothY_UIs_u2p14[4] 13926 t_APASmoothY_UIs_u2p14[5] 14090 t_APASmoothY_UIs_u2p14[6] 14254 t_APASmoothY_UIs_u2p14[7] 14418 t_APASmoothY_UIs_u2p14[7] 14418 t_APASmoothY_UIs_u2p14[8] 14582 t_APASmoothY_UIs_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewX_Kph_u8p8[6] 1229 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[9] 1966 t_APASmoothY_Uls_u2p14[0] 13271 t_APASmoothY_Uls_u2p14[1] 13435 t_APASmoothY_Uls_u2p14[2] 13599 t_APASmoothY_Uls_u2p14[3] 13763 t_APASmoothY_Uls_u2p14[4] 13926 t_APASmoothY_Uls_u2p14[6] 14090 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[6] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[0] 13271 t_APASmoothY_Uls_u2p14[1] 13435 t_APASmoothY_Uls_u2p14[2] 13599 t_APASmoothY_Uls_u2p14[3] 13763 t_APASmoothY_Uls_u2p14[4] 13926 t_APASmoothY_Uls_u2p14[6] 14090 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[1] 13435 t_APASmoothY_Uls_u2p14[2] 13599 t_APASmoothY_Uls_u2p14[3] 13763 t_APASmoothY_Uls_u2p14[4] 13926 t_APASmoothY_Uls_u2p14[5] 14090 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[2]	
t_APASmoothY_Uls_u2p14[3] 13763 t_APASmoothY_Uls_u2p14[4] 13926 t_APASmoothY_Uls_u2p14[5] 14090 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[4] 13926 t_APASmoothY_Uls_u2p14[5] 14090 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[5] 14090 t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_KASlewX_Kph_u8p8[0] 1536 t_KASlewX_Kph_u8p8[1] 2048 t_KASlewX_Kph_u8p8[2] 2560 t_KASlewX_Kph_u8p8[3] 3072 t_KASlewX_Kph_u8p8[4] 3584 t_KASlewX_Kph_u8p8[5] 4096 t_KASlewY_NmpSec_u4p12[0] 1229 t_KASlewY_NmpSec_u4p12[1] 1638 t_KASlewY_NmpSec_u4p12[2] 2048 t_KASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[6] 14254 t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[7] 14418 t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[8] 14582 t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_APASmoothY_Uls_u2p14[9] 14746 t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewX_Kph_u8p8[0] 1536 t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewX_Kph_u8p8[1] 2048 t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewX_Kph_u8p8[2] 2560 t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewX_Kph_u8p8[3] 3072 t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewX_Kph_u8p8[4] 3584 t_LKASlewX_Kph_u8p8[5] 4096 t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewY_NmpSec_u4p12[0] 1229 t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewY_NmpSec_u4p12[1] 1638 t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewY_NmpSec_u4p12[2] 2048 t_LKASlewY_NmpSec_u4p12[3] 2458	
t_LKASlewY_NmpSec_u4p12[3] 2458	
t LKASlawY NmpSec u4n12[4] 2867	
<u></u>	
t_LKASlewY_NmpSec_u4p12[5] 3277	
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f8.10000038	
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32 target_TrqArblim_Per1_AssistDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_DampingDDFactor_Uls_f32 target_TrqArblim_Per1_DampingDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_P	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_GMOSHOscillate_State_enum target_TrqArblim_Per1_GMOSHOscillate_State_enum	
target_Rte_inst_Ap_frqArblim.frqArblim_Per1_GwoShoscillate_state_enum target_frqArblim_Per1_GwoShoscillate_state_enum target_frqArblim_Per1_GwoShoscillate_state_enum target_frqArblim_Per1_GwoShoscillate_state_enum target_frqArblim_Per1_HwTorque_HwNm_f32	
target_Rte_inst_Ap_rtqArblim.TrqArblim.TrqArblim.Per1_nwTorque_nwNrii_is2 target_rtqArblim.Per1_nwTorque_nwNrii_is2 target_rtqArblim.Per1_lqTrqOv_HwNm_f32	
target_Rte_inst_Ap_rrqArblim.TrqArblim_Per1_tKACmd_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TerArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKAState_State_enum	
target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_TrqArblim_Per1_PosServEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value -4.21999979	
target_TrqArblim_Per1_ESCState_State_enum.value 3	
target_TrqArblim_Per1_ESCState_State_enum.value 3 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2	





Name	Input Value		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.61999989		
target_TrqArblim_Per1_LKAState_State_enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	13.1933002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.25999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0789999962	0.0790000036 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.00746727	-8.00747013 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.25999999	1.25999999 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.07453918	-7.07454014 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.0854996592	0.0854996964 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.5 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.061999999
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.42000008
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.42000008
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.5999999
k_APAEnableRate_pSec_f32	19.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.73500001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	1638
t_APADisableRateX_HwNm_u4p12[1]	2048
t_APADisableRateX_HwNm_u4p12[2]	2458
t_APADisableRateX_HwNm_u4p12[3]	2867
t_APADisableRateX_HwNm_u4p12[4]	3277
t_APADisableRateX_HwNm_u4p12[5]	3686
t_APADisableRateY_pSec_u7p9[0]	2048
t_APADisableRateY_pSec_u7p9[1]	2560
t_APADisableRateY_pSec_u7p9[2]	3072
t_APADisableRateY_pSec_u7p9[3]	3584
t_APADisableRateY_pSec_u7p9[4]	4096
t_APADisableRateY_pSec_u7p9[5]	4608
t_APASmoothX_Uls_u2p14[0]	655
t_APASmoothX_Uls_u2p14[1]	819
t_APASmoothX_Uls_u2p14[2]	983
t_APASmoothX_Uls_u2p14[3]	1147
t_APASmoothX_Uls_u2p14[4]	1311
t_APASmoothX_Uls_u2p14[5]	1475
t_APASmoothX_Uls_u2p14[6]	1638
t_APASmoothX_Uls_u2p14[7]	1802
t_APASmoothX_Uls_u2p14[8]	1966
t_APASmoothX_Uls_u2p14[9]	2130
t_APASmoothY_Uls_u2p14[0]	13435
t_APASmoothY_Uls_u2p14[1]	13599
t_APASmoothY_Uls_u2p14[2]	13763
t_APASmoothY_Uls_u2p14[3]	13926

2014-09-16, 12:30:22+0530



TrqArblim_Per1

		•	
Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	14090		
t_APASmoothY_Uls_u2p14[5]	14254		
t_APASmoothY_Uls_u2p14[6]	14418		
t_APASmoothY_Uls_u2p14[7]	14582		
t_APASmoothY_Uls_u2p14[8]	14746		
t_APASmoothY_Uls_u2p14[9]	14909		
t LKASlewX Kph u8p8[0]	2048		
t_LKASlewX_Kph_u8p8[1]	2560		
t LKASlewX Kph u8p8[2]	3072		
t_LKASlewX_Kph_u8p8[3]	3584		
t_LKASlewX_Kph_u8p8[4]	4096		
t_LKASlewX_Kph_u8p8[5]	4608		
t LKASlewY NmpSec u4p12[0]	1638		
t_LKASlewY_NmpSec_u4p12[1]	2048		
t_LKASlewY_NmpSec_u4p12[2]	2458		
t_LKASlewY_NmpSec_u4p12[3]	2867		
t_LKASlewY_NmpSec_u4p12[4]	3277		
t_LKASlewY_NmpSec_u4p12[5]	3686		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target TrqArblim Per1 AssistDDFactor UI	s f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_el		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target TrgArblim Per1 ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate State enum		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.01999998		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-4		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.42000008		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	16.9083004		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value			
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	1.61000001 6.6999981		
Name	Actual Value	Expected Value	Resu
		•	Resu
TrqArblim_APADisableRate_Uls_M_f32 TrqArblim ESC State Cnt M enum	0.0439999998	0.0439999998 ± 0000390625	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrpArblim_ResSquCCmd_MtrNm_M_f23	-7.38306427	-7.38305998 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.42000008	-5.42000008 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.59399986	-6.59399986 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.963745117	0.963744998 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.963745117	0.963744998 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target Traurnim Per1 In Ira()v HwNm f32 value			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call TrgΔrhlim Per1 CP1 ChecknointReached	1	Pte Call TrgArblim Per1 CP1 CheckpointReached	1	-

6.50349855

0.963745117

-3 ± 0.0000390625

-3 ± 0.0000390625

6.50349998 ± 0000390625

0.963744998 ± 0000390625

-3

target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value$



Test Step 3.6 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.075000003
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.80999994
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.21999979
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.099999
k_APAEnableRate_pSec_f32	24
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.26900005
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	2048
t_APADisableRateX_HwNm_u4p12[1]	2458
t_APADisableRateX_HwNm_u4p12[2]	2867
t_APADisableRateX_HwNm_u4p12[3]	3277
t_APADisableRateX_HwNm_u4p12[4]	3686
t_APADisableRateX_HwNm_u4p12[5]	4096
t_APADisableRateY_pSec_u7p9[0]	2560
t_APADisableRateY_pSec_u7p9[1]	3072
t_APADisableRateY_pSec_u7p9[2] t_APADisableRateY_pSec_u7p9[3]	3584 4096
t_APADisableRateY_pSec_u/p9[3] t APADisableRateY_pSec_u7p9[4]	4608
t_APADisableRateY_pSec_u7p9[4] t_APADisableRateY_pSec_u7p9[5]	5120
t_APASmoothX_Uls_u2p14[0]	819
t_APASmoothX_Uls_u2p14[1]	983
t_APASmoothX_Uls_u2p14[2]	1147
t_APASmoothX_UIs_u2p14[3]	1311
t_APASmoothX_Uls_u2p14[4]	1475
t_APASmoothX_Uls_u2p14[5]	1638
t APASmoothX Uls u2p14[6]	1802
t_APASmoothX_Uls_u2p14[7]	1966
t_APASmoothX_Uls_u2p14[8]	2130
t_APASmoothX_Uls_u2p14[9]	2294
t_APASmoothY_Uls_u2p14[0]	13599
t_APASmoothY_Uls_u2p14[1]	13763
t_APASmoothY_Uls_u2p14[2]	13926
t_APASmoothY_Uls_u2p14[3]	14090
t_APASmoothY_Uls_u2p14[4]	14254
t_APASmoothY_Uls_u2p14[5]	14418
t_APASmoothY_Uls_u2p14[6]	14582
t_APASmoothY_Uls_u2p14[7]	14746
t_APASmoothY_Uls_u2p14[8]	14909
t_APASmoothY_Uls_u2p14[9]	15073
t_LKASlewX_Kph_u8p8[0]	2560
t_LKASlewX_Kph_u8p8[1]	3072
t_LKASlewX_Kph_u8p8[2]	3584
t_LKASlewX_Kph_u8p8[3]	4096
t_LKASlewX_Kph_u8p8[4]	4608
t_LKASlewX_Kph_u8p8[5]	5120
t_LKASlewY_NmpSec_u4p12[0]	2048
t_LKASlewY_NmpSec_u4p12[1]	2458
t_LKASlewY_NmpSec_u4p12[2]	2867
t_LKASlewY_NmpSec_u4p12[3]	3277
t_LKASlewY_NmpSec_u4p12[4]	3686
t_LKASlewY_NmpSec_u4p12[5]	4096
target Rte Call Ap TrqArblim FaultInjection SCom FitInjection SignalPath Uls	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32 target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCInd_rtwNin1_i32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_frqArbiim_Peri_ESCGnd_nwivin_isz target_frqArbiim_Peri_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCISt_imited_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_IrqArblim_Per1_ESCIsLimited_Cnt_igc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESC101queDelivered_HwNIII_32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
0 T T 1 T 1 T T T T T T T T T T T T T T	





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3	2 target_TrqArblim_Per1_MinSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_t	732	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	lm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	lls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	m_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-2.58999991		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-9.10000038		
target_TrqArblim_Per1_LKAState_State_enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.96000004		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0549999997	0.0549999997 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.75513983	-6.75513983 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.21999979	-4.21999979 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.07299995	-6.07299995 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.954073787	0.954074025 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.954073787	0.954074025 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.1938086	-0.193809003 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.954073787	0.954074025 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0879999995
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.19999981
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0152000003
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.01999998
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.5999999
k_APAEnableRate_pSec_f32	28.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.802999973
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	2458
t_APADisableRateX_HwNm_u4p12[1]	2867
t_APADisableRateX_HwNm_u4p12[2]	3277
t_APADisableRateX_HwNm_u4p12[3]	3686
t_APADisableRateX_HwNm_u4p12[4]	4096
t_APADisableRateX_HwNm_u4p12[5]	4506
t_APADisableRateY_pSec_u7p9[0]	3072
t_APADisableRateY_pSec_u7p9[1]	3584
t_APADisableRateY_pSec_u7p9[2]	4096
t_APADisableRateY_pSec_u7p9[3]	4608
t_APADisableRateY_pSec_u7p9[4]	5120
t_APADisableRateY_pSec_u7p9[5]	5632
t_APASmoothX_Uls_u2p14[0]	983
t_APASmoothX_Uls_u2p14[1]	1147
t_APASmoothX_Uls_u2p14[2]	1311
t_APASmoothX_Uls_u2p14[3]	1475





Name	Input Value		
t_APASmoothX_Uls_u2p14[4]	1638		
t_APASmoothX_Uls_u2p14[5]	1802		
t_APASmoothX_Uls_u2p14[6]	1966		
t_APASmoothX_Uls_u2p14[7]	2130		
t_APASmoothX_Uls_u2p14[8]	2294		
t_APASmoothX_Uls_u2p14[9]	2458		
t_APASmoothY_Uls_u2p14[0]	13763		
t_APASmoothY_Uls_u2p14[1]	13926		
t_APASmoothY_Uls_u2p14[2]	14090		
t_APASmoothY_Uls_u2p14[3] t_APASmoothY_Uls_u2p14[4]	14254 14418		
t_APASmoothY_Uls_u2p14[5]	14582		
t_APASmoothY_Uls_u2p14[6]	14746		
t_APASmoothY_Uls_u2p14[7]	14909		
t_APASmoothY_Uls_u2p14[8]	15073		
t_APASmoothY_Uls_u2p14[9]	15237		
t_LKASlewX_Kph_u8p8[0]	3072		
t_LKASlewX_Kph_u8p8[1]	3584		
t_LKASlewX_Kph_u8p8[2]	4096		
t_LKASlewX_Kph_u8p8[3]	4608		
t_LKASlewX_Kph_u8p8[4]	5120		
t_LKASlewX_Kph_u8p8[5]	5632		
t_LKASlewY_NmpSec_u4p12[0] t_LKASlewY_NmpSec_u4p12[1]	2458 2867		
t_LKASlewY_NmpSec_u4p12[1]	3277		
t_LKASlewY_NmpSec_u4p12[3]	3686		
t_LKASlewY_NmpSec_u4p12[4]	4096		
t_LKASlewY_NmpSec_u4p12[5]	4506		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	: -6		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_t	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State enu		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-0.620000005		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target TrqArblim Per1 HwTorque HwNm f32.value	1 -1.17999995		
target_IrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.60999966		
target TrqArblim Per1 LKAState State enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	511		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.30999994		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.145000011	0.144999996 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.12369585	-6.12370014 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.30999994	2.30999994 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.59399986	-5.59399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.865885615	0.865885973 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_UIs_f32.value	0.865885615	0.865885973 ± 0000390625	V
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0 620000005 + 0000300635	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.620000005 -0.620000005	-0.620000005 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value target TrqArblim Per1 LKATorqueDelivered HwNm f32.value	0	-0.620000005 ± 0.0000390625 0 ± 0.0000390625	
target_TrqArblim_Per1_DrTrqOv_MtrNm_f32.value	7.40980434	7.40980005 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.865885615	0.865885973 ± 0000390625	
	1.1113000.0	1.1110000.010000000	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.8 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrgArblim_APADisableRate_Uls_M_f32	0.101000004
FrqArblim_ESC_State_Cnt_M_enum	1
FrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.59000015
FrgArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0174000002
FrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.82000005
TrgArblim PreLKATrgReg HwNm M f32	-5.099999
·	
<pre><_APAEnableRate_pSec_f32</pre>	33
<pre><_APAUseADmpScale_lgc</pre>	0
x_APAUseAsstScale_lgc	0
x_APAUseRetScale_lgc	0
C_ESCMax_HwNm_f32	0.337000012
c_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	2867
_APADisableRateX_HwNm_u4p12[1]	3277
_APADisableRateX_HwNm_u4p12[2]	3686
_APADisableRateX_HwNm_u4p12[3]	4096
_APADisableRateX_HwNm_u4p12[4]	4506
_APADisableRateX_HwNm_u4p12[5]	4915
_APADisableRateY_pSec_u7p9[0]	3584
_APADisableRateY_pSec_u7p9[1]	4096
_APADisableRateY_pSec_u7p9[2]	4608
_APADisableRateY_pSec_u7p9[3]	5120
_APADisableRateY_pSec_u7p9[4]	5632
_APADisableRateY_pSec_u7p9[5]	6144
_APASmoothX_Uls_u2p14[0]	1147
_APASmoothX_Uls_u2p14[1]	1311
_APASmoothX_UIs_u2p14[2]	1475
_APASmoothX_Uls_u2p14[3]	1638
_APASmoothX_Uls_u2p14[4]	1802
_APASmoothX_Uls_u2p14[5]	1966
_APASmoothX_Uls_u2p14[6]	2130
_APASmoothX_Uls_u2p14[7]	2294
_APASmoothX_Uls_u2p14[8]	2458
_APASmoothX_Uls_u2p14[9]	2621
_APASmoothY_Uls_u2p14[0]	13926
_APASmoothY_Uls_u2p14[1]	14090
_APASmoothY_Uls_u2p14[2]	14254
_APASmoothY_Uls_u2p14[3]	14418
_APASmoothY_Uls_u2p14[4]	14582
_APASmoothY_Uls_u2p14[5]	14746
_APASmoothY_Uls_u2p14[6]	14909
APASmoothY Uls u2p14[7]	15073
_APASmoothY_Uls_u2p14[8]	15237
_APASmoothY_Uls_u2p14[9]	15401
_KFASIII00(ITT_0IS_02p14[9] _LKASIewX_Kph_u8p8[0]	3584
	4096
_LKASlewX_Kph_u8p8[1]	
_LKASlewX_Kph_u8p8[2]	4608
_LKASlewX_Kph_u8p8[3]	5120
_LKASlewX_Kph_u8p8[4]	5632
_LKASlewX_Kph_u8p8[5]	6144
_LKASlewY_NmpSec_u4p12[0]	2867
_LKASlewY_NmpSec_u4p12[1]	3277
_LKASlewY_NmpSec_u4p12[2]	3686
_LKASlewY_NmpSec_u4p12[3]	4096
_LKASlewY_NmpSec_u4p12[4]	4506
_LKASlewY_NmpSec_u4p12[5]	4915
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	f5.30000019
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32





Name	Input Value		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target TrgArblim Per1 ESCCmd HwNm f3	2	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target TrgArblim Per1 ESCIsLimited Cnt Igc		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target TrgArblim Per1 ESCState State enum		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target TrqArblim Per1 ESCTorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target_TrqArblim_Per1_GMOSHOscillate_St		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm f	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target_TrqArblim_Per1_LKATorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleSp		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target TrqArblim Per1 OpTrqOv MtrNm f3.	- · -	
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor UIs f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target TrqArblim Per1 TrqOscCmd MtrNm		
target TrqArblim Per1 ESCCmd HwNm f32.value	0.579999983	_102	
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target TrqArblim Per1 GMOSHOscillate State enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.230000004		
target TrgArblim Per1 LKACmd HwNm f32.value	-8.11999989		
target TrqArblim Per1 LKAState State enum.value	1		
target TrgArblim Per1 MinSecureVehicleSpeed Kph f32.value	256.339996		
target TrqArblim Per1 PosServEnable Cnt Igc.value	1		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	2.66000009		
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	7.30000019		
Name	Actual Value	Expected Value	Resu
TrgArblim APADisableRate Uls M f32	0.166999996	0.166999996 ± 0000390625	
TrgArblim ESC State Cnt M enum	2	2	
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	-5.48873234	-5.48872995 ± 0.03125	٠,
TrqArblim PosSrvoCmd MtrNm M f32	2.66000009	2.66000009 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.09399986	-5.09399986 ± 0.03125	٠,
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target TrgArblim Per1 DampingDDFactor Uls f32.value	1	1 ± 0000390625	٠,
target TrgArblim Per1 ESCIsLimited Cnt Igc.value	1	1	
target TrgArblim Per1 ESCTorqueDelivered HwNm f32.value	0.337000012	0.337000012 ± 0000390625	٠,
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.337000012	0.337000012 ± 0.0000390625	•
target TrgArblim Per1 LKATorqueDelivered HwNm f32.value	0	0 ± 0.0000390625	٠,
target TrgArblim Per1 OpTrgOv MtrNm f32.value	0.417567909	0.417567998 ± 0000390625	•
1 1 1 1 1 D 1 D 1 D 5 D 1 1 1 1 1 1 1 1			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	~

1 ± 0000390625

Test Step 3.9 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.114
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.98000002
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0196000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.620000005
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.5999999
k_APAEnableRate_pSec_f32	37.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	3277
t_APADisableRateX_HwNm_u4p12[1]	3686
t_APADisableRateX_HwNm_u4p12[2]	4096
t_APADisableRateX_HwNm_u4p12[3]	4506
t_APADisableRateX_HwNm_u4p12[4]	4915
t_APADisableRateX_HwNm_u4p12[5]	5325





Name	Input Value		
t_APADisableRateY_pSec_u7p9[0]	4096		
t_APADisableRateY_pSec_u7p9[1]	4608		
t_APADisableRateY_pSec_u7p9[2]	5120		
t_APADisableRateY_pSec_u7p9[3]	5632		
t_APADisableRateY_pSec_u7p9[4]	6144 6656		
t_APADisableRateY_pSec_u7p9[5] t_APASmoothX_Uls_u2p14[0]	1311		
t_APASmoothX_UIs_u2p14[1]	1475		
t_APASmoothX_Uls_u2p14[2]	1638		
t_APASmoothX_Uls_u2p14[3]	1802		
t_APASmoothX_Uls_u2p14[4]	1966		
t_APASmoothX_Uls_u2p14[5]	2130		
t_APASmoothX_Uls_u2p14[6]	2294		
t_APASmoothX_Uls_u2p14[7]	2458		
t_APASmoothX_UIs_u2p14[8]	2621		
t_APASmoothX_Uls_u2p14[9] t_APASmoothY_Uls_u2p14[0]	2785 14090		
t_APASmoothY_Uls_u2p14[1]	14254		
t APASmoothY Uls u2p14[2]	14418		
t_APASmoothY_Uls_u2p14[3]	14582		
t_APASmoothY_Uls_u2p14[4]	14746		
t_APASmoothY_Uls_u2p14[5]	14909		
t_APASmoothY_Uls_u2p14[6]	15073		
t_APASmoothY_Uls_u2p14[7]	15237		
t_APASmoothY_Uls_u2p14[8]	15401		
t_APASmoothY_UIs_u2p14[9]	15565		
t_LKASlewX_Kph_u8p8[0] t_LKASlewX_Kph_u8p8[1]	4096 4608		
t_LKASlewX_Kph_u8p8[2]	5120		
t_LKASlewX_Kph_u8p8[3]	5632		
t_LKASlewX_Kph_u8p8[4]	6144		
t_LKASlewX_Kph_u8p8[5]	6656		
t_LKASlewY_NmpSec_u4p12[0]	3277		
t_LKASlewY_NmpSec_u4p12[1]	3686		
t_LKASlewY_NmpSec_u4p12[2]	4096		
t_LKASlewY_NmpSec_u4p12[3]	4506		
t_LKASlewY_NmpSec_u4p12[4]	4915		
t_LKASlewY_NmpSec_u4p12[5]	5325		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f.		, f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Ulstarget_TrqArblim_Per1_DampingDDFactor_	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target TrqArblim Per1 ESCIsLimited Cnt		
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3.		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_t32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_LKATorqueDelivere target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target TrqArblim Per1 OpTrqOv MtrNm f		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	1.77999997		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.6399999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.63000011 2		
target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	19.6399994		
target_TrqArblim_Per1_NiinSecureVerlicieSpeed_Kpr1_is2.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.00999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.5		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.0879999995	0.0879999995 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	3	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.85024786	-4.85024977 ± 0.03125	-
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.620000005	-0.620000005 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.59399986	-4.59399986 ± 0.03125	





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.923623025	0.923623025 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.923623025	0.923623025 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	7.45264626	7.45265007 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.923623025	0.923623025 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Fest Step 3.10 (Repeat Count = 1)	Input Value
Rte Call Ap TrqArblim FaultInjection SCom FltInjection(SignalPath Uls f32)	target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath UIs f32
Rte_Inst_Ap_TrqArblim FrqArblim APADisableRate Uls M f32	target_Rte_Inst_Ap_TrqArblim 0.127000004
	3
TrgArblim_ESC_State_Cnt_M_enum	-4.36999989
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	
rqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0218000002
rqArblim_PosSrvoCmd_MtrNm_M_f32	0.579999983
rqArblim_PreLKATrqReq_HwNm_M_f32	-4.0999999
_APAEnableRate_pSec_f32	42
_APAUseADmpScale_lgc	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
_ESCMax_HwNm_f32	4.8499999
x_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	3686
_APADisableRateX_HwNm_u4p12[1]	4096
_APADisableRateX_HwNm_u4p12[2]	4506
_APADisableRateX_HwNm_u4p12[3]	4915
_APADisableRateX_HwNm_u4p12[4]	5325
_APADisableRateX_HwNm_u4p12[5]	5734
_APADisableRateY_pSec_u7p9[0]	4608
_APADisableRateY_pSec_u7p9[1]	5120
APADisableRateY_pSec_u7p9[2]	5632
_APADisableRateY_pSec_u7p9[3]	6144
_APADisableRateY_pSec_u7p9[4]	6656
_APADisableRateY_pSec_u7p9[5]	7168
_APASmoothX_Uls_u2p14[0]	1475
_APASmoothX_Uls_u2p14[1]	1638
_APASmoothX_Uls_u2p14[2]	1802
_APASmoothX_Uls_u2p14[3]	1966
_APASmoothX_Uls_u2p14[4]	2130
_APASmoothX_UIs_u2p14[5]	2294
_APASmoothX_Uls_u2p14[6]	2458
_APASmoothX_UIs_u2p14[7]	2621
_APASmoothX_Uls_u2p14[8]	2785
_APASmoothX_Uls_u2p14[9]	2949
_APASmoothY_Uls_u2p14[0]	14254
APASmoothY Uls u2p14[1]	14418
APASmoothY_Uls_u2p14[1]	14582
_APASmoothY_Uls_u2p14[3]	14746
_APASmoothY_Uls_u2p14[4]	14909
APASmoothY_Uls_u2p14[4]	15073
APASmoothY_Uls_u2p14[6]	15237
	15401
APASmoothY_Uls_u2p14[7] APASmoothY_Uls_u2p14[8]	
: ::	15565
_APASmoothY_Uls_u2p14[9]	15729
_LKASlewX_Kph_u8p8[0]	4608
_LKASlewX_Kph_u8p8[1]	5120
_LKASlewX_Kph_u8p8[2]	5632
_LKASlewX_Kph_u8p8[3] _LKASlewX_Kph_u8p8[4]	6144 6656

2014-09-16, 12:30:22+0530



TraArblim Per1

TrqArblim_Per1			MACHEAL
Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	7168		
t_LKASlewY_NmpSec_u4p12[0]	3686		
t_LKASlewY_NmpSec_u4p12[1]	4096		
t_LKASlewY_NmpSec_u4p12[2]	4506		
t_LKASlewY_NmpSec_u4p12[3]	4915		
t_LKASlewY_NmpSec_u4p12[4]	5325		
t_LKASlewY_NmpSec_u4p12[5]	5734		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	r3.9000001		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleS	peed Kph f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor Uls f32	target TrqArblim Per1 ReturnDDFactor UI		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	 n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	2.98000002		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.04999995		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.13999987		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	24		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.3599999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.6999981		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.21100001	0.210999995 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.20824385	-4.20824003 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.3599999	3.3599999 ± 0.00024414062	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.07579994	-4.07579994 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	
	1	1	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0.680617154

1

0.680616975 ± 0000390625

1 ± 0000390625

Test Step 3.11 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.140000001	
TrqArblim_ESC_State_Cnt_M_enum	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.75999999	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0240000002	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.77999997	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.5999999	
k_APAEnableRate_pSec_f32	46.5	
k_APAUseADmpScale_lgc	1	

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value





1 1 3.30999994 1 4096 4506 4915
3.30999994 1 4096 4506
1 4096 4506
4096 4506
4506
5325
5734
6144
5120
5632
6144 6656
7168
7680
1638
1802
1966
2130
2294
2458
2621 2785
2949
3113
14418
14582
14746
14909
15073
15237
15401 15565
15729
15892
5120
5632
6144
6656
7168
7680 4096
4506
4915
5325
5734
6144
f3.20000005
target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_TrqArblim_Per1_DampingDDFactor_Uls_f32 target TrqArblim Per1 ESCCmd HwNm f32
target_IrqArblim_Per1_ESCCmd_HWNm_132 target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_TrqArblim_Per1_ESCState_State_enum
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_TrqArblim_Per1_HwTorque_HwNm_f32
target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_TrqArblim_Per1_LKACmd_HwNm_f32
target_TrqArblim_Per1_LKAState_State_enum
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
4.17999983
0
2
4.46000004 -6.6500001



Name	Input Value		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	32.5400009		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.23300001	0.232999995 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.56272006	-3.56272006 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.59399986	-3.5940001 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.77399683	0.773997009 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.77399683	0.773997009 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-1.98882806	-1.98882997 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.77399683	0.773997009 ± 0000390625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.12 (Repeat Count = 1)	· ·
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.152999997
TrqArblim ESC State Cnt M enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.1500001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.98000002
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.0999999
k_APAEnableRate_pSec_f32	51
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.20000005
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	4506
t_APADisableRateX_HwNm_u4p12[1]	4915
t_APADisableRateX_HwNm_u4p12[2]	5325
t_APADisableRateX_HwNm_u4p12[3]	5734
t_APADisableRateX_HwNm_u4p12[4]	6144
t_APADisableRateX_HwNm_u4p12[5]	6554
t_APADisableRateY_pSec_u7p9[0]	5632
t_APADisableRateY_pSec_u7p9[1]	6144
t_APADisableRateY_pSec_u7p9[2]	6656
t_APADisableRateY_pSec_u7p9[3]	7168
t_APADisableRateY_pSec_u7p9[4]	7680
t_APADisableRateY_pSec_u7p9[5]	8192
t_APASmoothX_Uls_u2p14[0]	1802
t_APASmoothX_Uls_u2p14[1]	1966
t_APASmoothX_Uls_u2p14[2]	2130
t_APASmoothX_Uls_u2p14[3]	2294
t_APASmoothX_Uls_u2p14[4]	2458
t_APASmoothX_Uls_u2p14[5]	2621
t_APASmoothX_Uls_u2p14[6]	2785
t_APASmoothX_Uls_u2p14[7]	2949
t_APASmoothX_Uls_u2p14[8]	3113
t_APASmoothX_Uls_u2p14[9]	3277
t_APASmoothY_Uls_u2p14[0]	14582
t_APASmoothY_Uls_u2p14[1]	14746
t_APASmoothY_Uls_u2p14[2]	14909
t_APASmoothY_Uls_u2p14[3]	15073
t_APASmoothY_Uls_u2p14[4]	15237

2014-09-16, 12:30:22+0530



TrqArblim_Per1

			1 10-10
Name	Input Value		
t_APASmoothY_Uls_u2p14[5]	15401		
t_APASmoothY_Uls_u2p14[6]	15565		
t_APASmoothY_Uls_u2p14[7]	15729		
t_APASmoothY_Uls_u2p14[8]	15892		
t_APASmoothY_Uls_u2p14[9]	16056		
t_LKASlewX_Kph_u8p8[0]	5632		
t_LKASlewX_Kph_u8p8[1]	6144		
t_LKASlewX_Kph_u8p8[2]	6656		
t_LKASlewX_Kph_u8p8[3]	7168		
t LKASlewX Kph u8p8[4]	7680		
t_LKASlewX_Kph_u8p8[5]	8192		
t_LKASlewY_NmpSec_u4p12[0]	4506		
t_LKASlewY_NmpSec_u4p12[1]	4915		
t_LKASlewY_NmpSec_u4p12[2]	5325		
t LKASlewY NmpSec u4p12[3]	5734		
t_LKASlewY_NmpSec_u4p12[4]	6144		
t_LKASlewY_NmpSec_u4p12[5]	6554		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target TrqArblim Per1 DampingDDFactor	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target TrqArblim Per1 ESCIsLimited Cnt		
		•	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_State_er target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_GMOSHOscillate_S target_TrqArblim_Per1_HwTorque_HwNm	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3.		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_U		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.86999989		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.15999985		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	38.2933006		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.10000038		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.254999995	0.254999995 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	1	1	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.91367602	-2.91368008 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.00024414062	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.20000005	1.20000005 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.20000005	1.20000005 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	
		0.000000000000000000000000000000000	I .
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~



Test Step 3.13 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.165999994
FrqArblim_ESC_State_Cnt_M_enum FrqArblim HwTorqueSV HwNm M Str.SV UIs f32	-2.53999996
	0.0284000002
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32 FrqArblim PosSrvoCmd MtrNm M f32	4.17999983
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.5999999
«_APAEnableRate_pSec_f32	55.5
APAUseADmpScale Igc	0
<pre><pre>APAUseAsstScale_lgc</pre></pre>	0
APAUseRetScale lgc	0
C_ESCMax_HwNm_f32	1.73500001
 LKAUseSlewCal lgc	0
_APADisableRateX_HwNm_u4p12[0]	4915
APADisableRateX HwNm u4p12[1]	5325
_APADisableRateX_HwNm_u4p12[2]	5734
_APADisableRateX_HwNm_u4p12[3]	6144
_APADisableRateX_HwNm_u4p12[4]	6554
_APADisableRateX_HwNm_u4p12[5]	6963
_APADisableRateY_pSec_u7p9[0]	6144
_APADisableRateY_pSec_u7p9[1]	6656
_APADisableRateY_pSec_u7p9[2]	7168
_APADisableRateY_pSec_u7p9[3]	7680
_APADisableRateY_pSec_u7p9[4]	8192
_APADisableRateY_pSec_u7p9[5]	8704
_APASmoothX_Uls_u2p14[0]	1966
_APASmoothX_Uls_u2p14[1]	2130
_APASmoothX_Uls_u2p14[2]	2294
_APASmoothX_Uls_u2p14[3]	2458
_APASmoothX_Uls_u2p14[4]	2621
_APASmoothX_Uls_u2p14[5]	2785
_APASmoothX_Uls_u2p14[6]	2949
_APASmoothX_Uls_u2p14[7]	3113
_APASmoothX_Uls_u2p14[8]	3277
_APASmoothX_Uls_u2p14[9]	3441
_APASmoothY_Uls_u2p14[0]	14746
_APASmoothY_Uls_u2p14[1]	14909
_APASmoothY_Uls_u2p14[2]	15073
_APASmoothY_Uls_u2p14[3]	15237
_APASmoothY_Uls_u2p14[4]	15401
_APASmoothY_Uls_u2p14[5]	15565
_APASmoothY_Uls_u2p14[6]	15729
_APASmoothY_Uls_u2p14[7]	15892
_APASmoothY_UIs_u2p14[8]	16056
_APASmoothY_Uls_u2p14[9]	16220
_LKASlewX_Kph_u8p8[0]	6144
_LKASlewX_Kph_u8p8[1]	6656
_LKASlewX_Kph_u8p8[2]	7168
_LKASlewX_Kph_u8p8[3]	7680 8192
_LKASlewX_Kph_u8p8[4] LKASlewX Kph u8p8[5]	8192 8704
_LKASiewX_Kpn_uop8[5] _LKASiewY_NmpSec_u4p12[0]	4915
_LKASlewY_NmpSec_u4p12[1]	5325
_LKASlewY_NmpSec_u4p12[1] _LKASlewY_NmpSec_u4p12[2]	5734
_LKASlewY_NmpSec_u4p12[3]	6144
_LKASlewY_NmpSec_u4p12[4]	6554
LKASlewY NmpSec u4p12[5]	6963
arget Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath UIs	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target TrqArblim Per1 DampingDDFactor Uls f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target TrqArblim Per1 ESCIsLimited Cnt Igc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
arget_rtte_inst_Ap_rrqArbiint.rrqArbiint_rerr_EttAotate_otate_endin	





Name	Input Value				
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicles	arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	get_TrqArblim_Per1_OpTrqOv_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_C	rget_TrqArblim_Per1_PosServEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	lm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	Jls_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	m_f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992				
target_TrqArblim_Per1_ESCState_State_enum.value	2				
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1				
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.28000021				
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008				
target_TrqArblim_Per1_LKAState_State_enum.value	2				
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	44.7433014				
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1				
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0				
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.30000019				
Name	Actual Value	Expected Value	Result		
TrqArblim_APADisableRate_Uls_M_f32	0.27700001	0.27700001 ± 0000390625	~		
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.26111197	-2.26111007 ± 0.03125	~		
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0	0 ± 0.000244140625	✓		
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.57821965	-2.57821989 ± 0.03125	~		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~		
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓		
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.73500001	1.73500001 ± 0000390625	~		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.73500001	1.73500001 ± 0.0000390625	~		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~		
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.30000019	8.30000019 ± 0000390625	~		
target TrqArblim Per1 ReturnDDFactor Uls f32.value	1	1 ± 0000390625	~		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.14 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.179000005
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.92999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.38000011
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.0999999
k_APAEnableRate_pSec_f32	60
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.26900005
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	5325
t_APADisableRateX_HwNm_u4p12[1]	5734
t_APADisableRateX_HwNm_u4p12[2]	6144
t_APADisableRateX_HwNm_u4p12[3]	6554
t_APADisableRateX_HwNm_u4p12[4]	6963
t_APADisableRateX_HwNm_u4p12[5]	7373
t_APADisableRateY_pSec_u7p9[0]	6656
t_APADisableRateY_pSec_u7p9[1]	7168
t_APADisableRateY_pSec_u7p9[2]	7680
t_APADisableRateY_pSec_u7p9[3]	8192
t_APADisableRateY_pSec_u7p9[4]	8704
t_APADisableRateY_pSec_u7p9[5]	9216
t_APASmoothX_Uls_u2p14[0]	2130
t_APASmoothX_Uls_u2p14[1]	2294
t_APASmoothX_Uls_u2p14[2]	2458
t_APASmoothX_Uls_u2p14[3]	2621

2014-09-16, 12:30:22+0530



Name	Immut Value		
Name	Input Value		
t_APASmoothX_Uls_u2p14[4]	2785		
t_APASmoothX_Uls_u2p14[5]	2949		
t_APASmoothX_Uls_u2p14[6]	3113		
t_APASmoothX_Uls_u2p14[7]	3277		
t_APASmoothX_Uls_u2p14[8]	3441		
t_APASmoothX_Uls_u2p14[9]	3604		
t_APASmoothY_Uls_u2p14[0]	14909		
t_APASmoothY_Uls_u2p14[1]	15073		
t_APASmoothY_Uls_u2p14[2]	15237		
t_APASmoothY_Uls_u2p14[3]	15401		
t_APASmoothY_Uls_u2p14[4]	15565		
t_APASmoothY_Uls_u2p14[5]	15729		
t_APASmoothY_Uls_u2p14[6]	15892		
t_APASmoothY_Uls_u2p14[7]	16056		
t_APASmoothY_Uls_u2p14[8]	16220		
t_APASmoothY_Uls_u2p14[9]	16384		
t_LKASlewX_Kph_u8p8[0]	6656		
t_LKASlewX_Kph_u8p8[1]	7168		
t_LKASlewX_Kph_u8p8[2]	7680		
t_LKASlewX_Kph_u8p8[3]	8192		
t_LKASlewX_Kph_u8p8[4]	8704		
t_LKASlewX_Kph_u8p8[5]	9216		
t_LKASlewY_NmpSec_u4p12[0]	5325		
t LKASlewY NmpSec u4p12[0]	5734		
	6144		
t_LKASlewY_NmpSec_u4p12[2]	6554		
t_LKASlewY_NmpSec_u4p12[3]			
t_LKASlewY_NmpSec_u4p12[4]	6963		
t_LKASlewY_NmpSec_u4p12[5]	7373		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	I_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target TrqArblim Per1 ReturnDDFactor Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021		
target TrqArblim Per1 ESCState State enum.value	3		
target TrqArblim Per1 GMOSHOscillate State enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.68999958		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.17999983		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_KAState_State_endin.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	51.1932983		
	1		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.4000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.5	1=	
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.299000025	0.298999995 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.60502791	-1.60502994 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.4000001	4.4000001 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.09399986	-2.0940001 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-2.09399986	-2.0940001 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-2.09399986	-2.0940001 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.31560016	1.31560004 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	
	1		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

est Step 3.15 (Repeat Count = 1)	Input Value
te_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32) tte_Inst_Ap_TrqArblim	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.192000002
rqArblim_ESC_State_Cnt_M_enum	3
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.32000005
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0328000002
rqArblim_PosSrvoCmd_MtrNm_M_f32	6.57999992
rqArblim_PreLKATrqReq_HwNm_M_f32	-1.60000002
_APAEnableRate_pSec_f32	64.5
_APAUseADmpScale_lgc	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
_ESCMax_HwNm_f32	0.802999973
_LKAUseSlewCal_lgc	0
APADisableRateX_HwNm_u4p12[0]	5734
APADisableRateX_HwNm_u4p12[1]	6144
APADisableRateX_HwNm_u4p12[2]	6554
APADisableRateX_HwNm_u4p12[3]	6963
APADisableRateX HwNm u4p12[4]	7373
APADisableRateX_HwNm_u4p12[5]	7782
	7168
APADisableRateY_pSec_u7p9[0]	7680
_APADisableRateY_pSec_u7p9[1]	
APADisableRateY_pSec_u7p9[2]	8192
_APADisableRateY_pSec_u7p9[3]	8704
_APADisableRateY_pSec_u7p9[4]	9216
_APADisableRateY_pSec_u7p9[5]	9728
_APASmoothX_Uls_u2p14[0]	2294
_APASmoothX_Uls_u2p14[1]	2458
_APASmoothX_Uls_u2p14[2]	2621
_APASmoothX_Uls_u2p14[3]	2785
_APASmoothX_Uls_u2p14[4]	2949
_APASmoothX_Uls_u2p14[5]	3113
APASmoothX_Uls_u2p14[6]	3277
APASmoothX_Uls_u2p14[7]	3441
APASmoothX_Uls_u2p14[8]	3604
APASmoothX_Uls_u2p14[9]	3768
APASmoothY_Uls_u2p14[0]	6226
APASmoothY Uls u2p14[1]	6390
APASmoothY_Uls_u2p14[2]	6554
	6717
APAS moothY_Uls_u2p14[3]	
APAS monthy_Uls_u2p14[4]	6881
APASmoothY_Uls_u2p14[5]	7045
APASmoothY_Uls_u2p14[6]	7209
_APASmoothY_Uls_u2p14[7]	7373
_APASmoothY_Uls_u2p14[8]	7537
_APASmoothY_Uls_u2p14[9]	7700
LKASlewX_Kph_u8p8[0]	7168
_LKASlewX_Kph_u8p8[1]	7680
_LKASlewX_Kph_u8p8[2]	8192
_LKASlewX_Kph_u8p8[3]	8704
LKASlewX_Kph_u8p8[4]	9216
LKASlewX_Kph_u8p8[5]	9728
LKASlewY_NmpSec_u4p12[0]	5734
LKASlewY_NmpSec_u4p12[1]	6144
LKASlewY_NmpSec_u4p12[2]	6554
	6963
_LKASlewY_NmpSec_u4p12[3] _LKASlewY_NmpSec_u4p12[4]	
	7373
_trasiew1_nmpsec_u4p12[4] _trasiewY_nmpsec_u4p12[5]	7782





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954			
target_TrqArblim_Per1_ESCState_State_enum.value	4			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.69000006			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	57.6432991			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-4.4000001			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.6999981			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.32100001	0.32100001 ± 0000390625	•	
TrqArblim_ESC_State_Cnt_M_enum	4	4	•	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.981504083	-0.981504023 ± 0.03125	•	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.4000001	-4.4000001 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.57972467	-1.57972002 ± 0.03125	•	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.57972467	-1.57972467 ± 0.0000390625	•	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-1.57972467	-1.57972467 ± 0.0000390625	•	
torget Traduction Dord OnTraduct Marking 622 value	0.00004000	0.03634006 + 0000300635	-4	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

8.03621292

8.03621006 ± 0000390625

1 ± 0000390625

Test Step 3.16 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.204999998
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.709999979
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0350000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.78000021
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002
k_APAEnableRate_pSec_f32	69
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.337000012
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	6144
t_APADisableRateX_HwNm_u4p12[1]	6554
t_APADisableRateX_HwNm_u4p12[2]	6963
t_APADisableRateX_HwNm_u4p12[3]	7373
t_APADisableRateX_HwNm_u4p12[4]	7782

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	8192		
t_APADisableRateY_pSec_u7p9[0]	7680		
t_APADisableRateY_pSec_u7p9[1]	8192		
t_APADisableRateY_pSec_u7p9[2]	8704		
t_APADisableRateY_pSec_u7p9[3]	9216		
t APADisableRateY pSec u7p9[4]	9728		
t_APADisableRateY_pSec_u7p9[5]	10240		
t_APASmoothX_Uls_u2p14[0]	2458		
t_APASmoothX_Uls_u2p14[1]	2621		
t_APASmoothX_Uls_u2p14[2]	2785		
t_APASmoothX_Uls_u2p14[3]	2949		
t_APASmoothX_Uls_u2p14[4]	3113		
t_APASmoothX_Uls_u2p14[5]	3277		
t_APASmoothX_Uls_u2p14[6]	3441		
t_APASmoothX_Uls_u2p14[7]	3604		
t_APASmoothX_Uls_u2p14[8]	3768		
t_APASmoothX_Uls_u2p14[9]	3932		
t_APASmoothY_Uls_u2p14[0]	6390		
t_APASmoothY_Uls_u2p14[1]	6554		
t_APASmoothY_Uls_u2p14[2]	6717		
t_APASmoothY_Uls_u2p14[3]	6881		
t_APASmoothY_Uls_u2p14[4]	7045		
t_APASmoothY_Uls_u2p14[5]	7209		
t_APASmoothY_Uls_u2p14[6]	7373		
t APASmoothY Uls u2p14[7]	7537		
t_APASmoothY_Uls_u2p14[8]	7700		
t_APASmoothY_Uls_u2p14[9]	7864		
t_LKASlewX_Kph_u8p8[0]	7680		
t_LKASlewX_Kph_u8p8[1]	8192		
t_LKASlewX_Kph_u8p8[2]	8704		
t_LKASlewX_Kph_u8p8[3]	9216		
	9728		
t_LKASlewX_Kph_u8p8[4]			
t_LKASlewX_Kph_u8p8[5]	10240		
t_LKASlewY_NmpSec_u4p12[0]	6144		
t_LKASlewY_NmpSec_u4p12[1]	6554		
t_LKASlewY_NmpSec_u4p12[2]	6963		
t_LKASlewY_NmpSec_u4p12[3]	7373		
t_LKASlewY_NmpSec_u4p12[4]	7782		
t_LKASlewY_NmpSec_u4p12[5]	8192		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	0.300000012		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_I	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_le	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_inst_Ap_rrqArblim.rrqArblim_Peri_PossivoCritd_withtin_isz target_Rte_inst_Ap_rrqArblim.rrqArblim_Peri_PossivoCritd_withtin_isz	target TrqArblim Per1 ReturnDDFactor Uls		
		_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_104	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.1800003		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.19999981		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	64.0932999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-4.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.60000038		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.174999997	0.174999997 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.03515005	-1.03515005 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.78000021	7.78000021 ± 0.000244140625	-
1,			

2014-09-16, 12:30:22+0530



Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.09399998	-1.09399998 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.564992547	0.564993024 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.17 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.217999995
TrqArblim_ESC_State_Cnt_M_enum	0
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	-0.100000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0372000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.97999954
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.600000024
k_APAEnableRate_pSec_f32	73.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.25
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	6554
t_APADisableRateX_HwNm_u4p12[1]	6963
t APADisableRateX HwNm u4p12[2]	7373
t_APADisableRateX_HwNm_u4p12[3]	7782
t_APADisableRateX_HwNm_u4p12[4]	8192
t_APADisableRateX_HwNm_u4p12[5]	8602
t_APADisableRateY_pSec_u7p9[0]	8192
t_APADisableRateY_pSec_u7p9[1]	8704
t_APADisableRateY_pSec_u7p9[2]	9216
t_APADisableRateY_pSec_u7p9[3]	9728
t_APADisableRateY_pSec_u7p9[4]	10240
t_APADisableRateY_pSec_u7p9[5]	10752
t APASmoothX Uls u2p14[0]	2621
t APASmoothX Uls u2p14[1]	2785
t_APASmoothX_Uls_u2p14[2]	2949
t_APASmoothX_UIs_u2p14[3]	3113
t_APASmoothX_Uls_u2p14[4]	3277
t_APASmoothX_UIs_u2p14[5]	3441
t APASmoothX Uls u2p14[6]	3604
t_APASmoothX_UIs_u2p14[7]	3768
t_APASmoothX_UIs_u2p14[8]	3932
t_APASmoothX_Uls_u2p14[9]	4096
t_APASmoothY_Uls_u2p14[0]	6554
t_APASmoothY_Uls_u2p14[0]	6717
t_APASmoothY_Uls_u2p14[1]	6881
t_APASmoothY_Uls_u2p14[2]	7045
t_APASmoothY_Uls_u2p14[4]	7209
t APASmoothY Uls u2p14[5]	7373
t_APASmoothY_Uls_u2p14[6]	7537
t_APASmoothY_Uls_u2p14[7]	7700
t_APASmoothY_Uls_u2p14[8]	7864
t_APASmoothY_Uls_u2p14[9]	8028
t_LKASlewX_Kph_u8p8[0]	8192
t_LKASlewX_Kph_u8p8[1]	8704
t LKASlewX Kph u8p8[2]	9216
t_LKASlewX_Kph_u8p8[3]	9728
	V-20

TrqArblim_Per1

2014-09-16, 12:30:22+0530



Name	Input Value			
t_LKASlewX_Kph_u8p8[4]	10240			
t_LKASlewX_Kph_u8p8[5]	10752			
t_LKASlewY_NmpSec_u4p12[0]	6554			
t LKASlewY NmpSec u4p12[1]	6963			
t LKASlewY NmpSec u4p12[2]	7373			
t LKASlewY NmpSec u4p12[3]	7782			
t LKASlewY NmpSec u4p12[4]	8192			
t_LKASlewY_NmpSec_u4p12[5]	8602			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	1			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_UI	s_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target TrqArblim Per1 ESCIsLimited Cnt			
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State e	num		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target TrqArblim Per1 ESCTorqueDelivere			
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate S			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_er			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere			
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleS			
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target TrqArblim Per1 OpTrqOv MtrNm f			
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cr			
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosSrvoCmd MtrNm f32		target_TrqArblim_er1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U			
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10	_ `		
target_TrqArblim_Per1_ESCState_State_enum.value	1			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	10			
target TrqArblim Per1 LKACmd HwNm f32.value	-3.71000004			
target TrqArblim Per1 LKAState State enum.value	0			
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	70.5432968			
target TrqArblim Per1 PosServEnable Cnt Igc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-4.0999999			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.10000038			
Name	Actual Value	Expected Value	Resul	
TrgArblim APADisableRate UIs M f32	0.36500001	0.36500001 ± 0000390625	IXESU	
TrgArblim ESC State Cnt M enum	1	1		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.27572003	0.27572 ± 0.03125		
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.0999999	-4.0999999 ± 0.000244140625		
TrgArblim PreLKATrgReg HwNm M f32	-0.594000041	-0.593999992 ± 0.000244140023		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.821153581	0.821153998 ± 0000390625		
target_TrqArblim_Per1_AssistDbFactor_Uls_f32.value	0.821153581	0.821153998 ± 0000390625		
target_irqArblim_Peri_DampingDDFactor_OIs_t32.value target_TrqArblim_Peri_DampingDDFactor_OIs_t32.value	1	0.821153998 ± 0000390625		
target_rrqArblim_Per1_ESCIsLimited_Cnt_igc.value target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	-1.25	-1.25 ± 0000390625		
	-1.25	-1.25 ± 0.000390625		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0	0 ± 0.0000390625		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value		-0.733269989 ± 0000390625		
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.733270347	-U.133209909 ± UUUU39U025		

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0.821153581

0.821153998 ± 0000390625

Test Step 3.18 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.231000006
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.50999999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0394000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.1800003
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.100000001

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAEnableRate_pSec_f32	78
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	6963
t_APADisableRateX_HwNm_u4p12[1]	7373 7782
t_APADisableRateX_HwNm_u4p12[2] t_APADisableRateX_HwNm_u4p12[3]	8192
t_APADisableRateX_HwNm_u4p12[4]	8602
t APADisableRateX HwNm u4p12[5]	9011
t_APADisableRateY_pSec_u7p9[0]	8704
t_APADisableRateY_pSec_u7p9[1]	9216
t_APADisableRateY_pSec_u7p9[2]	9728
t_APADisableRateY_pSec_u7p9[3]	10240
t_APADisableRateY_pSec_u7p9[4]	10752
t_APADisableRateY_pSec_u7p9[5]	11264
t_APASmoothX_UIs_u2p14[0]	2785 2949
t_APASmoothX_Uls_u2p14[1] t_APASmoothX_Uls_u2p14[2]	3113
t APASmoothX Uls u2p14[3]	3277
t_APASmoothX_Uls_u2p14[4]	3441
t_APASmoothX_Uls_u2p14[5]	3604
t_APASmoothX_UIs_u2p14[6]	3768
t_APASmoothX_Uls_u2p14[7]	3932
t_APASmoothX_Uls_u2p14[8]	4096
t_APASmoothX_Uls_u2p14[9]	4260
t_APASmoothY_Uls_u2p14[0]	6717
t_APASmoothY_Uls_u2p14[1]	6881
t_APASmoothY_Uls_u2p14[2]	7045
t_APASmoothY_Uls_u2p14[3] t_APASmoothY_Uls_u2p14[4]	7209 7373
t_APASmoothY_Uls_u2p14[5]	7537
t_APASmoothY_Uls_u2p14[6]	7700
t_APASmoothY_Uls_u2p14[7]	7864
t_APASmoothY_Uls_u2p14[8]	8028
t_APASmoothY_Uls_u2p14[9]	8192
t_LKASlewX_Kph_u8p8[0]	8704
t_LKASlewX_Kph_u8p8[1]	9216
t_LKASlewX_Kph_u8p8[2]	9728
t_LKASlewX_Kph_u8p8[3]	10240
t_LKASlewX_Kph_u8p8[4] t_LKASlewX_Kph_u8p8[5]	10752 11264
t_LKASlewY_NmpSec_u4p12[0]	6963
t_LKASlewY_NmpSec_u4p12[1]	7373
t_LKASlewY_NmpSec_u4p12[2]	7782
t_LKASlewY_NmpSec_u4p12[3]	8192
t_LKASlewY_NmpSec_u4p12[4]	8602
t_LKASlewY_NmpSec_u4p12[5]	9011
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_state_enum target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESC5tate_State_enum target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrgArblim Per1 GMOSHOscillate State enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_Kte_inst_Ap_1rqArbiim_Peri_1rqOscCmd_witrNm_132 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0
<u> </u>	



Name	Input Value		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	76.9933014		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.196999997	0.196999997 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.489905983	0.489906013 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.1800003	10.1800003 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.0940000042	-0.0939999968 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.24000001	-3.24000001 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.876287162	0.876286983 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TroArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.19 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.244000003
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.12
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0416000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10
FrqArblim_PreLKATrqReq_HwNm_M_f32	0.400000006
<_APAEnableRate_pSec_f32	82.5
C_APAUseADmpScale_lgc	0
<_APAUseAsstScale_lgc	0
<_APAUseRetScale_lgc	0
c_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	7373
_APADisableRateX_HwNm_u4p12[1]	7782
_APADisableRateX_HwNm_u4p12[2]	8192
_APADisableRateX_HwNm_u4p12[3]	8602
_APADisableRateX_HwNm_u4p12[4]	9011
_APADisableRateX_HwNm_u4p12[5]	9421
_APADisableRateY_pSec_u7p9[0]	9216
_APADisableRateY_pSec_u7p9[1]	9728
_APADisableRateY_pSec_u7p9[2]	10240
_APADisableRateY_pSec_u7p9[3]	10752
_APADisableRateY_pSec_u7p9[4]	11264
_APADisableRateY_pSec_u7p9[5]	11776
_APASmoothX_Uls_u2p14[0]	2949
_APASmoothX_Uls_u2p14[1]	3113
_APASmoothX_Uls_u2p14[2]	3277
_APASmoothX_Uls_u2p14[3]	3441
_APASmoothX_Uls_u2p14[4]	3604
_APASmoothX_Uls_u2p14[5]	3768
_APASmoothX_Uls_u2p14[6]	3932
_APASmoothX_Uls_u2p14[7]	4096
_APASmoothX_Uls_u2p14[8]	4260
_APASmoothX_Uls_u2p14[9]	4424
t_APASmoothY_Uls_u2p14[0]	0
t_APASmoothY_Uls_u2p14[1]	164
t APASmoothY Uls u2p14[2]	328

2014-09-16, 12:30:22+0530



TrqArblim_Per1

Name	Input Value			
t_APASmoothY_Uls_u2p14[3]	492			
t_APASmoothY_Uls_u2p14[4]	655			
t_APASmoothY_Uls_u2p14[5]	819			
t_APASmoothY_Uls_u2p14[6]	983			
t_APASmoothY_Uls_u2p14[7]	1147			
t_APASmoothY_Uls_u2p14[8]	1311			
t_APASmoothY_Uls_u2p14[9]	1475			
t_LKASlewX_Kph_u8p8[0]	9216			
t_LKASlewX_Kph_u8p8[1]	9728			
t_LKASlewX_Kph_u8p8[2]	10240			
t LKASlewX Kph u8p8[3]	10752			
t_LKASlewX_Kph_u8p8[4]	11264			
t_LKASlewX_Kph_u8p8[5]	11776			
t_LKASlewY_NmpSec_u4p12[0]	7373			
t_LKASlewY_NmpSec_u4p12[1]	7782			
t LKASlewY NmpSec u4p12[2]	8192			
t_LKASlewY_NmpSec_u4p12[3]	8602			
t_LKASlewY_NmpSec_u4p12[4]	9011			
t LKASlewY NmpSec u4p12[5]	9421			
target Rte Call Ap TrgArblim FaultInjection SCom FltInjection SignalPath Uls f				
target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor Uls f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target TrgArblim Per1 DampingDDFactor			
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	-		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target TrqArblim Per1 HwTorque HwNm f	_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996	_132		
	3			
target_TrqArblim_Per1_ESCState_State_enum.value	1			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	5.34000015			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-2.73000002			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1			
target_TrqArblim_Per1_LKAState_State_enum.value				
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	83.4432983			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.70000005			
	-3.70000005			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value		Function Value	B	
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.409000009	0.409000009 ± 0000390625		
TrqArblim_ESC_State_Cnt_M_enum	3	3	•	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.29555202	1.29554999 ± 0.03125	•	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.70000005	-3.70000005 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.382734507	0.382735014 ± 0.03125	•	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	-	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~	
torget Tradeline Dord InTraOv. Husbro 622 value	0.000704507			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	V

0.382734507

0.382734507

1.86376238

 $target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value$

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

0.382734507 ± 0.0000390625

0.382734507 ± 0.0000390625

1.86375999 ± 0000390625

1 ± 0000390625



Test Step 3.20 (Repeat Count = 1)	· ·
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.256999999
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.73000002
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.52000046
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.89999976
k_APAEnableRate_pSec_f32	87
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32 k_LKAUseSlewCal_lgc	4.28000021
t_APADisableRateX_HwNm_u4p12[0]	7782
t_APADisableRateX_HwNm_u4p12[1]	8192
t_APADisableRateX_HwNm_u4p12[2]	8602
t APADisableRateX HwNm u4p12[3]	9011
t_APADisableRateX_HwNm_u4p12[4]	9421
t_APADisableRateX_HwNm_u4p12[5]	9830
t_APADisableRateY_pSec_u7p9[0]	9728
t_APADisableRateY_pSec_u7p9[1]	10240
t_APADisableRateY_pSec_u7p9[2]	10752
t_APADisableRateY_pSec_u7p9[3]	11264
t_APADisableRateY_pSec_u7p9[4]	11776
t_APADisableRateY_pSec_u7p9[5]	12288
t_APASmoothX_Uls_u2p14[0]	3113
t_APASmoothX_Uls_u2p14[1]	3277
t_APASmoothX_Uls_u2p14[2]	3441
t_APASmoothX_Uls_u2p14[3]	3604
t_APASmoothX_Uls_u2p14[4]	3768
t_APASmoothX_UIs_u2p14[5]	3932
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	4096 4260
t_APASmoothX_Uls_u2p14[8]	4424
t_APASmoothX_UIs_u2p14[9]	4588
t_APASmoothY_Uls_u2p14[0]	164
t_APASmoothY_Uls_u2p14[1]	328
t_APASmoothY_Uls_u2p14[2]	492
t_APASmoothY_Uls_u2p14[3]	655
t_APASmoothY_Uls_u2p14[4]	819
t_APASmoothY_Uls_u2p14[5]	983
t_APASmoothY_Uls_u2p14[6]	1147
t_APASmoothY_Uls_u2p14[7]	1311
t_APASmoothY_Uls_u2p14[8]	1475
t_APASmoothY_Uls_u2p14[9]	1638
t_LKASlewX_Kph_u8p8[0]	9728
t_LKASlewX_Kph_u8p8[1]	10240
t_LKASlewX_Kph_u8p8[2]	10752
t_LKASlewX_Kph_u8p8[3]	11264 11776
t_LKASlewX_Kph_u8p8[4] t_LKASlewX_Kph_u8p8[5]	12288
t_LKASlewY_NmpSec_u4p12[0]	7782
t_LKASlewY_NmpSec_u4p12[1]	8192
t LKASlewY NmpSec u4p12[2]	8602
t_LKASlewY_NmpSec_u4p12[3]	9011
t_LKASlewY_NmpSec_u4p12[4]	9421
t_LKASlewY_NmpSec_u4p12[5]	9830
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	f. 3.0999999
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_frqArblim_Per1_LKAState_state_enum target_frqArblim_Per1_LKATorqueDelivered_HwNm_f32
goo_moc_np_rrqrsomin.rrqrsomin_rorr_cronrorqueDelivereu_riwiviii_i32	wagozqr nomin_1 or 1_Ero trorquoDonvorou_1 iwinin_loz





Name	Input Value		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kphromerral (Management of the Company of t$	n_f32 target_TrqArblim_Per1_MinS	ecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTr	qOv_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosS	servEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosS	rvoCmd_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_Retu	rnDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqO	scCmd_MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.86000013		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-2.24000001		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	89.8933029		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.430999994	0.430999994 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.39755797	1.39756 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.5	-3.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.893999994	0.893999994 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.150813162	-0.150812998 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.21 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.270000011
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.39999998
k_APAEnableRate_pSec_f32	91.5
k_APAUseADmpScale_lgc	1.
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	ī
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	1.
t_APADisableRateX_HwNm_u4p12[0]	8192
t_APADisableRateX_HwNm_u4p12[1]	8602
t_APADisableRateX_HwNm_u4p12[2]	9011
t_APADisableRateX_HwNm_u4p12[3]	9421
t_APADisableRateX_HwNm_u4p12[4]	9830
t_APADisableRateX_HwNm_u4p12[5]	10240
t_APADisableRateY_pSec_u7p9[0]	10240
t_APADisableRateY_pSec_u7p9[1]	10752
t_APADisableRateY_pSec_u7p9[2]	11264
t_APADisableRateY_pSec_u7p9[3]	11776
t_APADisableRateY_pSec_u7p9[4]	12288
t_APADisableRateY_pSec_u7p9[5]	12800
t_APASmoothX_Uls_u2p14[0]	3277
t_APASmoothX_Uls_u2p14[1]	3441
t_APASmoothX_Uls_u2p14[2]	3604

2014-09-16, 12:30:22+0530





Name	Input Value		
t_APASmoothX_UIs_u2p14[3]	3768		
t_APASmoothX_Uls_u2p14[4]	3932		
t_APASmoothX_Uls_u2p14[5]	4096		
t_APASmoothX_Uls_u2p14[6]	4260		
t_APASmoothX_Uls_u2p14[7]	4424		
t_APASmoothX_UIs_u2p14[8]	4588		
t_APASmoothX_UIs_u2p14[9]	4751 328		
t_APASmoothY_Uls_u2p14[0] t_APASmoothY_Uls_u2p14[1]	492		
t_APASmoothY_Uls_u2p14[2]	655		
t APASmoothY Uls u2p14[3]	819		
t_APASmoothY_Uls_u2p14[4]	983		
t_APASmoothY_Uls_u2p14[5]	1147		
t_APASmoothY_Uls_u2p14[6]	1311		
t_APASmoothY_Uls_u2p14[7]	1475		
t_APASmoothY_Uls_u2p14[8]	1638		
t_APASmoothY_Uls_u2p14[9]	1802		
t_LKASlewX_Kph_u8p8[0]	10240		
t_LKASlewX_Kph_u8p8[1]	10752		
t_LKASlewX_Kph_u8p8[2]	11264		
t_LKASlewX_Kph_u8p8[3]	11776		
t_LKASlewX_Kph_u8p8[4]	12288		
t_LKASlewX_Kph_u8p8[5]	12800		
t_LKASlewY_NmpSec_u4p12[0]	8192		
t_LKASlewY_NmpSec_u4p12[1]	8602		
t_LKASlewY_NmpSec_u4p12[2]	9011		
t_LKASlewY_NmpSec_u4p12[3]	9421		
t_LKASlewY_NmpSec_u4p12[4]	9830		
t_LKASlewY_NmpSec_u4p12[5]	10240		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_UIs_f		f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value target TrqArblim Per1 LKACmd HwNm f32.value	-9.85999966 0.5		
target TrqArblim Perl LKAState State enum.value	0.5		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	96.3432999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.230000004	0.223203003 ± 0000390625	Resul
TrqArblim ESC State Cnt M enum	0.23000004	0.223203003 ± 0000390023	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.77880001	1.77880001 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.03999996	-9.03999996 ± 0.000244140625	
TrgArblim_PreLKATrgReq_HwNm_M_f32	1.39399993	1.39400005 ± 0.03125	
target TrqArblim Per1 AssistDDFactor UIs f32.value	0.9885028	0.990368009 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.9885028	0.990368009 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.39399993	1.39400005 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.39399993	1.39400005 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0000390625	

0.9885028

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

0.990368009 ± 0000390625



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.22 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.282999992
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.95000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0482000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.56000042
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.8999998
<_APAEnableRate_pSec_f32	96
<pre><_APAUseADmpScale_lgc</pre>	0
<_APAUseAsstScale_lgc	0
<_APAUseRetScale_lgc	0
x_ESCMax_HwNm_f32	3.1400001
K LKAUseSlewCal Igc	0
	8602
 _APADisableRateX_HwNm_u4p12[1]	9011
:_APADisableRateX_HwNm_u4p12[2]	9421
: APADisableRateX_HwNm_u4p12[3]	9830
_APADisableRateX_HwNm_u4p12[4]	10240
_APADisableRateX_HwNm_u4p12[4] :_APADisableRateX_HwNm_u4p12[5]	10240
	10752
:_APADisableRateY_pSec_u7p9[0]	
:_APADisableRateY_pSec_u7p9[1]	11264
_APADisableRateY_pSec_u7p9[2]	11776
:_APADisableRateY_pSec_u7p9[3]	12288
:_APADisableRateY_pSec_u7p9[4]	12800
:_APADisableRateY_pSec_u7p9[5]	13312
:_APASmoothX_Uls_u2p14[0]	3441
:_APASmoothX_Uls_u2p14[1]	3604
:_APASmoothX_Uls_u2p14[2]	3768
:_APASmoothX_Uls_u2p14[3]	3932
_APASmoothX_Uls_u2p14[4]	4096
_APASmoothX_Uls_u2p14[5]	4260
_APASmoothX_Uls_u2p14[6]	4424
_APASmoothX_Uls_u2p14[7]	4588
	4751
 _APASmoothX_Uls_u2p14[9]	4915
APASmoothY_Uls_u2p14[0]	492
APASmoothY_Uls_u2p14[1]	655
:_APASmoothY_Uls_u2p14[2]	819
	983
:_APASmoothY_UIs_u2p14[3]	
t_APASmoothY_Uls_u2p14[4]	1147
:_APASmoothY_Uls_u2p14[5]	1311
:_APASmoothY_Uls_u2p14[6]	1475
:_APASmoothY_Uls_u2p14[7]	1638
_APASmoothY_Uls_u2p14[8]	1802
t_APASmoothY_Uls_u2p14[9]	1966
LKASlewX_Kph_u8p8[0]	10752
:_LKASlewX_Kph_u8p8[1]	11264
_LKASlewX_Kph_u8p8[2]	11776
_LKASlewX_Kph_u8p8[3]	12288
_LKASlewX_Kph_u8p8[4]	12800
_LKASlewX_Kph_u8p8[5]	13312
 :_LKASlewY_NmpSec_u4p12[0]	8602
: LKASlewY NmpSec u4p12[1]	9011
:_LKASlewY_NmpSec_u4p12[2]	9421
:_LKASlewY_NmpSec_u4p12[3]	9830
trvslew1_NmpSec_u4p12[4]	10240
	10650
:_LKASlewY_NmpSec_u4p12[5] :arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	
	1.14.0





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	r_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_	enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelive	ered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate	_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNn	1_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_	enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelive	red_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicle	eSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_0	Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	lm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.25		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.850000024		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	102.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.474999994	0.474999994 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	1	1	•
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.36195993	2.36195993 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	-3.0999999	-3.0999999 ± 0.000244140625	
TrgArblim_PreLKATrgReg_HwNm_M_f32	1.88499177	1.88498998 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.1400001	-3.1400001 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.62330723	8.62331009 ± 0000390625	
1 T ALE D 1 D 1 DD5 1 H 200 1			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	/

1 ± 0000390625

Test Step 3.23 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.296000004
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.55999994
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0504000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.07999992
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.4000001
k_APAEnableRate_pSec_f32	100.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	9011
t_APADisableRateX_HwNm_u4p12[1]	9421
t_APADisableRateX_HwNm_u4p12[2]	9830
t_APADisableRateX_HwNm_u4p12[3]	10240
t_APADisableRateX_HwNm_u4p12[4]	10650

2014-09-16, 12:30:22+0530



Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	11059		
t_APADisableRateY_pSec_u7p9[0]	11264		
t_APADisableRateY_pSec_u7p9[1]	11776		
t_APADisableRateY_pSec_u7p9[2]	12288		
t_APADisableRateY_pSec_u7p9[3]	12800		
t_APADisableRateY_pSec_u7p9[4]	13312		
t_APADisableRateY_pSec_u7p9[5]	13824		
t_APASmoothX_Uls_u2p14[0]	3604		
t_APASmoothX_Uls_u2p14[1]	3768		
t_APASmoothX_Uls_u2p14[2]	3932		
t_APASmoothX_Uls_u2p14[3]	4096		
t_APASmoothX_Uls_u2p14[4]	4260		
t_APASmoothX_Uls_u2p14[5]	4424		
t_APASmoothX_Uls_u2p14[6]	4588		
t_APASmoothX_Uls_u2p14[7]	4751		
t_APASmoothX_Uls_u2p14[8]	4915		
t_APASmoothX_Uls_u2p14[9]	5079		
t_APASmoothY_Uls_u2p14[0]	655		
t_APASmoothY_Uls_u2p14[1]	819		
t_APASmoothY_Uls_u2p14[2]	983		
t_APASmoothY_Uls_u2p14[3]	1147		
t_APASmoothY_Uls_u2p14[4]	1311		
t_APASmoothY_Uls_u2p14[5]	1475		
t_APASmoothY_Uls_u2p14[6]	1638		
t_APASmoothY_Uls_u2p14[7]	1802		
t_APASmoothY_Uls_u2p14[8]	1966		
t_APASmoothY_Uls_u2p14[9]	2130		
t_LKASlewX_Kph_u8p8[0]	11264		
t_LKASlewX_Kph_u8p8[1]	11776		
t_LKASlewX_Kph_u8p8[2]	12288		
t_LKASlewX_Kph_u8p8[3]	12800		
t_LKASlewX_Kph_u8p8[4]	13312		
t_LKASlewX_Kph_u8p8[5]	13824		
t_LKASlewY_NmpSec_u4p12[0]	9011 9421		
t_LKASlewY_NmpSec_u4p12[1]	9830		
t_LKASlewY_NmpSec_u4p12[2] t_LKASlewY_NmpSec_u4p12[3]	10240		
	10650		
t_LKASlewY_NmpSec_u4p12[4] t_LKASlewY_NmpSec_u4p12[5]	11059		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor Uls f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target TrgArblim Per1 ESCIsLimited Cnt I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target TrqArblim Per1 ESCState State en	•	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate St		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.11999989		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64000034		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.20000005		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	109.242996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0		l
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.241999999	0.241999999 ± 0000390625	V
TrqArblim_ESC_State_Cnt_M_enum	2	2	V
	2.04511086	2 0451201 ± 0 02125	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.94511986 -8.07999992	2.9451201 ± 0.03125 -8.07999992 ± 0.000244140625	V





Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.39400005	2.39400005 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.985007942	0.985008001 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.985007942	0.985008001 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-2.57999992	-2.57999992 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-2.57999992	-2.57999992 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.12113589	-0.121136002 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.985007942	0.985008001 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

lame	Input Value
te_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
te_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.308999985
rgArblim ESC State Cnt M enum	2
rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
rqArblim_PosSrvoCmd_MtrNm_M_f32	-7.5999999
rgArblim PreLKATrgReg HwNm M f32	2.9000001
_APAEnableRate_pSec_f32	105
APAUseADmpScale Igc	0
APAUseAsstScale lgc	0
	0
_APAUseRetScale_lgc	
_ESCMax_HwNm_f32	0.239999995
_LKAUseSlewCal_lgc	0
APADisableRateX_HwNm_u4p12[0]	9421
APADisableRateX_HwNm_u4p12[1]	9830
APADisableRateX_HwNm_u4p12[2]	10240
APADisableRateX_HwNm_u4p12[3]	10650
_APADisableRateX_HwNm_u4p12[4]	11059
_APADisableRateX_HwNm_u4p12[5]	11469
APADisableRateY_pSec_u7p9[0]	11776
_APADisableRateY_pSec_u7p9[1]	12288
APADisableRateY_pSec_u7p9[2]	12800
_APADisableRateY_pSec_u7p9[3]	13312
_APADisableRateY_pSec_u7p9[4]	13824
_APADisableRateY_pSec_u7p9[5]	14336
_APASmoothX_Uls_u2p14[0]	3768
_APASmoothX_Uls_u2p14[1]	3932
_APASmoothX_Uls_u2p14[2]	4096
_APASmoothX_Uls_u2p14[3]	4260
_APASmoothX_Uls_u2p14[4]	4424
APASmoothX_Uls_u2p14[5]	4588
APASmoothX_Uls_u2p14[6]	4751
APASmoothX_Uls_u2p14[7]	4915
_APASmoothX_Uls_u2p14[8]	5079
_APASmoothX_Uls_u2p14[9]	5243
_APASmoothY_Uls_u2p14[0]	819
_APASmoothY_Uls_u2p14[1]	983
_APASmoothY_Uls_u2p14[2]	1147
_APASmoothY_Uls_u2p14[3]	1311
_APASmoothY_Uls_u2p14[4]	1475
_APASmoothY_Uls_u2p14[5]	1638
_APASmoothY_Uls_u2p14[6]	1802
_APASmoothY_Uls_u2p14[7]	1966
APASmoothY_Uls_u2p14[8]	2130
APASmoothY_Uls_u2p14[9]	2294
LKASlewX_Kph_u8p8[0]	11776
LKASlewX_Kph_u8p8[1]	12288
LKASlewX_Kph_u8p8[2]	12800
LKASlewX_Kph_u8p8[3]	13312

2014-09-16, 12:30:22+0530



TrgArblim Per1

TrqArblim_Per1			MACUICAL
Name	Input Value		
t_LKASlewX_Kph_u8p8[4]	13824		
t_LKASlewX_Kph_u8p8[5]	14336		
t_LKASlewY_NmpSec_u4p12[0]	9421		
t LKASlewY NmpSec u4p12[1]	9830		
t LKASlewY NmpSec u4p12[2]	10240		
t_LKASlewY_NmpSec_u4p12[3]	10650		
t LKASlewY NmpSec u4p12[4]	11059		
t_LKASlewY_NmpSec_u4p12[5]	11469		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	f: 5.9000001		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_L	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFacto	r_Uls_f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target TrqArblim Per1 ESCCmd HwNm		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cn	t lgc	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelive		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_t	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target_TrqArblim_Per1_LKATorqueDelive		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicle		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable C		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosSrvoCmd MtrNm f32	target TrqArblim Per1 PosSrvoCmd Mtr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987	_	
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target TrqArblim Per1 HwTorque HwNm f32.value	-8.02999973		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.54999995		
target TrqArblim Per1 LKAState State enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	115.693001		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	-2.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.4000001		
Name	Actual Value	Expected Value	Resul
TrgArblim APADisableRate Uls M f32	0.252999991	0.252999991 ± 0000390625	Result
TrgArblim ESC State Cnt M enum	3	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.52828002	3.52828002 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.5999999	-7.5999999 ± 0.00024414062	
TrgArblim PreLKATrgReg HwNm M f32	2.88649678	2.88649988 ± 0.03125	20
target TrqArblim Per1 AssistDDFactor Uls f32.value	2.00049070	2.66649966 ± 0.03125 1 ± 0000390625	
	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0	1 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0 ± 0000390625	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value		0 ± 0000390625 2.88649678 ± 0.0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.88649678	2.88649678 ± 0.0000390625 2.88649678 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.88649678		
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.25963926	4.25964022 ± 0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

1 ± 0000390625

Test Step 3.25 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.321999997
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.78000021
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0548
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.11999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.400001
k_APAEnableRate_pSec_f32	109.5





Name	Input Value
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	9830
t_APADisableRateX_HwNm_u4p12[1]	10240
t_APADisableRateX_HwNm_u4p12[2]	10650
t_APADisableRateX_HwNm_u4p12[3]	11059
t_APADisableRateX_HwNm_u4p12[4]	11469
t_APADisableRateX_HwNm_u4p12[5]	11878
t_APADisableRateY_pSec_u7p9[0]	12288
t_APADisableRateY_pSec_u7p9[1]	12800
t_APADisableRateY_pSec_u7p9[2]	13312
t_APADisableRateY_pSec_u7p9[3] t_APADisableRateY_pSec_u7p9[4]	13824 14336
t_APADisableRateY_pSec_u7p9[5]	14848
t_APASmoothX_Uls_u2p14[0]	3932
t_APASmoothX_Uls_u2p14[1]	4096
t_APASmoothX_Uls_u2p14[2]	4260
t_APASmoothX_Uls_u2p14[3]	4424
t_APASmoothX_Uls_u2p14[4]	4588
t_APASmoothX_Uls_u2p14[5]	4751
t_APASmoothX_Uls_u2p14[6]	4915
t_APASmoothX_Uls_u2p14[7]	5079
t_APASmoothX_Uls_u2p14[8]	5243
t_APASmoothX_UIs_u2p14[9]	5407
t_APASmoothY_UIs_u2p14[0]	983
t_APASmoothY_Uls_u2p14[1]	1147 1311
t_APASmoothY_Uls_u2p14[2] t_APASmoothY_Uls_u2p14[3]	1475
t_APASmoothY_Uls_u2p14[4]	1638
t_APASmoothY_Uls_u2p14[5]	1802
t_APASmoothY_Uls_u2p14[6]	1966
t_APASmoothY_Uls_u2p14[7]	2130
t_APASmoothY_Uls_u2p14[8]	2294
t_APASmoothY_Uls_u2p14[9]	2458
t_LKASlewX_Kph_u8p8[0]	12288
t_LKASlewX_Kph_u8p8[1]	12800
t_LKASlewX_Kph_u8p8[2]	13312
t_LKASlewX_Kph_u8p8[3]	13824
t_LKASlewX_Kph_u8p8[4] t_LKASlewX_Kph_u8p8[5]	14336 14848
t_LKASlewY_NmpSec_u4p12[0]	9830
t_LKASlewY_NmpSec_u4p12[1]	10240
t_LKASlewY_NmpSec_u4p12[2]	10650
t_LKASlewY_NmpSec_u4p12[3]	11059
t_LKASlewY_NmpSec_u4p12[4]	11469
t_LKASlewY_NmpSec_u4p12[5]	11878
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_fltInje$	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HWNIII_S2 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target TrgArblim Per1 HwTorque HwNm f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
$target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value	-6.15999985 4
	1.8999998
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1 -7.42000008 1.89999998



Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	122.142998		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.4000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.541000009	0.541000009 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.11144018	4.11144018 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.5	-2.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.39400005	3.39400005 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.918836772	0.918837011 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.918836772	0.918837011 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-4.60290813	-4.60291004 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.918836772	0.918837011 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.26 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.335000008
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.3899987
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.057
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.63999987
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.9000001
k_APAEnableRate_pSec_f32	114
<_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	10240
:_APADisableRateX_HwNm_u4p12[1]	10650
_APADisableRateX_HwNm_u4p12[2]	11059
_APADisableRateX_HwNm_u4p12[3]	11469
_APADisableRateX_HwNm_u4p12[4]	11878
t_APADisableRateX_HwNm_u4p12[5]	12288
_APADisableRateY_pSec_u7p9[0]	12800
t_APADisableRateY_pSec_u7p9[1]	13312
t_APADisableRateY_pSec_u7p9[2]	13824
t_APADisableRateY_pSec_u7p9[3]	14336
t_APADisableRateY_pSec_u7p9[4]	14848
_APADisableRateY_pSec_u7p9[5]	15360
_APASmoothX_Uls_u2p14[0]	4096
:_APASmoothX_Uls_u2p14[1]	4260
t_APASmoothX_Uls_u2p14[2]	4424
t_APASmoothX_Uls_u2p14[3]	4588
t_APASmoothX_Uls_u2p14[4]	4751
t_APASmoothX_Uls_u2p14[5]	4915
	5079
APASmoothX Uls u2p14[7]	5243
APASmoothX_Uls_u2p14[8]	5407
	5571
t_APASmoothY_Uls_u2p14[0]	1147
t APASmoothY Uls u2p14[1]	1311
t_APASmoothY_Uls_u2p14[2]	1475
t APASmoothY Uls u2p14[3]	1638

2014-09-16, 12:30:22+0530



TrqArblim_Per1

114A15IIIII_1 CI I			(-4 - 10-10
Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	1802		
t_APASmoothY_Uls_u2p14[5]	1966		
t_APASmoothY_Uls_u2p14[6]	2130		
t_APASmoothY_Uls_u2p14[7]	2294		
t_APASmoothY_Uls_u2p14[8]	2458		
t_APASmoothY_Uls_u2p14[9]	2621		
t_LKASlewX_Kph_u8p8[0]	12800		
t_LKASlewX_Kph_u8p8[1]	13312		
t_LKASlewX_Kph_u8p8[2]	13824		
t_LKASlewX_Kph_u8p8[3]	14336		
t_LKASlewX_Kph_u8p8[4]	14848		
t_LKASlewX_Kph_u8p8[5]	15360		
t_LKASlewY_NmpSec_u4p12[0]	10240		
t_LKASlewY_NmpSec_u4p12[1]	10650		
t_LKASlewY_NmpSec_u4p12[2]	11059		
t_LKASlewY_NmpSec_u4p12[3]	11469		
t_LKASlewY_NmpSec_u4p12[4]	11878		
t_LKASlewY_NmpSec_u4p12[5]	12288		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_Injection_SignalP$	f: 7.30000019		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_er	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cr	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.67999983		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.80999994		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.25		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.593002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.20999993		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.563000023	0.563000023 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	0	0	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.69460011	4.69460011 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.29999995	-2.29999995 ± 0.00024414062	
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.88800168	3.88800001 ± 0.03125	· ·
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.00285077095	0.00285077002 ± 0000390625	5
target TrgArblim Per1 ReturnDDFactor UIs f32 value	11	1 + 0000390625	→

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

1 ± 0000390625



Test Step 3.27 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.34799999
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0592
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.4000001
k_APAEnableRate_pSec_f32	118.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	10650
t_APADisableRateX_HwNm_u4p12[1]	11059
t_APADisableRateX_HwNm_u4p12[2]	11469
t_APADisableRateX_HwNm_u4p12[3]	11878
t_APADisableRateX_HwNm_u4p12[4]	12288
t_APADisableRateX_HwNm_u4p12[5]	12698
t_APADisableRateY_pSec_u7p9[0]	13312
t_APADisableRateY_pSec_u7p9[1]	13824
t_APADisableRateY_pSec_u7p9[2]	14336
t_APADisableRateY_pSec_u7p9[3] t APADisableRateY pSec u7p9[4]	14848 15360
t_APADisableRateY_pSec_u7p9[4] t_APADisableRateY_pSec_u7p9[5]	15872
t_APASmoothX_Uls_u2p14[0]	4260
t_APASmoothX_Uls_u2p14[1]	4424
t_APASmoothX_Uls_u2p14[2]	4588
t_APASmoothX_Uls_u2p14[3]	4751
t_APASmoothX_Uls_u2p14[4]	4915
t_APASmoothX_Uls_u2p14[5]	5079
t_APASmoothX_Uls_u2p14[6]	5243
t_APASmoothX_UIs_u2p14[7]	5407
t_APASmoothX_Uls_u2p14[8]	5571
t_APASmoothX_Uls_u2p14[9]	5734
t_APASmoothY_Uls_u2p14[0]	1311
t_APASmoothY_Uls_u2p14[1]	1475
t_APASmoothY_Uls_u2p14[2]	1638
t_APASmoothY_Uls_u2p14[3]	1802
t_APASmoothY_Uls_u2p14[4]	1966
t APASmoothY Uls u2p14[5]	2130
t_APASmoothY_Uls_u2p14[6]	2294
t_APASmoothY_Uls_u2p14[7]	2458
t_APASmoothY_Uls_u2p14[8]	2621
t_APASmoothY_Uls_u2p14[9]	2785
t_LKASlewX_Kph_u8p8[0]	13312
t_LKASlewX_Kph_u8p8[1]	13824
t_LKASlewX_Kph_u8p8[2]	14336
t_LKASlewX_Kph_u8p8[3]	14848
t_LKASlewX_Kph_u8p8[4]	15360
t_LKASlewX_Kph_u8p8[5]	15872
t_LKASlewY_NmpSec_u4p12[0]	10650
t_LKASlewY_NmpSec_u4p12[1]	11059
t_LKASlewY_NmpSec_u4p12[2]	11469
t_LKASlewY_NmpSec_u4p12[3]	11878
t_LKASlewY_NmpSec_u4p12[4]	12288
t_LKASlewY_NmpSec_u4p12[5]	12698
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.19999981		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.5999999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	135.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.560000002		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.285999984	0.286000013 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.27776003	5.27776003 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985	-6.15999985 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.39379978	4.39379978 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.186563313	-0.186563 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.28 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.361000001
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.61000013
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0614
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.67999983
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.9000001
k_APAEnableRate_pSec_f32	123
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	11059
t_APADisableRateX_HwNm_u4p12[1]	11469
t_APADisableRateX_HwNm_u4p12[2]	11878
t_APADisableRateX_HwNm_u4p12[3]	12288
t_APADisableRateX_HwNm_u4p12[4]	12698
t_APADisableRateX_HwNm_u4p12[5]	13107
t_APADisableRateY_pSec_u7p9[0]	13824
t_APADisableRateY_pSec_u7p9[1]	14336
t_APADisableRateY_pSec_u7p9[2]	14848
t_APADisableRateY_pSec_u7p9[3]	15360
t_APADisableRateY_pSec_u7p9[4]	15872
t_APADisableRateY_pSec_u7p9[5]	16384
t_APASmoothX_UIs_u2p14[0]	4424
t_APASmoothX_Uls_u2p14[1]	4588
t_APASmoothX_Uls_u2p14[2]	4751

2014-09-16, 12:30:22+0530





Name	Input Value			
t_APASmoothX_Uls_u2p14[3]	4915			
t_APASmoothX_Uls_u2p14[4]	5079			
t_APASmoothX_Uls_u2p14[5]	5243			
t_APASmoothX_Uls_u2p14[6]	5407			
t_APASmoothX_Uls_u2p14[7]	5571			
t_APASmoothX_Uls_u2p14[8]	5734			
t_APASmoothX_Uls_u2p14[9]	5898			
t_APASmoothY_Uls_u2p14[0]	1475			
t_APASmoothY_Uls_u2p14[1]	1638			
t APASmoothY Uls u2p14[2]	1802			
t APASmoothY Uls u2p14[3]	1966			
t_APASmoothY_Uls_u2p14[4]	2130			
t_APASmoothY_Uls_u2p14[5]	2294			
t_APASmoothY_Uls_u2p14[6]	2458			
t_APASmoothY_Uls_u2p14[7]	2621			
t_APASmoothY_Uls_u2p14[8]	2785			
t_APASmoothY_Uls_u2p14[9]	2949			
t_LKASlewX_Kph_u8p8[0]	13824			
t_LKASlewX_Kph_u8p8[1]	14336			
t_LKASlewX_Kph_u8p8[2]	14848			
t_LKASlewX_Kph_u8p8[3]	15360			
	15872			
t_KASlewX_Kph_u8p8[4] t LKASlewX Kph u8p8[5]				
	11059			
t_LKASlewY_NmpSec_u4p12[0]				
t_LKASlewY_NmpSec_u4p12[1]	11469			
t_LKASlewY_NmpSec_u4p12[2]	11878			
t_LKASlewY_NmpSec_u4p12[3]	12288			
t_LKASlewY_NmpSec_u4p12[4]	12698			
t_LKASlewY_NmpSec_u4p12[5]	13107			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_t				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en			
target_Rte_Inst_Ap_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979			
target_TrqArblim_Per1_ESCState_State_enum.value	2			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.59000015			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.95000005			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	141.492996			
target TrgArblim Per1 PosServEnable Cnt Igc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.8999998			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.910000026			
Name	Actual Value	Expected Value	Resu	
	0.606999993	0.606999993 ± 0000390625	ivean	
TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cpt_M_equip	0.606999993	0.606999993 ± 0000390625		
TrgArblim_ESC_State_Cnt_M_enum TrgArblim_HwTorqueSV_Hwhm_M_Str_SV_Lile_f32				
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqArblim_PosScroCmd_MtrNm_M_f32	5.86091995	5.86091995 ± 0.03125		
TrqArblim_PosSrvoCmd_MtrNm_M_f32 TrqArblim_Prol_KATraPog_Hubbm_M_f32	-1.8999998	-1.89999998 ± 0.000244140625		
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.88950682	4.88951015 ± 0.03125		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.69000006	-3.69000006 ± 0000390625	•	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.207585543	-0.207586005 ± 0000390625		
target TrgArblim Per1 ReturnDDFactor Uls f32.value	1	1 ± 0000390625		

1

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

1 ± 0000390625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.29 (Repeat Count = 1) Name	Input Value
	target Rte Call Ap TrgArblim FaultInjection SCom FltInjection SignalPath Uls f32
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	
Rte_Inst_Ap_TrqArblim TrqArblim_APADisableRate_Uls_M_f32	target_Rte_Inst_Ap_TrqArblim 0.374000013
	0.574000013
TrqArblim_ESC_State_Cnt_M_enum	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.21999979
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0636000037
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.19999981
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.4000001
k_APAEnableRate_pSec_f32	127.5
k_APAUseADmpScale_lgc	1
<_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
<_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	11469
t_APADisableRateX_HwNm_u4p12[1]	11878
t_APADisableRateX_HwNm_u4p12[2]	12288
	12698
t_APADisableRateX_HwNm_u4p12[4]	13107
t_APADisableRateX_HwNm_u4p12[5]	13517
t_APADisableRateY_pSec_u7p9[0]	14336
t_APADisableRateY_pSec_u7p9[1]	14848
t APADisableRateY pSec u7p9[2]	15360
t_APADisableRateY_pSec_u7p9[3]	15872
t_APADisableRateY_pSec_u7p9[4]	16384
t_APADisableRateY_pSec_u7p9[5]	16896
t_APASmoothX_Uls_u2p14[0]	4588
t_APASmoothX_Uls_u2p14[1]	4751
t_APASmoothX_Uls_u2p14[2]	4915
t_APASmoothX_Uls_u2p14[3]	5079
t_APASmoothX_Uls_u2p14[4]	5243
t_APASmoothX_Uls_u2p14[5]	5407
t_APASmoothX_Uls_u2p14[6]	5571
t_APASmoothX_Uls_u2p14[7]	5734
t_APASmoothX_Uls_u2p14[8]	5898
t_APASmoothX_Uls_u2p14[9]	6062
t_APASmoothY_Uls_u2p14[0]	1638
t_APASmoothY_Uls_u2p14[1]	1802
t_APASmoothY_Uls_u2p14[2]	1966
t_APASmoothY_Uls_u2p14[3]	2130
t_APASmoothY_Uls_u2p14[4]	2294
	2458
t_APASmoothY_UIs_u2p14[5]	
t_APASmoothY_Uls_u2p14[6]	2621
t_APASmoothY_Uls_u2p14[7]	2785
t_APASmoothY_Uls_u2p14[8]	2949
t_APASmoothY_Uls_u2p14[9]	3113
t_LKASlewX_Kph_u8p8[0]	14336
t_LKASlewX_Kph_u8p8[1]	14848
t_LKASlewX_Kph_u8p8[2]	15360
t_LKASlewX_Kph_u8p8[3]	15872
t_LKASlewX_Kph_u8p8[4]	16384
t_LKASlewX_Kph_u8p8[5]	16896
t_LKASlewY_NmpSec_u4p12[0]	11469
t_LKASlewY_NmpSec_u4p12[1]	11878
t_LKASlewY_NmpSec_u4p12[2]	12288
t_LKASlewY_NmpSec_u4p12[3]	12698
t_LKASlewY_NmpSec_u4p12[4]	13107
t_LKASlewY_NmpSec_u4p12[5]	13517
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	
was the call by traditional faulthickness south fillinguith signal all US	_n 0.00000002
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	<u>f</u> 32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt	_lgc		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e	num		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliver	red_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_t	f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	arget_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicles	Speed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	lm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_L	Jls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	m_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977			
target_TrqArblim_Per1_ESCState_State_enum.value	3			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-4.98000002			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	3.2999995			
target_TrqArblim_Per1_LKAState_State_enum.value	3			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.942993			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.70000005			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.25999999			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.308000028	0.307999998 ± 0000390625	•	
TrqArblim_ESC_State_Cnt_M_enum	3	3	·	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6.44407988	6.44407988 ± 0.03125	-	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.19999981	-5.19999981 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.39340019	5.39340019 ± 0.03125	-	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.960578859	0.960578978 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.960578859	0.960578978 ± 0000390625	•	
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	•	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.20498994	-0.20499 ± 0000390625	✓	
torget TraArblim Port PoturnDDEcetor IIIe f22 value	0.000570050	0.000570070 + 0000300035		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	-

0.960578859

0.960578978 ± 0000390625

Test Step 3.30 (Repeat Count = 1)	√
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.386999995
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.82999992
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0658000037
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.71999979
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.9000001
k_APAEnableRate_pSec_f32	132
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	11878
t_APADisableRateX_HwNm_u4p12[1]	12288
t_APADisableRateX_HwNm_u4p12[2]	12698
t_APADisableRateX_HwNm_u4p12[3]	13107
t_APADisableRateX_HwNm_u4p12[4]	13517

2014-09-16, 12:30:22+0530





TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
APAPORASAFIANT See USPICE 1930	Name	Input Value		
APADROSSEPTION 500 1997 1998 1999 1998 1999	t APADisableRateX HwNm u4p12[5]	•		
APACRESCHAPT See _ princ 1982				
LAPACROMANTANT_SERV_JUP[5]				
APA-Blackbarkary See, project 1986				
LAPACeschalary Sec. 1978(4) 1976 197				
JAMES JAME				
APARSmooth, Use LgoP4(9)				
LAPASmoothy, Us. got/40 979 LAPASmoothy, Us. got/40 974				
LAPASmooth Us u294(5)				
PAPASmentott, Us. 129-149				
PAPASmonthy Us. 129-146 5071				
PAPASSONONX, Use Judy 149 574 PAPASSONONX, Use Judy 149 574 PAPASSONONX, Use Judy 149 520 PAPASSONONY, U				
PAPASSHOOKY, Us. p.20149				
LAPASmonthX_UB_x2P149 6002 LAPASmonthX_UB_x2P149 6026 LAPASmonthX_UB_x2P149 6026 LAPASmonthY_UB_x2P149 602				
CAPASSMONTAL UB_1267449 6926 CAPASSMONTAL UB_1267449 1906 CAPASSMONTAL UB_1267449 1906 CAPASSMONTAL UB_1267449 2130 CAPASSMONTAL UB_1267449 2430 CAPASSMONTAL UB_1267449 243	t_APASmoothX_UIs_u2p14[6]			
CAPASIMONIX_UB_UZ014 1	t_APASmoothX_Uls_u2p14[7]	5898		
LPAPSmoothY_UB_u20419 1902 190	t_APASmoothX_Uls_u2p14[8]	6062		
LPAPSmoothy Usi, spa14 1986 1987 1	t_APASmoothX_Uls_u2p14[9]	6226		
LPAPS/Senderty_Lills_Lip2ntq2 2396	t_APASmoothY_Uls_u2p14[0]	1802		
LAPASmonthy_Uis_upt=14	t_APASmoothY_Uls_u2p14[1]	1966		
LAPASmonthy Usu Logis 4 5 2498 251 2745	t_APASmoothY_Uls_u2p14[2]	2130		
LAPASmonthy_Us_up2rt4 5	t_APASmoothY_Uls_u2p14[3]	2294		
LAPASmonty_Ub_up_14[6] 286 LAPASmonty_Ub_up_14[7] 284 LAPASmonty_Ub_up_14[7] 287		2458		
LAPASmonthY Usu _gat 4 5 LLKASiewi, Ken, usp8(5) LLKASiewi, Ken, usp8(6) LLKASiewi, Ke				
APASmonthY Us u20+147 LPASmonthY Us u20+149 LPASMONTHY U20+				
LPASSnoothY_US_u20*149 LPASSNoothY_US_u				
LPASBWK_Not_ubsp8()				
LIKASBew/K Kph_u8p81 15892 1587				
LLKASiewX, Kpp, u8p8[2] 15874 15				
LKASsewX, Kpp, u8p8[2] 15872 15886 158				
LKASIewX_Kpt_uep8[4] 18384				
LKASlewX, Kph_up8[4]				
LIKASIEWY_NmpSe_ushp120				
LKASiewY NmpSec_u4p12[0]				
LLKASlewY_NmpSec_u4p12t 12288 12898 1				
LKASlewY NmpSec_u4p12 7 12698 13107 131517 131				
LKASlewY NmpSec_u4p12 3 13107 LKASlewY NmpSec_u4p12 4 13517 13926				
LLKASlewY_NmpSec_u4p124 13517 13526 13026 1				
LikASlewY.NmpSec_u4p12[5] target.Re_Call.Ap_TrqArblim.Faultinjection_SCom_Fithjection_SignalPath_Us_f. target.Re_Call.Ap_TrqArblim.TrqArblim.Per1_AssistDDFactor_Uls_f32 target.Re_Call.Ap_TrqArblim.TrqArblim.Per1_AssistDDFactor_Uls_f32 target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_BampingDDFactor_Uls_f32 target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_ESCIS.Imited_Cnt_Ug_c target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_EMCONDON_Collate_State_enum target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_EMCONDON_Collate_State_enum target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_LKAC.Md_HwNm_f32 target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_LKAC.Md_HwNm_f32 target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_LKAC.Md_HwNm_f32 target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_LKAC.TrqueDellevered.HwNm_f32 target.TrqArblim.Per1_KAC.TrqueDellevered.HwNm_f32 target.Re_Inst.Ap_TrqArblim.TrqArblim.Per1_OpTrqov_MtrNm_f32 target.TrqArblim.Per1_Norque.MtrNm_f32 target.TrqArblim.Per1_Norque.MtrNm_f32 target.TrqArblim.Per1_PosSrovCond_MtrNm_f32 target.TrqArblim.Per				
target_Rie_Call_Ap_TqArblim_Faultinjection_Scom_Fitinjection_SignalPath_Uis_f. 10.1000004 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_AssistDDFactor_Uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCCmd_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCCmd_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCStalt_State_enum target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCState_Pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCState_Pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCState_Pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCState_Pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCTorqueDelivered_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_HwTorque_HwWn_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_LRAState_pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_LRAState_pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_LRAState_pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_LRAState_pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_LRAState_pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_LRAState_pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_LRAState_pathen_uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_DoTrqov_Minn_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSevEnable_Cnt_Igo_target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSevEnable_Cnt_Igo_target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSevEnable_Cnt_Uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSevEnable_Cnt_Uis_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Perl_PosSevEnable_Cnt_Uis_f32 target_TrqArblim_Perl_PosSevEnable_Cnt_Uis_f32 target_TrqArblim_Perl_PosSevEnable_Cnt_Uis_f32 target_TrqArblim_Perl_PosSevEnable_Cnt_Uis_f32 target_TrqArblim_Perl_PosSevEnable_Cnt_Uis_f32 target_TrqArblim_Perl_Ret_MOSHOscillate_State_enum_value target_TrqArblim_Perl_Ret_MOSHOscillate_State_en				
target_Rie_Inst_Ap_TrqArbilm_TrqArbilm_Per1_AssistDFactor_Uis_f32 target_Rie_Inst_Ap_TrqArbilm_Per1_ESCEM_Per1_SCEM_FIRE_Inst_Ap_TrqArbilm_Per1_ESCEM_Per1_SCEM_FIRE_Inst_Ap_TrqArbilm_Per1_ESCEM_Per1_SCEM_FIRE_Inst_Ap_TrqArbilm_TrqArbilm_Per1_ESCEM_Per1_SCEM_FIRE_Inst_Ap_TrqArbilm_TrqArbilm_Per1_ESCEM_FIRE_Inst_Ap_TrqArbilm_TrqArbilm_Per1_ESCES_Inst_Bet_Ap_TrqArbilm_TrqArbilm_Per1_ESCES_Inst_Bet_Ap_TrqArbilm_TrqArbilm_Per1_ESCES_Inst_Bet_Ap_TrqArbilm_TrqArbilm_Per1_ESCES_Inst_Bet_Ap_TrqArbilm_TrqArbilm_Per1_ESCES_Inst_Bet_Ap_TrqArbilm_TrqArbilm_Per1_ESCES_Inst_Bet_Ap_TrqArbilm_TrqArbilm_Per1_ENCOS_Inst_Bet_Ap_TrqArbilm_TrqArbilm_Per1_HV07que_HvNm_f32 target_Rie_Inst_Ap_TrqArbilm_TrqArbilm_Per1_HV07que_HvNm_f32 target_Rie_Inst_Ap_TrqArbilm_TrqArbilm_Per1_HV07que_HvNm_f32 target_Rie_Inst_Ap_TrqArbilm_TrqArbilm_Per1_HV07que_HvNm_f32 target_Rie_Inst_Ap_TrqArbilm_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_HVASA_Inst_Bet_Ap_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_Rie_Inst_Ap_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_Rie_Inst_Ap_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCmd_MVNm_f32 target_TrqArbilm_Per1_PosSrvoCm				
target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_DampingDDFactor_Uls_f32 target_Re_Inst_Ap_TrqArblim_Pert_ESCCmd_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCCLemIdet_Cnt_Igc target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCSLemIdet_Cnt_Igc target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCSLemIdet_Cnt_Igc target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCSCate_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCTorqueDelivered_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCTorqueDelivered_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_HSCTORqueDelivered_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_HWTorque_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_IngTrqOv_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ILKACCM_HwNm_f32 target_TrqArblim_Pert_IngTrqOv_HwNm_f32 target_TrqArblim_Pert_IngTrqOv_HwNm_f32 target_TrqArblim_Pert_IngTrqOv_HwNm_f32 target_TrqArblim_Pert_ILKACCM_HwNm_f32 target_TrqArblim_Pert_INGACCM_HwNm_f32 target_TrqArblim_Pert_IN	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	10.1000004		
target_Rte_Inst_Ap_TrqArblim_Pert_ESCCmd_HwNm_f32 target_TrqArblim_Pert_ESCIstlmited_Cnt_Igo target_Tte_Inst_Ap_TrqArblim_Pert_ESCIstlmited_Cnt_Igo target_Tte_Inst_Ap_TrqArblim_Pert_ESCIstlate_enum target_TtrqArblim_Pert_ESCIstlate_State_enum target_TtrqArblim_Pert_ESCIstlate_State_enum target_TtrqArblim_Pert_ESCIstlate_State_enum target_TtrqArblim_Pert_ESCTorqueDelivered_HwNm_f32 target_TtrqArblim_Part_Aproxim_TrqArblim_Pert_ESCTorqueDelivered_HwNm_f32 target_TtrqArblim_Part_Aproxim_TrqArblim_Pert_HwTorque_HwNm_f32 target_TtrqArblim_Part_Aproxim_Part_IntqArblim_Pert_LitrqOv_HwNm_f32 target_TtrqArblim_Part_Aproxim_Part_IntqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_Aproxim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_TrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_LitrqOv_HwNm_f32 target_TtrqArblim_Part_IntqOv_HwNm_f32	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Re_Inst_Ap_TrqArblim_Pert_ESCIsLimited_Cnt_lgc target_Re_Inst_Ap_TrqArblim_Pert_ESCIsLimited_Cnt_lgc target_Re_Inst_Ap_TrqArblim_Pert_ESCIstate_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCIstate_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCIstate_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_LESCIorque_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_HwTorque_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_LIMTQvie_HwNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSrvcCmd_MtrNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSrvcCmd_MtrNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_PosSrvcCmd_MtrNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_ReturnDDFactor_Us_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Pert_TrqOsCcmd_MtrNm_f32 target_TrqArblim_Pert_ReturnDDFactor_Us_f32 target_TrqArblim_Pert_ReturnDDFactor_Us_f32 target_TrqArblim_Pert_ReturnDDFactor_Us_f32 target_TrqArblim_Pert_TrqOsCcmd_MtrNm_f32 target_TrqArblim_Pert_TrqOsC	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_I	Jls_f32	
target_Rte_Inst_Ap_TrqArbitm_Per1_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_Per1_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_Per1_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_Per1_ENCTOrqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_Per1_Mr0rque_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_Per1_Mr0rque_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_Per1_Qttrq0v_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_Per1_Qttrq0v_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_Per1_UKACTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_LKACTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_LKACTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_Mr0rque_HwNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_DoServeEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_DoServeCnd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_DoServeCnd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_PoServeCnd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_PoServeCnd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_PoServeCnd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_TrqOseCnd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_TrqOseCnd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbitm_TrqArbitm_Per1_TrqOseCnd_MtrNm_f32 target_TrqArbitm_Per1_ESCCTate_State_enum_value target_TrqArbitm_Per1_ESCCTate_State_enum_value target_TrqArbitm_Per1_ESCCTate_State_enum_value target_TrqArbitm_Per1_HwTorque_HwNm_f32_value target_TrqArbitm_Per1_HwTorque_HwNm_f32_value target_TrqArbitm_Per1_PoserveCnd_MtrNm_f32_value target_TrqArbit	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim_Per1_GNOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_GNOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_GNOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_HgTqov_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_LKATorque_Inverd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_Per1_More_VehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_Per1_More_VehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_Per1_SoSvord_MthNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_SoSvord_MthNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSvord_MthNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_SoSvord_MthNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCMd_HwNm_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCMd_HwNm_f32.value target_TrqArblim_Per1_ECSCState_State_enum_value 4 target_TrqArblim_Per1_ESCCMd_HwNm_f32.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_value 15 target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_value 16 target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_value 17 target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_value 18 target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_value 18 target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_value 19 target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_value 15 target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_value 15 target_TrqArblim_Per1_PosSevotenable_Cnt_Igc_va	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_le	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_GMOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACnd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACnd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACnd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACnd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACnd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_OFTqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_PosServCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_PosServCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_PosServCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_TrqOscCmd_MtrNm_f32 target_TrqArblim_Perl_ESCCMd_HwNm_f32.value target_TrqArblim_Perl_ESCCMd_HwNm_f32.value target_TrqArblim_Perl_HSCCMd_HwNm_f32.value target_TrqArblim_Perl_HWNm_f32.value target_TrqArblim_Perl_HKAState_State_enum_value target_TrqArblim_Perl_HKAState_State_enum_value target_TrqArblim_Perl_HKAState_State_enum_value target_TrqArblim_Perl_PosServCmd_MtrNm_f32.value target_TrqArblim_Perl_PosServCmd_MtrNm_f32.value target_TrqArblim_Perl_PosServCmd_MtrNm_f32.value target_TrqArblim_Perl_PosServCmd_MtrNm_f32.value target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Perl_PosServCmd_MtrNm_f32.value target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Perl_PosServCmd_MtrNm_f32.value target_TrqArblim_Perl_DosServCmd_MtrNm_f32.value targe	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSevEnable_Cnt_Igc target_TrqArblim_Per1_PosSevChable_Cnt_Igc target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_PosSevCand_MtrNm_f32 target_TrqArblim_Per1_RetumDDFactor_UIs_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_MtrQDFactor_UIs_f32 target_TrqArblim_Per1_MtrQDFactor_	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TeqArblim_Per1_IqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_Delivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PoSsevEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PossevComd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIS_f32 target_TrqArblim_Per1_ESCSCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_state_enum_value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKACstate_State_enum_value target_TrqArblim_Per1_LKACstate_State_enum_value target_TrqArblim_Per1_LKACstate_State_enum_value target_TrqArblim_Per1_LKACstate_State_enum_value target_TrqArblim_Per1_LKACstate_State_enum_value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSevEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSevEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSevEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSevEnable_Cnt_Igc.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.value target_TrqArblim_Per1_DesSevEnable_Cnt_Igc.	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc target_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOsComd_MtrNm_f32 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOsComd_MtrNm_f32 target_TrqArblim_Per1_Destro_Uls_f32 target_TrqArblim_Per1_EscComd_HwNm_f32.value -3.75999999 target_TrqArblim_Per1_LWTqrue_HwNm_f32.value 4 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 44 target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value 1 target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value 1 target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value 1	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_UKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosservEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosservCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_EscCond_HwNm_f32_value target_TrqArblim_Per1_EscCond_HwNm_f32_value target_TrqArblim_Per1_EscCond_HwNm_f32_value target_TrqArblim_Per1_HwTorque_HwNm_f32_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKACmd_HwNm_f32_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_PosservEnable_Cnt_Igc.value 154.393005 target_TrqArblim_Per1_PosservEnable_Cnt_Igc.value 154.393005 target_TrqArblim_Per1_PosservEnable_Cnt_Igc.value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 1.61000001 Name Actual Value Expected Value TrqArblim_APADisableRate_Uls_M_f32 Trq	target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Perl_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PoSservEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PossrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_ReturnDDFactor_Uls_f32 target_TrqArblim_Perl_ESCCMd_HwNm_f32_value target_TrqArblim_Perl_SoSrvoCmd_MtrNm_f32 target_TrqArblim_Perl_ESCState_State_enum_value target_TrqArblim_Perl_SoSrvoCmd_MtrNm_f32_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_LKACmd_HwNm_f32_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_PossrvoCmd_MtrNm_f32_value target_TrqArblim_Perl_PossrvoCmd_MtrNm_f32_value target_TrqArblim_Perl_PossrvoCmd_MtrNm_f32_value target_TrqArblim_Perl_PossrvoCmd_MtrNm_f32_value target_TrqArblim_Perl_PossrvoCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOssCcmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_DrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_DrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_TrqOsscCmd_MtrNm_f32_value target_TrqArblim_Perl_DrqOsscCmd_MtrNm_f32_v				
target_Rte_Inst_Ap_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_Per1_OPTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OPTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OPTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_HwTorque_HwNm_f32_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArbl				
target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_Per1_PosServOcM_MtrNm_f32 target_TrqArblim_Per1_PerServOcM_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCMd_HwNm_f32_value -3.75999999 target_TrqArblim_Per1_ESCState_State_enum.value 4 target_TrqArblim_Per1_HwTorque_HwNm_f32_value -3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServCend_MtrNm_f32_value 1 target_TrqArblim_Per1_PosServCend_MtrNm_f32_value 1.5 target_TrqArblim_Per1_PosServCend_MtrNm_f32_value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32_value 1.5 target_TrqArblim_Per1_PosServCend_MtrNm_f				
target_Rte_Inst_Ap_TrqArblim.TrqArblim.Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim.Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim.Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim.Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim.Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim.Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim.Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim.Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_EsCCmd_HwNm_f32.value -3.75999999 target_TrqArblim_Per1_EsCState_State_enum.value 4 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 4 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 4.36999989 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 4.36999989 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 3.651000023 0.651000023 ± 0.061000023 ± 0.000390625 TrqArblim_Per1_MvTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 7.0272398				
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_Rqbtim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCMd_HwNm_f32.value -3.75999999 target_TrqArblim_Per1_ESCState_State_enum.value 4 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 1 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServeEnable_Cnt_Igc.value 1 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0.03125 0.03125	•			
target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCCstate_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_state_enum.value 4 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_NinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0.0651000023 ± 0.000390625 TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 7.0272398				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value -3.75999999 target_TrqArblim_Per1_ESCState_State_enum.value 4 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 1 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 1 target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_UIs_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 7.0272398 7.0272398 ± 0.03125 7.0272398 ± 0.03125				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value -3.75999999 target_TrqArblim_Per1_ESCState_State_enum.value 4 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 1 target_TrqArblim_Per1_HwTorque_HwNm_f32.value -4.36999989 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125			_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value -3.75999999 target_TrqArblim_Per1_ESCState_State_enum.value 4 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 1 target_TrqArblim_Per1_HwTorque_HwNm_f32.value -4.36999989 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0.000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125	· · · · ·		_	
target_TrqArblim_Per1_ESCState_State_enum.value 4 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 1 target_TrqArblim_Per1_HwTorque_HwNm_f32.value -4.36999989 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0.000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 1 target_TrqArblim_Per1_HwTorque_HwNm_f32.value -4.3699989 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
target_TrqArblim_Per1_HwTorque_HwNm_f32.value -4.36999989 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
target_TrqArblim_Per1_LKACmd_HwNm_f32.value 3.6500001 target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
target_TrqArblim_Per1_LKAState_State_enum.value 4 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 154.393005 target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125	· · · · · · · · · · · · · · · · · · ·			
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value -1.5 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 1.61000001 Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
Name Actual Value Expected Value Res TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 4 TrqArblim_ESC_State_Cnt_M_enum 4 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125				
TrqArblim_APADisableRate_Uls_M_f32 0.651000023 0.651000023 ± 0000390625 TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 7.0272398 7.0272398 ± 0.03125	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.61000001		
TrqArblim_ESC_State_Cnt_M_enum 4 4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 7.0272398 7.0272398 ± 0.03125	Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 7.0272398 7.0272398 ± 0.03125	TrqArblim_APADisableRate_Uls_M_f32	0.651000023	0.651000023 ± 0000390625	~
	TrqArblim_ESC_State_Cnt_M_enum	4	4	~
				~
11941011111 POSSIVOUTIO MITINITI M 132 1-1.5 1-1	TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.5	-1.5 ± 0.000244140625	V

TrqArblim_Per1





Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.89101171	5.89100981 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.41468811	1.41469002 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

lame	Input Value
te_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
te_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.40000006
rgArblim ESC State Cnt M enum	4
rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.43999958
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0680000037
rqArblim_PosSrvoCmd_MtrNm_M_f32	-4.23999977
rqArblim_PreLKATrqReq_HwNm_M_f32	6.4000001
_APAEnableRate_pSec_f32	136.5
_APAUseADmpScale_lgc	1
APAUseAsstScale Igc	1
APAUseRetScale lgc	1
	3.24000001
_ESCMax_HwNm_f32	3.24000001
_LKAUseSlewCal_lgc	12288
APADisableRateX_HwNm_u4p12[0]	
APADisableRateX_HwNm_u4p12[1]	12698
APADisableRateX_HwNm_u4p12[2]	13107
APADisableRateX_HwNm_u4p12[3]	13517
APADisableRateX_HwNm_u4p12[4]	13926
_APADisableRateX_HwNm_u4p12[5]	14336
APADisableRateY_pSec_u7p9[0]	15360
_APADisableRateY_pSec_u7p9[1]	15872
APADisableRateY_pSec_u7p9[2]	16384
_APADisableRateY_pSec_u7p9[3]	16896
APADisableRateY_pSec_u7p9[4]	17408
APADisableRateY_pSec_u7p9[5]	17920
_APASmoothX_Uls_u2p14[0]	4915
_APASmoothX_Uls_u2p14[1]	5079
APASmoothX_Uls_u2p14[2]	5243
_APASmoothX_Uls_u2p14[3]	5407
_APASmoothX_Uls_u2p14[4]	5571
APASmoothX_Uls_u2p14[5]	5734
_APASmoothX_Uls_u2p14[6]	5898
_APASmoothX_Uls_u2p14[7]	6062
_APASmoothX_Uls_u2p14[8]	6226
_APASmoothX_Uls_u2p14[9]	6390
_APASmoothY_Uls_u2p14[0]	1966
APASmoothY_Uls_u2p14[1]	2130
_APASmoothY_Uls_u2p14[2]	2294
_APASmoothY_Uls_u2p14[3]	2458
APASmoothY_Uls_u2p14[4]	2621
APASmoothY_Uls_u2p14[5]	2785
APASmoothY_Uls_u2p14[6]	2949
APASmoothY_Uls_u2p14[7]	3113
APASmoothY_Uls_u2p14[8]	3277
APASmoothY_Uls_u2p14[9]	3441
LKASlewX Kph u8p8[0]	15360
LKASlewX_Kph_u8p8[1]	15872
_txsciewx_tpi_dopc[1] _txsciewx_Kph_u8p8[2]	16384
LKASlewX Kph u8p8[3]	16896
_txasiewx_kpir_usps[4]	17408

2014-09-16, 12:30:22+0530





		•	
Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	17920		
t_LKASlewY_NmpSec_u4p12[0]	12288		
t LKASlewY NmpSec u4p12[1]	12698		
t_LKASlewY_NmpSec_u4p12[2]	13107		
t LKASlewY NmpSec u4p12[3]	13517		
t LKASlewY NmpSec u4p12[4]	13926		
t LKASlewY NmpSec u4p12[5]	14336		
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f			
target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor Uls f32	target TrqArblim Per1 AssistDDFactor Uls	i f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target TrqArblim Per1 ESCTorqueDelivere		
target Rte Inst Ap TrgArblim.TrgArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate S		
target Rte Inst Ap TrgArblim.TrgArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
rarget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	_	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	<u>f32</u>	
arget_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.82000017		
arget_TrqArblim_Per1_ESCState_State_enum.value	0		
arget_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	-3.75999999		
arget_TrqArblim_Per1_LKACmd_HwNm_f32.value	4		
arget_TrqArblim_Per1_LKAState_State_enum.value	1		
arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	160.843002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
arget_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.29999995		
arget_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.96000004		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.330000013	0.330000013 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	0	0	
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	7.61039972	7.6104002 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	-4.23999977	-4.23999977 ± 0.000244140625	
FrgArblim_PreLKATrgReg_HwNm_M_f32	6.39300013	6.39300013 ± 0.03125	
arget TrgArblim Per1 AssistDDFactor Uls f32.value	0.950512052	0.950511992 ± 0000390625	
arget TrgArblim Per1 DampingDDFactor Uls f32.value	0.950512052	0.950511992 ± 0000390625	
arget TrgArblim Per1 ESCIsLimited Cnt Igc.value	0	0	
rarget_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrgArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	
target TrqArblim Per1 LKATorqueDelivered HwNm f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.209828764	-0.209829003 ± 0000390625	
target_riqualini_r or _opriqov_ivitatii_ioz.valac	0.0000000	0.050544000 + 0000000005	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	-

0.950512052

0.950511992 ± 0000390625

Test Step 3.32 (Repeat Count = 1)		
Name	Input Value	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.412999988	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.05000019	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0702000037	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.75999999	
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.9000001	
k_APAEnableRate_pSec_f32	141	

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	12698
t_APADisableRateX_HwNm_u4p12[1]	13107
t_APADisableRateX_HwNm_u4p12[2] t_APADisableRateX_HwNm_u4p12[3]	13517 13926
t_APADisableRateX_HwNm_u4p12[4]	14336
t_APADisableRateX_HwNm_u4p12[5]	14746
t_APADisableRateY_pSec_u7p9[0]	15872
t_APADisableRateY_pSec_u7p9[1]	16384
t_APADisableRateY_pSec_u7p9[2]	16896
t_APADisableRateY_pSec_u7p9[3]	17408
t_APADisableRateY_pSec_u7p9[4]	17920
t_APADisableRateY_pSec_u7p9[5]	18432
t_APASmoothX_Uls_u2p14[0] t_APASmoothX_Uls_u2p14[1]	5079 5243
t_APASmoothX_Uls_u2p14[1]	5407
t_APASmoothX_Uls_u2p14[3]	5571
t_APASmoothX_Uls_u2p14[4]	5734
t_APASmoothX_Uls_u2p14[5]	5898
t_APASmoothX_Uls_u2p14[6]	6062
t_APASmoothX_Uls_u2p14[7]	6226
t_APASmoothX_UIs_u2p14[8]	6390
t_APASmoothX_UIs_u2p14[9]	6554
t_APASmoothY_Uls_u2p14[0]	2130
t_APASmoothY_Uls_u2p14[1]	2294 2458
t_APASmoothY_Uls_u2p14[2] t_APASmoothY_Uls_u2p14[3]	2621
t_APASmoothY_Uls_u2p14[4]	2785
t_APASmoothY_Uls_u2p14[5]	2949
t_APASmoothY_Uls_u2p14[6]	3113
t_APASmoothY_Uls_u2p14[7]	3277
t_APASmoothY_Uls_u2p14[8]	3441
t_APASmoothY_Uls_u2p14[9]	3604
t_LKASlewX_Kph_u8p8[0]	15872
t_LKASlewX_Kph_u8p8[1] t_LKASlewX_Kph_u8p8[2]	16384 16896
t_LKASlewX_Kph_u8p8[3]	17408
t LKASlewX Kph u8p8[4]	17920
t_LKASlewX_Kph_u8p8[5]	18432
t_LKASlewY_NmpSec_u4p12[0]	12698
t_LKASlewY_NmpSec_u4p12[1]	13107
t_LKASlewY_NmpSec_u4p12[2]	13517
t_LKASlewY_NmpSec_u4p12[3]	13926
t_LKASlewY_NmpSec_u4p12[4]	14336
t_LKASlewY_NmpSec_u4p12[5] target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f:	14746 2
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989
target_TrqArblim_Per1_ESCState_State_enum.value	1
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-3.1500001
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.3499999





Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.10000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.30999994		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.694999993	0.694999993 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.19355965	8.19355965 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.10000002	-1.10000002 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.89251661	6.89251995 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.58000004	-1.58000004 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.58000004	-1.58000004 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.14183235	2.14182997 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.33 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.425999999
FrqArblim_ESC_State_Cnt_M_enum	1
FrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.65999985
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0724000037
FrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.27999997
FrqArblim_PreLKATrqReq_HwNm_M_f32	7.4000001
C_APAEnableRate_pSec_f32	145.5
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
c_ESCMax_HwNm_f32	4.28000021
_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	13107
_APADisableRateX_HwNm_u4p12[1]	13517
_APADisableRateX_HwNm_u4p12[2]	13926
_APADisableRateX_HwNm_u4p12[3]	14336
_APADisableRateX_HwNm_u4p12[4]	14746
_APADisableRateX_HwNm_u4p12[5]	15155
_APADisableRateY_pSec_u7p9[0]	16384
_APADisableRateY_pSec_u7p9[1]	16896
_APADisableRateY_pSec_u7p9[2]	17408
_APADisableRateY_pSec_u7p9[3]	17920
_APADisableRateY_pSec_u7p9[4]	18432
_APADisableRateY_pSec_u7p9[5]	18944
_APASmoothX_Uls_u2p14[0]	5243
_APASmoothX_Uls_u2p14[1]	5407
_APASmoothX_Uls_u2p14[2]	5571
_APASmoothX_Uls_u2p14[3]	5734
_APASmoothX_Uls_u2p14[4]	5898
_APASmoothX_Uls_u2p14[5]	6062
_APASmoothX_Uls_u2p14[6]	6226
_APASmoothX_Uls_u2p14[7]	6390
_APASmoothX_Uls_u2p14[8]	6554
_APASmoothX_Uls_u2p14[9]	6717
_APASmoothY_Uls_u2p14[0]	2294
_APASmoothY_Uls_u2p14[1]	2458
_APASmoothY_Uls_u2p14[2]	2621
_APASmoothY_Uls_u2p14[3]	2785
APASmoothY Uls u2p14[4]	2949

2014-09-16, 12:30:22+0530



TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[5]	3113		
t_APASmoothY_Uls_u2p14[6]	3277		
t_APASmoothY_Uls_u2p14[7]	3441		
t_APASmoothY_Uls_u2p14[8]	3604		
t_APASmoothY_Uls_u2p14[9]	3768		
t_LKASlewX_Kph_u8p8[0]	16384		
t_LKASlewX_Kph_u8p8[1]	16896		
t_LKASlewX_Kph_u8p8[2]	17408		
t_LKASlewX_Kph_u8p8[3]	17920		
t_LKASlewX_Kph_u8p8[4]	18432		
t_LKASlewX_Kph_u8p8[5]	18944		
t_LKASlewY_NmpSec_u4p12[0]	13107		
t_LKASlewY_NmpSec_u4p12[1]	13517		
t_LKASlewY_NmpSec_u4p12[2]	13926		
t_LKASlewY_NmpSec_u4p12[3]	14336		
t_LKASlewY_NmpSec_u4p12[4]	14746		
t_LKASlewY_NmpSec_u4p12[5]	15155		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f.		700	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArbling_Per1_ESCIsLimited_Cnt_I	•	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_Si	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_KATorqueDelivered_Hwini_is2 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_opTrqOv_MtrNm_f32	target_TrqArblim_Per1_MinSecureVehicleSp target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServErlable_Crit		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.42000008	: 	
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-2.53999996		
target TrqArblim Per1 LKACmd HwNm f32.value	4.69999981		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	173.742996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.899999976		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.66000009		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.351999998	0.351999998 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	2	2	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.77672005	8.77672005 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.27999997	-3.27999997 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.39260006	7.39260006 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.939457059	0.939456999 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.939457059	0.939456999 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4.28000021	-4.28000021 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.198580936	-0.198580995 ± 0000390625	~
	I a casa casa sa	T. Control of the Con	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

0.939457059

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

0.939456999 ± 0000390625



Test Step 3.34 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.43900001
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0746000037
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.79999995
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.9000001
k_APAEnableRate_pSec_f32	150
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	13517
t_APADisableRateX_HwNm_u4p12[1]	13926
t_APADisableRateX_HwNm_u4p12[2]	14336
t_APADisableRateX_HwNm_u4p12[3]	14746
t_APADisableRateX_HwNm_u4p12[4]	15155
t_APADisableRateX_HwNm_u4p12[5]	15565
t_APADisableRateY_pSec_u7p9[0]	16896
t_APADisableRateY_pSec_u7p9[1]	17408
t_APADisableRateY_pSec_u7p9[2]	17920
t_APADisableRateY_pSec_u7p9[3] t APADisableRateY pSec u7p9[4]	18432 18944
t_APADisableRateY_pSec_u7p9[4] t_APADisableRateY_pSec_u7p9[5]	19456
t_APASmoothX_Uls_u2p14[0]	5407
t_APASmoothX_Uls_u2p14[1]	5571
t_APASmoothX_Uls_u2p14[2]	5734
t_APASmoothX_Uls_u2p14[3]	5898
t_APASmoothX_Uls_u2p14[4]	6062
t_APASmoothX_Uls_u2p14[5]	6226
t_APASmoothX_Uls_u2p14[6]	6390
t_APASmoothX_UIs_u2p14[7]	6554
t_APASmoothX_Uls_u2p14[8]	6717
t_APASmoothX_Uls_u2p14[9]	6881
t_APASmoothY_Uls_u2p14[0]	2458
t_APASmoothY_Uls_u2p14[1]	2621
t_APASmoothY_Uls_u2p14[2]	2785
t_APASmoothY_Uls_u2p14[3]	2949
t_APASmoothY_Uls_u2p14[4]	3113
t_APASmoothY_Uls_u2p14[5]	3277
t_APASmoothY_Uls_u2p14[6]	3441
t_APASmoothY_Uls_u2p14[7]	3604
t_APASmoothY_Uls_u2p14[8]	3768
t_APASmoothY_Uls_u2p14[9]	3932
t_LKASlewX_Kph_u8p8[0]	16896
t_LKASlewX_Kph_u8p8[1]	17408
t_LKASlewX_Kph_u8p8[2]	17920
t_LKASlewX_Kph_u8p8[3]	18432
t_LKASlewX_Kph_u8p8[4]	18944
t_LKASlewX_Kph_u8p8[5]	19456
t_LKASlewY_NmpSec_u4p12[0]	13517
t_LKASlewY_NmpSec_u4p12[1]	13926
t_LKASlewY_NmpSec_u4p12[2]	14336
t_LKASlewY_NmpSec_u4p12[3]	14746
t_LKASlewY_NmpSec_u4p12[4]	15155
t_LKASlewY_NmpSec_u4p12[5]	15565
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath UIs	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_Enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph	n_f32 target_TrqArblim_Per	1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per	arget_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per	arget_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per	1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per	1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.21999979			
target_TrqArblim_Per1_ESCState_State_enum.value	3			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-1.92999995			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.05000019			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	180.192993			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.699999988			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.00999999			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.363000005	0.363000005 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	3	3	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.39797783	-9.39797974 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.79999995	-2.79999995 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.89400005	7.89400005 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.185984328	-0.185984001 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.35 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.451999992
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0768000036
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.31999993
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.39999962
k_APAEnableRate_pSec_f32	154.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.1400001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	13926
t_APADisableRateX_HwNm_u4p12[1]	14336
t_APADisableRateX_HwNm_u4p12[2]	14746
t_APADisableRateX_HwNm_u4p12[3]	15155
t_APADisableRateX_HwNm_u4p12[4]	15565
t_APADisableRateX_HwNm_u4p12[5]	15974
t_APADisableRateY_pSec_u7p9[0]	17408
t_APADisableRateY_pSec_u7p9[1]	17920
t_APADisableRateY_pSec_u7p9[2]	18432
t_APADisableRateY_pSec_u7p9[3]	18944
t_APADisableRateY_pSec_u7p9[4]	19456
t_APADisableRateY_pSec_u7p9[5]	19968
t_APASmoothX_Uls_u2p14[0]	5571
t_APASmoothX_Uls_u2p14[1]	5734
t_APASmoothX_Uls_u2p14[2]	5898
t_APASmoothX_Uls_u2p14[3]	6062





Name	Input Value		
t_APASmoothX_Uls_u2p14[4]	6226		
t_APASmoothX_Uls_u2p14[5]	6390		
t_APASmoothX_Uls_u2p14[6]	6554		
t_APASmoothX_Uls_u2p14[7]	6717		
t_APASmoothX_Uls_u2p14[8]	6881		
t_APASmoothX_Uls_u2p14[9]	7045		
t_APASmoothY_Uls_u2p14[0]	2621		
t_APASmoothY_Uls_u2p14[1]	2785		
t_APASmoothY_Uls_u2p14[2]	2949		
t_APASmoothY_Uls_u2p14[3]	3113		
t_APASmoothY_Uls_u2p14[4]	3277		
t_APASmoothY_Uls_u2p14[5]	3441		
t_APASmoothY_Uls_u2p14[6]	3604		
t_APASmoothY_Uls_u2p14[7]	3768		
t_APASmoothY_Uls_u2p14[8]	3932		
t_APASmoothY_Uls_u2p14[9]	4096		
t_LKASlewX_Kph_u8p8[0]	17408		
t_LKASlewX_Kph_u8p8[1]	17920		
t_LKASlewX_Kph_u8p8[2]	18432		
t_LKASlewX_Kph_u8p8[3]	18944		
t_LKASlewX_Kph_u8p8[4]	19456		
t_LKASlewX_Kph_u8p8[5]	19968		
t_LKASlewY_NmpSec_u4p12[0]	13926		
t_LKASlewY_NmpSec_u4p12[1]	14336		
t_LKASlewY_NmpSec_u4p12[2]	14746		
t_LKASlewY_NmpSec_u4p12[3]	15155		
t_LKASlewY_NmpSec_u4p12[4]	15565		
t_LKASlewY_NmpSec_u4p12[5]	15974		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	· -	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.01999998		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-1.32000005		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.4000001		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	186.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.3599999		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.761000037	0.760999978 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.13062382	9.13062 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.5	-0.5 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.39219952	8.39220047 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0951250046	-0.0951249972 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.36 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.465000004
FrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0790000036
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.84000003
FrqArblim_PreLKATrqReq_HwNm_M_f32	8.6999981
APAEnableRate_pSec_f32	159
APAUseADmpScale lgc	0
	0
sc_APAUseRetScale_lgc	0
C_ESCMax_HwNm_f32	2.57999992
CLKAUseSlewCal lgc	0
_APADisableRateX_HwNm_u4p12[0]	14336
_APADisableRateX_HwNm_u4p12[1]	14746
_APADisableRateX_HwNm_u4p12[2]	15155
_APADisableRateX_HwNm_u4p12[3]	15565
_APADisableRateX_HwNm_u4p12[4]	15974
_APADisableRateX_HwNm_u4p12[5]	16384
_APADisableRateY_pSec_u7p9[0]	17920
_APADisableRateY_pSec_u7p9[1]	18432
_APADisableRateY_pSec_u7p9[2]	18944
_APADisableRateY_pSec_u7p9[3]	19456
_APADisableRateY_pSec_u7p9[4]	19968
_APADisableRateY_pSec_u7p9[5]	20480
_APASmoothX_Uls_u2p14[0]	5734
_APASmoothX_Uls_u2p14[1]	5898
_APASmoothX_Uls_u2p14[2]	6062
_APASmoothX_UIs_u2p14[3]	6226
_APASmoothX_Uls_u2p14[4]	6390
_APASmoothX_Uls_u2p14[5]	6554
_APASmoothX_Uls_u2p14[6]	6717
_APASmoothX_Uls_u2p14[7]	6881
_APASmoothX_Uls_u2p14[8]	7045
_APASmoothX_Uls_u2p14[9]	7209
_APASmoothY_Uls_u2p14[0]	2785
_APASmoothY_Uls_u2p14[1]	2949
_APASmoothY_Uls_u2p14[2]	3113
_APASmoothY_Uls_u2p14[3]	3277
_APASmoothY_Uls_u2p14[4]	3441
_APASmoothY_Uls_u2p14[5]	3604
_APASmoothY_Uls_u2p14[6]	3768
_APASmoothY_Uls_u2p14[7]	3932
_APASmoothY_Uls_u2p14[8]	4096
_APASmoothY_Uls_u2p14[9]	4260
_LKASlewX_Kph_u8p8[0]	17920
_LKASlewX_Kph_u8p8[1]	18432
_LKASlewX_Kph_u8p8[2]	18944
_LKASlewX_Kph_u8p8[3]	19456
_LKASlewX_Kph_u8p8[4]	19968
_LKASlewX_Kph_u8p8[5]	20480
_LKASlewY_NmpSec_u4p12[0]	14336
_LKASlewY_NmpSec_u4p12[1]	14746
_LKASlewY_NmpSec_u4p12[2]	15155
LKASlewY_NmpSec_u4p12[3]	15565
_LKASlewY_NmpSec_u4p12[4]	15974
_LKASlewY_NmpSec_u4p12[5]	16384
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls	
	=11 5





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3.	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-0.709999979		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.75		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	193.093002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.300000012		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.4000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.782999992	0.782999992 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.056090001	-0.056090001 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.300000012	-0.300000012 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.69400024	8.69400024 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-4.46107626	-4.46108007 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

1 ± 0000390625

Test Step 3.37 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.477999985
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6.34000015
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0812000036
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.36000001
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8
k_APAEnableRate_pSec_f32	163.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	14746
t_APADisableRateX_HwNm_u4p12[1]	15155
t_APADisableRateX_HwNm_u4p12[2]	15565
t_APADisableRateX_HwNm_u4p12[3]	15974
t_APADisableRateX_HwNm_u4p12[4]	16384

2014-09-16, 12:30:22+0530



Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	16794		
t_APADisableRateY_pSec_u7p9[0]	18432		
t_APADisableRateY_pSec_u7p9[1]	18944		
t_APADisableRateY_pSec_u7p9[2]	19456		
t_APADisableRateY_pSec_u7p9[3]	19968		
t APADisableRateY pSec u7p9[4]	20480		
t_APADisableRateY_pSec_u7p9[5]	20992		
t_APASmoothX_Uls_u2p14[0]	5898		
t_APASmoothX_Uls_u2p14[1]	6062		
t_APASmoothX_Uls_u2p14[2]	6226		
t_APASmoothX_Uls_u2p14[3]	6390		
t_APASmoothX_Uls_u2p14[4]	6554		
t_APASmoothX_Uls_u2p14[5]	6717		
t_APASmoothX_Uls_u2p14[6]	6881		
t_APASmoothX_Uls_u2p14[7]	7045		
t_APASmoothX_Uls_u2p14[8]	7209		
t_APASmoothX_Uls_u2p14[9]	7373		
t_APASmoothY_Uls_u2p14[0]	2949		
t_APASmoothY_Uls_u2p14[1]	3113		
t_APASmoothY_Uls_u2p14[2]	3277		
t_APASmoothY_Uls_u2p14[3]	3441		
t_APASmoothY_Uls_u2p14[4]	3604		
t_APASmoothY_Uls_u2p14[5]	3768		
t_APASmoothY_Uls_u2p14[6]	3932		
t_APASmoothY_Uls_u2p14[7]	4096		
t_APASmoothY_Uls_u2p14[8]	4260		
t_APASmoothY_Uls_u2p14[9]	4424		
t_LKASlewX_Kph_u8p8[0]	18432		
t_LKASlewX_Kph_u8p8[1]	18944		
	19456		
t_LKASlewX_Kph_u8p8[2]			
t_LKASlewX_Kph_u8p8[3]	19968		
t_LKASlewX_Kph_u8p8[4]	20480		
t_LKASlewX_Kph_u8p8[5]	20992		
t_LKASlewY_NmpSec_u4p12[0]	14746		
t_LKASlewY_NmpSec_u4p12[1]	15155		
t_LKASlewY_NmpSec_u4p12[2]	15565		
t_LKASlewY_NmpSec_u4p12[3]	15974		
t_LKASlewY_NmpSec_u4p12[4]	16384		
t_LKASlewY_NmpSec_u4p12[5]	16794		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_fltInje$	7		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_0	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate St	ate enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target Rte Inst Ap TrgArblim.TrgArblim Per1 IqTrgOv HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target Rte Inst Ap TrgArblim.TrgArblim Per1 LKACmd HwNm f32	target TrgArblim Per1 LKACmd HwNm f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
	target_IrqArblim_Per1_Op1rqOv_mtrNm_13 target TrqArblim Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_132	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-0.620000005		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-0.100000001		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.0999999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	199.542999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.100000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.395999968	0.395999998 ± 0000390625	-
TrgArblim ESC State Cnt M enum	1	1	-
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.81707191	5.81707001 ± 0.03125	-
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.36000001	-1.36000001 ± 0.000244140625	
TTQ/TDIITI_F 0301 VOOTIIU_IVILIIVIII_IVI_I32	-1.0000001	-1.50000001 ± 0.000244140023	





Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.99179983	-7.99179983 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.23999995	-0.239999995 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.23999995	-0.239999995 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.116297916	-0.116297998 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

lame	Input Value
te_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
te_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.490999997
rgArblim ESC State Cnt M enum	1
rqArblim_Esc_state_cnr_m_enum rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.86000013
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0834000036
rqArblim_PosSrvoCmd_MtrNm_M_f32	-0.879999995
rqArblim_PreLKATrqReq_HwNm_M_f32	-7
_APAEnableRate_pSec_f32	168
APAUseADmpScale lgc	0
	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	
_ESCMax_HwNm_f32	0.140000001
_LKAUseSlewCal_lgc	0
APADisableRateX_HwNm_u4p12[0]	15155
APADisableRateX_HwNm_u4p12[1]	15565
APADisableRateX_HwNm_u4p12[2]	15974
_APADisableRateX_HwNm_u4p12[3]	16384
_APADisableRateX_HwNm_u4p12[4]	16794
_APADisableRateX_HwNm_u4p12[5]	17203
APADisableRateY_pSec_u7p9[0]	18944
_APADisableRateY_pSec_u7p9[1]	19456
_APADisableRateY_pSec_u7p9[2]	19968
_APADisableRateY_pSec_u7p9[3]	20480
_APADisableRateY_pSec_u7p9[4]	20992
_APADisableRateY_pSec_u7p9[5]	21504
_APASmoothX_Uls_u2p14[0]	6062
_APASmoothX_Uls_u2p14[1]	6226
_APASmoothX_Uls_u2p14[2]	6390
_APASmoothX_Uls_u2p14[3]	6554
_APASmoothX_Uls_u2p14[4]	6717
_APASmoothX_Uls_u2p14[5]	6881
APASmoothX_Uls_u2p14[6]	7045
APASmoothX_Uls_u2p14[7]	7209
APASmoothX_Uls_u2p14[8]	7373
APASmoothX_Uls_u2p14[9]	7537
APASmoothY_Uls_u2p14[0]	3113
_APASmoothY_Uls_u2p14[1]	3277
_APASmoothY_Uls_u2p14[2]	3441
_APASmoothY_Uls_u2p14[3]	3604
_APASmoothY_Uls_u2p14[4]	3768
_APASmoothY_Uls_u2p14[5]	3932
_APASmoothY_Uls_u2p14[6]	4096
_APASmoothY_Uls_u2p14[7]	4260
_APASmoothY_Uls_u2p14[8]	4424
_APASmoothY_Uls_u2p14[9]	4588
_LKASlewX_Kph_u8p8[0]	18944
LKASlewX_Kph_u8p8[1]	19456
LKASlewX_Kph_u8p8[2]	19968
LKASlewX_Kph_u8p8[3]	20480

2014-09-16, 12:30:22+0530



TrqArblim_Per1	5, 12:30:22+0530		Razorcat
Name	Input Value		
t_LKASlewX_Kph_u8p8[4]	20992		
t_LKASlewX_Kph_u8p8[5]	21504		
t LKASlewY NmpSec u4p12[0]	15155		
t LKASlewY NmpSec u4p12[1]	15565		
t LKASlewY NmpSec u4p12[2]	15974		
t_LKASlewY_NmpSec_u4p12[3]	16384		
t LKASlewY NmpSec u4p12[4]	16794		
t_LKASlewY_NmpSec_u4p12[5]	17203		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	8		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.50999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.44999981		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	205.992996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.100000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.407000005	0.407000005 ± 0000390625	· •
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.32874203	-5.32874012 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.879999995	-0.879999995 ± 0.00024414	1 0625 ✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.99399996	-6.99399996 ± 0.03125	-
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.140000001	0.140000001 ± 0000390625	· •
Annual Translation David In Translation (Constitution of Constitution of Const	0.4.40000004	0.440000004 + 0.000000000	05

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	•

0.140000001

-4.1812768

0.140000001 ± 0.0000390625

-4.18128014 ± 0000390625

0 ± 0.0000390625

1 ± 0000390625

Test Step 3.39 (Repeat Count = 1)		•
Name	Input Value	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.504000008	
TrqArblim_ESC_State_Cnt_M_enum	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.649999976	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.400000006	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6	
k_APAEnableRate_pSec_f32	172.5	

target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1.
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	15565
t_APADisableRateX_HwNm_u4p12[1]	15974
t_APADisableRateX_HwNm_u4p12[2]	16384
t_APADisableRateX_HwNm_u4p12[3]	16794
t_APADisableRateX_HwNm_u4p12[4]	17203
t_APADisableRateX_HwNm_u4p12[5]	17613 19456
t_APADisableRateY_pSec_u7p9[0] t_APADisableRateY_pSec_u7p9[1]	19968
t_APADisableRateY_pSec_u7p9[2]	20480
t_APADisableRateY_pSec_u7p9[3]	20992
t_APADisableRateY_pSec_u7p9[4]	21504
t_APADisableRateY_pSec_u7p9[5]	22016
t_APASmoothX_Uls_u2p14[0]	6226
t_APASmoothX_Uls_u2p14[1]	6390
t_APASmoothX_Uls_u2p14[2]	6554
t_APASmoothX_Uls_u2p14[3]	6717
t_APASmoothX_Uls_u2p14[4]	6881
t_APASmoothX_Uls_u2p14[5]	7045
t_APASmoothX_Uls_u2p14[6]	7209
t_APASmoothX_Uls_u2p14[7]	7373
t_APASmoothX_Uls_u2p14[8] t_APASmoothX_Uls_u2p14[9]	7537 7700
t_APASmoothY_Uls_u2p14[0]	3277
t_APASmoothY_UIs_u2p14[1]	3441
t_APASmoothY_Uls_u2p14[2]	3604
t_APASmoothY_Uls_u2p14[3]	3768
t_APASmoothY_Uls_u2p14[4]	3932
t_APASmoothY_Uls_u2p14[5]	4096
t_APASmoothY_Uls_u2p14[6]	4260
t_APASmoothY_Uls_u2p14[7]	4424
t_APASmoothY_Uls_u2p14[8]	4588
t_APASmoothY_Uls_u2p14[9]	4751
t_LKASlewX_Kph_u8p8[0]	19456
t_LKASlewX_Kph_u8p8[1]	19968
t_LKASlewX_Kph_u8p8[2] t LKASlewX Kph u8p8[3]	20480 20992
t_LKASlewX_Kph_u8p8[4]	21504
t LKASlewX Kph u8p8[5]	22016
t_LKASlewY_NmpSec_u4p12[0]	15565
t_LKASlewY_NmpSec_u4p12[1]	15974
t_LKASlewY_NmpSec_u4p12[2]	16384
t_LKASlewY_NmpSec_u4p12[3]	16794
t_LKASlewY_NmpSec_u4p12[4]	17203
t_LKASlewY_NmpSec_u4p12[5]	17613
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f:	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_132 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_ESCIstimiled_Crit_igc target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_Ont_igc target_TrqArblim_Per1_ESCState_State_enum
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target TrqArblim Per1 ESCTorqueDelivered HwNm f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 1.77999997
target_TrqArblim_Per1_ESCCritq_Hwntn_isz.value	3
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.12
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.80000019



Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	212.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.30000012		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.849000037	0.848999977 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.650590241	0.650590003 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.300000012	0.300000012 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.99139977	-5.99139977 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.0738574117	0.0738573968 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

Test Step 3.40 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.51700002
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.21000004
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390027
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.0799999982
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5
k_APAEnableRate_pSec_f32	177
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	15974
t_APADisableRateX_HwNm_u4p12[1]	16384
t_APADisableRateX_HwNm_u4p12[2]	16794
t_APADisableRateX_HwNm_u4p12[3]	17203
t_APADisableRateX_HwNm_u4p12[4]	17613
t_APADisableRateX_HwNm_u4p12[5]	18022
t_APADisableRateY_pSec_u7p9[0]	19968
t_APADisableRateY_pSec_u7p9[1]	20480
t_APADisableRateY_pSec_u7p9[2]	20992
t_APADisableRateY_pSec_u7p9[3]	21504
t_APADisableRateY_pSec_u7p9[4]	22016
t_APADisableRateY_pSec_u7p9[5]	22528
t_APASmoothX_Uls_u2p14[0]	6390
t_APASmoothX_Uls_u2p14[1]	6554
t_APASmoothX_Uls_u2p14[2]	6717
t_APASmoothX_Uls_u2p14[3]	6881
t_APASmoothX_Uls_u2p14[4]	7045
t_APASmoothX_Uls_u2p14[5]	7209
t_APASmoothX_Uls_u2p14[6]	7373
t_APASmoothX_Uls_u2p14[7]	7537
t_APASmoothX_Uls_u2p14[8]	7700
t_APASmoothX_Uls_u2p14[9]	7864
t_APASmoothY_Uls_u2p14[0]	3441
t_APASmoothY_Uls_u2p14[1]	3604
t_APASmoothY_Uls_u2p14[2]	3768
t_APASmoothY_Uls_u2p14[3]	3932

2014-09-16, 12:30:22+0530



TrqArblim_Per1

Name	Input Value			
t_APASmoothY_Uls_u2p14[4]	4096	4096		
t_APASmoothY_Uls_u2p14[5]	4260	4260		
t_APASmoothY_Uls_u2p14[6]	4424			
t_APASmoothY_Uls_u2p14[7]	4588	4588		
t_APASmoothY_Uls_u2p14[8]	4751			
t_APASmoothY_Uls_u2p14[9]	4915			
t_LKASlewX_Kph_u8p8[0]	19968			
t_LKASlewX_Kph_u8p8[1]	20480			
t_LKASlewX_Kph_u8p8[2]	20992			
t_LKASlewX_Kph_u8p8[3]	21504			
t_LKASlewX_Kph_u8p8[4]	22016			
t_LKASlewX_Kph_u8p8[5]	22528			
t_LKASlewY_NmpSec_u4p12[0]	15974			
t_LKASlewY_NmpSec_u4p12[1]	16384			
t_LKASlewY_NmpSec_u4p12[2]	16794			
t_LKASlewY_NmpSec_u4p12[3]	17203			
t_LKASlewY_NmpSec_u4p12[4]	17613			
t_LKASlewY_NmpSec_u4p12[5]	18022			
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_transfer = 0.0000000000000000000000000000000000$	f. 10			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	ium		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	n_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	2.98000002			
target_TrqArblim_Per1_ESCState_State_enum.value	4			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.73000002			
target TrgArblim Per1 LKACmd HwNm f32.value	7.1500001			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	218.893005			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.5			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.70000005			
Name	Actual Value	Expected Value	Result	
TrgArblim APADisableRate Uls M f32	0.43900001	0.43900001 ± 0000390625		
TrgArblim ESC State Cnt M enum	4	4		
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	1.58200288	1.58200002 ± 0.03125		
TrgArblim PosSrvoCmd MtrNm M f32	0.0799999982	0.0799999982 ± 0.000244140625		
TrgArblim PreLKATrgReg HwNm M f32	-4.99399996	-4.99399996 ± 0.03125		
target TrgArblim Per1 AssistDDFactor Uls f32.value	1	1 ± 0000390625		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625		
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0		
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	0	0 ± 0000390625		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625		
target TrqArblim Per1 OpTrqOv MtrNm f32.value	-3.69090486	-3.69090009 ± 0000390625		
target_TrgArblim_Per1_OptriqOV_MithIni_Joz.value	1	1 + 0000390625		

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

1 ± 0000390625



Test Step 3.41 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.529999971
TrgArblim ESC State Cnt M enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.76999998
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0500000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.560000002
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4
k_APAEnableRate_pSec_f32	181.5
k APAUseADmpScale Igc	1
k_APAUseAsstScale_lgc	1
k APAUseRetScale Igc	1
k_ESCMax_HwNm_f32	3.69000006
k LKAUseSlewCal lgc	1
t_APADisableRateX_HwNm_u4p12[0]	16384
t APADisableRateX_1WNm_u4p12[1]	16794
t_APADisableRateX_HwNm_u4p12[2]	17203
t APADisableRateX_1WNm_u4p12[3]	17613
	18022
t_APADisableRateX_HwNm_u4p12[4]	
t_APADisableRateX_HwNm_u4p12[5]	18432
t_APADisableRateY_pSec_u7p9[0]	20480
t_APADisableRateY_pSec_u7p9[1]	20992
t_APADisableRateY_pSec_u7p9[2]	21504
t_APADisableRateY_pSec_u7p9[3]	22016
t_APADisableRateY_pSec_u7p9[4]	22528
t_APADisableRateY_pSec_u7p9[5]	23040
t_APASmoothX_Uls_u2p14[0]	6554
t_APASmoothX_Uls_u2p14[1]	6717
t_APASmoothX_Uls_u2p14[2]	6881
t_APASmoothX_Uls_u2p14[3]	7045
t_APASmoothX_Uls_u2p14[4]	7209
t_APASmoothX_Uls_u2p14[5]	7373
t_APASmoothX_Uls_u2p14[6]	7537
t_APASmoothX_Uls_u2p14[7]	7700
t_APASmoothX_Uls_u2p14[8]	7864
t_APASmoothX_Uls_u2p14[9]	8028
t_APASmoothY_Uls_u2p14[0]	3604
t_APASmoothY_Uls_u2p14[1]	3768
t_APASmoothY_Uls_u2p14[2]	3932
t_APASmoothY_Uls_u2p14[3]	4096
t_APASmoothY_Uls_u2p14[4]	4260
t_APASmoothY_Uls_u2p14[5]	4424
t_APASmoothY_Uls_u2p14[6]	4588
t_APASmoothY_Uls_u2p14[7]	4751
t_APASmoothY_Uls_u2p14[8]	4915
t_APASmoothY_Uls_u2p14[9]	5079
t_LKASlewX_Kph_u8p8[0]	20480
t_LKASlewX_Kph_u8p8[1]	20992
t_LKASlewX_Kph_u8p8[2]	21504
t_LKASlewX_Kph_u8p8[3]	22016
t_LKASlewX_Kph_u8p8[4]	22528
t_LKASlewX_Kph_u8p8[5]	23040
t_LKASlewY_NmpSec_u4p12[0]	16384
t_LKASlewY_NmpSec_u4p12[1]	16794
t_LKASlewY_NmpSec_u4p12[2]	17203
t_LKASlewY_NmpSec_u4p12[3]	17613
t_LKASlewY_NmpSec_u4p12[4]	18022
t_LKASlewY_NmpSec_u4p12[5]	18432
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCIsLimited Cnt Igc	target TrqArblim Per1 ESCIsLimited Cnt Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_FeT_GWIOSFIOSCIIIate_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_FeT_HwT0rque_HwNtif_i32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNtif_i32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32



Name	Input Value			
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	· ·	arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target TrqArblim Per1 OpTrqOv MtrNm f3	arget_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cn	arget_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	n_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target TrqArblim Per1 TrqOscCmd MtrNm f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	4.17999983			
target_TrqArblim_Per1_ESCState_State_enum.value	0			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.33999991			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.5			
target_TrqArblim_Per1_LKAState_State_enum.value	4			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	225.343002			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.69999988			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.5			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.893000007	0.893000007 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	0	0	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.79849994	1.79849994 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.69999988	0.69999988 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.99099994	-3.99099994 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_UIs_f32.value	0.723172188	0.723172009 ± 0000390625	✓	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	~	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.193779469	0.193779007 ± 0000390625	~	
target TrqArblim Per1 ReturnDDFactor Uls f32.value	0.723172188	0.723172009 ± 0000390625	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	•

Test Step 3.42 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.32999992
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.023
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3
k_APAEnableRate_pSec_f32	186
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
<_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	16794
t_APADisableRateX_HwNm_u4p12[1]	17203
t_APADisableRateX_HwNm_u4p12[2]	17613
t_APADisableRateX_HwNm_u4p12[3]	18022
t_APADisableRateX_HwNm_u4p12[4]	18432
t_APADisableRateX_HwNm_u4p12[5]	18842
t_APADisableRateY_pSec_u7p9[0]	20992
t_APADisableRateY_pSec_u7p9[1]	21504
t_APADisableRateY_pSec_u7p9[2]	22016
t_APADisableRateY_pSec_u7p9[3]	22528
t_APADisableRateY_pSec_u7p9[4]	23040
t_APADisableRateY_pSec_u7p9[5]	23552
t_APASmoothX_Uls_u2p14[0]	6717
t_APASmoothX_Uls_u2p14[1]	6881
t_APASmoothX_Uls_u2p14[2]	7045





Name	Input Value			
t_APASmoothX_Uls_u2p14[3]	7209			
t_APASmoothX_Uls_u2p14[4]	7373			
t_APASmoothX_UIs_u2p14[5]	7537 7700			
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	7864			
t_APASmoothX_UIs_u2p14[8]	8028			
t_APASmoothX_Uls_u2p14[9]	8192			
t_APASmoothY_Uls_u2p14[0]	3768			
t_APASmoothY_Uls_u2p14[1]	3932			
t_APASmoothY_Uls_u2p14[2]	4096			
t_APASmoothY_Uls_u2p14[3]	4260			
t_APASmoothY_Uls_u2p14[4]	4424			
t_APASmoothY_Uls_u2p14[5]	4588			
t_APASmoothY_UIs_u2p14[6]	4751 4915			
t_APASmoothY_Uls_u2p14[7] t_APASmoothY_Uls_u2p14[8]	5079			
t_APASmoothY_UIs_u2p14[9]	5243			
t_LKASlewX_Kph_u8p8[0]	20992			
t_LKASlewX_Kph_u8p8[1]	21504			
t_LKASlewX_Kph_u8p8[2]	22016			
t_LKASlewX_Kph_u8p8[3]	22528			
t_LKASlewX_Kph_u8p8[4]	23040			
t_LKASlewX_Kph_u8p8[5]	23552			
t_LKASlewY_NmpSec_u4p12[0]	16794			
t_LKASlewY_NmpSec_u4p12[1] t_LKASlewY_NmpSec_u4p12[2]	17203 17613			
t_LKASlewY_NmpSec_u4p12[3]	18022			
t_LKASlewY_NmpSec_u4p12[4]	18432			
t_LKASlewY_NmpSec_u4p12[5]	18842			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_ti				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr target_TrqArblim_Per1_ReturnDDFactor_Ul	_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011	_102		
target TrgArblim Per1 ESCState State enum.value	1			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.95000005			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.23000002			
target_TrqArblim_Per1_LKAState_State_enum.value	0			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.792999			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.899999976 -3.29999995			
target_IrqArbiim_Per1_IrqOscCmd_MtrNm_f3z.value Name	-3.29999995 Actual Value	Expected Value	Result	
Name TrqArblim_APADisableRate_Uls_M_f32	0.372000009	0.372000009 ± 0000390625	Result	
TrgArblim ESC State Cnt M enum	1	0.372000009 ± 0000390625		
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	2.34425998	2.34425998 ± 0.03125		
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.89999976	0.899999976 ± 0.000244140625		
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.99399996	-2.99399996 ± 0.03125	•	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	2.75	2.75 ± 0000390625	•	
	10.75	2.75 ± 0.0000390625		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.75			
	0 -3.22300243	0 ± 0.0000390625 -3.22300005 ± 0000390625		



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.43 (Repeat Count = 1) Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	1
TrgArblim_ESC_State_Cnt_M_enum	1
	2.8900001
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0340000018
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.51999998
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2
k_APAEnableRate_pSec_f32	190.5
k_APAUseADmpScale_lgc	1
<_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	17203
t_APADisableRateX_HwNm_u4p12[1]	17613
t_APADisableRateX_HwNm_u4p12[2]	18022
t_APADisableRateX_HwNm_u4p12[3]	18432
t_APADisableRateX_HwNm_u4p12[4]	18842
t_APADisableRateX_HwNm_u4p12[5]	19251
t APADisableRateY pSec u7p9[0]	21504
t_APADisableRateY_pSec_u7p9[1]	22016
t_APADisableNateY_pSec_u7p9[1]	22528
t_APADisableNateY_pSec_u7p9[2]	23040
	23552
t_APADisableRateY_pSec_u7p9[4]	
t_APADisableRateY_pSec_u7p9[5]	24064
t_APASmoothX_Uls_u2p14[0]	6881
t_APASmoothX_Uls_u2p14[1]	7045
t_APASmoothX_Uls_u2p14[2]	7209
t_APASmoothX_Uls_u2p14[3]	7373
t_APASmoothX_Uls_u2p14[4]	7537
t_APASmoothX_Uls_u2p14[5]	7700
t_APASmoothX_Uls_u2p14[6]	7864
t_APASmoothX_Uls_u2p14[7]	8028
t_APASmoothX_Uls_u2p14[8]	8192
t_APASmoothX_Uls_u2p14[9]	8356
t_APASmoothY_Uls_u2p14[0]	3932
t APASmoothY Uls u2p14[1]	4096
t_APASmoothY_Uls_u2p14[2]	4260
t_APASmoothY_Uls_u2p14[3]	4424
t APASmoothY Uls u2p14[4]	4588
t_APASmoothY_Uls_u2p14[4]	4751
t APASmoothY Uls u2p14[6]	4915
t_APASmoothY_UIs_u2p14[7]	5079
t_APASmoothY_Uls_u2p14[8]	5243
t_APASmoothY_Uls_u2p14[9]	5407
t_LKASlewX_Kph_u8p8[0]	21504
t_LKASlewX_Kph_u8p8[1]	22016
t_LKASlewX_Kph_u8p8[2]	22528
t_LKASlewX_Kph_u8p8[3]	23040
t_LKASlewX_Kph_u8p8[4]	23552
t_LKASlewX_Kph_u8p8[5]	24064
t_LKASlewY_NmpSec_u4p12[0]	17203
t_LKASlewY_NmpSec_u4p12[1]	17613
	18022
t_LKASlewY_NmpSec_u4p12[3]	18432
t_LKASlewY_NmpSec_u4p12[4]	18842
t_LKASlewY_NmpSec_u4p12[5]	19251
r_ctolockv1_tmpoco_drp1z[o] target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
	Langua qui inimi i di i nodici. Di doloi di loc





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm	arget_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cn	arget_TrqArblim_Per1_ESClsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_	enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelive	red_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNn	n_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_t	732		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_6	enum		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelive	red_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicle	Speed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_0	Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtr	Nm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_	Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	lm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992			
target_TrqArblim_Per1_ESCState_State_enum.value	2			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.55999994			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.19999981			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	238.242996			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.10000002			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.0999999			
Name	Actual Value	Expected Value	Resul	
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	· •	
TrqArblim_ESC_State_Cnt_M_enum	2	2	•	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.91278005	2.91278005 ± 0.03125	· •	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.10000002	1.10000002 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.99060011	-1.99059999 ± 0.03125	•	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.66998291	0.669983029 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.66998291	0.669983029 ± 0000390625	•	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.23000002	1.23000002 ± 0000390625		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.23000002	1.23000002 ± 0.0000390625		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	'	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	-

0.363018811

0.66998291

0.36301899 ± 0000390625

0.669983029 ± 0000390625

Test Step 3.44 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.300000012
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.45000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0450000018
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1
k_APAEnableRate_pSec_f32	195
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	17613
t_APADisableRateX_HwNm_u4p12[1]	18022
t_APADisableRateX_HwNm_u4p12[2]	18432
t_APADisableRateX_HwNm_u4p12[3]	18842
t_APADisableRateX_HwNm_u4p12[4]	19251

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

2014-09-16, 12:30:22+0530





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	19661		
t_APADisableRateY_pSec_u7p9[0]	22016		
t_APADisableRateY_pSec_u7p9[1]	22528		
t_APADisableRateY_pSec_u7p9[2]	23040		
t_APADisableRateY_pSec_u7p9[3]	23552		
t_APADisableRateY_pSec_u7p9[4]	24064		
t_APADisableRateY_pSec_u7p9[5]	24576		
	7045		
t_APASmoothX_Uls_u2p14[0]			
t_APASmoothX_Uls_u2p14[1]	7209		
t_APASmoothX_Uls_u2p14[2]	7373		
t_APASmoothX_Uls_u2p14[3]	7537		
t_APASmoothX_Uls_u2p14[4]	7700		
t_APASmoothX_Uls_u2p14[5]	7864		
t_APASmoothX_Uls_u2p14[6]	8028		
t_APASmoothX_Uls_u2p14[7]	8192		
t_APASmoothX_Uls_u2p14[8]	8356		
t_APASmoothX_Uls_u2p14[9]	8520		
t_APASmoothY_Uls_u2p14[0]	4096		
t_APASmoothY_Uls_u2p14[1]	4260		
t APASmoothY Uls u2p14[2]	4424		
t_APASmoothY_Uls_u2p14[3]	4588		
t_APASmoothY_Uls_u2p14[4]	4751		
	4915		
t_APASmoothY_Uls_u2p14[5]			
t_APASmoothY_Uls_u2p14[6]	5079		
t_APASmoothY_Uls_u2p14[7]	5243		
t_APASmoothY_Uls_u2p14[8]	5407		
t_APASmoothY_Uls_u2p14[9]	5571		
t_LKASlewX_Kph_u8p8[0]	22016		
t_LKASlewX_Kph_u8p8[1]	22528		
t_LKASlewX_Kph_u8p8[2]	23040		
t_LKASlewX_Kph_u8p8[3]	23552		
t_LKASlewX_Kph_u8p8[4]	24064		
t_LKASlewX_Kph_u8p8[5]	24576		
t_LKASlewY_NmpSec_u4p12[0]	17613		
t_LKASlewY_NmpSec_u4p12[1]	18022		
t_LKASlewY_NmpSec_u4p12[2]	18432		
t_LKASlewY_NmpSec_u4p12[3]	18842		
t_LKASlewY_NmpSec_u4p12[4]	19251		
t LKASlewY NmpSec u4p12[5]	19661		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f		500	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lo		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enu	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	I_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target Rte Inst Ap TrgArblim.TrgArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3:		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target TrqArblim Per1 PosServEnable Cnt		
target Rte Inst Ap TrqArblim.TrqArblim Perl PosSevEnable_Cit_lgc target Rte Inst Ap TrqArblim.TrqArblim Perl PosSrvoCmd MtrNm f32			
	target_TrqArblim_Per1_PosSrvoCmd_MtrNm		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_132	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.17000008		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.55000019		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	244.692993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.9000001		
Name	Actual Value	Expected Value	Result
		•	Nesuit
TrqArblim_APADisableRate_Uls_M_f32	0.690000057	0.689999998 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	3	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.48239994	3.48239994 ± 0.03125	~

1.29999995

TrqArblim_PosSrvoCmd_MtrNm_M_f32

1.29999995 ± 0.000244140625





Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.994000018	-0.994000018 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.994000018	-0.994000018 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-0.994000018	-0.994000018 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.30500409	0.305004001 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.45 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.0250000004
FrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.01000023
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0560000017
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998
FrqArblim_PreLKATrqReq_HwNm_M_f32	0
c_APAEnableRate_pSec_f32	199.5
c_APAUseADmpScale_lgc	0
c_APAUseAsstScale_lgc	0
c_APAUseRetScale_lgc	0
C_ESCMax_HwNm_f32	1.58000004
c_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	18022
_APADisableRateX_HwNm_u4p12[1]	18432
_APADisableRateX_HwNm_u4p12[2]	18842
_APADisableRateX_HwNm_u4p12[3]	19251
_APADisableRateX_HwNm_u4p12[4]	19661
_APADisableRateX_HwNm_u4p12[5]	20070
_APADisableRateY_pSec_u7p9[0]	22528
_APADisableRateY_pSec_u7p9[1]	23040
_APADisableRateY_pSec_u7p9[2]	23552
_APADisableRateY_pSec_u7p9[3]	24064
_APADisableRateY_pSec_u7p9[4]	24576
_APADisableRateY_pSec_u7p9[5]	25088
_APASmoothX_UIs_u2p14[0]	7209
_APASmoothX_Uls_u2p14[1]	7373
_APASmoothX_UIs_u2p14[1]	7537
_APASmoothX_Uls_u2p14[3]	7700
	7864
_APASmoothX_UIs_u2p14[4]	
_APASmoothX_UIs_u2p14[5]	8028
_APASmoothX_Uls_u2p14[6]	8192
_APASmoothX_Uls_u2p14[7]	8356
_APASmoothX_Uls_u2p14[8]	8520
_APASmoothX_UIs_u2p14[9]	8684
_APASmoothY_Uls_u2p14[0]	4260
_APASmoothY_Uls_u2p14[1]	4424
_APASmoothY_Uls_u2p14[2]	4588
_APASmoothY_Uls_u2p14[3]	4751
_APASmoothY_Uls_u2p14[4]	4915
_APASmoothY_Uls_u2p14[5]	5079
_APASmoothY_Uls_u2p14[6]	5243
_APASmoothY_Uls_u2p14[7]	5407
_APASmoothY_Uls_u2p14[8]	5571
_APASmoothY_Uls_u2p14[9]	5734
_LKASlewX_Kph_u8p8[0]	22528
_LKASlewX_Kph_u8p8[1]	23040
_LKASlewX_Kph_u8p8[2]	23552
_LKASlewX_Kph_u8p8[3]	24064
LKASlewX Kph u8p8[4]	24576

2014-09-16, 12:30:22+0530





TIQAIDIIII_T ETT			CILCITO
Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	25088		
t_LKASlewY_NmpSec_u4p12[0]	18022		
t_LKASlewY_NmpSec_u4p12[1]	18432		
t_LKASlewY_NmpSec_u4p12[2]	18842		
t_LKASlewY_NmpSec_u4p12[3]	19251		
t LKASlewY NmpSec u4p12[4]	19661		
t LKASlewY NmpSec u4p12[5]	20070		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	; -5		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	gc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target TrqArblim Per1 ESCTorqueDelivere	d HwNm f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target TrqArblim Per1 LKATorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleS	peed Kph f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cn		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosSrvoCmd MtrNm f32	target TrqArblim Per1 PosSrvoCmd MtrNi	n f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor Uls f32	target TrqArblim Per1 ReturnDDFactor UI	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.78000021		
target TrgArblim Per1 LKACmd HwNm f32.value	8.89999962		
target TrgArblim Per1 LKAState State enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	251.143005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.70000005		
Name	Actual Value	Expected Value	Resu
TrqArblim APADisableRate Uls M f32	0	0 ± 0000390625	
TrgArblim ESC State Cnt M enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.05312014	4.05312014 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	-10.2399998	-10.2399998 ± 0.00024414062	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	0	0 ± 0.03125	
target TrgArblim Per1 AssistDDFactor UIs f32.value	1	1 ± 0000390625	
target TrqArblim Per1 DampingDDFactor Uls f32.value	1	1 ± 0000390625	
target TrqArblim Per1 ESCIsLimited Cnt Igc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0	0 ± 0.0000390625	
target TrqArblim Per1 LKATorqueDelivered HwNm f32.value	0	0 ± 0.0000390625	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓	

-2.70000005

-2.70000005 ± 0000390625

1 ± 0000390625

Name	Input Value
Rte Call Ap TrgArblim FaultInjection SCom FltInjection(SignalPath Uls f32)	target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.529999971
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.76999998
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0500000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.2299995
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4
k_APAEnableRate_pSec_f32	181.5
k APAUseADmpScale lgc	1

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value\\ target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

2014-09-16, 12:30:22+0530



Name	Input Value
	1
	1
	3.69000006
	1 16384
t_APADisableRateX_HwNm_u4p12[1]	16794
	17203
	17613
t_APADisableRateX_HwNm_u4p12[4]	18022
t_APADisableRateX_HwNm_u4p12[5]	18432
	20480
= = = 1 1 1	20992
	21504 22016
	22528
	23040
	6554
t_APASmoothX_Uls_u2p14[1]	6717
t_APASmoothX_Uls_u2p14[2]	6881
	7045
	7209
	7373
	7537 7700
	7864
	8028
	3604
t_APASmoothY_Uls_u2p14[1]	3768
t_APASmoothY_Uls_u2p14[2]	3932
	4096
	4260
	4424
	4588 4751
	4915
	5079
t_LKASlewX_Kph_u8p8[0]	20480
t_LKASlewX_Kph_u8p8[1]	20992
	21504
	22016
	22528
	23040 16384
	16794
	17203
	17613
t_LKASlewY_NmpSec_u4p12[4]	18022
t_LKASlewY_NmpSec_u4p12[5]	18432
•	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
	target_TrqArblim_Per1_DampingDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32
	target_IrqArblim_Per1_ESCCmd_Hwnm_r32 target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
	target_rrqArblim_rer1_ESGstate State enum
•	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
	target_TrqArblim_Per1_LKACmd_HwNm_f32
·	target_TrqArblim_Per1_LKAState_State_enum
	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
	4.17999983
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value	



Name	Input Value		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	225.343002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.69999988		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.893000007	0.893000007 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79849994	1.79849994 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.69999988	0.699999988 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.99099994	-3.99099994 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.193779469	0.193779007 ± 0000390625	~
target TrgArblim Per1 ReturnDDFactor Uls f32.value	0.723172188	0.723172009 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.47 (Repeat Count = 1)	· ·
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.075000003
TrqArblim ESC State Cnt M enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.13000011
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0780000016
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0
TrqArblim_PreLKATrqReq_HwNm_M_f32	2
k_APAEnableRate_pSec_f32	208.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	18842
t_APADisableRateX_HwNm_u4p12[1]	19251
t_APADisableRateX_HwNm_u4p12[2]	19661
t_APADisableRateX_HwNm_u4p12[3]	20070
t_APADisableRateX_HwNm_u4p12[4]	20480
t_APADisableRateX_HwNm_u4p12[5]	20890
t_APADisableRateY_pSec_u7p9[0]	23552
t_APADisableRateY_pSec_u7p9[1]	24064
t_APADisableRateY_pSec_u7p9[2]	24576
t_APADisableRateY_pSec_u7p9[3]	25088
t_APADisableRateY_pSec_u7p9[4]	25600
t_APADisableRateY_pSec_u7p9[5]	26112
t_APASmoothX_Uls_u2p14[0]	7537
t_APASmoothX_Uls_u2p14[1]	7700
t_APASmoothX_Uls_u2p14[2]	7864
t_APASmoothX_Uls_u2p14[3]	8028
t_APASmoothX_Uls_u2p14[4]	8192
t_APASmoothX_Uls_u2p14[5]	8356
t_APASmoothX_Uls_u2p14[6]	8520
t_APASmoothX_Uls_u2p14[7]	8684
t_APASmoothX_Uls_u2p14[8]	8847
t_APASmoothX_Uls_u2p14[9]	9011
t_APASmoothY_Uls_u2p14[0]	4588
t_APASmoothY_Uls_u2p14[1]	4751
t_APASmoothY_Uls_u2p14[2]	4915
t_APASmoothY_Uls_u2p14[3]	5079
t_APASmoothY_Uls_u2p14[4]	5243

2014-09-16, 12:30:22+0530



TrqArblim_Per1

TrqArbiim_Peri			MACICAL
Name	Input Value		
t_APASmoothY_Uls_u2p14[5]	5407		
t_APASmoothY_Uls_u2p14[6]	5571		
t_APASmoothY_Uls_u2p14[7]	5734		
t_APASmoothY_Uls_u2p14[8]	5898		
t_APASmoothY_Uls_u2p14[9]	6062		
	23552		
t_LKASlewX_Kph_u8p8[0]			
t_LKASlewX_Kph_u8p8[1]	24064		
t_LKASlewX_Kph_u8p8[2]	24576		
t_LKASlewX_Kph_u8p8[3]	25088		
t_LKASlewX_Kph_u8p8[4]	25600		
t_LKASlewX_Kph_u8p8[5]	26112		
t_LKASlewY_NmpSec_u4p12[0]	18842		
t_LKASlewY_NmpSec_u4p12[1]	19251		
t_LKASlewY_NmpSec_u4p12[2]	19661		
t_LKASlewY_NmpSec_u4p12[3]	20070		
t_LKASlewY_NmpSec_u4p12[4]	20480		
t_LKASlewY_NmpSec_u4p12[5]	20890		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_6	enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliver	red_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_C	nt lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtrl		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10	_	
target TrqArblim Per1 ESCState State enum.value	1		
target TrqArblim Per1 GMOSHOscillate State enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6		
target TrgArblim Per1 LKACmd HwNm f32.value	9.60000038		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
	264.042999		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.8999998		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.29999995	1=	1
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.492000043	0.492000014 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	1	1	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.19786024	5.19785976 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.8999998	1.89999998 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.99079978	1.98979998 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.846520245	0.846520007 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.846520245	0.846520007 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.25	-3.25 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	
target TrqArblim Per1 OpTrqOv MtrNm f32.value	0.291611522	0.291610986 ± 0000390625	
target TrgArblim Per1 ReturnDDFactor Uls f32 value	0.846520245	0.846520007 + 0000390625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

0.846520245

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

0.846520007 ± 0000390625



Test Step 3.48 (Repeat Count = 1)	Input Value
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.100000001
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.69000006
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.3499999
TrqArblim_PreLKATrqReq_HwNm_M_f32	3
<_APAEnableRate_pSec_f32	213
<_APAUseADmpScale_lgc	1
<_APAUseAsstScale_lgc	1
<_APAUseRetScale_lgc	1
<_ESCMax_HwNm_f32	3.1400001
_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	19251
APADisableRateX HwNm u4p12[1]	19661
APADisableRateX_HwNm_u4p12[2]	20070
_APADisableRateX_HwNm_u4p12[3]	20480
APADisableRateX HwNm u4p12[4]	20890
_Ar AbisableRateX_HwNm_u4p12[4] :_APADisableRateX_HwNm_u4p12[5]	21299
: APADisableRateY_nwint_u4p12[5]	24064
	24576
:_APADisableRateY_pSec_u7p9[1]	
:_APADisableRateY_pSec_u7p9[2]	25088
:_APADisableRateY_pSec_u7p9[3]	25600
:_APADisableRateY_pSec_u7p9[4]	26112
:_APADisableRateY_pSec_u7p9[5]	26624
z_APASmoothX_Uls_u2p14[0]	7700
:_APASmoothX_Uls_u2p14[1]	7864
:_APASmoothX_Uls_u2p14[2]	8028
:_APASmoothX_Uls_u2p14[3]	8192
_APASmoothX_Uls_u2p14[4]	8356
:_APASmoothX_Uls_u2p14[5]	8520
_APASmoothX_Uls_u2p14[6]	8684
_APASmoothX_Uls_u2p14[7]	8847
 :_APASmoothX_Uls_u2p14[8]	9011
APASmoothX_Uls_u2p14[9]	9175
APASmoothY_Uls_u2p14[0]	4751
: APASmoothY Uls u2p14[1]	4915
:_APASmoothY_Uls_u2p14[1]	5079
: APASmoothY_Uis_uzp14[z] : APASmoothY_Uis_u2p14[3]	5243
:_APASmoothY_Uls_u2p14[4]	5407
:_APASmoothY_Uls_u2p14[5]	5571
APASmoothY_Uls_u2p14[6]	5734
:_APASmoothY_Uls_u2p14[7]	5898
t_APASmoothY_Uls_u2p14[8]	6062
:_APASmoothY_Uls_u2p14[9]	6226
LKASlewX_Kph_u8p8[0]	24064
:_LKASlewX_Kph_u8p8[1]	24576
_LKASlewX_Kph_u8p8[2]	25088
_LKASlewX_Kph_u8p8[3]	25600
LKASlewX_Kph_u8p8[4]	26112
	26624
LKASlewY_NmpSec_u4p12[0]	19251
crate.orvi_nmpeec_u4p12[1]	19661
traceurrmpeee_u4p12[2]	20070
: LKASlewY NmpSec u4p12[3]	20480
_LKASlewY_NmpSec_u4p12[4]	20890
_LKASlewY_NmpSec_u4p12[4] _LKASlewY_NmpSec_u4p12[5]	21299
arget Rte Call Ap TrgArblim FaultInjection SCom FitInjection SignalPath Uls	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
alget Ne inst Ap HyArbiin. HuArbiin Fell LNASiale Siale enum	



Name	Input Value		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kphromerral (Secure VehicleSpeed_Kphromerral (Secure VehicleSpee$	n_f32 target_TrqArblim_Pe	r1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Pe	r1_OpTrqOv_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Pe	r1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Pe	r1_PosSrvoCmd_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Pe	r1_ReturnDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Pe	r1_TrqOscCmd_MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6.61000013		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	9.94999981		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	270.493011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.526000023	0.526000023 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.77188015	5.77188015 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.0999999	2.0999999 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.99060011	2.98959994 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.818063855	0.818063974 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.818063855	0.818063974 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.1400001	-3.1400001 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-1.71793401	-1.71792996 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.818063855	0.818063974 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.49 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.125
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.10000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.53999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	4
k_APAEnableRate_pSec_f32	217.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	19661
t_APADisableRateX_HwNm_u4p12[1]	20070
t_APADisableRateX_HwNm_u4p12[2]	20480
t_APADisableRateX_HwNm_u4p12[3]	20890
t_APADisableRateX_HwNm_u4p12[4]	21299
t_APADisableRateX_HwNm_u4p12[5]	21709
t_APADisableRateY_pSec_u7p9[0]	24576
t_APADisableRateY_pSec_u7p9[1]	25088
t_APADisableRateY_pSec_u7p9[2]	25600
t_APADisableRateY_pSec_u7p9[3]	26112
t_APADisableRateY_pSec_u7p9[4]	26624
t_APADisableRateY_pSec_u7p9[5]	27136
t_APASmoothX_Uls_u2p14[0]	7864
t_APASmoothX_Uls_u2p14[1]	8028
t_APASmoothX_Uls_u2p14[2]	8192





Name	Input Value		
t_APASmoothX_Uls_u2p14[3]	8356		
t_APASmoothX_Uls_u2p14[4]	8520		
t_APASmoothX_UIs_u2p14[5]	8684 8847		
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	9011		
t_APASmoothX_Uls_u2p14[8]	9175		
t_APASmoothX_Uls_u2p14[9]	9339		
t_APASmoothY_Uls_u2p14[0]	4915		
t_APASmoothY_Uls_u2p14[1]	5079		
t_APASmoothY_Uls_u2p14[2]	5243		
t_APASmoothY_Uls_u2p14[3]	5407		
t_APASmoothY_Uls_u2p14[4]	5571		
t_APASmoothY_Uls_u2p14[5]	5734		
t_APASmoothY_UIs_u2p14[6]	5898 6062		
t_APASmoothY_Uls_u2p14[7] t_APASmoothY_Uls_u2p14[8]	6226		
t_APASmoothY_Uls_u2p14[9]	6390		
t_LKASlewX_Kph_u8p8[0]	24576		
t_LKASlewX_Kph_u8p8[1]	25088		
t_LKASlewX_Kph_u8p8[2]	25600		
t_LKASlewX_Kph_u8p8[3]	26112		
t_LKASlewX_Kph_u8p8[4]	26624		
t_LKASlewX_Kph_u8p8[5]	27136		
t_LKASlewY_NmpSec_u4p12[0]	19661		
t_LKASlewY_NmpSec_u4p12[1]	20070 20480		
t_LKASlewY_NmpSec_u4p12[2] t_LKASlewY_NmpSec_u4p12[3]	20480		
t_LKASlewY_NmpSec_u4p12[4]	21299		
t_LKASlewY_NmpSec_u4p12[5]	21709		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_ti			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_Ulsf32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	target_TrqArblim_Per1_TrqOscCmd_MtrNn -9.03999996	1_132	
target TrgArblim Per1 ESCState State enum.value	3		
target TrqArblim Per1 GMOSHOscillate State enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.21999979		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.0500002		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	276.942993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.8999998	l=	1_
Name Tre Arbline ADADisable Date IIIa M #22	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cot_M_equim	0.0189999938 3	0.0189999994 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum TrqArblim HwTorqueSV HwNm M Str.SV UIs f32	6.34700012	6.34700012 ± 0.03125	Ĭ
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.53999996	-6.53999996 ± 0.000244140625	
TrgArblim_PreLKATrgReq_HwNm_M_f32	3.99039984	3.98939991 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.994300246	0.994300008 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.994300246	0.994300008 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
towart Translation Dord ECCTorage Delivered Liveline 622 value	0	0 ± 0000390625	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value		3 ± 0.0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	-
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value			



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.50 (Repeat Count = 1)	Input Value
	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.150000006
rqArblim_ESC_State_Cnt_M_enum	3
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6.80999994
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.111000001
rqArblim_PosSrvoCmd_MtrNm_M_f32	2.46000004
rqArblim_PreLKATrqReq_HwNm_M_f32	-8.80000019
_APAEnableRate_pSec_f32	222
_APAUseADmpScale_lgc	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
_ESCMax_HwNm_f32	0.239999995
_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	20070
_APADisableRateX_HwNm_u4p12[1]	20480
_APADisableRateX_HwNm_u4p12[2]	20890
_APADisableRateX_HwNm_u4p12[3]	21299
_APADisableRateX_HwNm_u4p12[4]	21709
_APADisableRateX_HwNm_u4p12[5]	22118
APADisableRateY pSec u7p9[0]	25088
_APADisableNateY_pSec_u7p3[0]	25600
_APADisableRateY_pSec_u7p9[1]	26112
APADisableRateY pSec u7p9[2] APADisableRateY pSec u7p9[3]	26624
_APADisableRateY_pSec_u7p9[4]	27136
_APADisableRateY_pSec_u7p9[5]	27648
_APASmoothX_Uls_u2p14[0]	8028
_APASmoothX_Uls_u2p14[1]	8192
_APASmoothX_Uls_u2p14[2]	8356
_APASmoothX_Uls_u2p14[3]	8520
_APASmoothX_Uls_u2p14[4]	8684
_APASmoothX_Uls_u2p14[5]	8847
_APASmoothX_Uls_u2p14[6]	9011
_APASmoothX_Uls_u2p14[7]	9175
_APASmoothX_Uls_u2p14[8]	9339
_APASmoothX_Uls_u2p14[9]	9503
_APASmoothY_Uls_u2p14[0]	5079
_APASmoothY_Uls_u2p14[1]	5243
_APASmoothY_Uls_u2p14[2]	5407
APASmoothY Uls u2p14[3]	5571
_APASmoothY_Uls_u2p14[4]	5734
_APASmoothY_Uls_u2p14[5]	5898
_APASmoothY_Uls_u2p14[6]	6062
APASmoothY Uls u2p14[7]	6226
_APASmoothY_UIs_u2p14[8]	6390
_APASmoothY_UIs_u2p14[9]	6554
_LKASlewX_Kph_u8p8[0]	25088
_LKASlewX_Kph_u8p8[1]	25600
_LKASlewX_Kph_u8p8[2]	26112
_LKASlewX_Kph_u8p8[3]	26624
_LKASlewX_Kph_u8p8[4]	27136
_LKASlewX_Kph_u8p8[5]	27648
_LKASlewY_NmpSec_u4p12[0]	20070
_LKASlewY_NmpSec_u4p12[1]	20480
t_LKASlewY_NmpSec_u4p12[2]	20890
LKASlewY_NmpSec_u4p12[3]	21299
	21709
:_LKASlewY_NmpSec_u4p12[5]	22118
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls	
arget_rite_eaii_np_riqnibiiii_i autiirijeetioii_eetiiii_i ittiljeetioii_eiglialFatti_Uis	_r ···





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliver	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_3	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicles	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_t	732	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Ci	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	lm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	lls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	m_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.82999992		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.21		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	283.393005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.594000041	0.593999982 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.92322016	6.92322016 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.5	2.5 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.79400063	-8.79399967 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.594036281	0.594035983 ± 0000390625	~
		4 . 000000005	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

1 ± 0000390625

Test Step 3.51 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.174999997
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.36999989
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.122000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019
k_APAEnableRate_pSec_f32	226.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	20480
t_APADisableRateX_HwNm_u4p12[1]	20890
t_APADisableRateX_HwNm_u4p12[2]	21299
t_APADisableRateX_HwNm_u4p12[3]	21709
t_APADisableRateX_HwNm_u4p12[4]	22118

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

2014-09-16, 12:30:22+0530





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	22528		
t_APADisableRateY_pSec_u7p9[0]	25600		
t_APADisableRateY_pSec_u7p9[1]	26112		
t_APADisableRateY_pSec_u7p9[2]	26624		
t_APADisableRateY_pSec_u7p9[3]	27136		
t_APADisableRateY_pSec_u7p9[4]	27648		
t_APADisableRateY_pSec_u7p9[5]	28160		
t_APASmoothX_Uls_u2p14[0]	8192		
t_APASmoothX_Uls_u2p14[1]	8356		
t_APASmoothX_Uls_u2p14[2]	8520		
t_APASmoothX_Uls_u2p14[3]	8684		
t_APASmoothX_Uls_u2p14[4]	8847		
t_APASmoothX_Uls_u2p14[5]	9011		
t_APASmoothX_Uls_u2p14[6]	9175		
t_APASmoothX_Uls_u2p14[7]	9339		
t_APASmoothX_Uls_u2p14[8]	9503		
t_APASmoothX_Uls_u2p14[9]	9667		
t_APASmoothY_Uls_u2p14[0]	5243		
t_APASmoothY_Uls_u2p14[1]	5407		
	5571		
t_APASmoothY_Uls_u2p14[2]	5734		
t_APASmoothY_Uls_u2p14[3]			
t_APASmoothY_Uls_u2p14[4]	5898		
t_APASmoothY_Uls_u2p14[5]	6062		
t_APASmoothY_Uls_u2p14[6]	6226		
t_APASmoothY_Uls_u2p14[7]	6390		
t_APASmoothY_Uls_u2p14[8]	6554		
t_APASmoothY_Uls_u2p14[9]	6717		
t_LKASlewX_Kph_u8p8[0]	25600		
t_LKASlewX_Kph_u8p8[1]	26112		
t_LKASlewX_Kph_u8p8[2]	26624		
t_LKASlewX_Kph_u8p8[3]	27136		
t_LKASlewX_Kph_u8p8[4]	27648		
t_LKASlewX_Kph_u8p8[5]	28160		
t_LKASlewY_NmpSec_u4p12[0]	20480		
t_LKASlewY_NmpSec_u4p12[1]	20890		
t_LKASlewY_NmpSec_u4p12[2]	21299		
t_LKASlewY_NmpSec_u4p12[3]	21709		
t_LKASlewY_NmpSec_u4p12[4]	22118		
t LKASlewY NmpSec u4p12[5]	22528		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3.		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_k		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target_TrqArblim_Per1_ESCState_State_enu		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target_TrqArblim_Per1_GMOSHOscillate_St		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32		_	
	target_TrqArblim_Per1_HwTorque_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	<u>f</u> 32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.43999958		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.230000004		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	289.842987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.70000005		
arget_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value -1.5			
Name	Actual Value	Expected Value	Result
	0.0649999902	0.0649999976 ± 0000390625	Result
TrqArblim_APADisableRate_Uls_M_f32			- 4
TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HuvTorqueSt/_Huvblm_M_Str_St/_Lile_f22	7.50052079	7 50053079 + 0 03435	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.50053978	7.50053978 ± 0.03125	~

2.75

 $TrqArblim_PosSrvoCmd_MtrNm_M_f32$

2.75 ± 0.000244140625





Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.78999996	8.78899956 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.979199529	0.979200006 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.979199529	0.979200006 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.0572012998	0.0572012998 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.979199529	0.979200006 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rgArblim APADisableRate Uls M f32	0.20000003
FrgArblim_ESC_State_Cnt_M_enum	0
FrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.92999983
FrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133000001
	3.0399996
rqArblim_PosSrvoCmd_MtrNm_M_f32	0
rqArblim_PreLKATrqReq_HwNm_M_f32	
_APAEnableRate_pSec_f32	231
_APAUseADmpScale_lgc	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
_ESCMax_HwNm_f32	0.75
_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	20890
_APADisableRateX_HwNm_u4p12[1]	21299
_APADisableRateX_HwNm_u4p12[2]	21709
_APADisableRateX_HwNm_u4p12[3]	22118
_APADisableRateX_HwNm_u4p12[4]	22528
_APADisableRateX_HwNm_u4p12[5]	22938
_APADisableRateY_pSec_u7p9[0]	26112
_APADisableRateY_pSec_u7p9[1]	26624
_APADisableRateY_pSec_u7p9[2]	27136
_APADisableRateY_pSec_u7p9[3]	27648
_APADisableRateY_pSec_u7p9[4]	28160
_APADisableRateY_pSec_u7p9[5]	28672
_APASmoothX_Uls_u2p14[0]	8356
_APASmoothX_Uls_u2p14[1]	8520
_APASmoothX_Uls_u2p14[2]	8684
_APASmoothX_Uls_u2p14[3]	8847
_APASmoothX_Uls_u2p14[4]	9011
_APASmoothX_Uls_u2p14[5]	9175
APASmoothX_Uls_u2p14[6]	9339
_APASmoothX_Uls_u2p14[7]	9503
_APASmoothX_Uls_u2p14[8]	9667
_APASmoothX_UIs_u2p14[9]	9830
APASmoothY_Uls_u2p14[0]	5407
APASmoothY_Uls_u2p14[1]	5571
_APASmoothY_Uls_u2p14[2]	5734
_APASmoothY_Uls_u2p14[3]	5898
_APASmoothY_UIs_u2p14[4]	6062
APASmoothY_Uls_u2p14[5]	6226
_APASmoothY_Uls_u2p14[6]	6390
APASmoothY_Uls_u2p14[7]	6554
APASmoothY_Uls_u2p14[8]	6717
APASmoothY_Uls_u2p14[9]	6881
	26112
KASiewX_Kph_u8p8[1]	26624
	27136
_LKASlewX_Kph_u8p8[2] LKASlewX Kph_u8p8[3]	27648

2014-09-16, 12:30:22+0530



IrqArblim_Per1			TOACICO	10
Name	Input Value			
t_LKASlewX_Kph_u8p8[4]	28160			
t_LKASlewX_Kph_u8p8[5]	28672			
t LKASlewY NmpSec u4p12[0]	20890			
t_LKASlewY_NmpSec_u4p12[1]	21299			
t LKASlewY NmpSec u4p12[2]	21709			
t_LKASlewY_NmpSec_u4p12[3]	22118			
t LKASlewY NmpSec u4p12[4]	22528			
t_LKASlewY_NmpSec_u4p12[5]	22938			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_t				
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target TrqArblim Per1 DampingDDFactor	_		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCCmd HwNm f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	_		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate S			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t	_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3			
target Rte Inst Ap TrgArblim.TrgArblim Per1 LKAState State enum	target_TrqArblim_Per1_LKAState_State_en			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivered			
target Rte Inst Ap TrgArblim.TrgArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleS			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3			
target Rte Inst Ap TrgArblim.TrgArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cnt			
target Rte Inst Ap TrgArblim.TrgArblim Per1 PosSrvoCmd MtrNm f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	_,		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ReturnDDFactor UIs f32	target_TrqArblim_Per1_ReturnDDFactor_UI			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999			
target_TrqArblim_Per1_ESCState_State_enum.value	1			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.05000019			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.12			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	296.292999			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.9000001			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.29999995			
Name	Actual Value	Expected Value	R	Result
TrqArblim_APADisableRate_Uls_M_f32	0.662	0.662 ± 0000390625		
TrqArblim_ESC_State_Cnt_M_enum	1	1		•
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	8.07895947	8.07896042 ± 0.03125		
TrgArblim PosSrvoCmd MtrNm M f32	2.9000001	2.9000001 ± 0.000244140625	5	•
TrgArblim PreLKATrgReg HwNm M f32	0	0 ± 0.03125	·	J
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625		J
target TrqArblim Per1 DampingDDFactor Uls f32.value	1	1 ± 0000390625		
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1		•
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	-0.75	-0.75 ± 0000390625		
target TrqArblim Per1 IqTrqOv HwNm f32.value	-0.75	-0.75 ± 0.0000390625		•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625		J
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.493716717	-0.493717015 ± 0000390625		•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625		J
	1.	. = 555555525		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	-
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.53 (Repeat Count = 1)	· ·
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.224999994
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.48999977
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.143999994
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.32999992
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.5999999
k_APAEnableRate_pSec_f32	235.5

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAUseADmpScale_lgc	0
k APAUseAsstScale Igc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	21299
t_APADisableRateX_HwNm_u4p12[1]	21709
t_APADisableRateX_HwNm_u4p12[2]	22118
t_APADisableRateX_HwNm_u4p12[3]	22528
t_APADisableRateX_HwNm_u4p12[4]	22938
t_APADisableRateX_HwNm_u4p12[5]	23347
t_APADisableRateY_pSec_u7p9[0]	26624
t_APADisableRateY_pSec_u7p9[1]	27136 27648
t_APADisableRateY_pSec_u7p9[2] t_APADisableRateY_pSec_u7p9[3]	28160
t_APADisableRateY_pSec_u7p9[4]	28672
t_APADisableRateY_pSec_u7p9[5]	29184
t_APASmoothX_Uls_u2p14[0]	8520
t_APASmoothX_Uls_u2p14[1]	8684
t_APASmoothX_Uls_u2p14[2]	8847
t_APASmoothX_Uls_u2p14[3]	9011
t_APASmoothX_Uls_u2p14[4]	9175
t_APASmoothX_Uls_u2p14[5]	9339
t_APASmoothX_Uls_u2p14[6]	9503
t_APASmoothX_Uls_u2p14[7]	9667
t_APASmoothX_Uls_u2p14[8]	9830
t_APASmoothX_Uls_u2p14[9] t APASmoothY Uls u2p14[0]	9994 5571
t_APASmoothY_Uls_u2p14[1]	5734
t_APASmoothY_Uls_u2p14[2]	5898
t_APASmoothY_Uls_u2p14[3]	6062
t_APASmoothY_Uls_u2p14[4]	6226
t_APASmoothY_Uls_u2p14[5]	6390
t_APASmoothY_Uls_u2p14[6]	6554
t_APASmoothY_Uls_u2p14[7]	6717
t_APASmoothY_Uls_u2p14[8]	6881
t_APASmoothY_Uls_u2p14[9]	7045
t_LKASlewX_Kph_u8p8[0]	26624
t_LKASlewX_Kph_u8p8[1]	27136
t_LKASlewX_Kph_u8p8[2]	27648
t_LKASlewX_Kph_u8p8[3] t_LKASlewX_Kph_u8p8[4]	28160 28672
t_LKASlewX_Kph_u8p8[5]	29184
t LKASlewY NmpSec u4p12[0]	21299
t_LKASlewY_NmpSec_u4p12[1]	21709
t_LKASlewY_NmpSec_u4p12[2]	22118
t_LKASlewY_NmpSec_u4p12[3]	22528
t_LKASlewY_NmpSec_u4p12[4]	22938
t_LKASlewY_NmpSec_u4p12[5]	23347
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f3	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target_IrqArblim_Per1_ESCIst_imited_Cnt_lgc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESC5tate_State_Hulli target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_Rte_inst_ap_irqarbiim.irqarbiim_Peri_irqUscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.1199989
target_TrqArblim_Per1_ESCCtind_HwiNin_isz.value target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.65999985
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.00999999





Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	302.743011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.10000002		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.110999987	0.111000001 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.65847969	8.65847969 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.32999992	3.32999992 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.59399986	4.59399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.125684112	0.125683993 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.54 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.25
FrqArblim_ESC_State_Cnt_M_enum	2
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.05000019
rqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.155000001
FrqArblim_PosSrvoCmd_MtrNm_M_f32	3.61999989
rqArblim_PreLKATrqReq_HwNm_M_f32	-4.5999999
C_APAEnableRate_pSec_f32	240
_APAUseADmpScale_lgc	1
x_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
c_ESCMax_HwNm_f32	3.69000006
<pre>c_LKAUseSlewCal_lgc</pre>	1
_APADisableRateX_HwNm_u4p12[0]	21709
_APADisableRateX_HwNm_u4p12[1]	22118
_APADisableRateX_HwNm_u4p12[2]	22528
_APADisableRateX_HwNm_u4p12[3]	22938
_APADisableRateX_HwNm_u4p12[4]	23347
_APADisableRateX_HwNm_u4p12[5]	23757
_APADisableRateY_pSec_u7p9[0]	27136
_APADisableRateY_pSec_u7p9[1]	27648
_APADisableRateY_pSec_u7p9[2]	28160
_APADisableRateY_pSec_u7p9[3]	28672
_APADisableRateY_pSec_u7p9[4]	29184
_APADisableRateY_pSec_u7p9[5]	29696
_APASmoothX_Uls_u2p14[0]	8684
_APASmoothX_Uls_u2p14[1]	8847
_APASmoothX_Uls_u2p14[2]	9011
_APASmoothX_Uls_u2p14[3]	9175
_APASmoothX_Uls_u2p14[4]	9339
_APASmoothX_Uls_u2p14[5]	9503
_APASmoothX_Uls_u2p14[6]	9667
_APASmoothX_Uls_u2p14[7]	9830
_APASmoothX_Uls_u2p14[8]	9994
_APASmoothX_Uls_u2p14[9]	10158
_APASmoothY_Uls_u2p14[0]	5734
_APASmoothY_Uls_u2p14[1]	5898
_APASmoothY_Uls_u2p14[2]	6062
_APASmoothY_Uls_u2p14[3]	6226
_APASmoothY_Uls_u2p14[4]	6390





Name	Input Value		
t_APASmoothY_Uls_u2p14[5]	6554		
t_APASmoothY_Uls_u2p14[6]	6717		
t_APASmoothY_Uls_u2p14[7]	6881		
t APASmoothY Uls u2p14[8]	7045		
t_APASmoothY_Uls_u2p14[9]	7209		
t_LKASlewX_Kph_u8p8[0]	27136		
t_LKASlewX_Kph_u8p8[1]	27648		
t_LKASlewX_Kph_u8p8[2]	28160		
t_LKASlewX_Kph_u8p8[3]	28672		
t LKASlewX Kph u8p8[4]	29184		
t_LKASlewX_Kph_u8p8[5]	29696		
t_LKASlewY_NmpSec_u4p12[0]	21709		
t_LKASlewY_NmpSec_u4p12[1]	22118		
t_LKASlewY_NmpSec_u4p12[2]	22528		
t_LKASlewY_NmpSec_u4p12[3]	22938		
t_LKASlewY_NmpSec_u4p12[4]	23347		
t_LKASlewY_NmpSec_u4p12[5]	23757		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target TrqArblim Per1 AssistDDFactor Uls	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target TrqArblim Per1 ESCCmd HwNm f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987	_	
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.9000001		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target TrgArblim Per1 MinSecureVehicleSpeed Kph f32.value	309.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32	0.730000019	0.730000019 ± 0000390625	- Ttoodic
TrqArblim ESC State Cnt M enum	3	3	-
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.74800014	7.74800014 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.2999995	3.29999995 ± 0.000244140625	•
TrgArblim PreLKATrgReg HwNm M f32	-4.58939981	-4.58839989 ± 0.03125	
target TrqArblim Per1 AssistDDFactor Uls f32.value	0.678798199	0.67879802 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	0	0 ± 0000390625	
target TrqArblim Per1 IqTrqOv HwNm f32.value	-3	-3 ± 0.000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	
target_TrqArblim_Per1_DpTrqOv_MtrNm_f32.value	0.159965992	0.159966007 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	
	1.1.1.1.00.00		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~



Fest Step 3.55 (Repeat Count = 1)	Invest Value
lame	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.275000006
rqArblim_ESC_State_Cnt_M_enum	3
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.60999966
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.165999994
rqArblim_PosSrvoCmd_MtrNm_M_f32	3.91000009
rqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038
_APAEnableRate_pSec_f32	0
APAUseADmpScale Igc	0
_APAUseAsstScale_lgc	0
APAUseRetScale Igc	0
_ESCMax_HwNm_f32	2.75
LKAUseSlewCal lgc	0
· · · · · · · · · · · · · · · · · ·	22118
_APADisableRateX_HwNm_u4p12[0]	22528
_APADisableRateX_HwNm_u4p12[1]	
_APADisableRateX_HwNm_u4p12[2]	22938
_APADisableRateX_HwNm_u4p12[3]	23347
_APADisableRateX_HwNm_u4p12[4]	23757
_APADisableRateX_HwNm_u4p12[5]	24166
_APADisableRateY_pSec_u7p9[0]	27648
_APADisableRateY_pSec_u7p9[1]	28160
_APADisableRateY_pSec_u7p9[2]	28672
_APADisableRateY_pSec_u7p9[3]	29184
_APADisableRateY_pSec_u7p9[4]	29696
_APADisableRateY_pSec_u7p9[5]	30208
_APASmoothX_Uls_u2p14[0]	8847
_APASmoothX_Uls_u2p14[1]	9011
_APASmoothX_Uls_u2p14[2]	9175
_APASmoothX_Uls_u2p14[3]	9339
_APASmoothX_Uls_u2p14[4]	9503
APASmoothX_Uls_u2p14[5]	9667
_APASmoothX_Uls_u2p14[6]	9830
_APASmoothX_Uls_u2p14[7]	9994
	10158
_APASmoothX_Uls_u2p14[8]	10322
_APASmoothX_Uls_u2p14[9]	
_APASmoothY_Uls_u2p14[0]	5898
_APASmoothY_Uls_u2p14[1]	6062
_APASmoothY_Uls_u2p14[2]	6226
_APASmoothY_Uls_u2p14[3]	6390
_APASmoothY_Uls_u2p14[4]	6554
_APASmoothY_Uls_u2p14[5]	6717
_APASmoothY_Uls_u2p14[6]	6881
_APASmoothY_Uls_u2p14[7]	7045
_APASmoothY_Uls_u2p14[8]	7209
_APASmoothY_Uls_u2p14[9]	7373
LKASlewX_Kph_u8p8[0]	27648
LKASlewX_Kph_u8p8[1]	28160
LKASlewX Kph u8p8[2]	28672
LKASlewX_Kph_u8p8[3]	29184
rvalewx_rpii_dopo[6] _LKASlewX_Kph_u8p8[4]	29696
LKASlewX_Kph_u8p8[5]	30208
	22118
_LKASlewY_NmpSec_u4p12[0]	
_LKASlewY_NmpSec_u4p12[1]	22528
_LKASlewY_NmpSec_u4p12[2]	22938
_LKASlewY_NmpSec_u4p12[3]	23347
_LKASlewY_NmpSec_u4p12[4]	23757
_LKASlewY_NmpSec_u4p12[5]	24166
rget Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	
rget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
rget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
arget Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	





Name	Input Value		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3.	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	1_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.15999985		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.21000004		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	3.78999996		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	315.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.69999988		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.157000005	0.157000005 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.21560001	8.21560001 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.91000009	3.91000009 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.09400082	-8.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.479015768	-0.479016006 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.56 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.30000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.35000038
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.177000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.19999981
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.0999999
k_APAEnableRate_pSec_f32	500
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938
t_APADisableRateX_HwNm_u4p12[2]	23347
t_APADisableRateX_HwNm_u4p12[3]	23757
t_APADisableRateX_HwNm_u4p12[4]	24166
t_APADisableRateX_HwNm_u4p12[5]	24576
t_APADisableRateY_pSec_u7p9[0]	28160
t_APADisableRateY_pSec_u7p9[1]	28672
t_APADisableRateY_pSec_u7p9[2]	29184
t_APADisableRateY_pSec_u7p9[3]	29696
t_APADisableRateY_pSec_u7p9[4]	30208
t_APADisableRateY_pSec_u7p9[5]	30720
t_APASmoothX_Uls_u2p14[0]	9011
t_APASmoothX_Uls_u2p14[1]	9175
t_APASmoothX_Uls_u2p14[2]	9339
t_APASmoothX_Uls_u2p14[3]	9503





Name	Input Value		
t_APASmoothX_Uls_u2p14[4]	9667		
t_APASmoothX_Uls_u2p14[5]	9830		
t_APASmoothX_Uls_u2p14[6]	9994		
t_APASmoothX_Uls_u2p14[7]	10158		
t_APASmoothX_Uls_u2p14[8]	10322		
t_APASmoothX_Uls_u2p14[9]	10486		
t_APASmoothY_Uls_u2p14[0]	6062		
t_APASmoothY_Uls_u2p14[1]	6226		
t_APASmoothY_Uls_u2p14[2]	6390		
t_APASmoothY_Uls_u2p14[3]	6554		
t_APASmoothY_Uls_u2p14[4]	6717		
t_APASmoothY_Uls_u2p14[5]	6881		
t_APASmoothY_Uls_u2p14[6]	7045		
t_APASmoothY_Uls_u2p14[7]	7209		
t_APASmoothY_Uls_u2p14[8]	7373		
t_APASmoothY_Uls_u2p14[9]	7537		
t_LKASlewX_Kph_u8p8[0]	28160		
t_LKASlewX_Kph_u8p8[1]	28672		
t_LKASlewX_Kph_u8p8[2]	29184		
t_LKASlewX_Kph_u8p8[3]	29696		
t_LKASlewX_Kph_u8p8[4]	30208		
t_LKASlewX_Kph_u8p8[5]	30720		
t_LKASlewY_NmpSec_u4p12[0]	22528		
t_LKASiewY_NmpSec_u4p12[0] t_LKASiewY_NmpSec_u4p12[1]	22938		
t_LKASlewY_NmpSec_u4p12[2]	23347		
t_LKASlewY_NmpSec_u4p12[3]	23757		
t_LKASlewY_NmpSec_u4p12[4]	24166		
t_LKASlewY_NmpSec_u4p12[5]	24576		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	-5.67999983	_	
target TrqArblim Per1 ESCState State enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target TrqArblim Per1 HwTorque HwNm f32.value	1.76999998		
target TrqArblim Per1 LKACmd HwNm f32.value	4.67999983		
target TrqArblim Per1 LKAState State enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	322.092987		
target_TrqArblim_Per1_posServEnable Cnt lgc.value	0		
	3.70000005		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.5	Promo etc d M 1	
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_UIs_M_f32	0.180000007	0.180000007 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-7.38176012	-7.38176012 ± 0.03125	Y
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.19999981	4.19999981 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.08899975	-7.08799982 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.933400869	0.933400989 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.933400869	0.933400989 ± 0000390625	✓
	0	0	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value		0 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 1 0000390023	
	0 -3	-3 ± 0.000390625	-
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value			
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~



Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.57 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.324999988
FrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.187999994
FrqArblim_PosSrvoCmd_MtrNm_M_f32	4.48999977
FrqArblim_PreLKATrqReq_HwNm_M_f32	-6.0999999
C_APAEnableRate_pSec_f32	250.339996
_APAUseADmpScale_lgc	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
c_ESCMax_HwNm_f32	3.24000001
LKAUseSlewCal Igc	0
APADisableRateX_HwNm_u4p12[0]	22938
APADisableRateX_HwNm_u4p12[1]	23347
_APADisableRateX_HwNm_u4p12[2]	23757
_APADisableRateX_HwNm_u4p12[3]	24166
APADisableRateX HwNm u4p12[4]	24576
_APADisableRateX_HwNm_u4p12[4]	24986
_APADisableRateY_pSec_u7p9[0]	28672
	29184
_APADisableRateY_pSec_u7p9[1]	
_APADisableRateY_pSec_u7p9[2]	29696
_APADisableRateY_pSec_u7p9[3]	30208
_APADisableRateY_pSec_u7p9[4]	30720
_APADisableRateY_pSec_u7p9[5]	31232
_APASmoothX_Uls_u2p14[0]	9175
_APASmoothX_Uls_u2p14[1]	9339
_APASmoothX_Uls_u2p14[2]	9503
_APASmoothX_Uls_u2p14[3]	9667
_APASmoothX_Uls_u2p14[4]	9830
_APASmoothX_Uls_u2p14[5]	9994
_APASmoothX_Uls_u2p14[6]	10158
_APASmoothX_Uls_u2p14[7]	10322
_APASmoothX_Uls_u2p14[8]	10486
_APASmoothX_Uls_u2p14[9]	10650
_APASmoothY_Uls_u2p14[0]	6226
_APASmoothY_Uls_u2p14[1]	6390
_APASmoothY_Uls_u2p14[2]	6554
_APASmoothY_Uls_u2p14[3]	6717
_APASmoothY_Uls_u2p14[4]	6881
_APASmoothY_Uls_u2p14[5]	7045
_APASmoothY_Uls_u2p14[6]	7209
_APASmoothY_Uls_u2p14[7]	7373
APASmoothY Uls u2p14[8]	7537
_APASmoothY_Uls_u2p14[6]	7700
_LKASlewX_Kph_u8p8[0]	28672
_LKASlewX_Kph_u8p8[1]	29184
_LKASlewX_Kph_u8p8[2]	29696
_LKASlewX_Kph_u8p8[3]	30208
_LKASlewX_Kph_u8p8[4]	30720
_LKASlewX_Kph_u8p8[5]	31232
_LKASlewY_NmpSec_u4p12[0]	22938
_LKASlewY_NmpSec_u4p12[1]	23347
_LKASlewY_NmpSec_u4p12[2]	23757
_LKASlewY_NmpSec_u4p12[3]	24166
_LKASlewY_NmpSec_u4p12[4]	24576
_LKASlewY_NmpSec_u4p12[5]	24986
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFac	tor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwN	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_0	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State	target_TrqArblim_Per1_ESCState_State_enum		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_ESCTorqueDeli	vered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillat	e_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwN	lm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNr	n_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State	_enum		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDeliv	vered_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehic	cleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNi	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable	_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_M	ltrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFacto	r_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_Mt	rNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.19999981			
target_TrqArblim_Per1_ESCState_State_enum.value	1			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.32999992			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.57000017			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	328.542999			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.9000001			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.300000012			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.825680017	0.825680017 ± 0000390625	-	
TrqArblim_ESC_State_Cnt_M_enum	1	1	•	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.86995983	-6.86995983 ± 0.03125	-	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.9000001	3.9000001 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.09399986	-6.09399986 ± 0.03125	-	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	-	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.24000001	-3.24000001 ± 0000390625	•	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.51337719	1.51338005 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	-	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.58 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.349999994
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.64999962
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.199000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.78000021
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.0999999
k_APAEnableRate_pSec_f32	246
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	23347
t_APADisableRateX_HwNm_u4p12[1]	23757
t_APADisableRateX_HwNm_u4p12[2]	24166
t_APADisableRateX_HwNm_u4p12[3]	24576
t_APADisableRateX_HwNm_u4p12[4]	24986





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	25395		
t APADisableRateY pSec u7p9[0]	29184		
t_APADisableRateY_pSec_u7p9[1]	29696		
t_APADisableRateY_pSec_u7p9[2]	30208		
t_APADisableRateY_pSec_u7p9[3]	30720		
t APADisableRateY pSec u7p9[4]	31232		
t_APADisableRateY_pSec_u7p9[5]	31744		
t_APASmoothX_Uls_u2p14[0]	9339		
t_APASmoothX_Uls_u2p14[1]	9503		
t_APASmoothX_Uls_u2p14[2]	9667		
t_APASmoothX_Uls_u2p14[3]	9830		
t_APASmoothX_Uls_u2p14[4]	9994		
t_APASmoothX_UIs_u2p14[5]	10158		
t_APASmoothX_UIs_u2p14[6]	10322		
t_APASmoothX_Uls_u2p14[7]	10486		
t_APASmoothX_UIs_u2p14[8]	10650		
t_APASmoothX_UIs_u2p14[9] t_APASmoothY_UIs_u2p14[0]	10813 6390		
t APASmoothY Uls u2p14[1]	6554		
t_APASmoothY_Uls_u2p14[2]	6717		
t_APASmoothY_Uls_u2p14[3]	6881		
t APASmoothY Uls u2p14[4]	7045		
t_APASmoothY_Uls_u2p14[5]	7209		
t_APASmoothY_Uls_u2p14[6]	7373		
t_APASmoothY_Uls_u2p14[7]	7537		
t_APASmoothY_Uls_u2p14[8]	7700		
t_APASmoothY_Uls_u2p14[9]	7864		
t_LKASlewX_Kph_u8p8[0]	29184		
t_LKASlewX_Kph_u8p8[1]	29696		
t_LKASlewX_Kph_u8p8[2]	30208		
t_LKASlewX_Kph_u8p8[3]	30720		
t_LKASlewX_Kph_u8p8[4]	31232		
t_LKASlewX_Kph_u8p8[5]	31744		
t_LKASlewY_NmpSec_u4p12[0]	23347		
t_LKASlewY_NmpSec_u4p12[1]	23757		
t_LKASlewY_NmpSec_u4p12[2]	24166		
t_LKASlewY_NmpSec_u4p12[3]	24576 24986		
t_LKASlewY_NmpSec_u4p12[4] t_LKASlewY_NmpSec_u4p12[5]	25395		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f3			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_le		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn target TrqArblim Per1 ReturnDDFactor Uls		
target Rte Inst Ap TrgArblim.TrgArblim Per1 TrgOscCmd MtrNm f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.8900001		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.46000004		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	334.993011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.100000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.842000008	0.842000008 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.35353947	-6.35353994 ± 0.03125	V
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.0999999	4.0999999 ± 0.000244140625	_



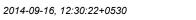


Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.09399986	-5.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.58000004	-1.58000004 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.58000004	-1.58000004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.5569886	1.55699003 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.59 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.375
FrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.30000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.20999993
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.0999999
<pre><_APAEnableRate_pSec_f32</pre>	255.75
<_APAUseADmpScale_lgc	1
<_APAUseAsstScale_lgc	1
<_APAUseRetScale_lgc	1
c_ESCMax_HwNm_f32	4.28000021
_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	23757
_APADisableRateX_HwNm_u4p12[1]	24166
_APADisableRateX_HwNm_u4p12[2]	24576
_APADisableRateX_HwNm_u4p12[3]	24986
_APADisableRateX_HwNm_u4p12[4]	25395
_APADisableRateX_HwNm_u4p12[5]	25805
_APADisableRateY_pSec_u7p9[0]	29696
APADisableRateY_pSec_u7p9[1]	30208
_APADisableRateY_pSec_u7p9[2]	30720
APADisableRateY_pSec_u7p9[3]	31232
_APADisableRateY_pSec_u7p9[4]	31744
_APADisableRateY_pSec_u7p9[5]	32256
_APASmoothX_Uls_u2p14[0]	9503
_APASmoothX_UIs_u2p14[1]	9667
_APASmoothX_UIs_u2p14[2]	9830
_APASmoothX_UIs_u2p14[3]	9994
_APASmoothX_UIs_u2p14[4]	10158
_APASmoothX_Uls_u2p14[5]	10322
	10486
_APASmoothX_UIs_u2p14[6]	
APASmoothX_UIs_u2p14[7]	10650
_APASmoothX_UIs_u2p14[8]	10813
_APASmoothX_UIs_u2p14[9]	10977
_APASmoothY_Uls_u2p14[0]	6554
_APASmoothY_Uls_u2p14[1]	6717
_APASmoothY_Uls_u2p14[2]	6881
_APASmoothY_Uls_u2p14[3]	7045
_APASmoothY_Uls_u2p14[4]	7209
_APASmoothY_Uls_u2p14[5]	7373
_APASmoothY_Uls_u2p14[6]	7537
_APASmoothY_Uls_u2p14[7]	7700
_APASmoothY_Uls_u2p14[8]	7864
_APASmoothY_Uls_u2p14[9]	8028
_LKASlewX_Kph_u8p8[0]	29696
_LKASlewX_Kph_u8p8[1]	30208
_LKASlewX_Kph_u8p8[2]	30720
_LKASlewX_Kph_u8p8[3]	31232
_LKASlewX_Kph_u8p8[4]	31744

TrqArblim_Per1





Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	32256		
t_LKASlewY_NmpSec_u4p12[0]	23757		
t_LKASlewY_NmpSec_u4p12[1]	24166		
t_LKASlewY_NmpSec_u4p12[2]	24576		
t_LKASlewY_NmpSec_u4p12[3]	24986		
LKASlewY_NmpSec_u4p12[4]	25395		
t_LKASlewY_NmpSec_u4p12[5]	25805		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	1.79999995		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.45000005		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.3499999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	341.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.100000001		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.258355469	0.248999998 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	3	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.83250046	-5.83249998 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017	5.07000017 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.08839989	-4.08739996 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
torget Translim Dord OnTraQv MtrNm f22 value	0.522076062	0.505002075 + 0000200625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

0.523976862

0.896651506

0.505002975 ± 0000390625

0.900394022 ± 0000390625

Test Step 3.60 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.40000006	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-7.94999981	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.221000001	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.36000013	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.0999999	
k_APAEnableRate_pSec_f32	265.5	

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	24166 24576
t_APADisableRateX_HwNm_u4p12[1] t_APADisableRateX_HwNm_u4p12[2]	24986
t_APADisableRateX_HwNm_u4p12[3]	25395
t_APADisableRateX_HwNm_u4p12[4]	25805
t_APADisableRateX_HwNm_u4p12[5]	26214
t_APADisableRateY_pSec_u7p9[0]	30208
t_APADisableRateY_pSec_u7p9[1]	30720
t_APADisableRateY_pSec_u7p9[2]	31232
t_APADisableRateY_pSec_u7p9[3]	31744
t_APADisableRateY_pSec_u7p9[4] t_APADisableRateY_pSec_u7p9[5]	32256 32768
t_APASmoothX_Uls_u2p14[0]	9667
t_APASmoothX_UIs_u2p14[1]	9830
t_APASmoothX_Uls_u2p14[2]	9994
t_APASmoothX_Uls_u2p14[3]	10158
t_APASmoothX_Uls_u2p14[4]	10322
t_APASmoothX_Uls_u2p14[5]	10486
t_APASmoothX_Uls_u2p14[6]	10650
t_APASmoothX_Uls_u2p14[7]	10813
t_APASmoothX_UIs_u2p14[8]	10977
t_APASmoothX_UIs_u2p14[9]	11141 6717
t_APASmoothY_Uls_u2p14[0] t_APASmoothY_Uls_u2p14[1]	6881
t_APASmoothY_Uls_u2p14[2]	7045
t_APASmoothY_Uls_u2p14[3]	7209
t_APASmoothY_Uls_u2p14[4]	7373
t_APASmoothY_Uls_u2p14[5]	7537
t_APASmoothY_Uls_u2p14[6]	7700
t_APASmoothY_Uls_u2p14[7]	7864
t_APASmoothY_Uls_u2p14[8]	8028
t_APASmoothY_Uls_u2p14[9]	8192
t_LKASlewX_Kph_u8p8[0]	30208 30720
t_LKASlewX_Kph_u8p8[1] t_LKASlewX_Kph_u8p8[2]	31232
t_LKASlewX_Kph_u8p8[3]	31744
t LKASlewX Kph u8p8[4]	32256
t_LKASlewX_Kph_u8p8[5]	32768
t_LKASlewY_NmpSec_u4p12[0]	24166
t_LKASlewY_NmpSec_u4p12[1]	24576
t_LKASlewY_NmpSec_u4p12[2]	24986
t_LKASlewY_NmpSec_u4p12[3]	25395
t_LKASlewY_NmpSec_u4p12[4]	25805
t_LKASlewY_NmpSec_u4p12[5]	26214
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_fitarget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	-0.800000012 target TrqArblim Per1 AssistDDFactor UIs f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_state_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	4.01000022
target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.01000023 8.23999977
targot_rightbilli_r or _Etvhoritu_riwiviii_loz.value	O.ECOOOOT I



Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	347.893005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.300000012		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.282000005	0.272000015 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.30683994	-5.30683994 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.36000013	5.36000013 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.884387553	0.888486981 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.61968267	0.597707987 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte Call Ap TrgArblim FaultInjection SCom FltInjection(SignalPath Uls f32)	target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath UIs f32
Rte Inst Ap TrgArblim	target Rte Inst Ap TrqArblim
TrgArblim APADisableRate Uls M f32	0.25
TrgArblim ESC State Cnt M enum	2
TrgArblim HwTorqueSV HwNm M Str.SV Uls f32	9.05000019
TrgArblim HwTorqueSV HwNm M Str.K Uls f32	0.1550000019
TrgArblim PosSrvoCmd MtrNm M f32	3.6199989
TrgArblim PreLKATrgReg HwNm M f32	-4.5999999
APAEnableRate pSec f32	240
APAUseADmpScale Igc	1
APAUseAsstScale lgc	1
APAUseRetScale Igc	1
CESCMax HwNm f32	3.69000006
K LKAUseSlewCal lgc	1
: APADisableRateX HwNm u4p12[0]	21709
: APADisableRateX HwNm u4p12[1]	22118
APADisableRateX_HwNm_u4p12[2]	22528
: APADisableRateX HwNm u4p12[3]	22938
APADisableRateX_HwNm_u4p12[4]	23347
: APADisableRateX HwNm u4p12[5]	23757
APADisableRateY pSec u7p9[0]	27136
: APADisableRateY_pSec_u7p9[1]	27648
: APADisableRateY_pSec_u7p9[2]	28160
: APADisableRateY pSec u7p9[3]	28672
: APADisableRateY_pSec_u7p9[4]	29184
: APADisableRateY pSec u7p9[5]	29696
APASmoothX Uls u2p14[0]	8684
: APASmoothX Uls u2p14[1]	8847
APASmoothX Uls u2p14[2]	9011
: APASmoothX Uls u2p14[3]	9175
APASmoothX_Uls_u2p14[4]	9339
	9503
APASmoothX Uls u2p14[6]	9667
APASmoothX Uls u2p14[7]	9830
APASmoothX Uls u2p14[8]	9994
=_APASmoothX_Uls_u2p14[9]	10158
APASmoothY_Uls_u2p14[0]	5734
APASmoothY Uls u2p14[1]	5898
= APASmoothY_Uls_u2p14[2]	6062
:_APASmoothY_Uls_u2p14[3]	6226
APASmoothY Uls u2p14[4]	6390

2014-09-16, 12:30:22+0530



TrqArblim_Per1

N	Innered Walters			
Name	Input Value			
t_APASmoothY_Uls_u2p14[5]	6554			
t_APASmoothY_Uls_u2p14[6]	6717			
t_APASmoothY_Uls_u2p14[7]	6881			
t_APASmoothY_Uls_u2p14[8]	7045			
t_APASmoothY_Uls_u2p14[9]	7209			
t_LKASlewX_Kph_u8p8[0]	27136			
t_LKASlewX_Kph_u8p8[1]	27648			
t_LKASlewX_Kph_u8p8[2]	28160			
t_LKASlewX_Kph_u8p8[3]	28672			
t_LKASlewX_Kph_u8p8[4]	29184			
t_LKASlewX_Kph_u8p8[5]	29696			
t_LKASlewY_NmpSec_u4p12[0]	21709			
t_LKASlewY_NmpSec_u4p12[1]	22118			
t_LKASlewY_NmpSec_u4p12[2]	22528			
t_LKASlewY_NmpSec_u4p12[3]	22938			
t_LKASlewY_NmpSec_u4p12[4]	23347			
t_LKASlewY_NmpSec_u4p12[5]	23757			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f		rae.		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	ı_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987			
target_TrqArblim_Per1_ESCState_State_enum.value	3			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.9000001			
target_TrqArblim_Per1_LKAState_State_enum.value	0			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.192993			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.2999995			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.730000019	0.730000019 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	3	3	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.74800014	7.74800014 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.2999995	3.29999995 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.58939981	-4.58839989 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	✓	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	~	
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

0.159965992

0.678798199

-3 ± 0.0000390625

0.159966007 ± 0000390625

0.67879802 ± 0000390625

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value



Test Step 3.62 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.449999988
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-7.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002
k_APAEnableRate_pSec_f32	285
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	24986
t_APADisableRateX_HwNm_u4p12[1]	25395
t_APADisableRateX_HwNm_u4p12[2]	25805
t_APADisableRateX_HwNm_u4p12[3]	26214
t_APADisableRateX_HwNm_u4p12[4]	26624
t_APADisableRateX_HwNm_u4p12[5]	27034
t_APADisableRateY_pSec_u7p9[0]	31232
t_APADisableRateY_pSec_u7p9[1]	31744
t_APADisableRateY_pSec_u7p9[2]	32256
t_APADisableRateY_pSec_u7p9[3]	32768
t_APADisableRateY_pSec_u7p9[4]	33280 33792
t_APADisableRateY_pSec_u7p9[5]	9994
t_APASmoothX_UIs_u2p14[0]	10158
t_APASmoothX_UIs_u2p14[1]	10322
t_APASmoothX_UIs_u2p14[2]	10322
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	10650
t_APASmoothX_Uls_u2p14[5]	10813
t_APASmoothX_Uls_u2p14[6]	10977
t_APASmoothX_Uls_u2p14[7]	11141
t_APASmoothX_Uls_u2p14[8]	11305
t_APASmoothX_Uls_u2p14[9]	11469
t_APASmoothY_Uls_u2p14[0]	7045
t_APASmoothY_Uls_u2p14[1]	7209
t_APASmoothY_Uls_u2p14[2]	7373
t_APASmoothY_Uls_u2p14[3]	7537
t_APASmoothY_Uls_u2p14[4]	7700
t APASmoothY Uls u2p14[5]	7864
t_APASmoothY_Uls_u2p14[6]	8028
t_APASmoothY_Uls_u2p14[7]	8192
t_APASmoothY_Uls_u2p14[8]	8356
t_APASmoothY_Uls_u2p14[9]	8520
t_LKASlewX_Kph_u8p8[0]	31232
t_LKASlewX_Kph_u8p8[1]	31744
t_LKASlewX_Kph_u8p8[2]	32256
t_LKASlewX_Kph_u8p8[3]	32768
t_LKASlewX_Kph_u8p8[4]	33280
t_LKASlewX_Kph_u8p8[5]	33792
t_LKASlewY_NmpSec_u4p12[0]	24986
t_LKASlewY_NmpSec_u4p12[1]	25395
t_LKASlewY_NmpSec_u4p12[2]	25805
t_LKASlewY_NmpSec_u4p12[3]	26214
t_LKASlewY_NmpSec_u4p12[4]	26624
t_LKASlewY_NmpSec_u4p12[5]	27034
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	rget_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	t_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	arget_TrqArblim_Per1_TrqOscCmd_MtrNm_f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989			
target_TrqArblim_Per1_ESCState_State_enum.value	1			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.13000011			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.0200005			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	360.792999			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.9000001			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.69999988			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.327999979	0.317999989 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	1	1	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.24166012	-4.24166012 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006	5.94000006 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.08779979	-1.08679998 ± 0.03125	•	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.858962417	0.863261998 ± 0000390625	✓	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~	
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	~	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-2.57999992	-2.57999992 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-2.57999992	-2.57999992 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.83776325	0.812222004 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.858962417	0.863261998 ± 0000390625	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	•

T + 0 + 0 00 (D + + 0 + + 1)	
Test Step 3.63 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.474999994
rqArblim_ESC_State_Cnt_M_enum	1
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.9000001
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.254000008
FrqArblim_PosSrvoCmd_MtrNm_M_f32	6.23000002
FrqArblim_PreLKATrqReq_HwNm_M_f32	-0.100000001
APAEnableRate_pSec_f32	294.75
:_APAUseADmpScale_lgc	1
:_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
_ESCMax_HwNm_f32	0.239999995
_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	25395
_APADisableRateX_HwNm_u4p12[1]	25805
_APADisableRateX_HwNm_u4p12[2]	26214
_APADisableRateX_HwNm_u4p12[3]	26624
_APADisableRateX_HwNm_u4p12[4]	27034
_APADisableRateX_HwNm_u4p12[5]	27443
_APADisableRateY_pSec_u7p9[0]	31744
_APADisableRateY_pSec_u7p9[1]	32256
_APADisableRateY_pSec_u7p9[2]	32768
_APADisableRateY_pSec_u7p9[3]	33280
_APADisableRateY_pSec_u7p9[4]	33792
_APADisableRateY_pSec_u7p9[5]	34304
_APASmoothX_Uls_u2p14[0]	10158
_APASmoothX_Uls_u2p14[1]	10322
_APASmoothX_Uls_u2p14[2]	10486

2014-09-16, 12:30:22+0530





Name	Input Value		
t_APASmoothX_Uls_u2p14[3]	10650		
t_APASmoothX_Uls_u2p14[4]	10813		
t_APASmoothX_Uls_u2p14[5]	10977		
t_APASmoothX_Uls_u2p14[6]	11141		
t_APASmoothX_Uls_u2p14[7]	11305		
t_APASmoothX_Uls_u2p14[8]	11469		
t_APASmoothX_Uls_u2p14[9]	11633		
t_APASmoothY_Uls_u2p14[0]	7209		
t_APASmoothY_Uls_u2p14[1]	7373		
t_APASmoothY_Uls_u2p14[2]	7537		
t_APASmoothY_Uls_u2p14[3]	7700		
t_APASmoothY_Uls_u2p14[4]	7864		
t_APASmoothY_Uls_u2p14[5]	8028		
t_APASmoothY_Uls_u2p14[6]	8192		
t_APASmoothY_Uls_u2p14[7]	8356		
t_APASmoothY_Uls_u2p14[8]	8520		
t_APASmoothY_Uls_u2p14[9]	8684		
t_LKASlewX_Kph_u8p8[0]	31744		
t_LKASlewX_Kph_u8p8[1]	32256		
t_LKASlewX_Kph_u8p8[2]	32768		
t_LKASlewX_Kph_u8p8[3]	33280		
t_LKASlewX_Kph_u8p8[4]	33792		
t_LKASlewX_Kph_u8p8[5]	34304		
t_LKASlewY_NmpSec_u4p12[0]	25395		
t_LKASlewY_NmpSec_u4p12[1]	25805		
t_LKASlewY_NmpSec_u4p12[2]	26214		
t_LKASlewY_NmpSec_u4p12[3]	26624		
t_LKASlewY_NmpSec_u4p12[4]	27034		
t_LKASlewY_NmpSec_u4p12[5]	27443		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_		. 400	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_Per1_ESCTorgueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_LKATorqueDelivere target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target TrqArblim Per1 OpTrqOv MtrNm f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn		
target Rte Inst Ap TrgArblim.TrgArblim Per1 PosSrvoCmd MtrNm f32	target TrqArblim Per1 PosSrvoCmd MtrNi		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	-5.42000008	v=	
target TrqArblim Per1 ESCState State enum.value	2		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.69000006		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2200003		
target TrqArblim Per1 LKAState State enum.value	4		
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	367.243011		
target TrqArblim Per1 PosServEnable Cnt Igc.value	1		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	5.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.899999976		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	Nesu
TrqArblim_APADIsableRate_UIS_M_132 TrqArblim_ESC_State_Cnt_M_enum	2	1 ± 0000390625	
	-3.70213985	-3.70214009 ± 0.03125	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.70213985 5.0999999	-3.70214009 ± 0.03125 5.0999999 ± 0.000244140625	
	-0.0940000042	-0.093999999 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.0940000042 1		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value		1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.469970703	0.469971001 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.239999995	-0.239999995 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.239999995	-0.239999995 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.60314941	3.60314989 ± 0000390625	
target TrgArblim Per1 ReturnDDFactor Llls f32 value	1.3	1 + 0000390625	

1

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value\\target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

1 ± 0000390625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.64 (Repeat Count = 1) Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32) Rte_Inst_Ap_TrqArblim	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32 target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.5
	2
TrqArblim_ESC_State_Cnt_M_enum	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.55000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.264999986
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.51999998
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.89999976
<_APAEnableRate_pSec_f32	304.5
k_APAUseADmpScale_lgc	1
<_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t APADisableRateY pSec u7p9[0]	32256
t_APADisableNateY_pSec_u7p9[0]	32768
t_APADisableRateY_pSec_u7p9[1] t_APADisableRateY_pSec_u7p9[2]	33280
t_APADisableRateY_pSec_u7p9[3]	33792
t_APADisableRateY_pSec_u7p9[4]	34304
t_APADisableRateY_pSec_u7p9[5]	34816
t_APASmoothX_Uls_u2p14[0]	10322
t_APASmoothX_Uls_u2p14[1]	10486
t_APASmoothX_Uls_u2p14[2]	10650
t_APASmoothX_Uls_u2p14[3]	10813
t_APASmoothX_Uls_u2p14[4]	10977
t_APASmoothX_Uls_u2p14[5]	11141
t_APASmoothX_Uls_u2p14[6]	11305
t_APASmoothX_Uls_u2p14[7]	11469
t_APASmoothX_Uls_u2p14[8]	11633
t_APASmoothX_Uls_u2p14[9]	11796
t_APASmoothY_Uls_u2p14[0]	7373
t APASmoothY Uls u2p14[1]	7537
t_APASmoothY_Uls_u2p14[2]	7700
t_APASmoothY_Uls_u2p14[3]	7864
t APASmoothY Uls u2p14[4]	8028
	8192
t_APASmoothY_Uls_u2p14[5]	8356
t_APASmoothY_Uls_u2p14[6]	
t_APASmoothY_Uls_u2p14[7]	8520
t_APASmoothY_Uls_u2p14[8]	8684
t_APASmoothY_Uls_u2p14[9]	8847
t_LKASlewX_Kph_u8p8[0]	32256
t_LKASlewX_Kph_u8p8[1]	32768
t_LKASlewX_Kph_u8p8[2]	33280
t_LKASlewX_Kph_u8p8[3]	33792
t_LKASlewX_Kph_u8p8[4]	34304
t_LKASlewX_Kph_u8p8[5]	34816
t_LKASlewY_NmpSec_u4p12[0]	25805
t_LKASlewY_NmpSec_u4p12[1]	26214
t_LKASlewY_NmpSec_u4p12[2]	26624
t_LKASlewY_NmpSec_u4p12[3]	27034
t_LKASlewY_NmpSec_u4p12[4]	27443
t_LKASlewY_NmpSec_u4p12[5]	27853
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	
govo_ounprrq/omn_r autunjoution_oooni_r itinjoution_oignalFatti_Ois_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	arget_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	state_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	<u>f</u> 32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3:	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	n_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.21999979			
target_TrqArblim_Per1_ESCState_State_enum.value	3			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6.25			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-9.86999989			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	373.692993			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	5.30000019			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.10000002			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.15800023	-3.15799999 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.30000019	5.30000019 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.893999994	0.893999994 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.460021973	0.460022002 ± 0000390625	~	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.460021973	0.460022002 ± 0000390625	✓	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TroArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	V

2.86188364

0.460021973

2.86188006 ± 0000390625

0.460022002 ± 0000390625

Test Step 3.65 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.25
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.05000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.61999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.5999999
k_APAEnableRate_pSec_f32	240
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	21709
t_APADisableRateX_HwNm_u4p12[1]	22118
t_APADisableRateX_HwNm_u4p12[2]	22528
t_APADisableRateX_HwNm_u4p12[3]	22938
t_APADisableRateX_HwNm_u4p12[4]	23347
t_APADisableRateX_HwNm_u4p12[5]	23757

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$





Name	Input Value		
t_APADisableRateY_pSec_u7p9[0]	27136		
t_APADisableRateY_pSec_u7p9[1]	27648		
t_APADisableRateY_pSec_u7p9[2]	28160		
t_APADisableRateY_pSec_u7p9[3]	28672		
t_APADisableRateY_pSec_u7p9[4]	29184		
t_APADisableRateY_pSec_u7p9[5]	29696		
t_APASmoothX_UIs_u2p14[0]	8684		
t_APASmoothX_Uls_u2p14[1]	8847		
t_APASmoothX_UIs_u2p14[2]	9011		
t_APASmoothX_Uls_u2p14[3]	9175		
t_APASmoothX_UIs_u2p14[4]	9339		
t_APASmoothX_UIs_u2p14[5]	9503		
t_APASmoothX_UIs_u2p14[6]	9667		
t_APASmoothX_UIs_u2p14[7]	9830		
t_APASmoothX_UIs_u2p14[8]	9994		
t_APASmoothX_Uls_u2p14[9]	10158 5734		
t_APASmoothY_UIs_u2p14[0]			
t_APASmoothY_Uls_u2p14[1]	5898		
t_APASmoothY_Uls_u2p14[2]	6062		
t_APASmoothY_Uls_u2p14[3]	6226		
t_APASmoothY_Uls_u2p14[4]	6390		
t_APASmoothY_Uls_u2p14[5]	6554		
t_APASmoothY_Uls_u2p14[6]	6717		
t_APASmoothY_Uls_u2p14[7]	6881		
t_APASmoothY_Uls_u2p14[8]	7045		
t_APASmoothY_Uls_u2p14[9]	7209		
t_LKASlewX_Kph_u8p8[0]	27136		
t_LKASlewX_Kph_u8p8[1]	27648		
t_LKASlewX_Kph_u8p8[2]	28160		
t_LKASlewX_Kph_u8p8[3]	28672		
t_LKASlewX_Kph_u8p8[4]	29184		
t_LKASlewX_Kph_u8p8[5]	29696		
t_LKASlewY_NmpSec_u4p12[0]	21709		
t_LKASlewY_NmpSec_u4p12[1]	22118		
t_LKASlewY_NmpSec_u4p12[2]	22528		
t_LKASlewY_NmpSec_u4p12[3]	22938		
t_LKASlewY_NmpSec_u4p12[4]	23347		
t_LKASlewY_NmpSec_u4p12[5]	23757		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f.			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lo		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArbling_Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	· -	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3.		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_132	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.9000001		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.2999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32			
	0.730000019	0.730000019 ± 0000390625	_
TrqArblim_ESC_State_Cnt_M_enum	3	3	•
TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3 7.74800014	3 7.74800014 ± 0.03125	, in the second
TrqArblim_ESC_State_Cnt_M_enum	3	3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.159965992	0.159966007 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

lame	Input Value
	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim 0.550000012
rqArblim_APADisableRate_Uls_M_f32	4
rqArblim_ESC_State_Cnt_M_enum	-5.8499999
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	
rqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.287
rqArblim_PosSrvoCmd_MtrNm_M_f32	7.099999
rqArblim_PreLKATrqReq_HwNm_M_f32	2.9000001
_APAEnableRate_pSec_f32	324
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
_ESCMax_HwNm_f32	1.27999997
_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	26624
_APADisableRateX_HwNm_u4p12[1]	27034
_APADisableRateX_HwNm_u4p12[2]	27443
_APADisableRateX_HwNm_u4p12[3]	27853
_APADisableRateX_HwNm_u4p12[4]	28262
_APADisableRateX_HwNm_u4p12[5]	28672
_APADisableRateY_pSec_u7p9[0]	33280
APADisableRateY_pSec_u7p9[1]	33792
APADisableRateY_pSec_u7p9[2]	34304
APADisableRateY_pSec_u7p9[3]	34816
APADisableRateY_pSec_u7p9[4]	35328
APADisableRateY_pSec_u7p9[5]	35840
APASmoothX_Uls_u2p14[0]	10650
_APASmoothX_Uls_u2p14[1]	10813
_APASmoothX_Uls_u2p14[2]	10977
APASmoothX_Uls_u2p14[3]	11141
APASmoothX_Uls_u2p14[4]	11305
APASmoothX_UIs_u2p14[5]	11469
_APASmoothX_Uls_u2p14[6]	11633
APASmoothX_Uls_u2p14[7]	11796
_APASmoothX_Uls_u2p14[8]	11960
_APASmoothX_Uls_u2p14[9]	12124
APASmoothY_Uls_u2p14[0]	7700
APASmoothY Uls u2p14[1]	7864
APASmoothY_Uls_u2p14[2]	8028
APASmoothY_Uls_u2p14[3]	8192
_APASmoothY_Uls_u2p14[4]	8356
APASmoothY Uls u2p14[5]	8520
_APASmoothY_Uls_u2p14[6]	8684
APASmoothY_Uls_u2p14[7]	8847
APASmoothY_Uls_u2p14[8]	9011
_APASmoothY_Uls_u2p14[o]	9175
	0
_LKASlewX_Kph_u8p8[0]	
_LKASlewX_Kph_u8p8[1]	0
_LKASlewX_Kph_u8p8[2]	0
_LKASlewX_Kph_u8p8[3] _LKASlewX_Kph_u8p8[4]	0

2014-09-16, 12:30:22+0530





			10.10	
Name	Input Value			
t_LKASlewX_Kph_u8p8[5]	0			
t_LKASlewY_NmpSec_u4p12[0]	26624	26624		
t LKASlewY NmpSec u4p12[1]	27034			
t LKASlewY NmpSec u4p12[2]	27443			
t_LKASlewY_NmpSec_u4p12[3]	27853			
t LKASlewY NmpSec u4p12[4]	28262			
t LKASlewY NmpSec u4p12[5]	28672			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	5.19999981			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	ium		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S			
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm	f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target TrqArblim Per1 LKATorqueDelivere	d HwNm f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleS	peed Kph f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target TrqArblim Per1 OpTrqOv MtrNm f32			
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cn	t Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target TrqArblim Per1 ReturnDDFactor Uls f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005			
target_TrqArblim_Per1_ESCState_State_enum.value	0			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.36999989			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-9.17000008			
target_TrqArblim_Per1_LKAState_State_enum.value	0			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	386.592987			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	5.6999981			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.5			
Name	Actual Value	Expected Value	Resul	
TrgArblim APADisableRate Uls M f32	1	1 ± 0000390625		
TrqArblim ESC State Cnt M enum	0	0		
TrqArblim HwTorqueSV HwNm M Str.SV UIs f32	-2.05586004	-2.05586004 ± 0.03125		
TrqArblim PosSrvoCmd MtrNm M f32	5.69999981	5.69999981 ± 0.000244140628	5	
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.88600016	2.88599992 ± 0.03125		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.440002441	0.440001994 ± 0000390625		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.440002441	0.440001994 ± 0000390625		
target TrgArblim Per1 ESCIsLimited Cnt Igc.value	0	0		
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.88600016	2.88599992 ± 0.0000390625	•	
target TrqArblim Per1 LKATorqueDelivered HwNm f32.value	2.88600016	2.88599992 ± 0.0000390625		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

3.19198608

0.440002441

3.1919899 ± 0000390625

0.440001994 ± 0000390625

Test Step 3.67 (Repeat Count = 1)	🕶 🕶
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.574999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.5
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.298000008
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.38999987
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.9000001
k_APAEnableRate_pSec_f32	333.75

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	27034
t_APADisableRateX_HwNm_u4p12[1]	27443
t_APADisableRateX_HwNm_u4p12[2] t APADisableRateX HwNm u4p12[3]	27853 28262
t_APADisableRateX_HwNm_u4p12[4]	28672
t_APADisableRateX_HwNm_u4p12[5]	29082
t_APADisableRateY_pSec_u7p9[0]	33792
t_APADisableRateY_pSec_u7p9[1]	34304
t_APADisableRateY_pSec_u7p9[2]	34816
t_APADisableRateY_pSec_u7p9[3]	35328
t_APADisableRateY_pSec_u7p9[4]	35840
t_APADisableRateY_pSec_u7p9[5]	36352
t_APASmoothX_Uls_u2p14[0] t_APASmoothX_Uls_u2p14[1]	10813 10977
t_APASmoothX_UIs_u2p14[2]	11141
t_APASmoothX_Uls_u2p14[3]	11305
t_APASmoothX_UIs_u2p14[4]	11469
t_APASmoothX_Uls_u2p14[5]	11633
t_APASmoothX_Uls_u2p14[6]	11796
t_APASmoothX_UIs_u2p14[7]	11960
t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_UIs_u2p14[9]	12288
t_APASmoothY_Uls_u2p14[0] t_APASmoothY_Uls_u2p14[1]	7864 8028
t_APASmoothY_UIs_u2p14[2]	8192
t_APASmoothY_Uls_u2p14[3]	8356
t_APASmoothY_Uls_u2p14[4]	8520
t_APASmoothY_Uls_u2p14[5]	8684
t_APASmoothY_Uls_u2p14[6]	8847
t_APASmoothY_Uls_u2p14[7]	9011
t_APASmoothY_Uls_u2p14[8]	9175
t_APASmoothY_UIs_u2p14[9]	9339 56320
t_LKASlewX_Kph_u8p8[0] t_LKASlewX_Kph_u8p8[1]	56320
t LKASlewX Kph u8p8[2]	56320
t_LKASlewX_Kph_u8p8[3]	56320
t_LKASlewX_Kph_u8p8[4]	56320
t_LKASlewX_Kph_u8p8[5]	56320
t_LKASlewY_NmpSec_u4p12[0]	27034
t_LKASlewY_NmpSec_u4p12[1]	27443
t_LKASlewY_NmpSec_u4p12[2]	27853
t_LKASlewY_NmpSec_u4p12[3] t_LKASlewY_NmpSec_u4p12[4]	28262 28672
t_LKASlewY_NmpSec_u4p12[4]	29082
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f:	
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_State_enum target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_inst_Ap_frqArblim.frqArblim_Peri_HwTorque_HwNm_f32 target_Rte_inst_Ap_frqArblim.TrqArblim_Peri_HwTorque_HwNm_f32	target_IrqArblim_Per1_HW10rque_HWNm_f32 target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
$target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 -0.620000005
target_TrqArblim_Per1_ESCCritq_Hwntn_isz.value	1
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.92999983
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.81999969



Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	393.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	5.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.442999989	0.442999989 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.49785995	-1.49785995 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.38999987	7.38999987 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.88679981	3.88579988 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.620000005	-0.620000005 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.620000005	-0.620000005 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.27134562	3.27134991 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.68 (Repeat Count = 1)	Innut Walter
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.600000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.1500001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.308999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.67999983
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.9000001
k_APAEnableRate_pSec_f32	343.5
<_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	27443
t_APADisableRateX_HwNm_u4p12[1]	27853
_APADisableRateX_HwNm_u4p12[2]	28262
t_APADisableRateX_HwNm_u4p12[3]	28672
t_APADisableRateX_HwNm_u4p12[4]	29082
t_APADisableRateX_HwNm_u4p12[5]	29491
t_APADisableRateY_pSec_u7p9[0]	34304
t_APADisableRateY_pSec_u7p9[1]	34816
t_APADisableRateY_pSec_u7p9[2]	35328
t_APADisableRateY_pSec_u7p9[3]	35840
t_APADisableRateY_pSec_u7p9[4]	36352
APADisableRateY pSec u7p9[5]	36864
t APASmoothX Uls u2p14[0]	10977
t_APASmoothX_Uls_u2p14[1]	11141
t APASmoothX Uls u2p14[2]	11305
t_APASmoothX_Uls_u2p14[3]	11469
t_APASmoothX_Uls_u2p14[4]	11633
t_APASmoothX_Uls_u2p14[5]	11796
t APASmoothX Uls u2p14[6]	11960
t APASmoothX Uls u2p14[7]	12124
t_APASmoothX_Uls_u2p14[8]	12288
t_APASmoothX_Uls_u2p14[9]	12452
t_APASmoothY_Uls_u2p14[0]	8028
t APASmoothY Uls u2p14[1]	8192
t_APASmoothY_Uls_u2p14[2]	8356
t APASmoothY Uls u2p14[3]	8520





Name	Input Value			
t_APASmoothY_Uls_u2p14[4]	8684			
t_APASmoothY_Uls_u2p14[5]	8847			
t_APASmoothY_Uls_u2p14[6]	9011			
t_APASmoothY_Uls_u2p14[7]	9175			
t_APASmoothY_Uls_u2p14[8]	9339			
t_APASmoothY_Uls_u2p14[9]	9503			
t_LKASlewX_Kph_u8p8[0]	34304			
t_LKASlewX_Kph_u8p8[1]	34816			
t_LKASlewX_Kph_u8p8[2]	35328			
t_LKASlewX_Kph_u8p8[3]	35840			
t_LKASlewX_Kph_u8p8[4]	36352			
t_LKASlewX_Kph_u8p8[5]	36864			
t_LKASlewY_NmpSec_u4p12[0]	27443			
t_LKASlewY_NmpSec_u4p12[1]	27853			
t_LKASlewY_NmpSec_u4p12[2]	28262			
t_LKASlewY_NmpSec_u4p12[3]	28672			
t_LKASlewY_NmpSec_u4p12[4]	29082			
t_LKASlewY_NmpSec_u4p12[5]	29491			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_UIs_				
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target TrqArblim Per1 PosServEnable Cnt Igc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServeInable_Cht_gc			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983			
target_TrqArblim_Per1_ESCState_State_enum.value	2			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.48999977			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.47000027			
target TrgArblim Per1 LKAState State enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	399.493011			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.0999999			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.8999998			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.466000021	0.465999991 ± 0000390625	✓ ×	
TrgArblim ESC State Cnt M enum	2	2	✓	
TrgArblim HwTorqueSV HwNm M Str.SV Uls f32	-0.935240269	-0.935239971 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.6799983	7.67999983 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.88565111	4.88560009 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.77166456	0.771664977 ± 0000390625	✓	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.77166456	0.771664977 ± 0000390625	~	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.579999983	0.579999983 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.579999983	0.579999983 ± 0.0000390625	✓	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.75361621	1.75362003 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.77166456	0.771664977 ± 0000390625	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~	



Test Step 3.69 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.625
TrgArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019
	0.319999993
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.96999979
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.9000001
k_APAEnableRate_pSec_f32	353.25
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	27853
t_APADisableRateX_HwNm_u4p12[1]	28262
t_APADisableRateX_HwNm_u4p12[2]	28672
t_APADisableRateX_HwNm_u4p12[3]	29082
t_APADisableRateX_HwNm_u4p12[4]	29491
t_APADisableRateX_HwNm_u4p12[5]	29901
t_APADisableRateY_pSec_u7p9[0]	34816
t_APADisableRateY_pSec_u7p9[1]	35328
t_APADisableRateY_pSec_u7p9[2]	35840
t_APADisableRateY_pSec_u7p9[3]	36352
t_APADisableRateY_pSec_u7p9[3]	36864
t_APADisableRateY_pSec_u7p9[4]	37376
t_APASmoothX_UIs_u2p14[0]	11141
t_APASmoothX_Uls_u2p14[1]	11305
t_APASmoothX_Uls_u2p14[2]	11469
t_APASmoothX_Uls_u2p14[3]	11633
t_APASmoothX_Uls_u2p14[4]	11796
t_APASmoothX_Uls_u2p14[5]	11960
t_APASmoothX_Uls_u2p14[6]	12124
t_APASmoothX_Uls_u2p14[7]	12288
t_APASmoothX_UIs_u2p14[8]	12452
t_APASmoothX_Uls_u2p14[9]	12616
t_APASmoothY_Uls_u2p14[0]	8192
t_APASmoothY_Uls_u2p14[1]	8356
t_APASmoothY_Uls_u2p14[2]	8520
t_APASmoothY_Uls_u2p14[3]	8684
t APASmoothY Uls u2p14[4]	8847
t APASmoothY Uls u2p14[5]	9011
t_APASmoothY_Uls_u2p14[6]	9175
	9339
t_APASmoothY_Uls_u2p14[7]	
t_APASmoothY_UIs_u2p14[8]	9503
t_APASmoothY_UIs_u2p14[9]	9667
t_LKASlewX_Kph_u8p8[0]	33280
t_LKASlewX_Kph_u8p8[1]	33792
t_LKASlewX_Kph_u8p8[2]	34304
t_LKASlewX_Kph_u8p8[3]	34816
t_LKASlewX_Kph_u8p8[4]	35328
t_LKASlewX_Kph_u8p8[5]	35840
t_LKASlewY_NmpSec_u4p12[0]	0
t_LKASlewY_NmpSec_u4p12[1]	0
t_LKASlewY_NmpSec_u4p12[2]	0
t_LKASlewY_NmpSec_u4p12[3]	0
t LKASlewY NmpSec u4p12[4]	0
t LKASlewY NmpSec u4p12[5]	0
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_rte_inst_Ap_TrqArblim.TrqArblim_Per1_Assistable actor_ols_is2 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target TrgArblim Per1 DampingDDFactor Uls f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
	target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32



Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3	target_TrqArblim_Per1_MinSecureVehicl	arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm	arget TrgArblim Per1 OpTrgOv MtrNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_	Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mi	arget TrqArblim Per1 PosSrvoCmd MtrNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor	_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_Mtr	Nm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	1.77999997			
target_TrqArblim_Per1_ESCState_State_enum.value	3			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.05000019			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.11999989			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	405.942993			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.30000019			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.0999999			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	3	3	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.368000031	-0.368000001 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.30000019	6.30000019 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.89400005	5.89400005 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.71716928	3.71717 ± 0000390625	✓	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	•

Test Step 3.70 (Repeat Count = 1)	✓
Name	Input Value
Rte Call Ap TrqArblim FaultInjection SCom FltInjection(SignalPath Uls f32)	target Rte Call Ap TrgArblim FaultInjection SCom FltInjection SignalPath Uls f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.649999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.44999981
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.331
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.26000023
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.900001
k_APAEnableRate_pSec_f32	363
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	28262
t_APADisableRateX_HwNm_u4p12[1]	28672
t_APADisableRateX_HwNm_u4p12[2]	29082
t_APADisableRateX_HwNm_u4p12[3]	29491
t_APADisableRateX_HwNm_u4p12[4]	29901
t_APADisableRateX_HwNm_u4p12[5]	30310
t_APADisableRateY_pSec_u7p9[0]	35328
t_APADisableRateY_pSec_u7p9[1]	35840
t_APADisableRateY_pSec_u7p9[2]	36352
t_APADisableRateY_pSec_u7p9[3]	36864
t_APADisableRateY_pSec_u7p9[4]	37376
t_APADisableRateY_pSec_u7p9[5]	37888
t_APASmoothX_Uls_u2p14[0]	11305
t_APASmoothX_Uls_u2p14[1]	11469
t_APASmoothX_Uls_u2p14[2]	11633

2014-09-16, 12:30:22+0530





Name	Input Value			
t_APASmoothX_Uls_u2p14[3]	11796			
t_APASmoothX_Uls_u2p14[4]	11960			
t_APASmoothX_Uls_u2p14[5]	12124			
t_APASmoothX_Uls_u2p14[6]	12288			
t_APASmoothX_Uls_u2p14[7]	12452			
t_APASmoothX_Uls_u2p14[8]	12616			
t_APASmoothX_Uls_u2p14[9]	12780			
t_APASmoothY_Uls_u2p14[0]	8356			
t_APASmoothY_Uls_u2p14[1]	8520			
t_APASmoothY_Uls_u2p14[2]	8684			
t_APASmoothY_Uls_u2p14[3] t_APASmoothY_Uls_u2p14[4]	9011			
t_APASmoothY_Uls_u2p14[5]	9175			
t_APASmoothY_Uls_u2p14[6]	9339			
t_APASmoothY_Uls_u2p14[7]	9503			
t_APASmoothY_Uls_u2p14[8]	9667			
t_APASmoothY_Uls_u2p14[9]	9830			
t_LKASlewX_Kph_u8p8[0]	33792			
t_LKASlewX_Kph_u8p8[1]	34304			
t_LKASlewX_Kph_u8p8[2]	34816			
t_LKASlewX_Kph_u8p8[3]	35328			
t_LKASlewX_Kph_u8p8[4]	35840			
t_LKASlewX_Kph_u8p8[5]	36352			
t_LKASlewY_NmpSec_u4p12[0]	55296			
t_LKASlewY_NmpSec_u4p12[1]	55296			
t_LKASlewY_NmpSec_u4p12[2]	55296			
t_LKASlewY_NmpSec_u4p12[3]	55296			
t_LKASlewY_NmpSec_u4p12[4]	55296			
t_LKASlewY_NmpSec_u4p12[5] target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_UIs_t	55296 9.19999981			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	: f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3 target_TrqArblim_Per1_PosServEnable_Cnf			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	2.98000002	02		
target_TrqArblim_Per1_ESCState_State_enum.value	4			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.60999966			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.76999998			
target_TrqArblim_Per1_LKAState_State_enum.value	0			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	412.393005			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.5			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.2999995			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.203859806	0.20386 ± 0.03125	· ·	
TrqArblim_PosSrvoCmd_MtrNm_M_f32 TrqArblim_Prel_KATrqReq_HwNm_M_f32	6.5 6.87300014	6.5 ± 0.000244140625 6.87300014 ± 0.03125		
TrqArblim_PreLKATrqReq_HwNm_M_f32 target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.400024414	6.87300014 ± 0.03125 0.400023997 ± 0000390625	~	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.400024414	0.400023997 ± 0000390625		
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0.400024414	0.400023997 ± 0000390023	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625		
target TrqArblim Per1 IqTrqOv HwNm f32.value	3	3 ± 0.0000390625	·	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625		
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	6.1998415	6.19984007 ± 0000390625	✓	

0.400024414

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

0.400023997 ± 0000390625



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.71 (Repeat Count = 1)	I WI
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.675000012
rqArblim_ESC_State_Cnt_M_enum	3
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.0999999
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342000008
rqArblim_PosSrvoCmd_MtrNm_M_f32	8.55000019
rqArblim_PreLKATrqReq_HwNm_M_f32	7.9000001
_APAEnableRate_pSec_f32	372.75
APAUseADmpScale Igc	0
	0
z_APAUseRetScale_lgc	0
z_ESCMax_HwNm_f32	1.58000004
 LKAUseSlewCal lgc	1
restances and all section and a section an	28672
_APADisableRateX_HwNm_u4p12[1]	29082
_APADisableRateX_HwNm_u4p12[2]	29491
_APADisableRateX_HwNm_u4p12[3]	29901
_APADisableRateX_HwNm_u4p12[4]	30310
_APADisableRateX_HwNm_u4p12[5]	30720
_APADisableRateY_pSec_u7p9[0]	35840
_APADisableRateY_pSec_u7p9[1]	36352
_APADisableRateY_pSec_u7p9[2]	36864
_APADisableRateY_pSec_u7p9[3]	37376
_APADisableRateY_pSec_u7p9[4]	37888
_APADisableRateY_pSec_u7p9[5]	38400
_APASmoothX_Uls_u2p14[0]	11469
_APASmoothX_Uls_u2p14[1]	11633
_APASmoothX_Uls_u2p14[2]	11796
_APASmoothX_UIs_u2p14[3]	11960
_APASmoothX_Uls_u2p14[4]	12124
_APASmoothX_Uls_u2p14[5]	12288
_APASmoothX_Uls_u2p14[6]	12452
_APASmoothX_Uls_u2p14[7]	12616
_APASmoothX_Uls_u2p14[8]	12780
_APASmoothX_Uls_u2p14[9]	12943
_APASmoothY_Uls_u2p14[0]	8520
_APASmoothY_Uls_u2p14[1]	8684
_APASmoothY_Uls_u2p14[2]	8847
_APASmoothY_Uls_u2p14[3]	9011
_APASmoothY_Uls_u2p14[4]	9175
_APASmoothY_Uls_u2p14[5]	9339
_APASmoothY_Uls_u2p14[6]	9503
_APASmoothY_Uls_u2p14[7]	9667
_APASmoothY_Uls_u2p14[8]	9830
_APASmoothY_UIs_u2p14[9]	9994
LKASlewX_Kph_u8p8[0]	34304
_LKASlewX_Kph_u8p8[1]	34816
LKASlewX Kph u8p8[2]	35328
_txasiewx_xpii_uopo[2] _txasiewx_xpii_uopo[2]	35840
_LKASlewX_Kph_u8p8[4]	36352
_LKASlewX_Kph_u8p8[5]	36864
_LKASlewY_NmpSec_u4p12[0]	6144
_LKASlewY_NmpSec_u4p12[1]	6554
_LKASlewY_NmpSec_u4p12[2]	6636
_LKASlewY_NmpSec_u4p12[3]	8929
_LKASlewY_NmpSec_u4p12[4]	12370
_LKASlewY_NmpSec_u4p12[5]	21627
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	_f: 10.1999998
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_6	enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelive	red_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtrl	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_l	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	m_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	4.17999983		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.35000038		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.42000008		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	418.842987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.5		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.535000026	0.535000026 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	0	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.89550018	-5.89550018 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.55000019	8.55000019 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.88944006	7.88944006 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	
torget TreArbling Dard OnTreOut Mitching 622 value	4.0700000	4.07060070 + 0000300635	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	•

4.8786993

4.87869978 ± 0000390625

1 ± 0000390625

Test Step 3.72 (Repeat Count = 1)	v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.69999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.75
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.352999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.84000015
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.10000002
k_APAEnableRate_pSec_f32	382.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	0
t_APADisableRateX_HwNm_u4p12[1]	0
t_APADisableRateX_HwNm_u4p12[2]	0
t_APADisableRateX_HwNm_u4p12[3]	0

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$





TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
LAPASDISSIPATION LAPASDISSIPATION 36000		Input Value		
LAPASCAMPORTAL 19-50-L. 1979 35552	(_HwNm_u4p12[4]	0		
APASanderbard 1960 1970	(_HwNm_u4p12[5]	0		
	'_pSec_u7p9[0]	36352		
LAMADischelant_pisec_princip	'_pSec_u7p9[1]	36864		
LAPADosePharmy Sec. print	'_pSec_u7p9[2]	37376		
APASanceberlary Desc. p.1965 39912				
LAPASonoxiv Us. updr4				
LAPASmonth V. Lu (2014)				
LAPAShook Us 12914 2 11960				
LAPASmorable Use Jacq 149 1228 LAPASmorable Use Jacq 149 1228 LAPASmorable Use Jacq 149 1228 LAPASmorable Use Jacq 149 122	_ ,			
LAPASmonNX_UB_u2p14 5 1258 LAPASmonNX_UB_u2p14 5 1269 LAPASmonNY_UB_u2p14 5 126				
LAPASmonthY, Us. u2p149 1296				
LAPASmoothY, List, 120-149 1295				
LAPASmontX_UB_v20148 1294 LAPASmontX_UB_v20148 13107 LAPASmontX_UB_v20148 13107 LAPASmontX_UB_v20148 13107 LAPASmontY_UB_v20148 13108	_ , , ,			
LAPASmonthX_UB_L_120148 LAPASmonthY_UB_L_120149 LAPASmonthY_UB_L_12	_ , . ;			
LAPASmontY_UB_U20149				
LAPASmoothy Us. µ2014(1)				
LAPASmonthy Jus. (2014) LAPASmonthy Jus. (201	_ ' ' ' '			
APASmonthy_Us_u2pt4 2 LAPASmonthy_Us_u2pt4 2 LAPASmonthy_Us_u2	_ , , ,			
LAPASmoothY_Ub_u2p14[5]				
LAPASmoothy_Us_u2p4[4] 9398 9907 LAPASmoothy_Us_u2p4[4] 9907 LAPASmoothy_Us_u2p4[4] 9907 LAPASmoothy_Us_u2p4[4] 9907 LAPASmoothy_Us_u2p4[4] 9904 1905 1	_ , . ;			
LAPASmonthy_Usi_uptist	_ , , ,			
LAPASmoothY_Us_uzp14[9]	_ , . ;			
LPASSmoothY_Us_u2p14 9	_ ,			
LPASSmoothY_UB_u2r14 8	_ ,			
LiAASiewX, Kph_u8p8[0]	_ , _ ,			
LikASiewX, Kpp, u8p8(1) 35328 35840				
LLKASiewX, Kph_u8p8[2] 35328 LLKASiewX, Kph_u8p8[3] 35328 LLKASiewX, Kph_u8p8[4] 35840 35840 LLKASiewX, Kph_u8p8[5] 3776 LLKASiewX, Kph_u8p8[5] 3776 3776 LLKASiewX, Mpsec_u4p12[9] 27853 LLKASiewX, Mpsec_u4p12[7] 28262 LLKASiewX, Mpsec_u4p12[7] 28262 28672 LLKASiewX, Mpsec_u4p12[8] 29962 28672 LLKASiewX, Mpsec_u4p12[8] 29981 29991 LLKASiewX, Mpsec_u4p12[8] 29991 29991 LLKASiewX, Mpsec_u4p12[8] 29991 29991 LLKASiewX, Mpsec_u4p12[8] 29991 29991 LLKASiewX, Mpsec_u4p12[8] 29991 29991 29991 LLKASiewX, Mpsec_u4p12[8] 29991 29991 LLKASiewX, Mpsec_u4p12[8] 29991 29	_ ' ' ' '			
LLKASiewX, Kph_u8p8[3] LLKASiewX, Kph_u8p8[4] LLKASiewX, Kph_u8p8[4] LLKASiewX, Kph_u8p8[6] 33586 LLKASiewX, Kph_u8p8[6] 37376 37376 37376 LLKASiewY, MmpSec_u4p12[0] LLKASiewY, MmpSec_u4p12[1] LLKASiewY, MmpSec_u4p12[1] LLKASiewY, MmpSec_u4p12[2] LLKASiewY, MmpSec_u4p12[3] LLKASiewY, MmpSec_u4p12[4] LLKASiewY, MmpSec_u4p1				
LIKASiewX Kph_u8p8[3] 38352 38664				
LLKASiewY, Krph_u8p8[4] LLKASiewX, Krph_u8p8[5] 37376	, 13			
LLKASiewY_NmpSec_u4p12(p)				
LKASlewY_NmpSec_u4p12(p)		37376		
LLKASiewY_NmpSec_u4p12[7] 288672 28872				
LLKASlewY_NmpSec_u4p12 3 29901 2				
LLKASlewY_NmpSec_u4p12[4] 29491	Sec_u4p12[2]	28672		
LikASlewY_NmpSec_u4p12[5]	Sec_u4p12[3]	29082		
target_Rie_Call_Ap_TrqArblim_Faultinjection_SCom_Fitinjection_SignalPath_Uls_f target_Rie_Inst_Ap_TrqArblim_Pert_AssistDFactor_Uls_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_DampingDDFactor_Uls_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCCMd_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCSCIst_Minder_Ont_Jgc target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCSCIst_Minder_Ont_Jgc target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCSCIst_Minder_Ont_Jgc target_TrqArblim_Pert_ESCSCIst_State_enum target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCSCIst_State_enum target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_ESCSCIst_State_enum target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_EMCTorqueDelivered_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_Rie_Inst_Ap_TrqArblim_TrqArblim_Pert_INAGNate_HwNm_f32 target_TrqArblim_Pert_INAGNate_HwNm_f32 target_TrqArblim_Pert_INAGNate_HwNm_f32 target_TrqArblim_Pert_PosSrvoCmd_MtrNm_f32 target_TrqArblim_Per	Sec_u4p12[4]	29491		
target_Re_Inst_Ap_TrqArblim_Per1_AssistDDFactor_UIs_f32 target_Re_Inst_Ap_TrqArblim_Per1_DampingDDFactor_UIs_f32 target_Re_Inst_Ap_TrqArblim_Per1_ESCOState_Mbm_f32 target_Re_Inst_Ap_TrqArblim_Per1_ESCOState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_ESCState_State_enum target_Re_Inst_Ap_TrqArblim_Per1_HMTorque_Hwhm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_HMTorque_Hwhm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACM_Hwhm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_DopTrqOv_MirNm_f32 target_Re_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSevoEnable_Cnt_Igc target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_PosSevoEnable_Cnt_Igc target_TrqArblim_Per1_PosSevoEnable_Cnt_Igc target_TrqArblim_Per1_PosSevoEnable_Cnt_Igc target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDFactor_UIs_f32	Sec_u4p12[5]	29901		
target_Rte_Inst_Ap_TrqArblim_Pert_DampingDDFactor_Uls_f32	_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f:	_Ulsf: 1.20000005		
target_Rte_Inst_Ap_TrqArbilm_Per1_ESCCLImited_CnL gc target_Rte_Inst_Ap_TrqArbilm_Per1_ESCIsLimited_CnL gc target_Rte_Inst_Ap_TrqArbilm_Per1_ESCIsLimited_CnL gc target_Rte_Inst_Ap_TrqArbilm_Per1_ESCIsLimited_CnL gc target_Rte_Inst_Ap_TrqArbilm_Per1_ESCISLimited_CnL gc target_Rte_Inst_Ap_TrqArbilm_Per1_ESCISCIstate_State_enum target_Rte_Inst_Ap_TrqArbilm_Per1_ESCSCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_MOSPHOScillate_State_enum target_Rte_Inst_Ap_TrqArbilm_Per1_Morque_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_TrqArbilm_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_Rer1_LkGACM_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_Rer1_LkGACM_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_LkGACM_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_LkGAState_State_enum target_Rte_Inst_Ap_TrqArbilm_Per1_LkGAState_State_enum target_Rte_Inst_Ap_TrqArbilm_Per1_LkGAState_State_enum target_Rte_Inst_Ap_TrqArbilm_Per1_LkGAState_State_enum target_Rte_Inst_Ap_TrqArbilm_Per1_Morque_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_Morque_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_Morque_HwNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_PoSSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_PoSSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_PoSSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArbilm_Per1_PoSSrvoCmd_MtrNm_f32 target_TrqArbilm_Per1_PoSSrvoCmd_MtrNm_f32 target_TrqArbilm_P	_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_322 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_322 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACTH_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACTH_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACTH_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACTH_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACTH_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACTH_HwNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DoTTqCv_MtrNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCmd_MtrNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCmd_MtrNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCmd_MtrNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCmd_MtrNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCmd_MtrNm_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqCsCmd_MtrNm_132 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uis_132 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCmd_MtrNm_132 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uis_132 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDFactor_Uis_132 target_TrqArblim_Per1_ReturnDFactor_Uis_132 target_TrqArblim_Per1_ReturnDFactor_U	_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_ESCState_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_GMOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LwRACmet_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmet_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmet_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmet_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmet_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmet_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmet_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_LKACmet_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_OpTqov_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_OpTqov_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_PosSevCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_PosSevCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_ReturnDDFactor_Uis_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Perl_ReturnDDFactor_Uis_f32 target_TrqArblim_Perl_ESCState_State_enum_value target_TrqArblim_Perl_ESCState_State_enum_value 1	_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_HwNorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_HwNorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_IqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PopServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosServCend_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_ReturnDDFactor_Uls_f32 target_TrqArblim_Perl_ESCCMd_HwNm_f32.value target_TrqArblim_Perl_ESCCMd_MtrNm_f32 target_TrqArblim_Perl_ESCCMd_MtrNm_f32.value target_TrqArblim_Perl_ESCCMd_MtrNm_f32.value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_LKAState_State_enum_value target_TrqArblim_Perl_DesServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServCend_MtrNm_f32.value target_TrqArblim_Perl_PosServ	_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	gc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNlm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HyTorque_HwNlm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNlm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNlm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNlm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNlm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_ElivNnm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmqueDelivered_HwNlm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOsCCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNlm_f32_value target_TrqArblim_Per1_ESCCmd_HwNlm_f32_value target_TrqArblim_Per1_EGCMd_HwNlm_f32_value target_TrqArblim_Per1_HwTorque_HwNlm_f32_value target_TrqArblim_Per1_LKACMd_HwNlm_f32_value target_TrqArblim_Per1_LKACMd_HwNlm_f32_value target_TrqArblim_Per1_LKACMd_HwNlm_f32_value target_TrqArblim_Per1_LKACMd_HwNlm_f32_value target_TrqArblim_Per1_PosServEnable_Cnt_lgc_value target_TrqArblim_Per1_PosServCnd_MtrNm_f32_value target_	_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	um	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNn_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DrtqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Us_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_ESCCMd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value 1 target_TrqArblim_Per1_ESCState_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 425.292999 target_TrqArblim_Per1_InSServEnable_Cnt_Igc.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 426.292999 target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value 427.0000005 Name Actual Value Expect Value 1 1 ± 0000390625	_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	_f32 target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServComd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_LKAState_State_enum.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_Igc_value 425.292999 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_PosServEnab	_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	m target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_UKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_ESCCMd_HwNm_f32.value target_TrqArblim_Per1_ESCCState_State_enum_value target_TrqArblim_Per1_ESCState_State_enum_value target_TrqArblim_Per1_ENCACmd_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_LKAState_State_enum_value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqAr	_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32_value target_TrqArblim_Per1_ESCCmd_HwNm_f32_value 1 target_TrqArblim_Per1_ESCCState_State_enum_value 1 target_TrqArblim_Per1_GMOSHOScillate_State_enum_value 2 target_TrqArblim_Per1_LKACmd_HwNm_f32_value 425_292999 target_TrqArblim_Per1_LKAState_State_enum_value 425_292999 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32_value 425_292999 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32_value 425_29299	_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim_Perl_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perl_PosServEnable_Cnt_lgc target_TrqArblim_Perl_PosServEnable_Cnt_lgc target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32 target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32 target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32 target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32 target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32 target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Perl_PosServEnable_Cnt_lgc value target_TrqArblim_Perl_PosServEnable_Cnt_lgc value target_TrqArblim_Perl_PosServEnable_Cnt_lgc value target_TrqArblim_Perl_PosServEnable_Cnt_lgc value target_TrqArblim_Perl_PosServEnable_Cnt_lgc value target_TrqArblim_Perl_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value TrqArblim_APADisableRate_Uls_M_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HvNm_f32_value target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOScillate_State_enum.value 2 target_TrqArblim_Per1_GMOSHOScillate_State_enum.value 42 target_TrqArblim_Per1_LKACstate_State_enum.value 42 target_TrqArblim_Per1_LKACstate_State_enum.value 42 target_TrqArblim_Per1_LKACstate_State_enum.value 42 target_TrqArblim_Per1_LKACstate_State_enum.value 42 target_TrqArblim_Per1_LKACstate_State_enum.value 42 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 425.292999 target_TrqArblim_Per1_PosServComd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.0000005 Name Actual Value Expect Value 1 target_Dacted Value 1 target_Dacted Value 1 target_Dacted Value 1 target_Dacted Value 1				
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCCstate_State_enum.value 1 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 42 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 425.292999 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 425.292999 target_TrqArblim_Per1_PosServCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_PosServCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_PosServCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_PosServCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 425.292999 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 425.292999				
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value 1 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 2 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 42 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_LKAState_State_enum.value 425.292999 target_TrqArblim_Per1_DosServEnable_Cnt_lgc.value 425.292999 target_TrqArblim_Per1_DosServEnable_Cnt_lgc.value 425.292000005 Name Actual Value Expected Value TrqArblim_APADisableRate_Uls_M_f32 1 1 1 0000390625				
target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqArblim_Per1_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 5.38000011 target_TrqArblim_Per1_ESCState_State_enum.value 1 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32.value -9 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 2 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_igc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625	-			
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 5.38000011 target_TrqArblim_Per1_ESCState_State_enum.value 1 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 49 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 47.07000017 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_LKAState_State_enum.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 425.292999 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 6.9000005 Name Actual Value Expected Value I ± 0000390625				
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 5.38000011 target_TrqArblim_Per1_ESCState_State_enum.value 1 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32.value -9 target_TrqArblim_Per1_LKACmd_HwNm_f32.value -7.07000017 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_igc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625			_	
target_TrqArblim_Per1_ESCState_State_enum.value 1 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32.value -9 target_TrqArblim_Per1_LKACmd_HwNm_f32.value -7.07000017 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32.value -9 target_TrqArblim_Per1_LKACmd_HwNm_f32.value -7.07000017 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
target_TrqArblim_Per1_HwTorque_HwNm_f32.value -9 target_TrqArblim_Per1_LKACmd_HwNm_f32.value -7.07000017 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6,9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
target_TrqArblim_Per1_LKACmd_HwNm_f32.value -7.07000017 target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6,9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
target_TrqArblim_Per1_LKAState_State_enum.value 2 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 425.292999 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 6.9000001 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.70000005 Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625 1 ± 0000390625				
Name Actual Value Expected Value I TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625				
TrqArblim_APADisableRate_Uls_M_f32 1 1 ± 0000390625			Evnested Value	Darri
1 - 2				Resul
Transpirm Levi State Cht M onum				
TrqArblim_ESC_State_Cnt_M_enum 1 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -5.60325003 -5.60325003 -5.60325003 ± 0.03125				





Name	Actual Value	Expected Value	Result
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.9000001	6.9000001 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.08539987	1.08539999 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	4.28000021	4.28000021 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.2779665	4.27796984 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	~

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

est Step 3.73 (Repeat Count = 1) ame	Input Value
	·
te_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
te_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.725000024
rqArblim_ESC_State_Cnt_M_enum	0
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.4000001
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.363999993
rqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011
rqArblim_PreLKATrqReq_HwNm_M_f32	1.60000002
_APAEnableRate_pSec_f32	392.25
_APAUseADmpScale_lgc	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
_ESCMax_HwNm_f32	3.25
_LKAUseSlewCal_lgc	0
APADisableRateX_HwNm_u4p12[0]	40960
APADisableRateX_HwNm_u4p12[1]	40960
APADisableRateX_HwNm_u4p12[2]	40960
APADisableRateX_HwNm_u4p12[3]	40960
APADisableRateX_HwNm_u4p12[4]	40960
APADisableRateX_HwNm_u4p12[5]	40960
APADisableRateY_pSec_u7p9[0]	36864
APADisableRateY_pSec_u7p9[1]	37376
APADisableRateY_pSec_u7p9[2]	37888
APADisableRateY_pSec_u7p9[3]	38400
APADisableRateY_pSec_u7p9[4]	38912
APADisableRateY pSec u7p9[5]	39424
APASmoothX_Uls_u2p14[0]	11796
APASmoothX Uls u2p14[1]	11960
APASmoothX_Uls_u2p14[2]	12124
APASmoothX_Uls_u2p14[3]	12288
APASmoothX_Uls_u2p14[4]	12452
APASmoothX_Uls_u2p14[5]	12616
APASmoothX_Uls_u2p14[6]	12780
APASmoothX_Uls_u2p14[7]	12943
APASmoothX_Uls_u2p14[8]	13107
APASmoothX Uls u2p14[9]	13271
APASmoothY_Uls_u2p14[0]	8847
APASmoothY Uls u2p14[1]	9011
APASmoothY_Uls_u2p14[1] APASmoothY_Uls_u2p14[2]	9175
APASmoothY_Uls_u2p14[3]	9339
	9503
APASmoothY_Uls_u2p14[4]	9667
APAS month Y_UIs_u2p14[5]	
APASmoothY_Uls_u2p14[6]	9830
APASmoothY_Uls_u2p14[7]	9994
APASmoothY_Uls_u2p14[8]	10158
APASmoothY_Uls_u2p14[9]	10322
LKASlewX_Kph_u8p8[0]	35328 35840
LKASlewX Kph u8p8[1]	

2014-09-16, 12:30:22+0530





	le aver		
Name	Input Value		
t_LKASlewX_Kph_u8p8[3]	36864		
t_LKASlewX_Kph_u8p8[4]	37376		
t_LKASlewX_Kph_u8p8[5]	37888		
t_LKASlewY_NmpSec_u4p12[0]	28262		
t_LKASlewY_NmpSec_u4p12[1]	28672		
t_LKASlewY_NmpSec_u4p12[2]	29082		
t_LKASlewY_NmpSec_u4p12[3]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901		
t_LKASlewY_NmpSec_u4p12[5]	30310		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_fltInje$	2.20000005		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State er	num	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target TrqArblim Per1 ESCTorqueDelivere	ed HwNm f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate S	tate enum	
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm	f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target TrqArblim Per1 IqTrqOv HwNm f3:		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target TrqArblim Per1 LKACmd HwNm f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSetVEnable_Ont_igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32		_	
	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64999962		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.71999979		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	431.743011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.9000001		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.58100003	0.58099997 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	2	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.31099987	-5.31099987 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011	9.13000011 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.59399998	1.59399998 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	3.25	3.25 ± 0000390625	
target TrqArblim Per1 IqTrqOv HwNm f32.value	3	3 ± 0.0000390625	
target TrgArblim Per1 LKATorqueDelivered HwNm f32.value	0	0 ± 0.0000390625	
0.7 - 4	·		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

5.76432991

5.76432991 ± 0000390625

1 ± 0000390625

Test Step 3.74 (Repeat Count = 1)	v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.75
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.04999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.375
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.42000008
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0999999

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAEnableRate_pSec_f32	402
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.1400001
k_LKAUseSlewCal_lgc	4096
t_APADisableRateX_HwNm_u4p12[0] t_APADisableRateX_HwNm_u4p12[1]	8192
t APADisableRateX HwNm u4p12[2]	12288
t_APADisableRateX_HwNm_u4p12[3]	16384
t_APADisableRateX_HwNm_u4p12[4]	20480
t_APADisableRateX_HwNm_u4p12[5]	24576
t_APADisableRateY_pSec_u7p9[0]	37376
t_APADisableRateY_pSec_u7p9[1]	37888
t_APADisableRateY_pSec_u7p9[2]	38400
t_APADisableRateY_pSec_u7p9[3]	38912
t_APADisableRateY_pSec_u7p9[4]	39424
t_APADisableRateY_pSec_u7p9[5]	39936
t_APASmoothX_Uls_u2p14[0]	11960
t_APASmoothX_Uls_u2p14[1]	12124
t_APASmoothX_Uls_u2p14[2]	12288
t_APASmoothX_UIs_u2p14[3]	12452 12616
t_APASmoothX_Uls_u2p14[4] t_APASmoothX_Uls_u2p14[5]	12780
t_APASmoothX_Uls_u2p14[6]	12943
t_APASmoothX_Uls_u2p14[7]	13107
t_APASmoothX_Uls_u2p14[8]	13271
t_APASmoothX_Uls_u2p14[9]	13435
t_APASmoothY_Uls_u2p14[0]	9011
t_APASmoothY_Uls_u2p14[1]	9175
t_APASmoothY_Uls_u2p14[2]	9339
t_APASmoothY_Uls_u2p14[3]	9503
t_APASmoothY_Uls_u2p14[4]	9667
t_APASmoothY_Uls_u2p14[5]	9830
t_APASmoothY_Uls_u2p14[6]	9994
t_APASmoothY_Uls_u2p14[7]	10158
t_APASmoothY_Uls_u2p14[8]	10322
t_APASmoothY_UIs_u2p14[9]	10486 35840
t_KASlewX_Kph_u8p8[0] t_KASlewX_Kph_u8p8[1]	36352
t LKASlewX Kph u8p8[2]	36864
t_LKASlewX_Kph_u8p8[3]	37376
t LKASlewX Kph u8p8[4]	37888
t_LKASlewX_Kph_u8p8[5]	38400
t_LKASlewY_NmpSec_u4p12[0]	28672
t_LKASlewY_NmpSec_u4p12[1]	29082
t_LKASlewY_NmpSec_u4p12[2]	29491
t_LKASlewY_NmpSec_u4p12[3]	29901
t_LKASlewY_NmpSec_u4p12[4]	30310
t_LKASlewY_NmpSec_u4p12[5]	30720
target Rte Call Ap TrqArblim FaultInjection SCom FitInjection SignalPath Uls for	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_Per1_DampingDDFactor_UIs_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCState State enum	target TrgArblim Per1 ESCState State enum
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrgArblim Per1 GMOSHOscillate State enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 7.78000021
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value	7.78000021
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019
O = 1	<u> </u>



Name	Input Value		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.01875019	-5.01875019 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.30000019	7.30000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0849998	2.08500004 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.359985352	0.359984994 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.359985352	0.359984994 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.67210722	4.67211008 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.359985352	0.359984994 ± 0000390625	

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.75 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.774999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.70000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.386000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.5999999
k_APAEnableRate_pSec_f32	411.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	29491
t_APADisableRateX_HwNm_u4p12[1]	29901
t_APADisableRateX_HwNm_u4p12[2]	30310
t_APADisableRateX_HwNm_u4p12[3]	30720
t_APADisableRateX_HwNm_u4p12[4]	31130
t_APADisableRateX_HwNm_u4p12[5]	31539
t_APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	0
t_APADisableRateY_pSec_u7p9[2]	0
t_APADisableRateY_pSec_u7p9[3]	0
t_APADisableRateY_pSec_u7p9[4]	0
t_APADisableRateY_pSec_u7p9[5]	0
t_APASmoothX_Uls_u2p14[0]	12124
t_APASmoothX_Uls_u2p14[1]	12288
t_APASmoothX_Uls_u2p14[2]	12452
t_APASmoothX_Uls_u2p14[3]	12616
t_APASmoothX_Uls_u2p14[4]	12780
t_APASmoothX_Uls_u2p14[5]	12943
t_APASmoothX_Uls_u2p14[6]	13107
t_APASmoothX_Uls_u2p14[7]	13271
t_APASmoothX_Uls_u2p14[8]	13435
t_APASmoothX_Uls_u2p14[9]	13599
t_APASmoothY_Uls_u2p14[0]	9175
t_APASmoothY_Uls_u2p14[1]	9339
t_APASmoothY_Uls_u2p14[2]	9503

2014-09-16, 12:30:22+0530



TrqArblim_Per1

· -			
Name	Input Value		
t_APASmoothY_UIs_u2p14[3]	9667		
t_APASmoothY_Uls_u2p14[4]	9830		
t_APASmoothY_Uls_u2p14[5]	9994	9994	
t_APASmoothY_Uls_u2p14[6]	10158	10158	
t_APASmoothY_Uls_u2p14[7]	10322		
t_APASmoothY_Uls_u2p14[8]	10486		
t_APASmoothY_Uls_u2p14[9]	10650		
t_LKASlewX_Kph_u8p8[0]	36352		
t_LKASlewX_Kph_u8p8[1]	36864		
t LKASlewX Kph u8p8[2]	37376		
t LKASlewX Kph u8p8[3]	37888		
t_LKASlewX_Kph_u8p8[4]	38400		
t LKASlewX Kph u8p8[5]	38912		
t_LKASlewY_NmpSec_u4p12[0]	29082		
t LKASlewY NmpSec u4p12[1]	29491		
t_LKASlewY_NmpSec_u4p12[2]	29901		
t_LKASlewY_NmpSec_u4p12[3]	30310		
t_LKASlewY_NmpSec_u4p12[4]	30720		
t LKASlewY NmpSec u4p12[5]	31130		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target TrqArblim Per1 AssistDDFactor Ul	: f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target TrqArblim Per1 ESCState State er		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target_TrqArblim_Per1_GMOSHOscillate_S		
		_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3.		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	1_132	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.94999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	444.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.774599969	0.774600029 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.72650003	-4.72650003 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59399986	2.5940001 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	•
target TraArblim Bort OnTraOv MtrNm f22 value	4 47177007	4 47177092 + 0000200625	

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TroArblim Per1 CP1 CheckpointReached	1	Rte Call TroArblim Per1 CP1 CheckpointReached	1	_

4.47177887

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

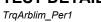
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

4.47177982 ± 0000390625

1 ± 0000390625



	💌
Test Step 3.76 (Repeat Count = 1) Name	Input Value
	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
	target_Rte_Inst_Ap_TrqArblim
	0.800000012
	3
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.3499999
	0.397000015
TrgArblim_PosSrvoCmd_MtrNm_M_f32	10
	3.0999999
	421.5
	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	29901
t_APADisableRateX_HwNm_u4p12[1]	30310
t_APADisableRateX_HwNm_u4p12[2]	30720
t_APADisableRateX_HwNm_u4p12[3]	31130
t_APADisableRateX_HwNm_u4p12[4]	31539
	31949
	65531
	65531
	65531
,	65531
	65531
	65531
t_APASmoothX_Uls_u2p14[0]	12288
t_APASmoothX_Uls_u2p14[1]	12452
	12616
	12780
	12943
t_APASmoothX_Uls_u2p14[5]	13107
t_APASmoothX_Uls_u2p14[6]	13271
t_APASmoothX_UIs_u2p14[7]	13435
t_APASmoothX_UIs_u2p14[8]	13599
t_APASmoothX_UIs_u2p14[9]	13763
	6226 6390
	6554
	6717
	6881
	7045
	7209
t_APASmoothY_Uls_u2p14[7]	7373
	7537
	7700
	36864
	37376
	37888
	38400
	38912
	39424
	29491
	29901
	30310
	30720
	31130
t_LKASlewY_NmpSec_u4p12[5]	31539
target Rte Call Ap TrgArblim FaultInjection SCom FltInjection SignalPath Uls f	5.19999981
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_state_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
Annual Dia land An Tandahina Tandahina Dani OMOOLO sillata Otata annua	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	
	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	arget_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm	1_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.1800003		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.43424988	-4.43424988 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.530029297	0.530028999 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.530029297	0.530028999 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.61877441	3.61876988 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.530029297	0.530028999 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.77 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.824999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.5999999
k_APAEnableRate_pSec_f32	431.25
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	30310
t_APADisableRateX_HwNm_u4p12[1]	30720
t_APADisableRateX_HwNm_u4p12[2]	31130
t_APADisableRateX_HwNm_u4p12[3]	31539
t_APADisableRateX_HwNm_u4p12[4]	31949
t_APADisableRateX_HwNm_u4p12[5]	32358
t_APADisableRateY_pSec_u7p9[0]	37888
t_APADisableRateY_pSec_u7p9[1]	38400
t_APADisableRateY_pSec_u7p9[2]	38912
t_APADisableRateY_pSec_u7p9[3]	39424
t_APADisableRateY_pSec_u7p9[4]	39936
t_APADisableRateY_pSec_u7p9[5]	40448
t_APASmoothX_Uls_u2p14[0]	12452
t_APASmoothX_Uls_u2p14[1]	12616
t_APASmoothX_Uls_u2p14[2]	12780

2014-09-16, 12:30:22+0530





Name	Input Value		
t_APASmoothX_Uls_u2p14[3]	12943		
t_APASmoothX_Uls_u2p14[4]	13107		
t_APASmoothX_Uls_u2p14[5]	13271		
t_APASmoothX_Uls_u2p14[6]	13435		
t_APASmoothX_Uls_u2p14[7]	13599		
t_APASmoothX_Uls_u2p14[8]	13763		
t_APASmoothX_Uls_u2p14[9]	13926		
t_APASmoothY_Uls_u2p14[0]	6390		
t_APASmoothY_Uls_u2p14[1]	6554		
t_APASmoothY_Uls_u2p14[2]	6717		
t_APASmoothY_Uls_u2p14[3]	6881		
t_APASmoothY_Uls_u2p14[4]	7045		
t_APASmoothY_Uls_u2p14[5]	7209		
t_APASmoothY_Uls_u2p14[6]	7373		
t_APASmoothY_Uls_u2p14[7]	7537		
t_APASmoothY_Uls_u2p14[8]	7700		
t_APASmoothY_UIs_u2p14[9]	7864		
t_LKASlewX_Kph_u8p8[0]	37376		
t_LKASlewX_Kph_u8p8[1]	37888		
t_LKASlewX_Kph_u8p8[2]	38400		
t_LKASlewX_Kph_u8p8[3]	38912		
t_LKASlewX_Kph_u8p8[4]	39424 39936		
t_LKASlewX_Kph_u8p8[5]			
t_LKASlewY_NmpSec_u4p12[0]	29901		
t_LKASlewY_NmpSec_u4p12[1]	30310 30720		
t_LKASlewY_NmpSec_u4p12[2]	31130		
t_LKASlewY_NmpSec_u4p12[3]	31539		
t_LKASlewY_NmpSec_u4p12[4]	31949		
t_LKASlewY_NmpSec_u4p12[5]			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f		f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_State_enum target_TrqArblim_Per1_HwTorque_HwNm_f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32			
target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target TrqArblim Per1 TrqOscCmd MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10	_ `	
target TrqArblim Per1 ESCState State enum.value	0		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.25		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.32000017		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	457.542999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.9000001		
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	3.70000005		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.676999986	0.676999986 ± 0000390625	,
TrgArblim ESC State Cnt M enum	0	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.1420002	-4.1420002 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.5	0.5 ± 0.000244140625	
TrgArblim_PreLKATrgReg_HwNm_M_f32	3.59399986	3.5940001 ± 0.03125	
target TrqArblim Per1 AssistDDFactor UIs f32.value	1	1 ± 0000390625	•
target TrqArblim Per1 DampingDDFactor UIs f32.value	1	1 ± 0000390625	
target TrqArblim Per1 ESCIsLimited Cnt Igc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	
	3.83202004	3.83202004 ± 0000390625	
target TrqArblim Per1 OpTrqOv MtrNm f32.value			

1

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

1 ± 0000390625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.78 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.850000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
FrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
FrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75
FrqArblim_PreLKATrqReq_HwNm_M_f32	4.099999
x_APAEnableRate_pSec_f32	441
	1
APAUseAsstScale Igc	1
<pre>APAUseRetScale_lgc</pre>	1
x_ESCMax_HwNm_f32	0.75
C_LKAUseSlewCal_lgc	1
	30720
_APADisableRateX_HwNm_u4p12[0]	
_APADisableRateX_HwNm_u4p12[1]	31130
_APADisableRateX_HwNm_u4p12[2]	31539
_APADisableRateX_HwNm_u4p12[3]	31949
_APADisableRateX_HwNm_u4p12[4]	32358
_APADisableRateX_HwNm_u4p12[5]	32768
_APADisableRateY_pSec_u7p9[0]	38400
_APADisableRateY_pSec_u7p9[1]	38912
_APADisableRateY_pSec_u7p9[2]	39424
_APADisableRateY_pSec_u7p9[3]	39936
_APADisableRateY_pSec_u7p9[4]	40448
_APADisableRateY_pSec_u7p9[5]	40960
_APASmoothX_Uls_u2p14[0]	0
_APASmoothX_Uls_u2p14[1]	0
_APASmoothX_Uls_u2p14[2]	0
_APASmoothX_Uls_u2p14[3]	0
_APASmoothX_Uls_u2p14[4]	0
_APASmoothX_Uls_u2p14[5]	0
APASmoothX_Uls_u2p14[6]	0
APASmoothX Uls u2p14[7]	0
_APASmoothX_Uls_u2p14[8]	0
_APASmoothX_UIs_u2p14[9]	0
_APASmoothY_UIs_u2p14[0]	6554
APASmoothY Uls u2p14[1]	6717
APASmoothY Uls u2p14[2]	6881
: ::	
_APASmoothY_Uls_u2p14[3]	7045
_APASmoothY_Uls_u2p14[4]	7209
_APASmoothY_Uls_u2p14[5]	7373
_APASmoothY_Uls_u2p14[6]	7537
_APASmoothY_Uls_u2p14[7]	7700
_APASmoothY_Uls_u2p14[8]	7864
_APASmoothY_Uls_u2p14[9]	8028
_LKASlewX_Kph_u8p8[0]	37888
_LKASlewX_Kph_u8p8[1]	38400
_LKASlewX_Kph_u8p8[2]	38912
_LKASlewX_Kph_u8p8[3]	39424
_LKASlewX_Kph_u8p8[4]	39936
LKASlewX_Kph_u8p8[5]	40448
LKASlewY_NmpSec_u4p12[0]	30310
LKASlewY_NmpSec_u4p12[1]	30720
_LKASlewY_NmpSec_u4p12[2]	31130
_traslew1_Niiip3ec_u4p12[2] _traslewY_NmpSec_u4p12[3]	31539
	31949
_LKASlewY_NmpSec_u4p12[4]	
_LKASlewY_NmpSec_u4p12[5]	32358
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
	target_TrqArblim_Per1_DampingDDFactor_Uls_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	arget_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e	num		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliver	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	Speed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_t	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Ci	nt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	lm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_L	ls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	n_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046			
target_TrqArblim_Per1_ESCState_State_enum.value	1			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.9000001			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.96999979			
target_TrqArblim_Per1_LKAState_State_enum.value	3			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	463.993011			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.10000038			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.9000001			
Name	Actual Value	Expected Value	Resu	
TrqArblim_APADisableRate_Uls_M_f32	0.700000048	0.699999988 ± 0000390625		
TrqArblim_ESC_State_Cnt_M_enum	1	1		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.84975004	-3.84975004 ± 0.03125		
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75	0.75 ± 0.000244140625		
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.08419991	4.08419991 ± 0.03125		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.6570068	0.657006979 ± 0000390625		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.6570068	0.657006979 ± 0000390625		
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1		
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.75	-0.75 ± 0000390625		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.75	-0.75 ± 0.0000390625		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625		
	1			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	-

0.257244885

0.6570068

0.257245004 ± 0000390625

0.657006979 ± 0000390625

Test Step 3.79 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.875
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.29999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.430000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.5999999
k_APAEnableRate_pSec_f32	450.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	31130
t_APADisableRateX_HwNm_u4p12[1]	31539
t_APADisableRateX_HwNm_u4p12[2]	31949
t_APADisableRateX_HwNm_u4p12[3]	32358
t_APADisableRateX_HwNm_u4p12[4]	32768

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

2014-09-16, 12:30:22+0530





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	33178		
t_APADisableRateY_pSec_u7p9[0]	38912		
t_APADisableRateY_pSec_u7p9[1]	39424		
t_APADisableRateY_pSec_u7p9[2]	39936		
t_APADisableRateY_pSec_u7p9[3]	40448		
t_APADisableRateY_pSec_u7p9[4]	40960		
t_APADisableRateY_pSec_u7p9[5]	41472		
t_APASmoothX_Uls_u2p14[0]	16384		
t_APASmoothX_Uls_u2p14[1]	16384		
t_APASmoothX_Uls_u2p14[2]	16384		
t_APASmoothX_Uls_u2p14[3]	16384		
t_APASmoothX_UIs_u2p14[4]	16384 16384		
t_APASmoothX_UIs_u2p14[5] t_APASmoothX_UIs_u2p14[6]	16384		
t_APASmoothX_UIs_u2p14[7]	16384		
t_APASmoothX_Uls_u2p14[8]	16384		
t_APASmoothX_Uls_u2p14[9]	16384		
t_APASmoothY_Uls_u2p14[0]	6717		
t_APASmoothY_Uls_u2p14[1]	6881		
t_APASmoothY_Uls_u2p14[2]	7045		
t_APASmoothY_Uls_u2p14[3]	7209		
t_APASmoothY_Uls_u2p14[4]	7373		
t_APASmoothY_Uls_u2p14[5]	7537		
t_APASmoothY_Uls_u2p14[6]	7700		
t_APASmoothY_Uls_u2p14[7]	7864		
t_APASmoothY_Uls_u2p14[8]	8028		
t_APASmoothY_Uls_u2p14[9]	8192		
t_LKASlewX_Kph_u8p8[0]	38400		
t_LKASlewX_Kph_u8p8[1]	38912		
t_LKASlewX_Kph_u8p8[2]	39424		
t_LKASlewX_Kph_u8p8[3]	39936		
t_LKASlewX_Kph_u8p8[4]	40448		
t_LKASlewX_Kph_u8p8[5]	40960		
t_LKASlewY_NmpSec_u4p12[0] t_LKASlewY_NmpSec_u4p12[1]	30720 31130		
t_LKASlewY_NmpSec_u4p12[2]	31539		
t_LKASlewY_NmpSec_u4p12[3]	31949		
t_LKASlewY_NmpSec_u4p12[4]	32358		
t LKASlewY NmpSec u4p12[5]	32768		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f	8.19999981		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_I	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_MinSecureVehicleSptarget_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Perl PosServEnable Cnt Igc	target_TrqArblim_Per1_OpTrqOv_MtrNm_is target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target TrqArblim Per1 TrqOscCmd MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.55000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.61999989		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	470.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.0999999	1=	
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HuvTorqueSt/_Huvblm_M_Str_St/_Lile_f22	2 55740090	2 55740090 + 0.02125	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.55749989 8.30000019	-3.55749989 ± 0.03125 8.30000019 ± 0.000244140625	
I OOCITOONIA MILITANI IVI IOZ	0.00000010	5.5555555 TO ± 0.000277 PUULU	

8.30000019

 $TrqArblim_PosSrvoCmd_MtrNm_M_f32$

8.30000019 ± 0.000244140625





Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.59399986	4.59399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	7.5027771	7.50277996 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.80 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.899999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.949999988
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.25
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.099999
<_APAEnableRate_pSec_f32	460.5
<_APAUseADmpScale_lgc	1
c_APAUseAsstScale_lgc	1
<_APAUseRetScale_lgc	1
c_ESCMax_HwNm_f32	3.69000006
LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	31539
_APADisableRateX_HwNm_u4p12[1]	31949
_APADisableRateX_HwNm_u4p12[2]	32358
_APADisableRateX_HwNm_u4p12[3]	32768
_APADisableRateX_HwNm_u4p12[4]	33178
_APADisableRateX_HwNm_u4p12[5]	33587
_APADisableRateY_pSec_u7p9[0]	39424
_APADisableRateY_pSec_u7p9[1]	39936
APADisableRateY pSec u7p9[2]	40448
APADisableRateY_pSec_u7p9[3]	40960
_APADisableRateY_pSec_u7p9[4]	41472
_APADisableRateY_pSec_u7p9[5]	41984
APASmoothX_Uls_u2p14[0]	12616
:_APASmoothX_UIs_u2p14[1]	12780
_APASmoothX_UIs_u2p14[2]	12943
APASmoothX_UIs_u2p14[3]	13107
_APASmoothX_Uls_u2p14[4]	13271
_APASmoothX_Uls_u2p14[5]	13435
	13599
_APASmoothX_UIs_u2p14[6]	
_APASmoothX_Uls_u2p14[7]	13763
:_APASmoothX_UIs_u2p14[8]	13926
_APASmoothX_UIs_u2p14[9]	14090
_APASmoothY_Uls_u2p14[0]	9175
_APASmoothY_Uls_u2p14[1]	9339
_APASmoothY_Uls_u2p14[2]	9503
_APASmoothY_Uls_u2p14[3]	9667
_APASmoothY_Uls_u2p14[4]	9830
_APASmoothY_Uls_u2p14[5]	9994
_APASmoothY_Uls_u2p14[6]	10158
_APASmoothY_Uls_u2p14[7]	10322
_APASmoothY_Uls_u2p14[8]	10486
:_APASmoothY_Uls_u2p14[9]	10650
_LKASlewX_Kph_u8p8[0]	38912
LKASlewX_Kph_u8p8[1]	39424
_LKASlewX_Kph_u8p8[2]	39936
_LKASlewX_Kph_u8p8[3]	40448
:_LKASlewX_Kph_u8p8[4]	40960

2014-09-16, 12:30:22+0530



TrqArblim_Per1

IrqArbiim_Per1			MACILAG
Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	41472		
t_LKASlewY_NmpSec_u4p12[0]	31130		
t LKASlewY NmpSec u4p12[1]	31539		
t LKASlewY NmpSec u4p12[2]	31949		
t LKASlewY NmpSec u4p12[3]	32358		
t LKASlewY NmpSec u4p12[4]	32768		
t LKASlewY NmpSec u4p12[5]	33178		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_U	ls f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target TrgArblim Per1 ESCCmd HwNm		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCIsLimited Cnt Igc	target TrqArblim Per1 ESCIsLimited Cnt		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State e		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCTorqueDelivered HwNm f32	target TrgArblim Per1 ESCTorqueDeliver		
target Rte Inst Ap TrgArblim.TrgArblim Per1 GMOSHOscillate State enum	target TrgArblim Per1 GMOSHOscillate		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrgArblim Per1 HwTorque HwNm	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f		
target Rte Inst Ap TrgArblim.TrgArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State e		
target Rte Inst Ap TrgArblim.TrgArblim Per1 LKATorqueDelivered HwNm f32	target TrgArblim Per1 LKATorqueDeliver		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicles		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_		
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc	target TrqArblim Per1 PosServEnable C		
target Rte Inst Ap TrgArblim.TrgArblim Per1 PosSrvoCmd MtrNm f32	target TrqArblim Per1 PosSrvoCmd MtrN		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_L	_	
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	-8.56000042	02	
target TrqArblim Per1 ESCState State enum.value	3		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target TrqArblim Per1 HwTorque HwNm f32.value	-6.1999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.26999998		
target TrqArblim Per1 LKAState State enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	476.893005		
target TrqArblim Per1 PosServEnable Cnt Igc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.30000019		
Name	Actual Value	Expected Value	Result
TrgArblim APADisableRate Uls M f32	1	1 ± 0000390625	Result
TrgArblim ESC State Cnt M enum	3	3	
	1	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.26525021 8.5	-3.26524997 ± 0.03125 8.5 ± 0.000244140625	
	5.08379984	5.08379984 ± 0.03125	
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.349975586	0.349976003 ± 0000390625	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value		0.349976003 ± 0000390625 0.349976003 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.349975586	0.349976003 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0 ± 0000390625	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	3	3 ± 0.000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value			
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte Call TrgArblim Per1 CP0 CheckpointReached	1	Rte Call TrqArblim Per1 CP0 CheckpointReached	1	1100001 €
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

0.349975586

0.349976003 ± 0000390625

Test Step 3.81 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.925000012	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.600000024	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.451999992	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.5999999	
k_APAEnableRate_pSec_f32	470.25	

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc k_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	31949
t_APADisableRateX_HwNm_u4p12[1]	32358
t_APADisableRateX_HwNm_u4p12[2]	32768
t_APADisableRateX_HwNm_u4p12[3]	33178
t_APADisableRateX_HwNm_u4p12[4]	33587
t_APADisableRateX_HwNm_u4p12[5]	33997
t_APADisableRateY_pSec_u7p9[0]	39936
t_APADisableRateY_pSec_u7p9[1] t_APADisableRateY_pSec_u7p9[2]	40448 40960
t_APADisableRateY_pSec_u7p9[3]	41472
t_APADisableRateY_pSec_u7p9[4]	41984
t_APADisableRateY_pSec_u7p9[5]	42496
t_APASmoothX_Uls_u2p14[0]	12780
t_APASmoothX_Uls_u2p14[1]	12943
t_APASmoothX_Uls_u2p14[2]	13107
t_APASmoothX_UIs_u2p14[3]	13271
t_APASmoothX_UIs_u2p14[4]	13435
t_APASmoothX_UIs_u2p14[5]	13599
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	13763 13926
t_APASmoothX_Uis_u2p14[7]	14090
t APASmoothX Uls u2p14[9]	14254
t_APASmoothY_Uls_u2p14[0]	0
t_APASmoothY_Uls_u2p14[1]	0
t_APASmoothY_Uls_u2p14[2]	0
t_APASmoothY_Uls_u2p14[3]	0
t_APASmoothY_Uls_u2p14[4]	0
t_APASmoothY_Uls_u2p14[5]	0
t_APASmoothY_Uls_u2p14[6]	0
t_APASmoothY_UIs_u2p14[7]	0
t_APASmoothY_Uls_u2p14[8] t_APASmoothY_Uls_u2p14[9]	0
t_LKASlewX_Kph_u8p8[0]	39424
t_LKASlewX_Kph_u8p8[1]	39936
t_LKASlewX_Kph_u8p8[2]	40448
t_LKASlewX_Kph_u8p8[3]	40960
t_LKASlewX_Kph_u8p8[4]	41472
t_LKASlewX_Kph_u8p8[5]	41984
t_LKASlewY_NmpSec_u4p12[0]	31539
t_LKASlewY_NmpSec_u4p12[1]	31949
t_LKASlewY_NmpSec_u4p12[2] t_LKASlewY_NmpSec_u4p12[3]	32358 32768
t LKASlewY NmpSec u4p12[4]	33178
t_LKASlewY_NmpSec_u4p12[5]	33587
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f.	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum target TrqArblim Per1 HwTorque HwNm f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32 target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992
target_TrqArblim_Per1_ESCState_State_enum.value	2 0
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999
angon or i_rim rorquo_rimini_loz.valuc	
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008



Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.768999994	0.768999994 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.97300005	-2.97300005 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58360004	5.58360004 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-2.75	-2.75 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-2.75	-2.75 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	-

Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
Rte Inst Ap TrgArblim	target_Rte_Inst_Ap_TrqArblim
ГгgArblim APADisableRate Uls M f32	0.94999988
FrgArblim_ESC_State_Cnt_M_enum	4
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.25
FrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrgArblim PosSrvoCmd MtrNm M f32	1.75
TrgArblim PreLKATrgReg HwNm M f32	6.0999999
<pre>c_APAEnableRate_pSec_f32</pre>	480
APAUseADmpScale Igc	0
CAPAUseAsstScale_lgc	0
<pre><_APAUseRetScale_lgc</pre>	0
<pre>c_Ar Abserted cale_igd </pre> <pre>c_ESCMax_HwNm_f32</pre>	1.23000002
CLKAUseSlewCal lgc	0
_APADisableRateX_HwNm_u4p12[0]	32358
_APADisableNateX_HwNm_u4p12[1]	32768
_APADisableNateX_HwNm_u4p12[1]	33178
_APADisableRateX_HwNm_u4p12[3]	33587
APADisableRateX HwNm u4p12[4]	33997
APADisableRateX_HwNm_u4p12[5]	34406
APADisableRateY pSec u7p9[0]	40448
_APADisableRateY_pSec_u7p9[1]	40960
_APADisableRateY_pSec_u7p9[2]	41472
APADisableRateY pSec u7p9[3]	41984
_APADisableRateY_pSec_u7p9[4]	42496
APADisableRateY pSec u7p9[5]	43008
_APASmoothX_Uls_u2p14[0]	12943
APASmoothX Uls u2p14[1]	13107
_APASmoothX_Uls_u2p14[1]	13271
_APASmoothX_UIs_u2p14[2]	13435
_APASmoothX_Uls_u2p14[4]	13599
_APASmoothX_UIs_u2p14[5]	13763
APASmoothX Uls u2p14[6]	13926
_APASmoothX_UIs_u2p14[7]	14090
_APASmoothX_Uls_u2p14[8]	14254
_APASmoothX_Uls_u2p14[9]	14418
APASmoothY Uls u2p14[0]	16384
APASmoothY Uls u2p14[1]	16384
_APASmoothY_Uls_u2p14[2]	16384
_APASmoothY_Uls_u2p14[3]	16384





Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	16384		
t_APASmoothY_Uls_u2p14[5]	16384		
t_APASmoothY_Uls_u2p14[6]	16384		
t_APASmoothY_Uls_u2p14[7]	16384		
t_APASmoothY_Uls_u2p14[8]	16384		
t_APASmoothY_Uls_u2p14[9]	16384		
t_LKASlewX_Kph_u8p8[0]	39936		
t_LKASlewX_Kph_u8p8[1]	40448		
t_LKASlewX_Kph_u8p8[2]	40960		
t_LKASlewX_Kph_u8p8[3]	41472		
t_LKASlewX_Kph_u8p8[4]	41984		
t_LKASlewX_Kph_u8p8[5]	42496		
t_LKASlewY_NmpSec_u4p12[0]	31949		
t_LKASlewY_NmpSec_u4p12[1]	32358		
t_LKASlewY_NmpSec_u4p12[2]	32768		
t_LKASlewY_NmpSec_u4p12[3]	33178		
t_LKASlewY_NmpSec_u4p12[4]	33587		
t_LKASlewY_NmpSec_u4p12[5]	33997		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_fraction_SignalPath_Uls$	5		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_I	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.5		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.56999993		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	489.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.60000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.6999981		
Name	Actual Value	Expected Value	Resul
TrgArblim APADisableRate Uls M f32	0.791999996	0.791999996 ± 0000390625	
TrgArblim ESC State Cnt M enum	3	3	•
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	-2.68074989	-2.68074989 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	1.75	1.75 ± 0.000244140625	
TrgArblim PreLKATrgReq HwNm M f32	6.09399986	6.09399986 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target TrqArblim Per1 ESCIsLimited Cnt Igc.value	0	0	
	0	0 ± 0000390625	
target TrgArblim Per1 ESCTorqueDelivered HwNm t32 value	1 -		•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value target_TrgArblim_Per1_IgTrgQv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625 3 ± 0.0000390625	
	3 3 1.38600004	3 ± 0.0000390625 3 ± 0.0000390625 1.38600004 ± 0000390625	

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓



Test Step 3.83 (Repeat Count = 1)	· ·
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.975000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.100000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.5999999
k_APAEnableRate_pSec_f32	489.75
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	32768
t_APADisableRateX_HwNm_u4p12[1]	33178
t_APADisableRateX_HwNm_u4p12[2]	33587
t_APADisableRateX_HwNm_u4p12[3]	33997
t_APADisableRateX_HwNm_u4p12[4]	34406
t_APADisableRateX_HwNm_u4p12[5]	34816
t_APADisableRateY_pSec_u7p9[0]	40960
t_APADisableRateY_pSec_u7p9[1]	41472
t_APADisableRateY_pSec_u7p9[2]	41984
t_APADisableRateY_pSec_u7p9[3]	42496
t_APADisableRateY_pSec_u7p9[4]	43008 43520
t_APADisableRateY_pSec_u7p9[5]	13107
t_APASmoothX_UIs_u2p14[0]	13271
t_APASmoothX_Uls_u2p14[1]	13435
t_APASmoothX_UIs_u2p14[2]	13599
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	13763
t_APASmoothX_Uls_u2p14[5]	13926
t_APASmoothX_Uls_u2p14[6]	14090
t_APASmoothX_Uls_u2p14[7]	14254
t_APASmoothX_UIs_u2p14[8]	14418
t_APASmoothX_UIs_u2p14[9]	14582
t_APASmoothY_Uls_u2p14[0]	12616
t_APASmoothY_Uls_u2p14[1]	12780
t_APASmoothY_Uls_u2p14[2]	12943
t_APASmoothY_Uls_u2p14[3]	13107
t APASmoothY Uls u2p14[4]	13271
t APASmoothY Uls u2p14[5]	13435
t_APASmoothY_Uls_u2p14[6]	13599
t_APASmoothY_Uls_u2p14[7]	13763
t_APASmoothY_Uls_u2p14[8]	13926
t_APASmoothY_Uls_u2p14[9]	14090
t_LKASlewX_Kph_u8p8[0]	40448
t_LKASlewX_Kph_u8p8[1]	40960
t_LKASlewX_Kph_u8p8[2]	41472
t_LKASlewX_Kph_u8p8[3]	41984
t_LKASlewX_Kph_u8p8[4]	42496
t_LKASlewX_Kph_u8p8[5]	43008
t_LKASlewY_NmpSec_u4p12[0]	32358
t_LKASlewY_NmpSec_u4p12[1]	32768
t_LKASlewY_NmpSec_u4p12[2]	33178
t_LKASlewY_NmpSec_u4p12[3]	33587
t_LKASlewY_NmpSec_u4p12[4]	33997
t_LKASlewY_NmpSec_u4p12[5]	34406
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32





Name	Input Value		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.11999989		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.1500001		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	496.243011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.10000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.38850021	-2.38849998 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.10000038	-8.10000038 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.58319998	6.58319998 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-2.06588173	-2.06588006 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.84 (Repeat Count = 1)	la companya da managaran da mana
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.69999988
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.352999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.84000015
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.10000002
k_APAEnableRate_pSec_f32	382.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
<_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	24166
t_APADisableRateX_HwNm_u4p12[1]	24576
t_APADisableRateX_HwNm_u4p12[2]	24986
t_APADisableRateX_HwNm_u4p12[3]	25395
t_APADisableRateX_HwNm_u4p12[4]	25805
t_APADisableRateX_HwNm_u4p12[5]	26214
t_APADisableRateY_pSec_u7p9[0]	43008
t_APADisableRateY_pSec_u7p9[1]	43520
t_APADisableRateY_pSec_u7p9[2]	44032
t_APADisableRateY_pSec_u7p9[3]	44544
t_APADisableRateY_pSec_u7p9[4]	45056
t_APADisableRateY_pSec_u7p9[5]	45568
t_APASmoothX_Uls_u2p14[0]	8356
t_APASmoothX_Uls_u2p14[1]	8520
t_APASmoothX_Uls_u2p14[2]	8684

2014-09-16, 12:30:22+0530

TrqArblim_Per1



		<u> </u>	
Name	Input Value		
t_APASmoothX_Uls_u2p14[3]	8847		
t_APASmoothX_Uls_u2p14[4]	9011		
t_APASmoothX_Uls_u2p14[5]	9175		
t_APASmoothX_Uls_u2p14[6]	9339		
t_APASmoothX_Uls_u2p14[7]	9503		
t_APASmoothX_Uls_u2p14[8]	9667		
t_APASmoothX_UIs_u2p14[9]	9830		
t_APASmoothY_Uls_u2p14[0]	5407		
t_APASmoothY_Uls_u2p14[1]	5571		
t_APASmoothY_Uls_u2p14[2]	5734		
t_APASmoothY_Uls_u2p14[3]	5898		
t_APASmoothY_Uls_u2p14[4]	6062		
t_APASmoothY_Uls_u2p14[5]	6226		
t_APASmoothY_Uls_u2p14[6]	6390		
t_APASmoothY_Uls_u2p14[7]	6554		
t_APASmoothY_Uls_u2p14[8]	6717		
t_APASmoothY_UIs_u2p14[9]	6881		
t_LKASlewX_Kph_u8p8[0]	34816		
t_LKASlewX_Kph_u8p8[1]	35328		
t_LKASlewX_Kph_u8p8[2]	35840		
t_LKASlewX_Kph_u8p8[3]	36352		
t_LKASlewX_Kph_u8p8[4]	36864		
t_LKASlewX_Kph_u8p8[5]	37376 27853		
t_LKASlewY_NmpSec_u4p12[0]			
t_LKASlewY_NmpSec_u4p12[1] t_LKASlewY_NmpSec_u4p12[2]	28262 28672		
t_LKASlewY_NmpSec_u4p12[3]	29082		
t_LKASlewY_NmpSec_u4p12[4]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	: f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	n_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10.2299995		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.07000017		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	425.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.70000005		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.60325003	-5.60325003 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.9000001	6.9000001 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.08539987	1.08539999 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.58001709	0.140015006 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.58001709	0.140015006 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.08539987	1.08539987 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.08539987	1.08539987 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.89788222	-2.06588006 ± 0000390625	
target TrgArblim Per1 ReturnDDFactor Llls f32 value	0.58001709	0.140015006 + 0000390625	

0.58001709

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

0.140015006 ± 0000390625



Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Fest Step 3.85 (Repeat Count = 1)	Input Value
	<u> </u>
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.725000024
FrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.4000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.363999993
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011
FrqArblim_PreLKATrqReq_HwNm_M_f32	1.60000002
<pre>c_APAEnableRate_pSec_f32</pre>	392.25
C_APAUseADmpScale_lgc	0
_APAUseAsstScale_lgc	0
<pre>c_APAUseRetScale_lgc</pre>	0
c_ESCMax_HwNm_f32	4.28000021
 climbalcolor: 100%	0
_APADisableRateX_HwNm_u4p12[0]	24576
_APADisableRateX_HwNm_u4p12[1]	24986
_APADisableNateX_HwNm_u4p12[2]	25395
_APADisableRateX_HwNm_u4p12[3]	25805
	26214
_APADisableRateX_HwNm_u4p12[4]	
_APADisableRateX_HwNm_u4p12[5]	26624
_APADisableRateY_pSec_u7p9[0]	43520
_APADisableRateY_pSec_u7p9[1]	44032
_APADisableRateY_pSec_u7p9[2]	44544
_APADisableRateY_pSec_u7p9[3]	45056
_APADisableRateY_pSec_u7p9[4]	45568
_APADisableRateY_pSec_u7p9[5]	46080
_APASmoothX_Uls_u2p14[0]	8520
_APASmoothX_Uls_u2p14[1]	8684
_APASmoothX_Uls_u2p14[2]	8847
_APASmoothX_Uls_u2p14[3]	9011
_APASmoothX_Uls_u2p14[4]	9175
_APASmoothX_Uls_u2p14[5]	9339
_APASmoothX_Uls_u2p14[6]	9503
_APASmoothX_Uls_u2p14[7]	9667
_APASmoothX_Uls_u2p14[8]	9830
_APASmoothX_Uls_u2p14[9]	9994
_APASmoothY_Uls_u2p14[0]	5571
APASmoothY Uls u2p14[1]	5734
_APASmoothY_UIs_u2p14[2]	5898
_APASmoothY_UIs_u2p14[3]	6062
_APASmoothY_Uls_u2p14[4]	6226
_APASmoothY_Uls_u2p14[5]	6390
_APASmoothY_Uls_u2p14[6]	6554
_APASmoothY_Uls_u2p14[7]	6717
_APASmoothY_Uls_u2p14[8]	6881
_APASmoothY_Uls_u2p14[9]	7045
_LKASlewX_Kph_u8p8[0]	35328
_LKASlewX_Kph_u8p8[1]	35840
_LKASlewX_Kph_u8p8[2]	36352
_LKASlewX_Kph_u8p8[3]	36864
LKASlewX_Kph_u8p8[4]	37376
_LKASlewX_Kph_u8p8[5]	37888
LKASlewY NmpSec u4p12[0]	28262
LKASlewY NmpSec u4p12[1]	28672
_LKASlewY_NmpSec_u4p12[1]	29082
_LKASlewY_NmpSec_u4p12[3] _LKASlewY_NmpSec_u4p12[4]	29491
	29901
_LKASlewY_NmpSec_u4p12[5]	30310





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target TrgArblim Per1 ESCIsLimited Cnt Igc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f3	target_TrqArblim_Per1_ESCTorqueDelivered_HwN	lm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwN	m_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3000000000000000000000000000000000000$	2 target_TrqArblim_Per1_MinSecureVehicleSpeed_k	(ph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64999962		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.71999979		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	431.743011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.9000001		
Name	Actual Value Exp	ected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.555000007 0.555	5000007 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	2 2		•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.31099987 -5.31	099987 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011 9.130	000011 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.59399998 1.593	399998 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1 1 ± 0	000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1 1 ± 0	000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1 1		•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	4.28000021 4.280	000021 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3 3 ± 0	.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0 ± 0	.0000390625	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

4.80018139

4.80017996 ± 0000390625

1 ± 0000390625

Test Step 3.86 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.75
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.04999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.375
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.42000008
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0999999
k_APAEnableRate_pSec_f32	402
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	24986
t_APADisableRateX_HwNm_u4p12[1]	25395
t_APADisableRateX_HwNm_u4p12[2]	25805
t_APADisableRateX_HwNm_u4p12[3]	26214
t_APADisableRateX_HwNm_u4p12[4]	26624

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

2014-09-16, 12:30:22+0530



Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	27034		
t_APADisableRateY_pSec_u7p9[0]	42496		
t_APADisableRateY_pSec_u7p9[1]	43008		
t_APADisableRateY_pSec_u7p9[2]	43520		
t_APADisableRateY_pSec_u7p9[3]	44032		
	44544		
t_APADisableRateY_pSec_u7p9[4]			
t_APADisableRateY_pSec_u7p9[5]	45056		
t_APASmoothX_Uls_u2p14[0]	8684		
t_APASmoothX_Uls_u2p14[1]	8847		
t_APASmoothX_Uls_u2p14[2]	9011		
t_APASmoothX_Uls_u2p14[3]	9175		
t_APASmoothX_Uls_u2p14[4]	9339		
t_APASmoothX_Uls_u2p14[5]	9503		
t_APASmoothX_Uls_u2p14[6]	9667		
t_APASmoothX_Uls_u2p14[7]	9830		
t_APASmoothX_Uls_u2p14[8]	9994		
t_APASmoothX_Uls_u2p14[9]	10158		
t_APASmoothY_Uls_u2p14[0]	5734		
t_APASmoothY_Uls_u2p14[1]	5898		
t_APASmoothY_Uls_u2p14[2]	6062		
t_APASmoothY_Uls_u2p14[3]	6226		
t_APASmoothY_Uls_u2p14[4]	6390		
t_APASmoothY_Uls_u2p14[5]	6554		
t_APASmoothY_Uls_u2p14[6]	6717		
t_APASmoothY_Uls_u2p14[7]	6881		
t_APASmoothY_Uls_u2p14[8]	7045		
t APASmoothY Uls u2p14[9]	7209		
t_LKASlewX_Kph_u8p8[0]	35840		
t_LKASlewX_Kph_u8p8[1]	36352		
	36864		
t_LKASlewX_Kph_u8p8[2]			
t_LKASlewX_Kph_u8p8[3]	37376		
t_LKASlewX_Kph_u8p8[4]	37888		
t_LKASlewX_Kph_u8p8[5]	38400		
t_LKASlewY_NmpSec_u4p12[0]	28672		
t_LKASlewY_NmpSec_u4p12[1]	29082		
t_LKASlewY_NmpSec_u4p12[2]	29491		
t_LKASlewY_NmpSec_u4p12[3]	29901		
t_LKASlewY_NmpSec_u4p12[4]	30310		
t_LKASlewY_NmpSec_u4p12[5]	30720		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_fltInje$	3.20000005		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_end	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target Rte Inst Ap TrgArblim.TrgArblim Per1 LKACmd HwNm f32	target TrgArblim Per1 LKACmd HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3.		
target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtiNin_is2 target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_mtrNm_is. target_TrqArblim_Per1_PosServEnable_Cnt		
		- ·	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_132	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.01875019	-5.01875019 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.30000019	7.30000019 ± 0.000244140625	-





Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0849998	2.08500004 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.21201801	3.21201992 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

lame	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.774999976
rqArblim_ESC_State_Cnt_M_enum	4
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.70000005
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.386000007
rqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004
rgArblim PreLKATrgReg HwNm M f32	2.5999999
_APAEnableRate_pSec_f32	411.75
_APAUseADmpScale_lgc	0
APAUseAsstScale Igc	0
APAUseRetScale Igc	0
ESCMax_HwNm_f32	3.1400001
LKAUseSlewCal Igc	0
rvvosesiewcar_igc APADisableRateX_HwNm_u4p12[0]	25395
APADisableRateX_HwNm_u4p12[1]	25805
_APADisableRateX_HwNm_u4p12[1]	26214
APADisableRateX_HwNm_u4p12[3]	26624
APADisableRateX_HwNm_u4p12[4]	27034
APADisableRateX_nwhin_u4p12[4] APADisableRateX_HwNm_u4p12[5]	27443
APADisableRateY_pSec_u7p9[0]	43008
APADisableRateY pSec u7p9[1]	43520
, , , ,	44032
APADisableRateY_pSec_u7p9[2]	44544
APADisableRateY_pSec_u7p9[3]	45056
APADisableRateY_pSec_u7p9[4]	45568
APADisableRateY_pSec_u7p9[5]	
APASmoothX_Uls_u2p14[0]	8847
APAS month X Ulb u 2014 [1]	9011
APASmoothX_Uls_u2p14[2]	9175
APASmoothX_Uls_u2p14[3]	9339
APASmoothX_Uls_u2p14[4]	9503
APASmoothX_Uls_u2p14[5]	9667
APASmoothX_Uls_u2p14[6]	9830
APASmoothX_UIs_u2p14[7]	9994
APASmoothX_Uls_u2p14[8]	10158
APASmoothX_UIs_u2p14[9]	10322
APASmoothY_Uls_u2p14[0]	5898
APASmoothY_Uls_u2p14[1]	6062
APASmoothY_UIs_u2p14[2]	6226
APASmoothY_Uls_u2p14[3]	6390
APASmoothY_UIs_u2p14[4]	6554
APASmoothY_Uls_u2p14[5]	6717
APASmoothY_Uls_u2p14[6]	6881
APASmoothY_Uls_u2p14[7]	7045
APASmoothY_Uls_u2p14[8]	7209
APASmoothY_Uls_u2p14[9]	7373
LKASlewX_Kph_u8p8[0]	36352
_LKASlewXKph_u8p8[1]	36864
_LKASlewX_Kph_u8p8[2]	37376
_LKASlewX_Kph_u8p8[3]	37888

2014-09-16, 12:30:22+0530



TrgArblim Per1

TrqArblim_Per1			MAZUICAL
Name	Input Value		
t_LKASlewX_Kph_u8p8[4]	38400		
t_LKASlewX_Kph_u8p8[5]	38912		
t_LKASlewY_NmpSec_u4p12[0]	29082		
t LKASlewY NmpSec u4p12[1]	29491		
t LKASlewY NmpSec u4p12[2]	29901		
t_LKASlewY_NmpSec_u4p12[3]	30310		
t LKASlewY NmpSec u4p12[4]	30720		
t_LKASlewY_NmpSec_u4p12[5]	31130		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	f. 4.19999981		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_U	lls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	r_Uls_f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target TrqArblim Per1 ESCCmd HwNm		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnf	: lgc	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelive		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrgArblim Per1 HwTorque HwNm	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target_TrqArblim_Per1_LKATorqueDeliver		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicle		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_		
target Rte Inst Ap TrgArblim.TrgArblim Per1 PosServEnable Cnt Igc	target TrgArblim Per1 PosServEnable C		
target Rte Inst Ap TrgArblim.TrgArblim Per1 PosSrvoCmd MtrNm f32	target_TrqArblim_F_ct1_rosocrvenable_ont_ige target TrqArblim Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.19999981	_	
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target TrqArblim Per1 GMOSHOscillate State enum.value	0		
target TrqArblim Per1 HwTorque HwNm f32.value	-7.94999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998		
target TrqArblim Per1 LKAState State enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	444.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	7.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.29999995		
Name	Actual Value	Expected Value	Resul
TrqArblim APADisableRate Uls M f32	0.606999993	0.606999993 ± 0000390625	Result
TrgArblim ESC State Cnt M enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.72650003	-4.72650003 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.00024414062	
TrgArblim PreLKATrgReg HwNm M f32	2.59399986	2.5940001 ± 0.03125	20
target TrqArblim Per1 AssistDDFactor Uls f32.value	2.59399966	1 ± 0000390625	
target_rrqArblim_Per1_AssistDbPactor_Uis_i3z.value target_rrqArblim_Per1_DampingDDFactor_Uis_i3z.value	1	1 ± 0000390625	
· - · · · ·	0	1 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0 ± 0000390625	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value			
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625 2.5940001 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986		
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.51673651	2.51674008 ± 0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

1 ± 0000390625

Test Step 3.88 (Repeat Count = 1)		
Name	Input Value	
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.800000012	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.3499999	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.397000015	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10	
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999	
k_APAEnableRate_pSec_f32	421.5	

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAUseAptScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t_APADisableRateY_pSec_u7p9[0]	43520
t_APADisableRateY_pSec_u7p9[1]	44032
t_APADisableRateY_pSec_u7p9[2]	44544
t_APADisableRateY_pSec_u7p9[3]	45056 45568
t_APADisableRateY_pSec_u7p9[4] t_APADisableRateY_pSec_u7p9[5]	46080
t_APASmoothX_UIs_u2p14[0]	9011
t APASmoothX Uls u2p14[1]	9175
t_APASmoothX_Uls_u2p14[2]	9339
t_APASmoothX_Uls_u2p14[3]	9503
t_APASmoothX_Uls_u2p14[4]	9667
t_APASmoothX_UIs_u2p14[5]	9830
t_APASmoothX_Uls_u2p14[6]	9994
t_APASmoothX_Uls_u2p14[7]	10158
t_APASmoothX_Uls_u2p14[8]	10322
t_APASmoothX_UIs_u2p14[9]	10486
t_APASmoothY_Uls_u2p14[0]	6062
t_APASmoothY_Uls_u2p14[1]	6226
t_APASmoothY_Uls_u2p14[2]	6390 6554
t_APASmoothY_Uls_u2p14[3] t_APASmoothY_Uls_u2p14[4]	6717
t_APASmoothY_Uls_u2p14[5]	6881
t_APASmoothY_Uls_u2p14[6]	7045
t_APASmoothY_Uls_u2p14[7]	7209
t_APASmoothY_Uls_u2p14[8]	7373
t_APASmoothY_Uls_u2p14[9]	7537
t_LKASlewX_Kph_u8p8[0]	36864
t_LKASlewX_Kph_u8p8[1]	37376
t_LKASlewX_Kph_u8p8[2]	37888
t_LKASlewX_Kph_u8p8[3]	38400
t_LKASlewX_Kph_u8p8[4]	38912
t_LKASlewX_Kph_u8p8[5]	39424 29491
t_LKASlewY_NmpSec_u4p12[0] t_LKASlewY_NmpSec_u4p12[1]	29901
t_LKASlewY_NmpSec_u4p12[2]	30310
t_LKASlewY_NmpSec_u4p12[3]	30720
t LKASlewY NmpSec u4p12[4]	31130
t_LKASlewY_NmpSec_u4p12[5]	31539
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f:	5.19999981
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32 target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACritid_HWNIT_IS2
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	7.500000
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008





Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.43424988	-4.43424988 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.54216909	3.54217005 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte Inst Ap TrgArblim	target_Rte_Inst_Ap_TrqArblim
TrgArblim APADisableRate Uls M f32	0.925000012
FrgArblim_ESC_State_Cnt_M_enum	1
FrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.600000024
FrgArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
FrgArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrgArblim PreLKATrgReg HwNm M f32	5.5999999
<pre><_APAEnableRate_pSec_f32</pre>	470.25
APAUseADmpScale Igc	1
<pre><_APAUseAsstScale_lgc</pre>	1
<pre><_APAUseRetScale_lgc</pre>	1
CESCMax_HwNm_f32	0.23999995
C LKAUseSlewCal Igc	1
APADisableRateX HwNm u4p12[0]	22118
_APADisableRateX_HwNm_u4p12[1]	22528
_APADisableRateX_HwNm_u4p12[2]	22938
_APADisableRateX_HwNm_u4p12[3]	23347
APADisableRateX HwNm u4p12[4]	23757
APADisableRateX HwNm u4p12[5]	24166
APADisableRateY pSec u7p9[0]	33280
_APADisableRateY_pSec_u7p9[1]	33792
_APADisableRateY_pSec_u7p9[2]	34304
APADisableRateY pSec u7p9[3]	34816
APADisableRateY_pSec_u7p9[4]	35328
APADisableRateY pSec u7p9[5]	35840
_APASmoothX_Uls_u2p14[0]	9175
APASmoothX Uls u2p14[1]	9339
_APASmoothX_Uls_u2p14[2]	9503
_APASmoothX_Uls_u2p14[3]	9667
_APASmoothX_Uls_u2p14[4]	9830
_APASmoothX_Uls_u2p14[5]	9994
APASmoothX Uls u2p14[6]	10158
_APASmoothX_Uls_u2p14[7]	10322
_APASmoothX_Uls_u2p14[8]	10486
_APASmoothX_Uls_u2p14[9]	10650
APASmoothY Uls u2p14[0]	6226
APASmoothY Uls u2p14[1]	6390
_APASmoothY_Uls_u2p14[2]	6554
APASmoothY_Uls_u2p14[3]	6717

2014-09-16, 12:30:22+0530



TrqArblim_Per1

TrqArbiim_Peri			MACITAL
Name	Input Value		
t APASmoothY Uls u2p14[4]	6881		
t_APASmoothY_Uls_u2p14[5]	7045		
t_APASmoothY_Uls_u2p14[6]	7209		
t APASmoothY Uls u2p14[7]	7373		
t_APASmoothY_Uls_u2p14[8]	7537		
t_APASmoothY_Uls_u2p14[9]	7700		
t_LKASlewX_Kph_u8p8[0]	39424		
t_LKASlewX_Kph_u8p8[1]	39936		
t LKASlewX Kph u8p8[2]	40448		
t_LKASlewX_Kph_u8p8[3]	40960		
t_LKASlewX_Kph_u8p8[4]	41472		
t_LKASlewX_Kph_u8p8[5]	41984		
t_LKASlewY_NmpSec_u4p12[0]	31539		
t_LKASlewY_NmpSec_u4p12[1]	31949		
t_LKASlewY_NmpSec_u4p12[2]	32358		
t_LKASlewY_NmpSec_u4p12[3]	32768		
t_LKASlewY_NmpSec_u4p12[4]	33178		
t_LKASlewY_NmpSec_u4p12[5]	33587		
target Rte Call Ap TrgArblim FaultInjection SCom FltInjection SignalPath UIs f			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target TrqArblim Per1 AssistDDFactor Uls	; f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target TrqArblim Per1 DampingDDFactor	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	· -	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target TrqArblim Per1 PosServEnable Cn		
target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target TrqArblim Per1 HwTorque HwNm f32.value	-5.8499999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	8.6999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.795000017	0.795000017 ± 0000390625	Resu
TrgArblim ESC State Cnt M enum	0.795000017	0.795000017 ± 0000390625	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.97300005	-2.97300005 ± 0.03125	
TrqArblim PosSrvoCmd MtrNm M f32	1.5	1.5 ± 0.000244140625	
TrqArblim PreLKATrqReq HwNm M f32	5.58360004	5.58360004 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.626373291	0.626372993 ± 0000390625	
target_TrqArblim_Per1_AssistDbPactor_Uls_f32.value	0.626373291	0.626372993 ± 0000390625 0.626372993 ± 0000390625	
target_irqArblim_Peri_DampingDDFactor_Uis_ts2.value target_trqArblim_Peri_DampingDDFactor_Uis_ts2.value	0.626373291	0.626372993 ± 0000390625	
· - ·	0		
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value		0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0.560440063

0.626373291

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

0.560440004 ± 0000390625

0.626372993 ± 0000390625



Test Step 3.90 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.94999988
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.463
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.0999999
k_APAEnableRate_pSec_f32	480
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938
t_APADisableRateX_HwNm_u4p12[2]	23347
t_APADisableRateX_HwNm_u4p12[3]	23757
t_APADisableRateX_HwNm_u4p12[4]	24166
t_APADisableRateX_HwNm_u4p12[5]	24576
t_APADisableRateY_pSec_u7p9[0]	34304
t_APADisableRateY_pSec_u7p9[1]	34816
t_APADisableRateY_pSec_u7p9[2]	35328
t_APADisableRateY_pSec_u7p9[3]	35840
t_APADisableRateY_pSec_u7p9[4]	36352
t_APADisableRateY_pSec_u7p9[5]	36864
t_APASmoothX_UIs_u2p14[0]	9339
t_APASmoothX_UIs_u2p14[1]	9503
t_APASmoothX_UIs_u2p14[2]	9667
t_APASmoothX_Uls_u2p14[3]	9830 9994
t_APASmoothX_UIs_u2p14[4]	10158
t_APASmoothX_Uls_u2p14[5] t_APASmoothX_Uls_u2p14[6]	10322
t_APASmoothX_UIs_u2p14[7]	10486
t_APASmoothX_Uls_u2p14[8]	10650
t_APASmoothX_Uls_u2p14[9]	10813
t_APASmoothY_Uls_u2p14[0]	6390
t_APASmoothY_Uls_u2p14[1]	6554
t_APASmoothY_Uls_u2p14[2]	6717
t_APASmoothY_Uls_u2p14[3]	6881
t_APASmoothY_Uls_u2p14[4]	7045
t_APASmoothY_Uls_u2p14[5]	7209
t_APASmoothY_Uls_u2p14[6]	7373
t_APASmoothY_Uls_u2p14[7]	7537
t_APASmoothY_Uls_u2p14[8]	7700
t_APASmoothY_Uls_u2p14[9]	7864
t_LKASlewX_Kph_u8p8[0]	39936
t_LKASlewX_Kph_u8p8[1]	40448
t_LKASlewX_Kph_u8p8[2]	40960
t_LKASlewX_Kph_u8p8[3]	41472
t_LKASlewX_Kph_u8p8[4]	41984
t_LKASlewX_Kph_u8p8[5]	42496
t_LKASlewY_NmpSec_u4p12[0]	31949
t_LKASlewY_NmpSec_u4p12[1]	32358
t_LKASlewY_NmpSec_u4p12[2]	32768
t_LKASlewY_NmpSec_u4p12[3]	33178
t_LKASlewY_NmpSec_u4p12[4]	33587
t_LKASlewY_NmpSec_u4p12[5]	33997
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3	2 target_TrqArblim_Per1_MinSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_0	Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	lm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.5		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.56999993		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	489.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.60000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.6999981		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.815999985	0.815999985 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.68074989	-2.68074989 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75	1.75 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.09399986	6.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.140000001	-0.140000001 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.140000001	-0.140000001 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.685412109	0.68541199 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.91 (Repeat Count = 1)			
Name	Input Value		
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32		
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim		
TrqArblim_APADisableRate_Uls_M_f32	0.975000024		
TrqArblim_ESC_State_Cnt_M_enum	3		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007		
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2		
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.5999999		
k_APAEnableRate_pSec_f32	489.75		
k_APAUseADmpScale_lgc	1		
k_APAUseAsstScale_lgc	1		
k_APAUseRetScale_lgc	1		
k_ESCMax_HwNm_f32	0.75		
<_LKAUseSlewCal_lgc	1		
_APADisableRateX_HwNm_u4p12[0]	22938		
_APADisableRateX_HwNm_u4p12[1]	23347		
_APADisableRateX_HwNm_u4p12[2]	23757		
_APADisableRateX_HwNm_u4p12[3]	24166		
_APADisableRateX_HwNm_u4p12[4]	24576		
_APADisableRateX_HwNm_u4p12[5]	24986		
_APADisableRateY_pSec_u7p9[0]	29696		
_APADisableRateY_pSec_u7p9[1]	30208		
_APADisableRateY_pSec_u7p9[2]	30720		
_APADisableRateY_pSec_u7p9[3]	31232		
_APADisableRateY_pSec_u7p9[4]	31744		
t_APADisableRateY_pSec_u7p9[5]	32256		
_APASmoothX_Uls_u2p14[0]	9503		
t_APASmoothX_Uls_u2p14[1]	9667		
t_APASmoothX_Uls_u2p14[2]	9830		
t_APASmoothX_Uls_u2p14[3]	9994		





Name	Input Value				
t_APASmoothX_Uls_u2p14[4]	10158				
t_APASmoothX_Uls_u2p14[5]	10322				
t_APASmoothX_Uls_u2p14[6]	10486				
t_APASmoothX_Uls_u2p14[7]	10650				
t_APASmoothX_Uls_u2p14[8]	10813				
t_APASmoothX_Uls_u2p14[9]	10977				
t_APASmoothY_Uls_u2p14[0]	6554				
t_APASmoothY_Uls_u2p14[1]	6717				
t_APASmoothY_Uls_u2p14[2]	6881				
t_APASmoothY_Uls_u2p14[3]	7045				
t_APASmoothY_Uls_u2p14[4]	7209				
t_APASmoothY_Uls_u2p14[5]	7373				
t_APASmoothY_Uls_u2p14[6]	7537				
t_APASmoothY_Uls_u2p14[7]	7700				
t_APASmoothY_Uls_u2p14[8]	7864				
t_APASmoothY_Uls_u2p14[9]	8028				
t_LKASlewX_Kph_u8p8[0]	40448				
t_LKASlewX_Kph_u8p8[1]	40960				
t LKASlewX Kph u8p8[2]	41472				
t_LKASlewX_Kph_u8p8[3]	41984				
t_LKASlewX_Kph_u8p8[4]	42496				
t_LKASlewX_Kph_u8p8[5]	43008				
t_LKASlewY_NmpSec_u4p12[0]	29901				
t_LKASlewY_NmpSec_u4p12[1]		30310			
t_LKASlewY_NmpSec_u4p12[2]	30720				
t_LKASlewY_NmpSec_u4p12[3]	31130				
t_LKASlewY_NmpSec_u4p12[4]	31539				
t_LKASlewY_NmpSec_u4p12[5]	31949				
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_fltInje$	8.1999981				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc				
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3				
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target_TrqArblim_Per1_LKAState_State_enum				
· · · · · ·	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32					
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32				
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992				
target_TrqArblim_Per1_ESCState_State_enum.value	2				
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1				
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.1500001				
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003				
target_TrqArblim_Per1_LKAState_State_enum.value	1				
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	496.243011				
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1				
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.10000038				
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.9000001				
Name	Actual Value	Expected Value	Result		
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625			
TrqArblim_ESC_State_Cnt_M_enum	2	2			
	-2.38850021	-2.38849998 ± 0.03125			
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32					
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.10000038 6.5943007	-8.10000038 ± 0.000244140625			
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.5843997	6.58440018 ± 0.03125			
target_TrqArblim_Per1_AssistDDFactor_UIs_f32.value	0.510009766	0.510010004 ± 0000390625	V		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.510009766	0.510010004 ± 0000390625	V		
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	~		
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.75	0.75 ± 0000390625	~		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.75	0.75 ± 0.0000390625	✓		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~		
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.931078911	0.93107897 ± 0000390625	~		
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.510009766	0.510010004 ± 0000390625	✓		



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.92 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.850000024
FrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.419
FrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75
FrqArblim_PreLKATrqReq_HwNm_M_f32	4.0999999
<pre><_APAEnableRate_pSec_f32</pre>	441
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
c_ESCMax_HwNm_f32	1.27999997
_LKAUseSlewCal_lgc	1
APADisableRateX_HwNm_u4p12[0]	23347
	23757
_APADisableRateX_HwNm_u4p12[2]	24166
_APADisableRateX_HwNm_u4p12[3]	24576
_APADisableRateX_HwNm_u4p12[4]	24986
_Ar AbisableRateX_HwNm_u4p12[5]	25395
_APADisableNateX_nwitin_u4p12[3]	30208
_APADisableRateY_pSec_u7p9[0] _APADisableRateY_pSec_u7p9[1]	30720
	31232
_APADisableRateY_pSec_u7p9[2]	
_APADisableRateY_pSec_u7p9[3]	31744
_APADisableRateY_pSec_u7p9[4]	32256
_APADisableRateY_pSec_u7p9[5]	32768
_APASmoothX_Uls_u2p14[0]	9667
_APASmoothX_Uls_u2p14[1]	9830
_APASmoothX_Uls_u2p14[2]	9994
_APASmoothX_Uls_u2p14[3]	10158
_APASmoothX_Uls_u2p14[4]	10322
_APASmoothX_Uls_u2p14[5]	10486
_APASmoothX_Uls_u2p14[6]	10650
_APASmoothX_Uls_u2p14[7]	10813
_APASmoothX_Uls_u2p14[8]	10977
_APASmoothX_Uls_u2p14[9]	11141
_APASmoothY_Uls_u2p14[0]	6717
_APASmoothY_Uls_u2p14[1]	6881
_APASmoothY_Uls_u2p14[2]	7045
_APASmoothY_Uls_u2p14[3]	7209
_APASmoothY_Uls_u2p14[4]	7373
APASmoothY_Uls_u2p14[5]	7537
_APASmoothY_Uls_u2p14[6]	7700
_APASmoothY_Uls_u2p14[7]	7864
_APASmoothY_Uls_u2p14[8]	8028
_APASmoothY_Uls_u2p14[9]	8192
KASIewX_Kph_u8p8[0]	37888
_LKASlewX_Kph_u8p8[1]	38400
	38912
_LKASlewX_Kph_u8p8[2]	39424
_LKASlewX_Kph_u8p8[3]	
_LKASlewX_Kph_u8p8[4]	39936
_LKASlewX_Kph_u8p8[5]	40448
_LKASlewY_NmpSec_u4p12[0]	30310
_LKASlewY_NmpSec_u4p12[1]	30720
_LKASlewY_NmpSec_u4p12[2]	31130
_LKASlewY_NmpSec_u4p12[3]	31539
_LKASlewY_NmpSec_u4p12[4]	31949
_LKASlewY_NmpSec_u4p12[5]	32358
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls	s_f; 9.19999981
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.9000001		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.96999979		
target_TrqArblim_Per1_LKAState_State_enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	463.993011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.10000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.731999993	0.731999993 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	3	3	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.84975004	-3.84975004 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75	0.75 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.08419991	4.08419991 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.634000003	0.634000003 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.634000003	0.634000003 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.274500012	0.274500012 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.634000003	0.634000003 ± 0000390625	~

Test Step Call Trace				
•				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Pte Call TraArblim Per1 CP1 ChecknointReached	1	Pte Call TraArblim Per1 CP1 ChecknointReached	1	

Test Step 3.93 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.875
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.29999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.430000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.5999999
k_APAEnableRate_pSec_f32	450.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	19661
t_APADisableRateX_HwNm_u4p12[1]	20070
t_APADisableRateX_HwNm_u4p12[2]	20480
t_APADisableRateX_HwNm_u4p12[3]	20890

2014-09-16, 12:30:22+0530



Name	Input Value		
t_APADisableRateX_HwNm_u4p12[4]	21299		
t_APADisableRateX_HwNm_u4p12[5]	21709		
t_APADisableRateY_pSec_u7p9[0]	30720		
t_APADisableRateY_pSec_u7p9[1]	31232		
t_APADisableRateY_pSec_u7p9[2]	31744		
t_APADisableRateY_pSec_u7p9[3]	32256		
t_APADisableRateY_pSec_u7p9[4]	32768		
t_APADisableRateY_pSec_u7p9[5] t_APASmoothX_Uls_u2p14[0]	9830		
t_APASmoothX_Uls_u2p14[1]	9994		
t_APASmoothX_UIs_u2p14[2]	10158		
t_APASmoothX_Uls_u2p14[3]	10322		
t_APASmoothX_Uls_u2p14[4]	10486		
t_APASmoothX_Uls_u2p14[5]	10650		
t_APASmoothX_Uls_u2p14[6]	10813		
t_APASmoothX_UIs_u2p14[7]	10977		
t_APASmoothX_Uls_u2p14[8]	11141		
t_APASmoothX_Uls_u2p14[9]	11305		
t_APASmoothY_UIs_u2p14[0] t APASmoothY UIs_u2p14[1]	6881 7045		
t_APASmoothY_Uls_u2p14[2]	7209		
t_APASmoothY_Uls_u2p14[3]	7373		
t_APASmoothY_Uls_u2p14[4]	7537		
t_APASmoothY_Uls_u2p14[5]	7700		
t_APASmoothY_Uls_u2p14[6]	7864		
t_APASmoothY_Uls_u2p14[7]	8028		
t_APASmoothY_Uls_u2p14[8]	8192		
t_APASmoothY_Uls_u2p14[9]	8356		
t_LKASlewX_Kph_u8p8[0]	38400		
t_LKASlewX_Kph_u8p8[1]	38912		
t_LKASlewX_Kph_u8p8[2]	39424		
t_LKASlewX_Kph_u8p8[3]	39936		
t_LKASlewX_Kph_u8p8[4] t_LKASlewX_Kph_u8p8[5]	40448 40960		
t_LKASlewY_NmpSec_u4p12[0]	30720		
t_LKASlewY_NmpSec_u4p12[1]	31130		
t_LKASlewY_NmpSec_u4p12[2]	31539		
t_LKASlewY_NmpSec_u4p12[3]	31949		
t_LKASlewY_NmpSec_u4p12[4]	32358		
t_LKASlewY_NmpSec_u4p12[5]	32768		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_flows$	10.1999998		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3.		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_local target_TrqArblim_Per1_ESCState_State_enu		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCTorgueDelivered HwNm f32	target TrqArblim Per1 ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate St		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f3	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3:		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	target_TrqArblim_Per1_TrqOscCmd_MtrNm_ 8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.55000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.61999989		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	470.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	V
TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV_HwMm_M_Str_SV_Hllp_f32	2.55740000	2.55740000 + 0.02425	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.55749989	-3.55749989 ± 0.03125	

2014-09-16, 12:30:22+0530

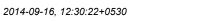




Name	Actual Value	Expected Value	Result
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.30000019	8.30000019 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.59399986	4.59399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.33308125	8.33308029 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

lame	Input Value
te_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
te_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.89999976
rqArblim_ESC_State_Cnt_M_enum	1
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.949999988
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
rqArblim_PosSrvoCmd_MtrNm_M_f32	1.25
rgArblim PreLKATrgReg HwNm M f32	5.0999999
_APAEnableRate_pSec_f32	460.5
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
APAUseRetScale Igc	1
ESCMax_HwNm_f32	0
LKAUseSlewCal lgc	1
APADisableRateX_HwNm_u4p12[0]	20070
APADisableRateX_HwNm_u4p12[1]	20480
APADisableRateX_HwNm_u4p12[2]	20890
APADisableRateX_HwNm_u4p12[3]	21299
APADisableRateX HwNm u4p12[4]	21709
APADisableRateX_HwNm_u4p12[5]	22118
APADisableRateY_pSec_u7p9[0]	29696
APADisableRateY pSec u7p9[1]	30208
APADisableRateY_pSec_u7p9[2]	30720
APADisableRateY_pSec_u7p9[3]	31232
APADisableRateY_pSec_u7p9[4]	31744
APADisableRateY_pSec_u7p9[5]	32256
APASmoothX_UIs_u2p14[0]	9994
APASmoothX_Uls_u2p14[1]	10158
APASmoothX_Uls_u2p14[2]	10322
APASmoothX_Uls_u2p14[3]	10486
APASmoothX_Uls_u2p14[4]	10650
APASmoothX_UIs_u2p14[5]	10813
APASmoothX_UIs_u2p14[6]	10977
APASmoothX_Uls_u2p14[7]	11141
APASmoothX_Uls_u2p14[8]	11305
APASmoothX_Uls_u2p14[9]	11469
APASmoothY_Uls_u2p14[0]	7045
APASmoothY_UIs_u2p14[1]	7209
APASmoothY Uls u2p14[2]	7373
APASmoothY_Uls_u2p14[3]	7537
APASmoothY_Uls_u2p14[4]	7700
APASmoothY_Uls_u2p14[5]	7864
APASmoothY_Uls_u2p14[6]	8028
APASmoothY_Uls_u2p14[7]	8192
APASmoothY_Uls_u2p14[8]	8356
APASmoothY Uls u2p14[9]	8520
LKASlewX_Kph_u8p8[0]	38912
	39424
LKASlewX Kph u8p8[2]	39936
	40448





Name	Input Value		
t_LKASlewX_Kph_u8p8[4]	40960		
t_LKASlewX_Kph_u8p8[5]	41472		
t_LKASlewY_NmpSec_u4p12[0]	31130		
t_LKASlewY_NmpSec_u4p12[1]	31539		
t_LKASlewY_NmpSec_u4p12[2]	31949		
t_LKASlewY_NmpSec_u4p12[3]	32358		
t_LKASlewY_NmpSec_u4p12[4]	32768		
t_LKASlewY_NmpSec_u4p12[5]	33178		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Ulstranger$	_f: 1.20000005		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e	enum	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f3222222222222222222222222222222222222$	target_TrqArblim_Per1_ESCTorqueDelive	red_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f3222222222222222222222222222222222222$	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3$	2 target_TrqArblim_Per1_MinSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtrl	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_L	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	m_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.1800003		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.26999998		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	476.893005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	2	2	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.26525021	-3.26524997 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.5	8.5 ± 0.000244140625	· ·
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.08379984	5.08379984 ± 0.03125	· ·
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.479980469	0.479979992 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.479980469	0.479979992 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	-
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.72016621	8.72017002 ± 0000390625	•
	0.479980469	0.479979992 ± 0000390625	

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.95 (Repeat Count = 1)	· ·
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.949999988
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.0999999

2014-09-16, 12:30:22+0530



	Input Value
	480
	0
	0
	5
	0
	22528
	22938
t_APADisableRateX_HwNm_u4p12[2]	23347
t_APADisableRateX_HwNm_u4p12[3]	23757
	24166
, , ,	24576
	34304 34816
= = = 1 11	35328
	35840
	36352
	36864
t_APASmoothX_Uls_u2p14[0]	9339
t_APASmoothX_Uls_u2p14[1]	9503
	9667
	9830
	9994
t_APASmoothX_Uls_u2p14[5] t_APASmoothX_Uls_u2p14[6]	10158 10322
	10486
t_APASmoothX_Uls_u2p14[8]	10650
t_APASmoothX_Uls_u2p14[9]	10813
t_APASmoothY_Uls_u2p14[0]	6390
	6554
	6717
	6881
	7045 7209
	7373
	7537
	7700
t_APASmoothY_Uls_u2p14[9]	7864
t_LKASlewX_Kph_u8p8[0]	39936
	40448
	40960
	41472
	41984 42496
	31949
	32358
t_LKASlewY_NmpSec_u4p12[2]	32768
t_LKASlewY_NmpSec_u4p12[3]	33178
	33587
	33997
target Rte Call Ap TrqArblim FaultInjection SCom FitInjection SignalPath Uls f.	
	target_TrqArblim_Per1_AssistDDFactor_Uls_f32 target_TrqArblim_Per1_DampingDDFactor_Uls_f32
	target_TrqArblim_Per1_bampingbbFactor_ois_132 target_TrqArblim_Per1_ESCCmd_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
0 = = = 1	target_TrqArblim_Per1_ESCState_State_enum
·	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
· · · · ·	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
	target_TrqArblim_Per1_LKACmd_HwNm_f32
	target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
	target_TrqArblim_Per1_LKA1orqueDelivered_HwNm_132 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_opTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	
	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value	-3.75999999 1
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value	-3.75999999



Name	Input Value		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.56999993		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	489.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.60000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.69999981		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.815999985	0.815999985 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.68074989	-2.68074989 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75	1.75 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.09399986	6.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.75999999	-0.140000001 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.685412109	0.68541199 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.96 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.449999988
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-7.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002
k_APAEnableRate_pSec_f32	285
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	20890
t_APADisableRateX_HwNm_u4p12[1]	21299
t_APADisableRateX_HwNm_u4p12[2]	21709
t_APADisableRateX_HwNm_u4p12[3]	22118
t_APADisableRateX_HwNm_u4p12[4]	22528
t_APADisableRateX_HwNm_u4p12[5]	22938
t_APADisableRateY_pSec_u7p9[0]	33280
t_APADisableRateY_pSec_u7p9[1]	33792
t_APADisableRateY_pSec_u7p9[2]	34304
t_APADisableRateY_pSec_u7p9[3]	34816
t_APADisableRateY_pSec_u7p9[4]	35328
t_APADisableRateY_pSec_u7p9[5]	35840
t_APASmoothX_Uls_u2p14[0]	10813
t_APASmoothX_Uls_u2p14[1]	10977
t_APASmoothX_UIs_u2p14[2]	11141
t_APASmoothX_Uls_u2p14[3]	11305
t_APASmoothX_Uls_u2p14[4]	11469
t_APASmoothX_Uls_u2p14[5]	11633
t_APASmoothX_Uls_u2p14[6]	11796
t_APASmoothX_Uls_u2p14[7]	11960
t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_Uls_u2p14[9]	12288
t_APASmoothY_Uls_u2p14[0]	7864
t_APASmoothY_Uls_u2p14[1]	8028
t_APASmoothY_Uls_u2p14[2]	8192
t_APASmoothY_Uls_u2p14[3]	8356

2014-09-16, 12:30:22+0530



Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	8520		
t_APASmoothY_Uls_u2p14[5]	8684		
t_APASmoothY_Uls_u2p14[6]	8847		
t_APASmoothY_Uls_u2p14[7]	9011		
t_APASmoothY_Uls_u2p14[8]	9175		
t_APASmoothY_Uls_u2p14[9]	9339		
t_LKASlewX_Kph_u8p8[0]	35328		
t_LKASlewX_Kph_u8p8[1]	35840		
t_LKASlewX_Kph_u8p8[2]	36352		
t_LKASlewX_Kph_u8p8[3]	36864		
t_LKASlewX_Kph_u8p8[4]	37376		
t_LKASlewX_Kph_u8p8[5]	37888		
t_LKASlewY_NmpSec_u4p12[0]	28262		
t_LKASlewY_NmpSec_u4p12[1]	28672		
t_LKASlewY_NmpSec_u4p12[2]	29082		
t_LKASlewY_NmpSec_u4p12[3]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901		
t_LKASlewY_NmpSec_u4p12[5]	30310		
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Ulstarget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Ulstarget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Ulstarget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Ulstarget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Ulstarget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_Scom_FltInjection_SignalPath_Ulstarget_Rte_Call_Ap_TrqArblim_FaultInjection_Scom_FltInjection_SignalPath_Ulstarget_Rte_Call_Ap_TrqArblim_FaultInjection_Scom_FltInjection_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_Ulstarget_Rte_SignalPath_R$	_f: 3.20000005		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3322222222222222222222222222222222222$	2 target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cr	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNr	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.13000011		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.0200005		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	360.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.69999988		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.319999993	0.310000002 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.24166012	-4.24166012 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006	5.94000006 ± 0.00024414062	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.09399998	-1.09399998 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	•
target TrqArblim Per1 OpTrqOv MtrNm f32.value	0.9123469	0.883835971 ± 0000390625	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	•



Fest Step 3.97 (Repeat Count = 1)	Input Value
ame	Input Value
tte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FitInjection_SignalPath_Uls_f32
te_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.800000012
rqArblim_ESC_State_Cnt_M_enum rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
rqArblim_PosSrvoCmd_MtrNm_M_f32	10
rqArblim_Prossrvooma_wirdin_w_i32	3.099999
_APAEnableRate_pSec_f32	421.5
APAUseADmpScale lgc	1
_APAUseAsstScale_lgc	1
APAUseRetScale lgc	1
ESCMax_HwNm_f32	2.57999992
LKAUseSlewCal lgc	1
APADisableRateX_HwNm_u4p12[0]	25805
APADisableRateX HwNm u4p12[1]	26214
APADisableRateX_HwNm_u4p12[2]	26624
APADisableRateX_HwNm_u4p12[3]	27034
APADisableRateX_HwNm_u4p12[4]	27443
APADisableRateX_HwNm_u4p12[5]	27853
APADisableRateY_pSec_u7p9[0]	43520
APADisableRateY_pSec_u7p9[1]	44032
_APADisableRateY_pSec_u7p9[2]	44544
_APADisableRateY_pSec_u7p9[3]	45056
_APADisableRateY_pSec_u7p9[4]	45568
_APADisableRateY_pSec_u7p9[5]	46080
APASmoothX_Uls_u2p14[0]	9011
_APASmoothX_Uls_u2p14[1]	9175
_APASmoothX_Uls_u2p14[2]	9339
_APASmoothX_Uls_u2p14[3]	9503
_APASmoothX_Uls_u2p14[4]	9667
_APASmoothX_Uls_u2p14[5]	9830
_APASmoothX_Uls_u2p14[6]	9994
_APASmoothX_Uls_u2p14[7]	10158
_APASmoothX_Uls_u2p14[8]	10322
_APASmoothX_UIs_u2p14[9]	10486
_APASmoothY_Uls_u2p14[0]	6062
_APASmoothY_Uls_u2p14[1]	6226
_APASmoothY_Uls_u2p14[2]	6390
_APASmoothY_Uls_u2p14[3]	6554
_APASmoothY_Uls_u2p14[4]	6717
_APASmoothY_Uls_u2p14[5]	6881 7045
APAS moothY_UIs_u2p14[6]	
APASmoothY_Uls_u2p14[7]	7209 7373
APAS moothY_Uls_u2p14[8]	7537
_APASmoothY_UIs_u2p14[9] _LKASlewX_Kph_u8p8[0]	36864
_tKASiewX_Kpn_u8p8[u] _tKASiewX_Kph_u8p8[1]	37376
LKASlewX_Kph_u8p8[2]	37888
_LKASiewX_Kph_u8p8[3]	37888 38400
LKASlewX_Kph_u8p8[4]	38912
LKASlewX_Kph_u8p8[5]	39424
_trasiews_rph_acpo[5] _trasiewy_nmpSec_u4p12[0]	29491
LKASlewY_NmpSec_u4p12[1]	29901
LKASlewY_NmpSec_u4p12[2]	30310
LKASlewY_NmpSec_u4p12[3]	30720
LKASlewY_NmpSec_u4p12[4]	31130
LKASlewY_NmpSec_u4p12[5]	31539
rget Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls	
rget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
rget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
rget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
rget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum





Name	Input Value				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	arget_TrqArblim_Per1_PosServEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	n_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979				
target_TrqArblim_Per1_ESCState_State_enum.value	4				
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2				
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999				
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008				
target_TrqArblim_Per1_LKAState_State_enum.value	1				
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987				
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1				
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981				
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5				
Name	Actual Value	Expected Value	Result		
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~		
TrqArblim_ESC_State_Cnt_M_enum	4	4	~		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.43424988	-4.43424988 ± 0.03125	~		
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.6999981	7.69999981 ± 0.000244140625	~		
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	~		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	~		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	~		
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~		
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~		
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.54216909	3.54217005 ± 0000390625	~		
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	~		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	•

Test Step 3.98 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_lgc	1
<_APAUseAsstScale_lgc	1
<_APAUseRetScale_lgc	1
C_ESCMax_HwNm_f32	4
<_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	25805
_APADisableRateX_HwNm_u4p12[1]	26214
_APADisableRateX_HwNm_u4p12[2]	26624
_APADisableRateX_HwNm_u4p12[3]	27034
_APADisableRateX_HwNm_u4p12[4]	27443
_APADisableRateX_HwNm_u4p12[5]	27853
_APADisableRateY_pSec_u7p9[0]	28160
_APADisableRateY_pSec_u7p9[1]	28672
_APADisableRateY_pSec_u7p9[2]	29184
_APADisableRateY_pSec_u7p9[3]	29696
_APADisableRateY_pSec_u7p9[4]	30208
_APADisableRateY_pSec_u7p9[5]	30720
_APASmoothX_Uls_u2p14[0]	9011
_APASmoothX_Uls_u2p14[1]	9175
t_APASmoothX_Uls_u2p14[2]	9339

2014-09-16, 12:30:22+0530





Name	Input Value			
t_APASmoothX_Uls_u2p14[3]	9503			
t_APASmoothX_Uls_u2p14[4]	9667			
t_APASmoothX_Uls_u2p14[5]	9830			
t_APASmoothX_UIs_u2p14[6]	9994			
t_APASmoothX_UIs_u2p14[7]	10158 10322			
t_APASmoothX_Uls_u2p14[8] t_APASmoothX_Uls_u2p14[9]	10486			
t_APASmoothY_Uls_u2p14[0]	6062			
t_APASmoothY_Uls_u2p14[1]	6226			
t_APASmoothY_Uls_u2p14[2]	6390			
t_APASmoothY_Uls_u2p14[3]	6554			
t_APASmoothY_Uls_u2p14[4]	6717			
t_APASmoothY_Uls_u2p14[5]	6881			
t_APASmoothY_Uls_u2p14[6]	7045			
t_APASmoothY_Uls_u2p14[7]	7209			
t_APASmoothY_Uls_u2p14[8]	7373			
t_APASmoothY_Uls_u2p14[9]	7537			
t_LKASlewX_Kph_u8p8[0]	36864			
t_LKASlewX_Kph_u8p8[1]	37376			
t_LKASlewX_Kph_u8p8[2]	37888			
t_LKASlewX_Kph_u8p8[3]	38400			
t_LKASlewX_Kph_u8p8[4] t_LKASlewX_Kph_u8p8[5]	38912 39424			
t_LKASlewY_NmpSec_u4p12[0]	29491			
t_LKASlewY_NmpSec_u4p12[1]	29901			
t_LKASlewY_NmpSec_u4p12[2]	30310			
t_LKASlewY_NmpSec_u4p12[3]	30720			
t_LKASlewY_NmpSec_u4p12[4]	31130			
t_LKASlewY_NmpSec_u4p12[5]	31539			
$target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_FltInje$	f. 5.19999981			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_lqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	eed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3.	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042			
target_TrqArblim_Per1_ESCState_State_enum.value	2			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2 -7.5999999			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.6700008			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	451.092987			
target TrqArblim Per1 PosServEnable Cnt Igc.value	1			
target TrgArblim Per1 PosSrvoCmd MtrNm f32.value	7.69999981			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5			
Name	Actual Value	Expected Value	Resu	
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625		
TrqArblim_ESC_State_Cnt_M_enum	2	2		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.43424988	-4.43424988 ± 0.03125		
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625		
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4	-4 ± 0000390625	•	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0 3 54216909	0 ± 0.0000390625		

3.54216909

0.539978027

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

3.54217005 ± 0000390625

0.539978027 ± 0000390625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Fest Step 3.99 (Repeat Count = 1)	Input Value
	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_UIs_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.75
rqArblim_ESC_State_Cnt_M_enum	
rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.375
rqArblim_PosSrvoCmd_MtrNm_M_f32	9.42000008
rqArblim_PreLKATrqReq_HwNm_M_f32	2.0999999
_APAEnableRate_pSec_f32	402
_APAUseADmpScale_lgc	1
_APAUseAsstScale_Igc	1
_APAUseRetScale_lgc	1
_ESCMax_HwNm_f32	3
_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	24986
_APADisableRateX_HwNm_u4p12[1]	25395
_APADisableRateX_HwNm_u4p12[2]	25805
APADisableRateX HwNm u4p12[3]	26214
_APADisableRateX_HwNm_u4p12[4]	26624
_APADisableRateX_HwNm_u4p12[5]	27034
APADisableNateX_TWMIII_u4p12[3] APADisableNateX pSec u7p9[0]	26112
	26624
_APADisableRateY_pSec_u7p9[1]	
_APADisableRateY_pSec_u7p9[2]	27136
_APADisableRateY_pSec_u7p9[3]	27648
_APADisableRateY_pSec_u7p9[4]	28160
_APADisableRateY_pSec_u7p9[5]	28672
_APASmoothX_Uls_u2p14[0]	8684
_APASmoothX_Uls_u2p14[1]	8847
_APASmoothX_Uls_u2p14[2]	9011
_APASmoothX_Uls_u2p14[3]	9175
_APASmoothX_Uls_u2p14[4]	9339
_APASmoothX_Uls_u2p14[5]	9503
_APASmoothX_Uls_u2p14[6]	9667
_APASmoothX_Uls_u2p14[7]	9830
_APASmoothX_UIs_u2p14[8]	9994
_APASmoothX_Uls_u2p14[9]	10158
_APASmoothY_Uls_u2p14[0]	5734
_APASmoothY_Uls_u2p14[0]	5898
	6062
_APASmoothY_Uls_u2p14[2]	
_APASmoothY_UIs_u2p14[3]	6226
_APASmoothY_Uls_u2p14[4]	6390
_APASmoothY_Uls_u2p14[5]	6554
_APASmoothY_Uls_u2p14[6]	6717
_APASmoothY_Uls_u2p14[7]	6881
_APASmoothY_Uls_u2p14[8]	7045
_APASmoothY_Uls_u2p14[9]	7209
_LKASlewX_Kph_u8p8[0]	35840
_LKASlewX_Kph_u8p8[1]	36352
LKASlewX_Kph_u8p8[2]	36864
LKASlewX_Kph_u8p8[3]	37376
LKASlewX Kph u8p8[4]	37888
rv_idewx_tpi_depp[+] _LKASlewX_Kph_u8p8[5]	38400
_LKASlewY_NmpSec_u4p12[0]	28672
LKASlewY NmpSec_u4p12[1]	29082
_LKASlewY_NmpSec_u4p12[2]	29491
_LKASlewY_NmpSec_u4p12[3]	29901
_LKASlewY_NmpSec_u4p12[4]	30310
_LKASlewY_NmpSec_u4p12[5]	30720
arget_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	f. 6.19999981





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDF	actor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_Hw	/Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited	_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueD	elivered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscil	late_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_H	wNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwN	lm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_Hw	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_Sta	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDe	elivered_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVe	hicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_Mtr	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnab	le_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd	_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFac	tor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_	MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	3	3	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.01875019	-5.01875019 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.30000019	7.30000019 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0849998	2.08500004 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.21201801	3.21201992 ± 0000390625	•
		0.550007070 - 000000005	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0.559997559

0.559997976 ± 0000390625

Test Step 3.100 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938
t_APADisableRateX_HwNm_u4p12[2]	23347
t_APADisableRateX_HwNm_u4p12[3]	23757

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

2014-09-16, 12:30:22+0530



Name	Input Value		
t_APADisableRateX_HwNm_u4p12[4]	24166		
t_APADisableRateX_HwNm_u4p12[5]	24576		
t_APADisableRateY_pSec_u7p9[0]	29184		
t_APADisableRateY_pSec_u7p9[1]	29696		
t_APADisableRateY_pSec_u7p9[2]	30208		
t_APADisableRateY_pSec_u7p9[3]	30720		
t_APADisableRateY_pSec_u7p9[4]	31232		
t_APADisableRateY_pSec_u7p9[5]	31744		
t_APASmoothX_Uls_u2p14[0]	10158		
t_APASmoothX_Uls_u2p14[1]	10322		
t_APASmoothX_Uls_u2p14[2]	10486		
t_APASmoothX_Uls_u2p14[3]	10650		
t_APASmoothX_Uls_u2p14[4]	10813		
t_APASmoothX_Uls_u2p14[5]	10977		
t_APASmoothX_UIs_u2p14[6]	11141		
t_APASmoothX_Uls_u2p14[7] t_APASmoothX_Uls_u2p14[8]	11469		
t_APASmoothX_UIs_u2p14[9]	11633		
t_APASmoothY_Uls_u2p14[0]	6717		
t_APASmoothY_Uls_u2p14[1]	6881		
t_APASmoothY_Uls_u2p14[2]	7045		
t_APASmoothY_Uls_u2p14[3]	7209		
t_APASmoothY_Uls_u2p14[4]	7373		
t_APASmoothY_Uls_u2p14[5]	7537		
t_APASmoothY_Uls_u2p14[6]	7700		
t_APASmoothY_Uls_u2p14[7]	7864		
t_APASmoothY_Uls_u2p14[8]	8028		
t_APASmoothY_Uls_u2p14[9]	8192		
t_LKASlewX_Kph_u8p8[0]	34816		
t_LKASlewX_Kph_u8p8[1]	35328		
t_LKASlewX_Kph_u8p8[2]	35840		
t_LKASlewX_Kph_u8p8[3]	36352		
t_LKASlewX_Kph_u8p8[4]	36864 37376		
t_LKASlewX_Kph_u8p8[5] t_LKASlewY_NmpSec_u4p12[0]	27853		
t_LKASlewY_NmpSec_u4p12[1]	28262		
t_LKASlewY_NmpSec_u4p12[2]	28672		
t_LKASlewY_NmpSec_u4p12[3]	29082		
t_LKASlewY_NmpSec_u4p12[4]	29491		
t_LKASlewY_NmpSec_u4p12[5]	29901		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	8.19999981		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_L	lls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_Sta	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32	•	
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State enu		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm	ı_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.599999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	7.69999981		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32	1	1 ± 0000390625	result
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.43424988	-4.43424988 ± 0.03125	~
	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		

2014-09-16, 12:30:22+0530





Name	Actual Value	Expected Value	Result
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.09399986	3.0940001 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.8499999	3.8499999 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.101 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.5999999
k_APAEnableRate_pSec_f32	470.25
k_APAUseADmpScale_lgc	1
k APAUseAsstScale Igc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22938
t_APADisableRateX_HwNm_u4p12[1]	23347
t APADisableRateX HwNm u4p12[2]	23757
t_APADisableRateX_HwNm_u4p12[3]	24166
t_APADisableRateX_HwNm_u4p12[4]	24576
t_APADisableRateX_HwNm_u4p12[5]	24986
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	10813
t_APASmoothX_UIs_u2p14[1]	10977
t_APASmoothX_UIs_u2p14[2]	11141
t_APASmoothX_UIs_u2p14[3]	11305
t_APASmoothX_UIs_u2p14[4]	11469
t_APASmoothX_UIs_u2p14[5]	11633
t_APASmoothX_Uls_u2p14[6]	11796
t_APASmoothX_Uls_u2p14[7]	11960
t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_Uls_u2p14[9]	12288
t_APASmoothY_UIs_u2p14[0]	6881 7045
t_APASmoothY_UIs_u2p14[1]	7045
t_APASmoothY_Uls_u2p14[2]	
t_APASmoothY_Uls_u2p14[3]	7373 7537
t_APASmoothY_Uls_u2p14[4]	
t_APASmoothY_UIs_u2p14[5]	7700
t_APASmoothY_Uls_u2p14[6]	7864
t_APASmoothY_Uls_u2p14[7]	8028
t_APASmoothY_Uls_u2p14[8]	8192
t_APASmoothY_Uls_u2p14[9]	8356
t_LKASlewX_Kph_u8p8[0]	35328
t_LKASlewX_Kph_u8p8[1]	35840
t_LKASlewX_Kph_u8p8[2]	36352
t_LKASlewX_Kph_u8p8[3]	36864

t_LKASlewY_NmpSec_u4p12[3]

t_LKASlewY_NmpSec_u4p12[4]

2014-09-16, 12:30:22+0530

29491

29901

target_TrqArblim_Per1_IqTrqOv_HwNm_f32

target_TrqArblim_Per1_LKACmd_HwNm_f32

target_TrqArblim_Per1_LKAState_State_enum

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32

target_TrqArblim_Per1_PosServEnable_Cnt_lgc

target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$

target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32



 TrqArblim_Per1

 Name
 Input Value

 t_LKASlewX_Kph_u8p8[4]
 37376

 t_LKASlewX_Kph_u8p8[5]
 37888

 t_LKASlewY_NmpSec_u4p12[0]
 28262

 t_LKASlewY_NmpSec_u4p12[1]
 28672

 t_LKASlewY_NmpSec_u4p12[2]
 29082

t_LKASlewY_NmpSec_u4p12[5] 30310
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f: 9.19999981
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32 target_TrqArblim_Per1_AssistDDFactor_Uls_f32

target_Rte_Inst_Ap_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum
target_TrqArblim_Per1_ESCState_State_enum

target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_Per1_HwTorque_HwNm_f32
target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_TrqArblim_Per1_HwTorque_HwNm_f32

target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum

target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc

target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value

target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value

target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosStynCmd_MtrNm_f32_value

 $target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value\\target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value$

target_TrqArbiim_Peri_TrqOscCmd_intrium_f32.value	4.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.809000015	0.809000015 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.97300005	-2.97300005 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58520031	5.58519983 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.618896842	0.618897021 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	~

8.97999954

-3.92000008

483.342987

8.69999981

0 -5.8499999

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	•

Test Step 3.102 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.80000019

2014-09-16, 12:30:22+0530



Name	Input Value
k_APAEnableRate_pSec_f32	0
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	0
t_APADisableRateX_HwNm_u4p12[1]	0
t_APADisableRateX_HwNm_u4p12[2]	0
t_APADisableRateX_HwNm_u4p12[3]	0
t_APADisableRateX_HwNm_u4p12[4]	0
t_APADisableRateX_HwNm_u4p12[5]	0
t_APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	0
t_APADisableRateY_pSec_u7p9[2]	0
t_APADisableRateY_pSec_u7p9[3]	0
t_APADisableRateY_pSec_u7p9[4] t_APADisableRateY_pSec_u7p9[5]	0
t_APASmoothX_Uls_u2p14[0]	0
t_APASmoothX_Uls_u2p14[1]	0
t_APASmoothX_Uls_u2p14[2]	0
t_APASmoothX_Uls_u2p14[3]	0
t_APASmoothX_Uls_u2p14[4]	0
t_APASmoothX_Uls_u2p14[5]	0
t_APASmoothX_Uls_u2p14[6]	0
t_APASmoothX_Uls_u2p14[7]	0
t_APASmoothX_Uls_u2p14[8]	0
t_APASmoothX_Uls_u2p14[9]	0
t_APASmoothY_Uls_u2p14[0]	0
t_APASmoothY_Uls_u2p14[1]	0
t_APASmoothY_Uls_u2p14[2]	0
t_APASmoothY_Uls_u2p14[3]	0
t_APASmoothY_Uls_u2p14[4]	0
t_APASmoothY_Uls_u2p14[5]	0
t_APASmoothY_Uls_u2p14[6]	0
t_APASmoothY_Uls_u2p14[7]	0
t_APASmoothY_Uls_u2p14[8]	0
t_APASmoothY_Uls_u2p14[9]	0
t_LKASlewX_Kph_u8p8[0]	0
t_LKASlewX_Kph_u8p8[1] t LKASlewX Kph u8p8[2]	0
t LKASlewX Kph u8p8[3]	0
t_LKASlewX_Kph_u8p8[4]	0
t_LKASlewX_Kph_u8p8[5]	0
t_LKASlewY_NmpSec_u4p12[0]	0
t_LKASlewY_NmpSec_u4p12[1]	0
t_LKASlewY_NmpSec_u4p12[2]	0
t_LKASlewY_NmpSec_u4p12[3]	0
t_LKASlewY_NmpSec_u4p12[4]	0
t_LKASlewY_NmpSec_u4p12[5]	0
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f	-10.2399998
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrnArblim_Per1_PosSeryEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target_rrqArblim_Per1_ReturnDDPactor_ois_132 target_rrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10.2299995
target_TrqArblim_Per1_ESCState_State_enum.value	0
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10



Name	Input Value		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10	-10 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998	-10.2399998 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.77299976	-8.77299976 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte Inst Ap TrgArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	1
TrgArblim ESC State Cnt M enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
TrgArblim HwTorqueSV HwNm M Str.K UIs f32	0.715390027
TrgArblim PosSrvoCmd MtrNm M f32	10.2299995
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019
k APAEnableRate pSec f32	500
k APAUseADmpScale Igc	1
k APAUseAsstScale lgc	1
k APAUseRetScale Igc	1
k_ESCMax_HwNm_f32	5
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	40960
t APADisableRateX HwNm u4p12[1]	40960
t_APADisableRateX_HwNm_u4p12[2]	40960
t APADisableRateX HwNm u4p12[3]	40960
t APADisableRateX HwNm u4p12[4]	40960
t APADisableRateX HwNm u4p12[5]	40960
t_APADisableRateY_pSec_u7p9[0]	65535
t APADisableRateY pSec u7p9[1]	65535
t_APADisableRateY_pSec_u7p9[2]	65535
t APADisableRateY pSec u7p9[3]	65535
t APADisableRateY pSec u7p9[4]	65535
t APADisableRateY pSec u7p9[5]	65535
t APASmoothX Uls u2p14[0]	16384
t_APASmoothX_Uls_u2p14[1]	16384
t APASmoothX Uls u2p14[2]	16384
t_APASmoothX_Uls_u2p14[3]	16384
t_APASmoothX_Uls_u2p14[4]	16384
t_APASmoothX_Uls_u2p14[5]	16384
t_APASmoothX_Uls_u2p14[6]	16384
t_APASmoothX_Uls_u2p14[7]	16384
t_APASmoothX_Uls_u2p14[8]	16384
t_APASmoothX_Uls_u2p14[9]	16384
t_APASmoothY_Uls_u2p14[0]	16384
t APASmoothY Uls u2p14[1]	16384
t_APASmoothY_Uls_u2p14[2]	16384
t APASmoothY Uls u2p14[3]	16384

2014-09-16, 12:30:22+0530



0 ± 0000390625

TrqArblim_Per1

			10-10
Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	16384		
t_APASmoothY_Uls_u2p14[5]	16384		
t_APASmoothY_Uls_u2p14[6]	16384	16384	
t_APASmoothY_Uls_u2p14[7]	16384	16384	
t APASmoothY Uls u2p14[8]	16384		
t_APASmoothY_Uls_u2p14[9]	16384		
t_LKASlewX_Kph_u8p8[0]	56320		
t_LKASlewX_Kph_u8p8[1]	56320		
t_LKASlewX_Kph_u8p8[2]	56320		
t LKASlewX Kph u8p8[3]	56320		
t LKASlewX Kph u8p8[4]	56320		
t_LKASlewX_Kph_u8p8[5]	56320		
t_LKASlewY_NmpSec_u4p12[0]	55296		
t_LKASlewY_NmpSec_u4p12[1]	55296		
t LKASlewY NmpSec u4p12[2]	55296		
t_LKASlewY_NmpSec_u4p12[3]	55296		
t_LKASlewY_NmpSec_u4p12[4]	55296		
t_LKASlewY_NmpSec_u4p12[5]	55296		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_t			
target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor Uls f32	target TrqArblim Per1 AssistDDFactor Uls	: f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCISLimited_Cnt_		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum			
	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	10		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	511		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10	10 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.00024414062	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.77299976	8.77299976 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	
target_TraArblim_Dor1_DeturnDDEactor_Ula_f22_value	0	0.00000010 ± 0000000020	

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value\\target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$



Test Step 3.104 (Repeat Count = 1)	range in the second
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.204999998
TrqArblim_ESC_State_Cnt_M_enum	4
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.709999979
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0350000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.78000021
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002
k_APAEnableRate_pSec_f32	69
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.337000012
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	6144
t_APADisableRateX_HwNm_u4p12[1]	6554
t_APADisableRateX_HwNm_u4p12[2]	6963
t_APADisableRateX_HwNm_u4p12[3]	7373
t_APADisableRateX_HwNm_u4p12[4]	7782
t_APADisableRateX_HwNm_u4p12[5]	8192
t_APADisableRateY_pSec_u7p9[0]	7680
t_APADisableRateY_pSec_u7p9[1]	8192
t_APADisableRateY_pSec_u7p9[2]	8704
t_APADisableRateY_pSec_u7p9[3]	9216
t_APADisableRateY_pSec_u7p9[4]	9728
t_APADisableRateY_pSec_u7p9[5]	10240
t_APASmoothX_Uls_u2p14[0]	2458
t_APASmoothX_Uls_u2p14[1]	2621
t_APASmoothX_UIs_u2p14[2]	2785
t_APASmoothX_UIs_u2p14[3]	2949
t_APASmoothX_UIs_u2p14[4]	3113
t_APASmoothX_UIs_u2p14[5]	3277
t_APASmoothX_UIs_u2p14[6]	3441 3604
t_APASmoothX_UIs_u2p14[7]	3768
t_APASmoothX_UIs_u2p14[8]	3932
t_APASmoothX_UIs_u2p14[9]	6390
t_APASmoothY_Uls_u2p14[0] t_APASmoothY_Uls_u2p14[1]	6554
t_APASmoothY_Uls_u2p14[2]	6717
t_APASmoothY_Uls_u2p14[3]	6881
t_APASmoothY_Uls_u2p14[4]	7045
t APASmoothY Uls u2p14[5]	7209
t APASmoothY Uls u2p14[6]	7373
t_APASmoothY_Uls_u2p14[7]	7537
t_APASmoothY_Uls_u2p14[8]	7700
t_APASmoothY_Uls_u2p14[9]	7864
t LKASlewX Kph u8p8[0]	7680
t_LKASlewX_Kph_u8p8[1]	8192
t_LKASlewX_Kph_u8p8[2]	8704
t LKASlewX Kph u8p8[3]	9216
t_LKASlewX_Kph_u8p8[4]	9728
t LKASlewX Kph u8p8[5]	10240
t_LKASlewY_NmpSec_u4p12[0]	6144
t_LKASlewY_NmpSec_u4p12[1]	6554
t_LKASlewY_NmpSec_u4p12[2]	6963
t_LKASlewY_NmpSec_u4p12[3]	7373
t_LKASlewY_NmpSec_u4p12[4]	7782
t_LKASlewY_NmpSec_u4p12[5]	8192
target Rte Call Ap TrqArblim FaultInjection SCom FltInjection SignalPath Uls f	r o
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_IrqArblim_Per1_LKACmd_HwNm_T32 target_TrqArblim_Per1_LKAState_State_enum



Name	Input Value		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.1800003		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.19999981		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	64.0932999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-4.3000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.60000038		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.174999997	0.174999997 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.03515005	-1.03515005 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.78000021	7.78000021 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.09399998	-1.09399998 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	-
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-1.09399998	-1.09399998 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.564992547	0.564993024 ± 0000390625	~
target TrqArblim Per1 ReturnDDFactor Uls f32.value	0.927378833	0.927379012 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.105 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.400000006
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.94999981
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.221000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.36000013
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.099999
k_APAEnableRate_pSec_f32	265.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	24166
t_APADisableRateX_HwNm_u4p12[1]	24576
t_APADisableRateX_HwNm_u4p12[2]	24986
t_APADisableRateX_HwNm_u4p12[3]	25395
t_APADisableRateX_HwNm_u4p12[4]	25805
t_APADisableRateX_HwNm_u4p12[5]	26214
t_APADisableRateY_pSec_u7p9[0]	30208
t_APADisableRateY_pSec_u7p9[1]	30720
t_APADisableRateY_pSec_u7p9[2]	31232
t_APADisableRateY_pSec_u7p9[3]	31744
t_APADisableRateY_pSec_u7p9[4]	32256
t_APADisableRateY_pSec_u7p9[5]	32768
t_APASmoothX_Uls_u2p14[0]	9667
t_APASmoothX_Uls_u2p14[1]	9830
t_APASmoothX_Uls_u2p14[2]	9994

2014-09-16, 12:30:22+0530

TrqArblim_Per1



Name	Input Value		
t_APASmoothX_Uls_u2p14[3]	10158		
t_APASmoothX_Uls_u2p14[4]	10322		
t_APASmoothX_Uls_u2p14[5]	10486		
t_APASmoothX_Uls_u2p14[6]	10650		
t_APASmoothX_UIs_u2p14[7]	10813		
t_APASmoothX_Uls_u2p14[8]	10977		
t_APASmoothX_UIs_u2p14[9]	11141		
t_APASmoothY_Uls_u2p14[0]	6717		
t_APASmoothY_Uls_u2p14[1]	6881		
t_APASmoothY_Uls_u2p14[2]	7045		
t_APASmoothY_Uls_u2p14[3]	7209		
t_APASmoothY_Uls_u2p14[4]	7373		
t_APASmoothY_Uls_u2p14[5]	7537		
t_APASmoothY_Uls_u2p14[6]	7700		
t_APASmoothY_Uls_u2p14[7]	7864		
t_APASmoothY_Uls_u2p14[8]	8028		
t_APASmoothY_Uls_u2p14[9]	8192		
t_LKASlewX_Kph_u8p8[0]	30208		
t_LKASlewX_Kph_u8p8[1]	30720		
t_LKASlewX_Kph_u8p8[2]	31232		
t_LKASlewX_Kph_u8p8[3]	31744		
t_LKASlewX_Kph_u8p8[4]	32256 32768		
t_LKASlewX_Kph_u8p8[5]	24166		
t_LKASlewY_NmpSec_u4p12[0] t_LKASlewY_NmpSec_u4p12[1]	24576		
t_LKASlewY_NmpSec_u4p12[1]	24986		
t_LKASlewY_NmpSec_u4p12[3]	25395		
t_LKASlewY_NmpSec_u4p12[4]	25805		
t_LKASlewY_NmpSec_u4p12[5]	26214		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed Kph f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.01000023		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.23999977		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	347.893005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.300000012		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.282000005	0.272000015 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.30683994	-5.30683994 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.36000013	5.36000013 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	_
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.884387553	0.888486981 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	-
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.61968267	0.597707987 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

1 ± 0000390625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.106 (Repeat Count = 1) Name	Input Value
Rte Call Ap TrgArblim FaultInjection SCom FltInjection(SignalPath Uls f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrgArblim_ESC_State_Cnt_M_enum	4
	-0.600000024
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.599999
k_APAEnableRate_pSec_f32	470.25
k_APAUseADmpScale_lgc	1
<_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22938
t_APADisableRateX_HwNm_u4p12[1]	23347
t_APADisableRateX_HwNm_u4p12[2]	23757
t_APADisableRateX_HwNm_u4p12[3]	24166
t_APADisableRateX_HwNm_u4p12[4]	24576
t_APADisableRateX_HwNm_u4p12[5]	24986
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableNateY_pSec_u7p9[1]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
	31744
t_APADisableRateY_pSec_u7p9[4]	
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	10813
t_APASmoothX_Uls_u2p14[1]	10977
t_APASmoothX_Uls_u2p14[2]	11141
t_APASmoothX_Uls_u2p14[3]	11305
t_APASmoothX_Uls_u2p14[4]	11469
t_APASmoothX_Uls_u2p14[5]	11633
t_APASmoothX_Uls_u2p14[6]	11796
t_APASmoothX_Uls_u2p14[7]	11960
t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_Uls_u2p14[9]	12288
t_APASmoothY_Uls_u2p14[0]	6881
t_APASmoothY_Uls_u2p14[1]	7045
t_APASmoothY_Uls_u2p14[2]	7209
t_APASmoothY_Uls_u2p14[3]	7373
t APASmoothY Uls u2p14[4]	7537
t_APASmoothY_Uls_u2p14[4]	7700
t APASmoothY Uls u2p14[6]	7864
t_APASmoothY_UIs_u2p14[7]	8028
t_APASmoothY_Uls_u2p14[8]	8192
t_APASmoothY_Uls_u2p14[9]	8356
t_LKASlewX_Kph_u8p8[0]	35328
t_LKASlewX_Kph_u8p8[1]	35840
t_LKASlewX_Kph_u8p8[2]	36352
t_LKASlewX_Kph_u8p8[3]	36864
t_LKASlewX_Kph_u8p8[4]	37376
t_LKASlewX_Kph_u8p8[5]	37888
t_LKASlewY_NmpSec_u4p12[0]	28262
t_LKASlewY_NmpSec_u4p12[1]	28672
	29082
t_LKASlewY_NmpSec_u4p12[3]	29491
t_LKASlewY_NmpSec_u4p12[4]	29901
t_LKASlewY_NmpSec_u4p12[5]	30310
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
	tanget inquisiti i or i nodotedi dotor ora roz





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliver	red_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f	T32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicles	Speed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_t	f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	Nm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	m_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954			
target_TrqArblim_Per1_ESCState_State_enum.value	4			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.69999981			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.809000015	0.809000015 ± 0000390625	✓	
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.97300005	-2.97300005 ± 0.03125	✓	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	✓	
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58520031	5.58519983 ± 0.03125	✓	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	✓	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	~	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

0.618896842

0.587402105

3 ± 0.0000390625

0.618897021 ± 0000390625

0.587401986 ± 0000390625

Test Step 3.107 (Repeat Count = 1)	✓
Name	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00125584996
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.80000019
k_APAEnableRate_pSec_f32	0
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	0
t_APADisableRateX_HwNm_u4p12[1]	0
t_APADisableRateX_HwNm_u4p12[2]	0
t_APADisableRateX_HwNm_u4p12[3]	0
t_APADisableRateX_HwNm_u4p12[4]	0

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

2014-09-16, 12:30:22+0530



Namo	Input Value			
Name t_APADisableRateX_HwNm_u4p12[5]	Input Value			
t APADisableRateY pSec u7p9[0]	0			
t_APADisableRateY_pSec_u7p9[1]	0			
t_APADisableRateY_pSec_u7p9[2]	0			
t_APADisableRateY_pSec_u7p9[3]	0			
t_APADisableRateY_pSec_u7p9[4]	0			
t_APADisableRateY_pSec_u7p9[5]	0			
t_APASmoothX_Uls_u2p14[0]	0			
t_APASmoothX_Uls_u2p14[1]	0			
t_APASmoothX_Uls_u2p14[2]	0			
t_APASmoothX_UIs_u2p14[3]	0			
t_APASmoothX_UIs_u2p14[4]	0			
t_APASmoothX_UIs_u2p14[5]	0 0			
t_APASmoothX_Uls_u2p14[6]	0			
t_APASmoothX_Uls_u2p14[7] t_APASmoothX_Uls_u2p14[8]	0			
t_APASmoothX_Uls_u2p14[9]	0			
t_APASmoothY_Uls_u2p14[0]	0			
t APASmoothY Uls u2p14[1]	0			
t_APASmoothY_Uls_u2p14[2]	0			
t_APASmoothY_Uls_u2p14[3]	0			
t_APASmoothY_Uls_u2p14[4]	0			
t_APASmoothY_Uls_u2p14[5]	0			
t_APASmoothY_Uls_u2p14[6]	0			
t_APASmoothY_Uls_u2p14[7]	0			
t_APASmoothY_Uls_u2p14[8]	0			
t_APASmoothY_Uls_u2p14[9]	0			
t_LKASlewX_Kph_u8p8[0]	0			
t_LKASlewX_Kph_u8p8[1]	0			
t_LKASlewX_Kph_u8p8[2]	0			
t_LKASlewX_Kph_u8p8[3] t_LKASlewX_Kph_u8p8[4]	0			
t_LKASlewX_Kph_u8p8[5]	0			
t_LKASlewY_NmpSec_u4p12[0]	0			
t_LKASlewY_NmpSec_u4p12[1]	0			
t_LKASlewY_NmpSec_u4p12[2]	0			
t_LKASlewY_NmpSec_u4p12[3]	0			
t_LKASlewY_NmpSec_u4p12[4]	0			
t_LKASlewY_NmpSec_u4p12[5]	0			
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	-10.2399998			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3:			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lg			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_State_enutarget TrqArblim Per1_ESCTorqueDelivered			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate St			
target Rte Inst Ap TrgArblim.TrgArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm f3	-		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered			
$target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	eed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm	_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	132		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10.2299995			
target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998			
target_TrqArblim_Per1_LKAState_State_enum.value	0			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.80000019			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	0	0	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10	-10 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998	-10.2399998 ± 0.000244140625		

2014-09-16, 12:30:22+0530





Name	Actual Value	Expected Value	Result
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.77299976	-8.77299976 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

lame	Input Value
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection(SignalPath_Uls_f32)	target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f32
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	1
rgArblim ESC State Cnt M enum	4
rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
rqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.715390027
rqArblim_PosSrvoCmd_MtrNm_M_f32	10.2299995
rqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019
APAEnableRate pSec f32	500
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
APAUseRetScale lqc	1
_ESCMax_HwNm_f32	5
_LKAUseSlewCal_lgc	1
_PADisableRateX_HwNm_u4p12[0]	40960
_APADisableRateX_HwNm_u4p12[1]	40960
_APADisableRateX_HwNm_u4p12[1] _APADisableRateX_HwNm_u4p12[2]	40960
APADisableRateX HwNm_u4p12[3]	40960
	40960
_APADisableRateX_HwNm_u4p12[4]	
APADisableRateX_HwNm_u4p12[5]	40960
_APADisableRateY_pSec_u7p9[0]	65535
_APADisableRateY_pSec_u7p9[1]	65535
_APADisableRateY_pSec_u7p9[2]	65535
APADisableRateY_pSec_u7p9[3]	65535
APADisableRateY_pSec_u7p9[4]	65535
APADisableRateY_pSec_u7p9[5]	65535
_APASmoothX_Uls_u2p14[0]	16384
_APASmoothX_Uls_u2p14[1]	16384
_APASmoothX_Uls_u2p14[2]	16384
_APASmoothX_Uls_u2p14[3]	16384
_APASmoothX_Uls_u2p14[4]	16384
_APASmoothX_Uls_u2p14[5]	16384
_APASmoothX_Uls_u2p14[6]	16384
_APASmoothX_Uls_u2p14[7]	16384
_APASmoothX_Uls_u2p14[8]	16384
_APASmoothX_Uls_u2p14[9]	16384
_APASmoothY_Uls_u2p14[0]	16384
APASmoothY_Uls_u2p14[1]	16384
_APASmoothY_Uls_u2p14[2]	16384
APASmoothY_Uls_u2p14[3]	16384
APASmoothY_Uls_u2p14[4]	16384
_APASmoothY_Uls_u2p14[5]	16384
APASmoothY_Uls_u2p14[6]	16384
_APASmoothY_Uls_u2p14[7]	16384
_APASmoothY_Uls_u2p14[8]	16384
_APASmoothY_Uls_u2p14[9]	16384
_LKASlewX_Kph_u8p8[0]	56320
_LKASlewX_Kph_u8p8[1]	56320
_LKASlewX_Kph_u8p8[2]	56320
_LKASlewX_Kph_u8p8[3]	56320
LKASlewX_Kph_u8p8[4]	56320

2014-09-16, 12:30:22+0530



TrqArblim_Per1

IrqArbiim_Peri			MACITAL
Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	56320		
t_LKASlewY_NmpSec_u4p12[0]	55296		
t_LKASlewY_NmpSec_u4p12[1]	55296		
t_LKASlewY_NmpSec_u4p12[2]	55296		
t LKASlewY NmpSec u4p12[3]	55296		
t_LKASlewY_NmpSec_u4p12[4]	55296		
t_LKASlewY_NmpSec_u4p12[5]	55296		
target_Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection_SignalPath_Uls_f	10.2299995		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
arget_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	10		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	511		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10	10 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.00024414062	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.77299976	8.77299976 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	
toward Tradybling Dard OnTrady Marking 622 value	0.0000010	0.0000010 + 0000300635	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	Rte_Call_Ap_TrqArblim_FaultInjection_SCom_FltInjection	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	-

8.80000019

0

8.80000019 ± 0000390625

0 ± 0000390625

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value