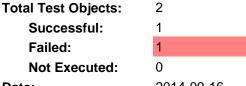
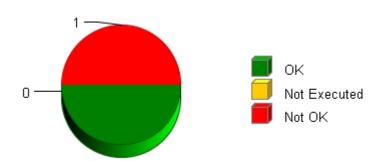


Summary

Overall Test Object Results (including Coverage)







Selected Project Items

Test Object "CBD_UnitTest/TrqArblim/TrqArblim_Init" Test Object "CBD_UnitTest/TrqArblim/TrqArblim_Per1"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

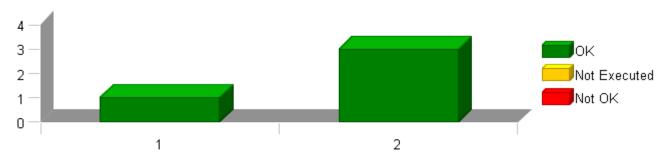
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Modified Condition / Decision Coverage,

Multiple Condition Coverage

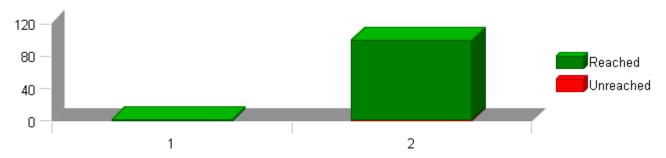
Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

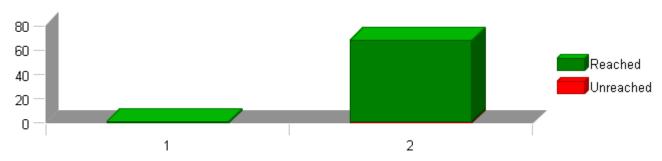


Statement (C0) Coverage: Total Statements for Each Test Object



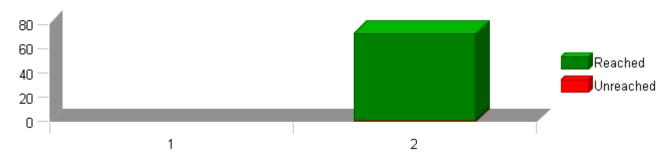
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

MC/DC Coverage: Total Condition Combinations for Each Test Object

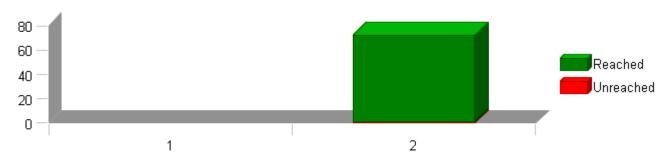


The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

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Test Object List

Project TrqArblim

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	MC/DC	МСС	Test Cases	Result
	TrqArblim	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed	×
	CBD_UnitTest	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed	×
	TrqArblim	99 %	98.55 %	98.61 %	98.61 %	4 of 4 passed	
1	TrqArblim_Init	100 %	100 %	-	-	1 of 1 passed	•
2	TrqArblim_Per1	99 %	98.52 %	98.61 %	98.61 %	3 of 3 passed	×

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 Project
 TrqArblim

 Module
 TrqArblim

 Test Object
 TrqArblim_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	99 %
Branch (C1) Coverage	98.52 %
MCC Coverage	98.61 %
MC/DC Coverage	98.61 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_TrqArblim
Configuration File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqArblim\src\Ap_TrqArblim.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_OFF -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utp\contract \Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path)\tinclude
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_OFF -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqArblim\utp\contract \Ap_TrqArblim -I\$(PROJECTROOT)\TrqArblim\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$ (Compiler Install Path)\include

Comments/Descript	tion/Specification
Name	Text
Module 'TrqArblim'	Report************************************
	Name of Tester:Priti Mangalekar Code File(s) Under Test: Ap_TrqArblim.c Code File(s) Version:5 Module Design Document:TrqArblim_MDD.docx Module Design Document Version:6 Data Dictionary Version:5 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):1456 Total RAM Used (Bytes):133 Total CALS Used (Bytes):104 Special Test Requirements: Test Date:9/16/2014 Comments: Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested. Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference. Note 3:In function ""TrqArblim_Per1"" path ""(D_ONE_ULS_F32 <= ((D_2MS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" cannot be covered as ""((D_2MS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" always gets updated with a value less than 1."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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Attributes	Attributes				
Name	Value				
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj				
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src				
Library	libc.a				
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd				
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl				
Time Enabled	false				
Time Unit	Cycles				
Timer Prescale	0				
Timer Resolution	1				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg				
Workspace File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP				



Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 2665.00 Cycles TS1.2 2522.00 Cycles

Description

Vector Description:

TS 1.1Shortest execution Path: (APAlowDisableRate_pSec_T_f32 >= -0.2F)=True,(PosServEnable_Cnt_T_lgc == TRUE)=False, (GMOSHOscillate_State_T_enum == Haptic_Active)=False,(k_APAUseAsstScale_lgc == TRUE)=True,(k_APAUseRetScale_lgc == TRUE)=True, (k_APAUseRetScale_lgc == TRUE)=True, (k_APAUseAbmpScale_lgc == TRUE)=False, (k_LKAUseSlewCal_lgc == FALSE)=False, (FinalSlew_NmpSec_T_f32 < D_FINALSLEWLMT_NMPSec_F32)=False, (Abs_f32_m(ESCCmd_HwNm_T_532) > k_ESCMax_HwNm_f32)=False, (CfTqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=False, (CfTqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=False, (CfTqArblim_ESC_State_T_fals >= -0.2F)=False, (PosServEnable_Cnt_T_lgc == TRUE)=False, (CfTqArblim_ESC_State_T_fals >= -0.2F)=False, (FosServEnable_Cnt_T_lgc == TRUE)=False, (CfTqArblim_ESC_State_T_fals >= TRUE)=False, (CfTqArblim_ESC_State_Cnt_M_fals >= TRUE)=False, (CfTqArblim_fals >= TRUE)=False, (Cf

Test Case 2: Path Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 2392.00 Cycles
TS2.2 2725.00 Cycles
TS2.3 2732.00 Cycles
TS2.4 2423.00 Cycles
TS2.5 2459.00 Cycles
TS2.6 2309.00 Cycles
TS2.7 2608.00 Cycles
TS2.8 2472.00 Cycles
TS2.9 2301.00 Cycles
TS2.10 2425.00 Cycles
TS2.11 2457.00 Cycles
TS2.12 2239.00 Cycles
TS2.12 239.00 Cycles
TS2.13 2374.00 Cycles
TS2.14 2425.00 Cycles

Description

Vector Description:

TS2.1""(APAlowDisableRate_pSec_T_f32 >= -0.2F)=False,((LKA_State_T_enum != LKA_Active)=True || ((GMOSHOscillate_State_T_enum != Haptic_Inactive) || (PosServEnable_Cnt_T_lgc == TRUE))),(k_LKAUseSlewCal_lgc == FALSE)=True""
TS2.2(k_APAUseAstScale_lgc == TRUE)=True,(k_APAUseRetScale_lgc == TRUE)=True, (k_APAUseADmpScale_lgc == TRUE)=True
TS2.3(PosServEnable_Cnt_T_lgc == TRUE)=True,(GMOSHOscillate_State_T_enum == Haptic_Active)=True
TS2.3("(LKA_State_T_enum != LKA_Active) = False|| ((GMOSHOScillate_State_T_enum == Haptic_Active)=True
TS2.4""((LKA_State_T_enum != LKA_Active) = False|| ((PosServEnable_Cnt_T_lgc == TRUE))=True"
TS2.50pTrqOv_MtrNm_T_f32 = Limit_m(DpTrqov_MtrNm_T_f32,-D_OPTRQOVLMT_MTRNM_F32,D_OPTRQOVLMT_MTRNM_F32)=False
TS2.6LKATrqReq_HwNm_T_f32 = Limit_m(LKADesired_HwNm_T_f32, (D_2MS_SEC_F32 * -FinalSlew_NmpSec_T_f32) + TrqArblim_PreLKATrqReq_HwNm_M_f32), ((D_2MS_SEC_F32 * FinalSlew_NmpSec_T_f32)+ TrqArblim_PreLKATrqReq_HwNm_M_f32). ((D_2MS_SEC_F32 * FinalSlew_NmpSec_T_f32)+ TrqArblim_PreLKATrqReq_HwNm_M_f32)=False
TS2.8(APAlowDisableRate_pSec_T_f32 >= -0.2F)=True
TS2.9((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active))||(TrqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=True
TS2.10((TrqArblim_ESC_State_Cnt_M_enum == ESC_Active))||(TrqArblim_ESC_State_Cnt_M_enum == ESC_TemporarilyLimited))=False
TS2.11((ESC_State_T_enum != ESC_Active)&&(ESC_State_T_enum != ESC_TemporarilyLimited))=False
TS2.11((ESC_State_T_enum != ESC_Active)&&(ESC_State_T_enum != ESC_TemporarilyLimited))=False
TS2.12(1CPADSiableRate_Uls_T_f32 = Limit_m(D_ZERO_ULS_F32, (D_ZMS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))=False
TS2.13APADisableRate_Uls_T_f32 = Limit_m(D_ZERO_ULS_F32, (D_ZMS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))=False
TS2.13APADisableRate_Uls_M_f32, ((D_ZMS_SEC_F32 * k_APAEnableRate_pSec_f32) + TrqArblim_APADisableRate_Uls_M_f32))=False
TS2.14(Abs_f32_m(ESCCmd_HwNm_T_f32) > k_ESCMax_HwNm_f32)=False

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.00999999978	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85999966	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.60000038	
k_APAEnableRate_pSec_f32	1.5	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	2.24000001	
k_LKAUseSlewCal_lgc	0	
t APADisableRateX HwNm u4p12[0]	0	





Nome	Innut Value		
	Input Value 410		
	819		
	1229		
	1638		
; ; ;	2048		
	0		
	512		
	1024		
t_APADisableRateY_pSec_u7p9[3]	1536		
t_APADisableRateY_pSec_u7p9[4]	2048		
t_APADisableRateY_pSec_u7p9[5]	2560		
t_APASmoothX_Uls_u2p14[0]	0		
t_APASmoothX_Uls_u2p14[1]	164		
t_APASmoothX_Uls_u2p14[2]	328		
	492		
	655		
	819		
	983		
	1147		
	1311		
	1475		
	12780		
	12943		
	13107 13271		
t_APASmoothY_Uls_u2p14[4] t APASmoothY Uls u2p14[5]	13435 13599		
	13763		
	13926		
	14090		
	14254		
	0		
	512		
	1024		
	1536		
t_LKASlewX_Kph_u8p8[4]	2048		
t_LKASlewX_Kph_u8p8[5]	2560		
t_LKASlewY_NmpSec_u4p12[0]	0		
t_LKASlewY_NmpSec_u4p12[1]	410		
t_LKASlewY_NmpSec_u4p12[2]	819		
t_LKASlewY_NmpSec_u4p12[3]	1229		
t_LKASlewY_NmpSec_u4p12[4]	1638		
	2048		
	target_TrqArblim_Per1_AssistDDFactor_Uls_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_U		
	target_TrqArblim_Per1_ESCCmd_HwNm_f3:		
	target_TrqArblim_Per1_ESCIsLimited_Cnt_lg		
	target_TrqArblim_Per1_ESCState_State_enu		
	target_TrqArblim_Per1_ESCTorqueDelivered		
	target_TrqArblim_Per1_GMOSHOscillate_Starget_TrqArblim_Per1_HwTorque_HwNlm_ff	_	
	target_TrqArblim_Per1_HwTorque_HwNm_f3 target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
	target TrqArblim Per1 LKACmd HwNm f32		
	target_TrqArblim_Per1_LKAState_State_enu		
	target TrqArblim Per1 LKATorqueDelivered		
	target_TrqArblim_Per1_MinSecureVehicleSp		
	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
	target TrqArblim Per1 PosServEnable Cnt		
0 = = = 1 = 1	target_TrqArblim_Per1_PosSrvoCmd_MtrNm	- •	
	target_TrqArblim_Per1_ReturnDDFactor_Uls		
	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_	
	-7.82000017		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.64000034		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	2		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
talget_ind talmin_i or i_r coccin_mane_ont_igenate	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.20999993		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.20999993	Expected Value	Result

TrqArblim_Per1





Name	Actual Value	Expected Value	Result
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.85956001	-9.85956001 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003	-10.2200003 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.5732336	-8.57322979 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1)	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim 0.023
FrqArblim_APADisableRate_Uls_M_f32	
rqArblim_ESC_State_Cnt_M_enum	0
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.25
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
rqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046
rqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038
_APAEnableRate_pSec_f32	6
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
_ESCMax_HwNm_f32	4.8499999
_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	410
_APADisableRateX_HwNm_u4p12[1]	819
_APADisableRateX_HwNm_u4p12[2]	1229
_APADisableRateX_HwNm_u4p12[3]	1638
_APADisableRateX_HwNm_u4p12[4]	2048
_APADisableRateX_HwNm_u4p12[5]	2458
_APADisableRateY_pSec_u7p9[0]	512
_APADisableRateY_pSec_u7p9[1]	1024
_APADisableRateY_pSec_u7p9[2]	1536
_APADisableRateY_pSec_u7p9[3]	2048
_APADisableRateY_pSec_u7p9[4]	2560
_APADisableRateY_pSec_u7p9[5]	3072
_APASmoothX_Uls_u2p14[0]	164
_APASmoothX_Uls_u2p14[1]	328
_APASmoothX_Uls_u2p14[2]	492
_APASmoothX_Uls_u2p14[3]	655
_APASmoothX_Uls_u2p14[4]	819
_APASmoothX_Uls_u2p14[5]	983
_APASmoothX_Uls_u2p14[6]	1147
_APASmoothX_Uls_u2p14[7]	1311
_APASmoothX_Uls_u2p14[8]	1475
_APASmoothX_Uls_u2p14[9]	1638
_APASmoothY_Uls_u2p14[0]	12943
_APASmoothY_Uls_u2p14[1]	13107
_APASmoothY_Uls_u2p14[2]	13271
_APASmoothY_Uls_u2p14[3]	13435
_APASmoothY_Uls_u2p14[4]	13599
_APASmoothY_Uls_u2p14[5]	13763
APASmoothY_Uls_u2p14[6]	13926
_APASmoothY_Uls_u2p14[7]	14090
_APASmoothY_Uls_u2p14[8]	14254
_APASmoothY_Uls_u2p14[9]	14418
_LKASlewX_Kph_u8p8[0]	512
_LKASlewX_Kph_u8p8[1]	1024
LKASlewX Kph u8p8[2]	1536
	2048

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TraArblim Per1

TrqArblim_Per1			MACHICAL
Name	Input Value		
t_LKASlewX_Kph_u8p8[4]	2560		
t_LKASlewX_Kph_u8p8[5]	3072		
t_LKASlewY_NmpSec_u4p12[0]	410		
t_LKASlewY_NmpSec_u4p12[1]	819		
t_LKASlewY_NmpSec_u4p12[2]	1229		
_LKASlewY_NmpSec_u4p12[3]	1638		
t_LKASlewY_NmpSec_u4p12[4]	2048		
t_LKASlewY_NmpSec_u4p12[5]	2458		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3:	2	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	1_f32	
arget_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989		
arget_TrqArblim_Per1_ESCState_State_enum.value	1		
arget_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.22999954		
arget_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995		
arget_TrqArblim_Per1_LKAState_State_enum.value	1		
arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	5.86000013		
arget_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
arget_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.560000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.0999999		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.0109999999	0.0109999999 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	1	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.24571609	-9.24571991 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046	-9.02000046 ± 0.00024414062	25
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.09400082	-8.09399986 ± 0.03125	
arget_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	
arget_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	
arget_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	
arget_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4.8499999	-4.8499999 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	
arget_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	
torget TraArblim Bord OnTraOv MtrNm f33 value	0.0794795151	0.0794795002 + 0000200625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TroArblim Per1 CP1 CheckpointReached	1	_

-0.0784785151

0.99129951

-0.0784785002 ± 0000390625

0.991299987 ± 0000390625

Test Step 2.3 (Repeat Count = 1)		v
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.0359999985	
TrqArblim_ESC_State_Cnt_M_enum	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.64000034	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.82000017	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.5999999	
k_APAEnableRate_pSec_f32	10.5	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value





Name	Input Value
k_ESCMax_HwNm_f32	3.30999994
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	819
t_APADisableRateX_HwNm_u4p12[1]	1229
t_APADisableRateX_HwNm_u4p12[2]	1638
t_APADisableRateX_HwNm_u4p12[3]	2048 2458
t_APADisableRateX_HwNm_u4p12[4] t_APADisableRateX_HwNm_u4p12[5]	2867
t_APADisableRateY_pSec_u7p9[0]	1024
t_APADisableRateY_pSec_u7p9[1]	1536
t_APADisableRateY_pSec_u7p9[2]	2048
t_APADisableRateY_pSec_u7p9[3]	2560
t_APADisableRateY_pSec_u7p9[4]	3072
t_APADisableRateY_pSec_u7p9[5]	3584
t_APASmoothX_UIs_u2p14[0]	328 492
t_APASmoothX_Uls_u2p14[1] t_APASmoothX_Uls_u2p14[2]	655
t_APASmoothX_Uls_u2p14[3]	819
t APASmoothX Uls u2p14[4]	983
t_APASmoothX_Uls_u2p14[5]	1147
t_APASmoothX_Uls_u2p14[6]	1311
t_APASmoothX_Uls_u2p14[7]	1475
t_APASmoothX_Uls_u2p14[8]	1638
t_APASmoothX_UIs_u2p14[9]	1802
t_APASmoothY_UIs_u2p14[0]	13107
t_APASmoothY_Uls_u2p14[1] t_APASmoothY_Uls_u2p14[2]	13271 13435
t_APASmoothY_Uls_u2p14[3]	13599
t_APASmoothY_Uls_u2p14[4]	13763
t_APASmoothY_Uls_u2p14[5]	13926
t_APASmoothY_Uls_u2p14[6]	14090
t_APASmoothY_Uls_u2p14[7]	14254
t_APASmoothY_Uls_u2p14[8]	14418
t_APASmoothY_Uls_u2p14[9]	14582
t_LKASlewX_Kph_u8p8[0] t_LKASlewX_Kph_u8p8[1]	1024 1536
t_LKASlewX_Kph_u8p8[2]	2048
t_LKASlewX_Kph_u8p8[3]	2560
t_LKASlewX_Kph_u8p8[4]	3072
t_LKASlewX_Kph_u8p8[5]	3584
t_LKASlewY_NmpSec_u4p12[0]	819
t_LKASlewY_NmpSec_u4p12[1]	1229
t_LKASlewY_NmpSec_u4p12[2]	1638 2048
t_LKASlewY_NmpSec_u4p12[3] t_LKASlewY_NmpSec_u4p12[4]	2458
t_LKASlewY_NmpSec_u4p12[5]	2867
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_State_enum target_TrqArblim_Per1_HwTorque_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target_TrqArblim_Per1_nwTorque_nwNrii_132
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target TrgArblim Per1 LKACmd HwNm f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_Rte_inst_Ap_irqArbiim.irqArbiim_Peri_irqOscCmd_mtrnm_is2 target_TrqArblim_Peri_ESCCmd_HwNm_f32.value	-5.42000008
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.82000017
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0
target_TrqArblim_Per1_LKAState_State_enum.value	2
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	9.43000031
	l .
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1 0.910000026

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Name	Input Value		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.057	0.057 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.62835217	-8.62835026 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.910000026	0.910000026 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.59399986	-7.59399986 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.30999994	-3.30999994 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	6.34341431	6.34341002 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Nama	Input Value
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.101000004
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.59000015
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.82000005
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.0999999
k_APAEnableRate_pSec_f32	33
k_APAUseADmpScale_lgc	0
<_APAUseAsstScale_lgc	0
<_APAUseRetScale_lgc	0
x_ESCMax_HwNm_f32	0.337000012
_LKAUseSlewCal_lgc	0
:_APADisableRateX_HwNm_u4p12[0]	2867
_APADisableRateX_HwNm_u4p12[1]	3277
_APADisableRateX_HwNm_u4p12[2]	3686
:_APADisableRateX_HwNm_u4p12[3]	4096
:_APADisableRateX_HwNm_u4p12[4]	4506
_APADisableRateX_HwNm_u4p12[5]	4915
_APADisableRateY_pSec_u7p9[0]	3584
_APADisableRateY_pSec_u7p9[1]	4096
_APADisableRateY_pSec_u7p9[2]	4608
_APADisableRateY_pSec_u7p9[3]	5120
_APADisableRateY_pSec_u7p9[4]	5632
_APADisableRateY_pSec_u7p9[5]	6144
_APASmoothX_Uls_u2p14[0]	1147
_APASmoothX_Uls_u2p14[1]	1311
_APASmoothX_Uls_u2p14[2]	1475
_APASmoothX_Uls_u2p14[3]	1638
_APASmoothX_UIs_u2p14[4]	1802
_APASmoothX_UIs_u2p14[5]	1966
_APASmoothX_Uls_u2p14[6]	2130
_APASmoothX_UIs_u2p14[7]	2294
_APASmoothX_Uls_u2p14[8]	2458
_APASmoothX_Uls_u2p14[9]	2621
_APASmoothY_Uls_u2p14[0]	13926
_APASmoothY_Uls_u2p14[1]	14090
t_APASmoothY_Uls_u2p14[2]	14254
t_APASmoothY_Uls_u2p14[3]	14418
_APASmoothY_Uls_u2p14[4]	14582
APASmoothY_Uls_u2p14[5]	14746
	14909
t_APASmoothY_Uls_u2p14[7]	15073
t APASmoothY Uls u2p14[8]	15237
t_APASmoothY_Uls_u2p14[9]	15401
t_LKASlewX_Kph_u8p8[0]	3584

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	TIQAIDIIII_Peri			TUACITU
LLKASlewX, Kph_u8p8[2]	Name	Input Value		
LKASiewX_Kph_u8p8[2]	LKASlewX Kph u8p8[1]	4096		
LIKASiewX , Kpn , u8p8[3] LIKASiewX , Kpn , u8p8[4] 5832 5862 5867		4608		
LLKASiewX_Kgn_u8p8[4] 5532 5144		5120		
LIKASlewY.nmpSec_u4p12[0]	_LKASlewX_Kph_u8p8[4]	5632		
LIXASiewY_NmpSec_u4p12[1] 3277 LIXASiewY_NmpSec_u4p12[2] 4966 LIXASiewY_NmpSec_u4p12[3] 4966 LIXASiewY_NmpSec_u4p12[4] 4506 LIXASiewY_NmpSec_u4p12[5] 4915 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_DampingDDFactor_Uis_f32 target_TraArbim_Perl_DampingDDFactor_Uis_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_DampingDDFactor_Uis_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_ESCGMd_HwMin_f32 target_TraArbim_Perl_ESCGMd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_ESCGMd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_ESCGTotapleDelivered_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_ESCGTotapleDelivered_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_ESCGTotapleDelivered_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_ETGGTOTapleDelivered_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_IATGV_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_IATGV_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_IATGV_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_LKACnd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_LKACnd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_LKACnd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_LKACnd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_LKACnd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_LKACnd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_LKACnd_HwMin_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_LKACnd_HwMin_f32 target_TraArbim_Perl_Min_SecureVehicleSpeed_Kpi_f32 target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TraArbim_TraArbim_Perl_PosServEnable_Cnt_Igc target_TraArbim_Perl_PosServEnable_Cnt_Igc target_TraArbim_Perl_PosServEnable_Cnt_Igc target_TraArbim_Perl_PosServEnable_Cnt_Igc target_TraArbim_Perl_PosServEnable_Cnt_Igc target_TraArbim_Perl_PosServEnable_Cnt_Igc target_TraArbim_Perl_PosServEnable_Cnt_Igc target_TraArbim_Perl_PosServEnable_Cnt_Igc target_TraArbim_Perl_PosServEnable_Cnt_Igc	 _LKASlewX_Kph_u8p8[5]	6144		
LLKASiewY_NmpSec_u4p12[2]	_LKASlewY_NmpSec_u4p12[0]	2867		
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target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_ESCSLsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_ESCSLsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_ESCState_state_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_IdTqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Pref_IdTqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_LKACMd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_LKACState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_LKACState_State_enum target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_LKACTqrqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_LKACTqrqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_PoStrovCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_PoSsrovCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_PoSsrovCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_PoSsrovCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_PoSsrovCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Perf_PoSsrovCmd_MtrNm_f32 target_TrqArblim_Perf_PoSSrovCmd_MtrNm_f32 target_TrqArblim_Perf_PoSSrovCmd_MtrNm_f32 target_TrqArblim_Perf_ESCCMd_HwNm_f32.value 0.579999983 target_TrqArblim_Perf_HwTorque_HwNm_f32.value 10.230000004 target_TrqArblim_Perf_HwNGqt_HwNm_f32.value 11.230000004 target_TrqArblim_Perf_PoSSrovCmd_MtrNm_f32.value 12.66000009 target_TrqArblim_Perf_PoSSrovCmd_MtrNm_f32.value 25.6339996 target_TrqArblim_Perf_PoSSrovCmd_MtrNm_f32.value 25.66000009 target_TrqArblim_Perf_PoSSrovCmd_MtrNm_f32.value 25.66000009 target_TrqArblim_Perf_PoSSrovCmd_MtrNm_f32.value 25.66000009 target_TrqArblim_Perf_PoSSrovCmd_MtrNm_f32.value 25.6339996 target_TrqArblim_Perf_PoSSrovC	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
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target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 0.579999983 target_TrqArblim_Per1_ESCState_State_enum.value 2 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 0.230000004 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 1 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 1 target_TrqArblim_Per1_PosScrvEnable_Cnt_Igc.value 1 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 2.66000009	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 0.579999983 target_TrqArblim_Per1_ESCCmd_MtrNm_f32 target_TrqArblim_Per1_ESCState_State_enum.value 10 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 0.230000004 target_TrqArblim_Per1_LKACMd_HwNm_f32.value 1arget_TrqArblim_Per1_LKAState_State_enum.value 1arget_TrqArblim_Per1_LKAState_State_enum.value 1arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 2566.339996 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1arget_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1arget_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1arget_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 266000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 270000019 Name Actual Value Expected Value	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f33	2	
target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 0.579999983 target_TrqArblim_Per1_ESCState_State_enum.value 10 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 0.230000004 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 10 target_TrqArblim_Per1_LKACmd_HwNm_f32.value 11 target_TrqArblim_Per1_LKAState_State_enum.value 12 target_TrqArblim_Per1_LKAState_State_enum.value 13 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 14 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 15 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 16 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 17 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 17 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 17 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 17 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 18 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 266000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 27 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 28 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 29 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 40 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 40 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 40 target_Trq	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value 2 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 0.230000004 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 2266.339996 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2 target_TrqArblim_Per1_NinSecureVehicleSpeed_Kph_f32.value 2 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 3 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 4 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 4 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 4 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 4 tar	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	I_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value target_TrqArblim_Per1_PosServCmd_MtrNm_f32.value target_TrqArblim_Per1_PosServCmd_MtrNm_f32.value target_TrqArblim_Per1_PosServCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_TrqOscCmd_Mt	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_UKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_DsServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value Retroptorium_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value Retroptorium_TrqArblim_P	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_DsServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosServEnable_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value 0.579999983 target_TrqArblim_Per1_ESCState_State_enum.value 2 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 0 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 0.230000004 target_TrqArblim_Per1_LKACmd_HwNm_f32.value -8.11999989 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 256.339996 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_TrqArblim_Per1_ESCState_State_enum.value 2 target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 0 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 0.230000004 target_TrqArblim_Per1_LKACmd_HwNm_f32.value -8.11999989 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 256.339996 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value	arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_f32	
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value 0 target_TrqArblim_Per1_HwTorque_HwNm_f32.value 0.230000004 target_TrqArblim_Per1_LKACmd_HwNm_f32.value -8.11999989 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 256.339996 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value	arget_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value 0.230000004 target_TrqArblim_Per1_LKACmd_HwNm_f32.value -8.11999989 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 256.339996 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value	arget_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value -8.11999989 target_TrqArblim_Per1_LKAState_State_enum.value 1 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 256.339996 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value Reference of the control o	arget_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value Reference of the control of t	arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.230000004		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value 256.339996 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value Re	arget_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.11999989		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value 1 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value	.arget_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 2.66000009 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value Re	arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	256.339996		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value 7.30000019 Name Actual Value Expected Value Received Value	.arget_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
Name Actual Value Expected Value Re	arget_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.66000009		
100 100	.arget_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.30000019		
	Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	TrqArblim_APADisableRate_Uls_M_f32	0.166999996	0.166999996 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum 2	FrqArblim_ESC_State_Cnt_M_enum	2	2	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 -5.48873234 -5.48872995 ± 0.03125	FrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.48873234	-5.48872995 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_32 2.66000009 2.66000009 ± 0.000244140625	FrqArblim_PosSrvoCmd_MtrNm_M_f32	2.66000009	2.66000009 ± 0.00024414062	25
TrqArblim_PreLKATrqReq_HwNm_M_f32 -5.09399986 ± 0.03125	FrqArblim_PreLKATrqReq_HwNm_M_f32	-5.09399986	-5.09399986 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 1 ± 0000390625	arget_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 1 ± 0000390625	arget_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value 1	arget_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0.337000012 0.337000012 0.337000012	arget_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.337000012	0.337000012 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value 0.337000012	arget_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.337000012	0.337000012 ± 0.0000390625	5
$target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value \\ 0 \\ target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value \\ 0 \\ target_TrqArblim_HwNm_f32.value \\ 0 \\ target_TrqArblim_HwNm_f3$	arget_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 0.417567999 ± 0000390625	arget_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.417567909	0.417567998 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value 1 ± 0000390625	arget_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 2.5 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.152999997	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.98000002	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.0999999	
k_APAEnableRate_pSec_f32	51	





	Input Value
	1
	1
	1 1.20000005
k_ESCMax_HwNm_f32 k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	4506
t_APADisableRateX_HwNm_u4p12[1]	4915
t_APADisableRateX_HwNm_u4p12[2]	5325
t_APADisableRateX_HwNm_u4p12[3]	5734
t_APADisableRateX_HwNm_u4p12[4]	6144
t_APADisableRateX_HwNm_u4p12[5]	6554
t_APADisableRateY_pSec_u7p9[0]	5632
t_APADisableRateY_pSec_u7p9[1] t_APADisableRateY_pSec_u7p9[2]	6144 6656
	7168
t_APADisableRateY_pSec_u7p9[4]	7680
	8192
t_APASmoothX_Uls_u2p14[0]	1802
t_APASmoothX_Uls_u2p14[1]	1966
	2130
t_APASmoothX_UIs_u2p14[3]	2294
t_APASmoothX_UIs_u2p14[4]	2458
t_APASmoothX_Uls_u2p14[5]	2621 2785
	2949
	3113
t_APASmoothX_Uls_u2p14[9]	3277
t_APASmoothY_Uls_u2p14[0]	14582
t_APASmoothY_Uls_u2p14[1]	14746
t_APASmoothY_Uls_u2p14[2]	14909
t_APASmoothY_Uls_u2p14[3]	15073
t_APASmoothY_UIs_u2p14[4]	15237
t_APASmoothY_Uls_u2p14[5] t_APASmoothY_Uls_u2p14[6]	15401 15565
t_APASmoothY_UIs_u2p14[7]	15729
t_APASmoothY_Uls_u2p14[8]	15892
t_APASmoothY_Uls_u2p14[9]	16056
t_LKASlewX_Kph_u8p8[0]	5632
	6144
t_LKASlewX_Kph_u8p8[2]	6656
	7168
t_LKASlewX_Kph_u8p8[4] t_LKASlewX_Kph_u8p8[5]	7680 8192
t_LKASlewY_NmpSec_u4p12[0]	4506
t_LKASlewY_NmpSec_u4p12[1]	4915
t_LKASlewY_NmpSec_u4p12[2]	5325
t_LKASlewY_NmpSec_u4p12[3]	5734
t_LKASlewY_NmpSec_u4p12[4]	6144
t_LKASlewY_NmpSec_u4p12[5]	6554
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_IrqArblim_Per1_ESCCmd_HWNm_132 target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target TrqArblim Per1 ESCState State enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011
0 = 1 = = =	1
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.86999989
target_TrqArblim_Per1_LKACmd_HwNm_f32.value target TrqArblim Per1 LKAState State enum.value	-6.15999985 0

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Name	Input Value		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	38.2933006		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.10000038		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.254999995	0.254999995 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.91367602	-2.91368008 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.20000005	1.20000005 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.20000005	1.20000005 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.244000003
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.12
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0416000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.40000006
k_APAEnableRate_pSec_f32	82.5
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	7373
t_APADisableRateX_HwNm_u4p12[1]	7782
t_APADisableRateX_HwNm_u4p12[2]	8192
t_APADisableRateX_HwNm_u4p12[3]	8602
t_APADisableRateX_HwNm_u4p12[4]	9011
t_APADisableRateX_HwNm_u4p12[5]	9421
t_APADisableRateY_pSec_u7p9[0]	9216
t APADisableRateY pSec u7p9[1]	9728
t_APADisableRateY_pSec_u7p9[2]	10240
t_APADisableRateY_pSec_u7p9[3]	10752
t_APADisableRateY_pSec_u7p9[4]	11264
t_APADisableRateY_pSec_u7p9[5]	11776
t_APASmoothX_Uls_u2p14[0]	2949
t APASmoothX Uls u2p14[1]	3113
t_APASmoothX_Uls_u2p14[2]	3277
t APASmoothX Uls u2p14[3]	3441
t APASmoothX Uls u2p14[4]	3604
t_APASmoothX_Uls_u2p14[5]	3768
t APASmoothX Uls u2p14[6]	3932
t_APASmoothX_Uls_u2p14[7]	4096
t_APASmoothX_Uls_u2p14[8]	4260
t APASmoothX Uls u2p14[9]	4424
t_APASmoothY_Uls_u2p14[0]	0
t_APASmoothY_Uls_u2p14[1]	164
t APASmoothY Uls u2p14[1]	328
	492
t_APASmoothY_Uls_u2p14[3]	655
t_APASmoothY_Uls_u2p14[4]	
t_APASmoothY_UIs_u2p14[5]	819
t_APASmoothY_Uls_u2p14[6]	983
t_APASmoothY_Uls_u2p14[7]	1147

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TrqArblim_Per1

TrqArblim_Per1			MACHAG
Name	Input Value		
t_APASmoothY_Uls_u2p14[8]	1311		
t_APASmoothY_Uls_u2p14[9]	1475		
t LKASlewX Kph u8p8[0]	9216		
t_LKASlewX_Kph_u8p8[1]	9728		
t LKASlewX Kph u8p8[2]	10240		
t_LKASlewX_Kph_u8p8[3]	10752		
t LKASlewX Kph u8p8[4]	11264		
t_LKASlewX_Kph_u8p8[5]	11776		
t_LKASlewY_NmpSec_u4p12[0]	7373		
t_LKASlewY_NmpSec_u4p12[1]	7782		
t_LKASlewY_NmpSec_u4p12[2]	8192		
t_LKASlewY_NmpSec_u4p12[3]	8602		
t_LKASlewY_NmpSec_u4p12[4]	9011		
t_LKASlewY_NmpSec_u4p12[5]	9421		
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_Si	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.34000015		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-2.73000002		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	83.4432983		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.409000009	0.409000009 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.29555202	1.29554999 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.70000005	-3.70000005 ± 0.0002441406	25
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.382734507	0.382735014 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.382734507	0.382734507 ± 0.0000390625	· •
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.382734507	0.382734507 ± 0.0000390625	; ~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.86376238	1.86375999 ± 0000390625	✓
target TraArblim Per1 PeturpDPEactor Lile f32 value	1	1 + 0000300625	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

1 ± 0000390625

Test Step 2.7 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.34799999	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

TrqArblim_Per1

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Name	Input Value
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.4000001
k_APAEnableRate_pSec_f32	118.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc k_ESCMax_HwNm_f32	1 1.27999997
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	10650
t_APADisableRateX_HwNm_u4p12[1]	11059
t_APADisableRateX_HwNm_u4p12[2]	11469
t_APADisableRateX_HwNm_u4p12[3]	11878
t_APADisableRateX_HwNm_u4p12[4]	12288
t_APADisableRateX_HwNm_u4p12[5]	12698
t_APADisableRateY_pSec_u7p9[0]	13312 13824
t_APADisableRateY_pSec_u7p9[1] t_APADisableRateY_pSec_u7p9[2]	14336
t_APADisableRateY_pSec_u7p9[3]	14848
t APADisableRateY pSec u7p9[4]	15360
t_APADisableRateY_pSec_u7p9[5]	15872
t_APASmoothX_UIs_u2p14[0]	4260
t_APASmoothX_Uls_u2p14[1]	4424
t_APASmoothX_Uls_u2p14[2]	4588
t_APASmoothX_Uls_u2p14[3]	4751
t_APASmoothX_UIs_u2p14[4]	4915
t_APASmoothX_Uls_u2p14[5] t_APASmoothX_Uls_u2p14[6]	5079 5243
t_APASmoothX_Uls_u2p14[7]	5407
t_APASmoothX_Uls_u2p14[8]	5571
t_APASmoothX_Uls_u2p14[9]	5734
t_APASmoothY_Uls_u2p14[0]	1311
t_APASmoothY_Uls_u2p14[1]	1475
t_APASmoothY_Uls_u2p14[2]	1638
t_APASmoothY_Uls_u2p14[3]	1802
t_APASmoothY_UIs_u2p14[4]	1966
t_APASmoothY_Uls_u2p14[5] t_APASmoothY_Uls_u2p14[6]	2130 2294
t_APASmoothY_Uls_u2p14[7]	2458
t APASmoothY Uls u2p14[8]	2621
t_APASmoothY_Uls_u2p14[9]	2785
t_LKASlewX_Kph_u8p8[0]	13312
t_LKASlewX_Kph_u8p8[1]	13824
t_LKASlewX_Kph_u8p8[2]	14336
t_LKASlewX_Kph_u8p8[3]	14848
t_LKASlewX_Kph_u8p8[4]	15360
t_LKASlewX_Kph_u8p8[5] t_LKASlewY_NmpSec_u4p12[0]	15872 10650
t LKASlewY NmpSec u4p12[1]	11059
t LKASlewY NmpSec u4p12[2]	11469
t LKASlewY NmpSec u4p12[3]	11878
t_LKASlewY_NmpSec_u4p12[4]	12288
t_LKASlewY_NmpSec_u4p12[5]	12698
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCState_State_enum
target_kte_inst_Ap_frqArblim.frqArblim_Peri_ESCState_state_enum target_kte_inst_Ap_frqArblim.frqArblim_Peri_ESCTorqueDelivered_HwNm_f32	target_IrqArbiim_Peri_ESCState_State_enum target TrqArbiim_Peri_ESCTorqueDelivered_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrgArblim Per1 GMOSHOscillate State enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServEnable_Cnt_Igc target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
gotoo_mot_rp_rrq/abann.rrq/abann_r or i_r osorvoomd_warvani_toz	target TrgArblim Per1 ReturnDDFactor Uls f32
target Rte Inst Ap TrgArblim TrgArblim Per1 ReturnDDFactor Uls f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 -5.19999981
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	

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Name	Input Value		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.5999999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	135.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.560000002		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.285999984	0.286000013 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.27776003	5.27776003 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985	-6.15999985 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.39379978	4.39379978 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.186563313	-0.186563 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrgArblim APADisableRate Uls M f32	0.77499976	
TrgArblim ESC State Cnt M enum	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.38600007	
TrgArblim PosSrvoCmd MtrNm M f32	9.71000004	
TrgArblim PreLKATrgReg HwNm M f32	2.5999999	
k APAEnableRate pSec f32	411.75	
k_APAUseADmpScale_lgc	0	
k APAUseAsstScale lgc	0	
k APAUseRetScale lgc	0	
k_ESCMax_HwNm_f32	2.57999992	
k_LKAUseSlewCal_lgc	0	
t APADisableRateX HwNm u4p12[0]	29491	
t_APADisableRateX_HwNm_u4p12[1]	29901	
t_APADisableRateX_HwNm_u4p12[2]	30310	
t_APADisableRateX_HwNm_u4p12[3]	30720	
t_APADisableRateX_HwNm_u4p12[4]	31130	
t_APADisableRateX_HwNm_u4p12[5]	31539	
t_APADisableRateY_pSec_u7p9[0]	0	
t_APADisableRateY_pSec_u7p9[1]	0	
t_APADisableRateY_pSec_u7p9[2]	0	
t_APADisableRateY_pSec_u7p9[3]	0	
t_APADisableRateY_pSec_u7p9[4]	0	
t_APADisableRateY_pSec_u7p9[5]	0	
t_APASmoothX_Uls_u2p14[0]	12124	
t_APASmoothX_Uls_u2p14[1]	12288	
t_APASmoothX_Uls_u2p14[2]	12452	
t_APASmoothX_Uls_u2p14[3]	12616	
t_APASmoothX_Uls_u2p14[4]	12780	
t_APASmoothX_Uls_u2p14[5]	12943	
t_APASmoothX_Uls_u2p14[6]	13107	
t_APASmoothX_Uls_u2p14[7]	13271	
t_APASmoothX_Uls_u2p14[8]	13435	
t_APASmoothX_Uls_u2p14[9]	13599	
t_APASmoothY_Uls_u2p14[0]	9175	
t_APASmoothY_Uls_u2p14[1]	9339	
t_APASmoothY_Uls_u2p14[2]	9503	
t_APASmoothY_Uls_u2p14[3]	9667	
t_APASmoothY_Uls_u2p14[4]	9830	

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TrqArblim_Per1

TrqArbiim_Peri			ad Citato
Name	Input Value		
t_APASmoothY_Uls_u2p14[5]	9994		
t_APASmoothY_Uls_u2p14[6]	10158		
t_APASmoothY_Uls_u2p14[7]	10322		
t_APASmoothY_Uls_u2p14[8]	10486		
t_APASmoothY_Uls_u2p14[9]	10650		
t_LKASlewX_Kph_u8p8[0]	36352		
t_LKASlewX_Kph_u8p8[1]	36864		
t_LKASlewX_Kph_u8p8[2]	37376		
t_LKASlewX_Kph_u8p8[3]	37888		
t_LKASlewX_Kph_u8p8[4]	38400		
t_LKASlewX_Kph_u8p8[5]	38912		
t_LKASlewY_NmpSec_u4p12[0]	29082		
t_LKASlewY_NmpSec_u4p12[1]	29491		
t_LKASlewY_NmpSec_u4p12[2]	29901		
t_LKASlewY_NmpSec_u4p12[3]	30310		
t_LKASlewY_NmpSec_u4p12[4]	30720		
t_LKASlewY_NmpSec_u4p12[5]	31130		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	•	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target TrqArblim Per1 ReturnDDFactor Ul		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.94999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	444.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.2999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.774599969	0.774600029 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.72650003	-4.72650003 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.000244140625	-
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59399986	2.5940001 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TroArblim Per1 CP1 CheckpointReached	1	Rte Call TroArblim Per1 CP1 CheckpointReached	1	_

4.47177887

Test Step 2.9 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.200000003

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

4.47177982 ± 0000390625

1 ± 0000390625

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TrqArblim_Per1	TALOILAU
Name	Input Value
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.92999983
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	0
k_APAEnableRate_pSec_f32	231
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
<_ESCMax_HwNm_f32	0.75
<_LKAUseSlewCal_lgc https://www.ude1260	20890
:_APADisableRateX_HwNm_u4p12[0] :_APADisableRateX_HwNm_u4p12[1]	21299
t_APADisableNateX_HwNm_u4p12[2]	21709
:_APADisableRateX_HwNm_u4p12[3]	22118
t_APADisableRateX_HwNm_u4p12[4]	22528
t_APADisableRateX_HwNm_u4p12[5]	22938
t APADisableRateY pSec u7p9[0]	26112
APADisableRateY_pSec_u7p9[1]	26624
APADisableRateY_pSec_u7p9[2]	27136
 :_APADisableRateY_pSec_u7p9[3]	27648
APADisableRateY_pSec_u7p9[4]	28160
_APADisableRateY_pSec_u7p9[5]	28672
_APASmoothX_Uls_u2p14[0]	8356
_APASmoothX_Uls_u2p14[1]	8520
:_APASmoothX_Uls_u2p14[2]	8684
:_APASmoothX_Uls_u2p14[3]	8847
:_APASmoothX_Uls_u2p14[4]	9011
t_APASmoothX_Uls_u2p14[5]	9175
:_APASmoothX_Uls_u2p14[6]	9339
_APASmoothX_Uls_u2p14[7]	9503
:_APASmoothX_Uls_u2p14[8]	9667
APASmoothX_Uls_u2p14[9]	9830
t_APASmoothY_Uls_u2p14[0]	5407
t_APASmoothY_Uls_u2p14[1] t_APASmoothY_Uls_u2p14[2]	5571 5734
t_APASmoothY_Uls_u2p14[3]	5898
t_APASmoothY_Uls_u2p14[4]	6062
t_APASmoothY_Uls_u2p14[5]	6226
t_APASmoothY_Uls_u2p14[6]	6390
t_APASmoothY_Uls_u2p14[7]	6554
t_APASmoothY_Uls_u2p14[8]	6717
t_APASmoothY_Uls_u2p14[9]	6881
t_LKASlewX_Kph_u8p8[0]	26112
t_LKASlewX_Kph_u8p8[1]	26624
t_LKASlewX_Kph_u8p8[2]	27136
t_LKASlewX_Kph_u8p8[3]	27648
t_LKASlewX_Kph_u8p8[4]	28160
t_LKASlewX_Kph_u8p8[5]	28672
t_LKASlewY_NmpSec_u4p12[0]	20890
LKASlewY_NmpSec_u4p12[1]	21299
_LKASlewY_NmpSec_u4p12[2]	21709
LKASlewY_NmpSec_u4p12[3]	22118
_LKASlewY_NmpSec_u4p12[4]	22528
LKASlewY_NmpSec_u4p12[5]	22938
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32 arget Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
arget_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCist.imited_Cnt_igc arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESC/state_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESC5tate_state_entiti arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArbiim_Per1_ESC5tate_state_erium target_TrqArbiim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
rarget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
rarget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
rarget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
rarget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
	Assessed Transferring Daniel Destroy DDFs at an III- 100
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32



Name	Input Value		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.05000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.12		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	296.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.662	0.662 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.07895947	8.07896042 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.9000001	2.9000001 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	0	0 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.75	-0.75 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.75	-0.75 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.493716717	-0.493717015 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 2.10 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.375
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.30000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.20999993
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.0999999
k_APAEnableRate_pSec_f32	255.75
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	23757
t_APADisableRateX_HwNm_u4p12[1]	24166
t_APADisableRateX_HwNm_u4p12[2]	24576
t_APADisableRateX_HwNm_u4p12[3]	24986
t_APADisableRateX_HwNm_u4p12[4]	25395
t_APADisableRateX_HwNm_u4p12[5]	25805
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	9503
t_APASmoothX_Uls_u2p14[1]	9667
t_APASmoothX_Uls_u2p14[2]	9830
t_APASmoothX_Uls_u2p14[3]	9994
t_APASmoothX_Uls_u2p14[4]	10158
t_APASmoothX_Uls_u2p14[5]	10322
t_APASmoothX_Uls_u2p14[6]	10486
t_APASmoothX_Uls_u2p14[7]	10650
t_APASmoothX_Uls_u2p14[8]	10813
t_APASmoothX_Uls_u2p14[9]	10977
t_APASmoothY_Uls_u2p14[0]	6554
t_APASmoothY_Uls_u2p14[1]	6717

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TrqArblim_Per1

TrqArbiim_Per1			MACICAL	
Name	Input Value			
t_APASmoothY_Uls_u2p14[2]	6881			
t APASmoothY Uls u2p14[3]	7045			
t_APASmoothY_Uls_u2p14[4]	7209			
t_APASmoothY_Uls_u2p14[5]	7373			
t APASmoothY Uls u2p14[6]	7537			
t_APASmoothY_Uls_u2p14[7]	7700			
t_APASmoothY_Uls_u2p14[8]	7864			
t_APASmoothY_Uls_u2p14[9]	8028			
t_LKASlewX_Kph_u8p8[0]	29696			
t_LKASlewX_Kph_u8p8[1]	30208			
t_LKASlewX_Kph_u8p8[2]	30720			
t_LKASlewX_Kph_u8p8[3]	31232			
t_LKASlewX_Kph_u8p8[4]	31744			
t_LKASlewX_Kph_u8p8[5]	32256			
t_LKASlewY_NmpSec_u4p12[0]	23757			
t_LKASlewY_NmpSec_u4p12[1]	24166			
t_LKASlewY_NmpSec_u4p12[2]	24576			
t_LKASlewY_NmpSec_u4p12[3]	24986			
t_LKASlewY_NmpSec_u4p12[4]	25395			
t_LKASlewY_NmpSec_u4p12[5]	25805			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	1_132		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977			
target_TrqArblim_Per1_ESCState_State_enum.value	3			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.45000005 7.3499999			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value	1			
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	341.442993			
target TrgArblim Per1 PosServEnable Cnt lgc.value	0			
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	4.30000019			
target_TrqArblim_Per1_TrqOscVocInd_MtrNm_f32.value	0.10000001			
		Expected Value	Pog.:!4	
Name Trackbline ADADioableDate IIIe M #22	Actual Value 0,258355469	Expected Value 0.248999998 ± 0000390625	Result	
TrqArblim_APADisableRate_Uls_M_f32				
TrqArblim_ESC_State_Cnt_M_enum TrqArblim HwTorqueSV HwNm M Str.SV UIs f32	3 E 922E0046	3 -5.83249998 ± 0.03125		
TrqArblim PosSrvoCmd MtrNm M f32	-5.83250046 5.07000017	-5.83249998 ± 0.03125 5.07000017 ± 0.00024414062		
TrgArblim_PreLKATrgReg_HwNm_M_f32	-4.08839989	-4.08739996 ± 0.03125	.5	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625		
target_rrqArblim_Per1_AssistDbPactor_ols_isz.value target TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625		
target_TrqArblim_Per1_DampingDDFactor_ois_isz.value target_TrqArblim_Per1_ESCIsLimited_Cnt_lqc.value	0.696651506	0.900394022 ± 0000390625		
target_IrqArblim_Per1_ESCIsLimited_Cnt_igc.value target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625		
target_IrqArblim_Peri_ESCTorqueDelivered_HwNm_f32.value target TrqArblim_Peri lqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625		
· · · · · · · · · · · · · · · · · · ·	-3			
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.523976862	-3 ± 0.0000390625 0.505002975 ± 0000390625		
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.323810002	0.505002975 ± 0000390625	· ·	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

0.896651506

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

0.900394022 ± 0000390625



Test Step 2.11 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.600000024
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.599999
k_APAEnableRate_pSec_f32	470.25 1
k_APAUseADmpScale_lgc k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k ESCMax HwNm f32	2
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22938
t_APADisableRateX_HwNm_u4p12[1]	23347
t_APADisableRateX_HwNm_u4p12[2]	23757
t_APADisableRateX_HwNm_u4p12[3]	24166
t_APADisableRateX_HwNm_u4p12[4]	24576
t_APADisableRateX_HwNm_u4p12[5]	24986
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1] t APADisableRateY pSec u7p9[2]	30208 30720
t_APADisableRateY_pSec_u7p9[2]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	10813
t_APASmoothX_Uls_u2p14[1]	10977
t_APASmoothX_Uls_u2p14[2]	11141
t_APASmoothX_Uls_u2p14[3]	11305
t_APASmoothX_Uls_u2p14[4]	11469
t_APASmoothX_UIs_u2p14[5]	11633
t_APASmoothX_UIs_u2p14[6]	11796 11960
t_APASmoothX_Uls_u2p14[7] t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_Uls_u2p14[9]	12288
t_APASmoothY_Uls_u2p14[0]	6881
t_APASmoothY_Uls_u2p14[1]	7045
t_APASmoothY_Uls_u2p14[2]	7209
t_APASmoothY_Uls_u2p14[3]	7373
t_APASmoothY_Uls_u2p14[4]	7537
t_APASmoothY_Uls_u2p14[5]	7700
t_APASmoothY_Uls_u2p14[6]	7864
t_APASmoothY_UIs_u2p14[7]	8028 8192
t_APASmoothY_Uls_u2p14[8] t APASmoothY Uls u2p14[9]	8356
t_LKASlewX_Kph_u8p8[0]	35328
t LKASlewX Kph u8p8[1]	35840
t_LKASlewX_Kph_u8p8[2]	36352
t_LKASlewX_Kph_u8p8[3]	36864
t_LKASlewX_Kph_u8p8[4]	37376
t_LKASlewX_Kph_u8p8[5]	37888
t_LKASlewY_NmpSec_u4p12[0]	28262
t_LKASlewY_NmpSec_u4p12[1]	28672
t_LKASlewY_NmpSec_u4p12[2]	29082
t_LKASlewY_NmpSec_u4p12[3]	29491
t_LKASlewY_NmpSec_u4p12[4] t_LKASlewY_NmpSec_u4p12[5]	29901 30310
target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor UIs f32	target TrgArblim Per1 AssistDDFactor UIs f32
target_Rte_Inst_Ap_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
	-





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServ	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvo	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDl	DFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscC	md_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954			
target_TrqArblim_Per1_ESCState_State_enum.value	4			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.69999981			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.809000015	0.809000015 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	4	4	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.97300005	-2.97300005 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	✓	
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58520031	5.58519983 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	✓	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	~	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.618896842	0.618897021 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.80000019
k_APAEnableRate_pSec_f32	0
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	0
t_APADisableRateX_HwNm_u4p12[1]	0
t_APADisableRateX_HwNm_u4p12[2]	0
t_APADisableRateX_HwNm_u4p12[3]	0
t_APADisableRateX_HwNm_u4p12[4]	0
t_APADisableRateX_HwNm_u4p12[5]	0
t_APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	0
t_APADisableRateY_pSec_u7p9[2]	0
t_APADisableRateY_pSec_u7p9[3]	0
t_APADisableRateY_pSec_u7p9[4]	0
t_APADisableRateY_pSec_u7p9[5]	0
t_APASmoothX_Uls_u2p14[0]	0
t_APASmoothX_Uls_u2p14[1]	0
t_APASmoothX_Uls_u2p14[2]	0
t_APASmoothX_Uls_u2p14[3]	0
t_APASmoothX_Uls_u2p14[4]	0
t_APASmoothX_Uls_u2p14[5]	0
t_APASmoothX_Uls_u2p14[6]	0
t_APASmoothX_Uls_u2p14[7]	0

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TrqArblim_Per1

' -			
Name	Input Value		
t_APASmoothX_Uls_u2p14[8]	0		
t_APASmoothX_Uls_u2p14[9]	0		
t_APASmoothY_Uls_u2p14[0]	0		
t_APASmoothY_Uls_u2p14[1]	0		
t_APASmoothY_Uls_u2p14[2]	0		
t_APASmoothY_Uls_u2p14[3]	0		
t_APASmoothY_Uls_u2p14[4]	0		
t_APASmoothY_Uls_u2p14[5]	0		
t_APASmoothY_Uls_u2p14[6]	0		
t_APASmoothY_Uls_u2p14[7]	0		
t_APASmoothY_Uls_u2p14[8]	0		
t_APASmoothY_Uls_u2p14[9]	0		
t_LKASlewX_Kph_u8p8[0]	0		
t_LKASlewX_Kph_u8p8[1]	0		
t_LKASlewX_Kph_u8p8[2]	0		
t_LKASlewX_Kph_u8p8[3]	0		
t_LKASlewX_Kph_u8p8[4]	0		
t_LKASlewX_Kph_u8p8[5]	0		
t_LKASlewY_NmpSec_u4p12[0]	0		
t_LKASlewY_NmpSec_u4p12[1]	0		
t_LKASlewY_NmpSec_u4p12[2]	0		
t_LKASlewY_NmpSec_u4p12[3]	0		
t_LKASlewY_NmpSec_u4p12[4]	0		
t_LKASlewY_NmpSec_u4p12[5]	0		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	<u>*</u>	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	target_TrqArblim_Per1_TrqOscCmd_MtrNm -10.2299995	_132	
target TrqArblim Per1 ESCState State enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10		
target TrgArblim Per1 LKACmd HwNm f32.value	-10.2399998		
target TrgArblim Per1 LKAState State enum.value	0		
target_rrqArblim_Per1_LKAState_State_enum.value target_rrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_rrqArblim_ren_wiinSecureveriicieSpeed_Rpin_isz.value target TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_rrqArblim_Per1_PosServErlable_Crit_igc.value	-8.80000019		
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	-8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32	O O	0 ± 0000390625	Result
TrqArblim ESC State Cnt M enum	0	0 ± 0000390625	
TrqArblim HwTorqueSV HwNm M Str.SV UIs f32	-10	-10 ± 0.03125	
TrqArblim PosSrvoCmd MtrNm M f32	-10.2399998	-10 ± 0.03125 -10.2399998 ± 0.000244140625	•
	-8.77299976		
TrqArblim_PreLKATrqReq_HwNm_M_f32 target TrqArblim Per1 AssistDDFactor UIs f32.value	1	-8.77299976 ± 0.03125 1 ± 0000390625	•
	1	1 ± 0000390625 1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	1 ± 0000390625	
target_rrqArblim_Per1_ESCIst.imited_Crit_igc.value target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	0	0 ± 0000390625	
target_TrgArblim_Per1_ESCTorqueDelivereu_HwNm_i32.value	V	3 ± 0.000390625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

-3

-3

-0

 $target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value$

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

-3 ± 0.0000390625

-3 ± 0.0000390625

0 ± 0000390625

1 ± 0000390625



Test Step 2.13 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	1
TrgArblim ESC State Cnt M enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390027
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.2299995
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019
k_APAEnableRate_pSec_f32	500
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	5
k_LKAUseSlewCal_lgc	1 40960
t_APADisableRateX_HwNm_u4p12[0] t_APADisableRateX_HwNm_u4p12[1]	40960
t_APADisableRateX_HwNm_u4p12[2]	40960
t_APADisableRateX_HwNm_u4p12[3]	40960
t_APADisableRateX_HwNm_u4p12[4]	40960
t_APADisableRateX_HwNm_u4p12[5]	40960
t_APADisableRateY_pSec_u7p9[0]	65535
t_APADisableRateY_pSec_u7p9[1]	65535
t_APADisableRateY_pSec_u7p9[2]	65535
t_APADisableRateY_pSec_u7p9[3]	65535
t_APADisableRateY_pSec_u7p9[4]	65535
t_APADisableRateY_pSec_u7p9[5]	65535
t_APASmoothX_Uls_u2p14[0]	16384
t_APASmoothX_Uls_u2p14[1]	16384
t_APASmoothX_UIs_u2p14[2]	16384
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	16384 16384
t_APASmoothX_Uls_u2p14[5]	16384
t_APASmoothX_Uls_u2p14[6]	16384
t_APASmoothX_Uls_u2p14[7]	16384
t_APASmoothX_Uls_u2p14[8]	16384
t_APASmoothX_Uls_u2p14[9]	16384
t_APASmoothY_Uls_u2p14[0]	16384
t_APASmoothY_Uls_u2p14[1]	16384
t_APASmoothY_Uls_u2p14[2]	16384
t_APASmoothY_Uls_u2p14[3]	16384
t_APASmoothY_Uls_u2p14[4]	16384
t_APASmoothY_Uls_u2p14[5]	16384
t_APASmoothY_Uls_u2p14[6]	16384
t_APASmoothY_UIs_u2p14[7]	16384
t_APASmoothY_UIs_u2p14[8]	16384
t_APASmoothY_Uls_u2p14[9] t LKASlewX Kph u8p8[0]	16384 56320
t_LKASlewX_Kph_u8p8[1]	56320
t_LKASlewX_Kph_u8p8[2]	56320
t_LKASlewX_Kph_u8p8[3]	56320
t_LKASlewX_Kph_u8p8[4]	56320
t_LKASlewX_Kph_u8p8[5]	56320
t_LKASlewY_NmpSec_u4p12[0]	55296
t_LKASlewY_NmpSec_u4p12[1]	55296
t_LKASlewY_NmpSec_u4p12[2]	55296
t_LKASlewY_NmpSec_u4p12[3]	55296
t_LKASlewY_NmpSec_u4p12[4]	55296
t_LKASlewY_NmpSec_u4p12[5]	55296
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_IrqArblim_Per1_ESCState_state_enum target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_IrqArblim.IrqArblim_Peri_ESCTorqueDelivered_HwNm_ts2 target_Rte_Inst_Ap_IrqArblim.TrqArblim_Peri_ESCTorqueDelivered_HwNm_ts2	target_IrqArbiim_Per1_ESCTorqueDelivered_HwNm_132 target TrqArblim_Per1_GMOSHOscillate State enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
1 2 2 2 12 1 1 2 2 1 1 1 1 2 2 1 1 1 1	





Name	Input Value		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	10		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.2299995		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	511		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	10 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.77299976	8.77299976 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	~
target TrqArblim Per1 ReturnDDFactor UIs f32.value	0	0 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TroArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap TrqArblim	target Rte Inst Ap TrqArblim
TrqArblim APADisableRate Uls M f32	0.270000011
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim HwTorqueSV HwNm M Str.SV Uls f32	2.33999991
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqArblim PosSrvoCmd MtrNm M f32	-9.03999996
TrgArblim PreLKATrgReg HwNm M f32	1.3999998
k APAEnableRate pSec f32	91.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	8192
t_APADisableRateX_HwNm_u4p12[1]	8602
t_APADisableRateX_HwNm_u4p12[2]	9011
t_APADisableRateX_HwNm_u4p12[3]	9421
t_APADisableRateX_HwNm_u4p12[4]	9830
t_APADisableRateX_HwNm_u4p12[5]	10240
t_APADisableRateY_pSec_u7p9[0]	10240
t_APADisableRateY_pSec_u7p9[1]	10752
t_APADisableRateY_pSec_u7p9[2]	11264
t_APADisableRateY_pSec_u7p9[3]	11776
t_APADisableRateY_pSec_u7p9[4]	12288
t_APADisableRateY_pSec_u7p9[5]	12800
t_APASmoothX_Uls_u2p14[0]	3277
t_APASmoothX_Uls_u2p14[1]	3441
t_APASmoothX_Uls_u2p14[2]	3604
t_APASmoothX_Uls_u2p14[3]	3768
t_APASmoothX_Uls_u2p14[4]	3932
t_APASmoothX_Uls_u2p14[5]	4096

TrqArblim_Per1





	le cere		
Name	Input Value		
t_APASmoothX_Uls_u2p14[6]	4260		
t_APASmoothX_Uls_u2p14[7]	4424		
t_APASmoothX_UIs_u2p14[8]	4588 4751		
t_APASmoothX_Uls_u2p14[9] t_APASmoothY_Uls_u2p14[0]	328		
t_APASmoothY_Uls_u2p14[0]	492		
t_APASmoothY_Uls_u2p14[2]	655		
t_APASmoothY_Uls_u2p14[3]	819		
t_APASmoothY_Uls_u2p14[4]	983		
t_APASmoothY_Uls_u2p14[5]	1147		
t_APASmoothY_Uls_u2p14[6]	1311		
t_APASmoothY_Uls_u2p14[7]	1475		
t_APASmoothY_Uls_u2p14[8]	1638		
t_APASmoothY_Uls_u2p14[9]	1802		
t_LKASlewX_Kph_u8p8[0]	10240		
t_LKASlewX_Kph_u8p8[1]	10752		
t_LKASlewX_Kph_u8p8[2]	11264		
t_LKASlewX_Kph_u8p8[3]	11776		
t_LKASlewX_Kph_u8p8[4]	12288		
t_LKASlewX_Kph_u8p8[5]	12800		
t_LKASlewY_NmpSec_u4p12[0]	8192		
t_LKASlewY_NmpSec_u4p12[1]	8602		
t_LKASlewY_NmpSec_u4p12[2]	9011		
t_LKASlewY_NmpSec_u4p12[3]	9421		
t_LKASlewY_NmpSec_u4p12[4]	9830 10240		
t_LKASlewY_NmpSec_u4p12[5] target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	, f32	
target Rte Inst Ap TrgArblim.TrgArblim Per1 DampingDDFactor Uls f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.85999966		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.5		
target_TrqArblim_Per1_LKAState_State_enum.value target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	0 96.3432999		
	96.3432999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019		
Name	Actual Value	Expected Value	Resu
TrqArblim APADisableRate Uls M f32	0.230000004	0.223203003 ± 0000390625	Resu
TrqArblim ESC State Cnt M enum	0.23000004	0.223203003 ± 0000390023	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.77880001	1.77880001 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	-9.03999996	-9.03999996 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.39399993	1.39400005 ± 0.03125	
target TrgArblim Per1 AssistDDFactor Uls f32.value	0.9885028	0.990368009 ± 0000390625	
target TrgArblim Per1 DampingDDFactor Uls f32.value	0.9885028	0.990368009 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.39399993	1.39400005 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.39399993	1.39400005 ± 0.0000390625	
target_rig/tibiiin_reri_Ere trorqueBenverea_riwitin_loz:valae			
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0000390625	

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Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	•

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Test Case 3: Boundary Test

TrqArblim_Per1

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TrqArblim_Per1



Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS3.1 2392.00 Cycles TS3.2 2725.00 Cycles TS3.3 2732.00 Cycles TS3.4 2550.00 Cycles TS3.5 2793.00 Cycles TS3.6 2391.00 Cycles TS3.7 2509.00 Cycles TS3.8 2361.00 Cycles TS3.9 2674.00 Cycles TS3.9 2674.00 Cycles TS3.10 2325.00 Cycles TS3.11 2345.00 Cycles TS3.12 2402.00 Cycles TS3.13 2267.00 Cycles TS3.14 2504.00 Cycles TS3.15 2256.00 Cycles TS3.16 2503.00 Cycles TS3.17 2342.00 Cycles TS3.18 2554.00 Cycles TS3.18 2564.00 Cycles TS3.19 2264.00 Cycles TS3.20 2344.00 Cycles TS3.20 2394.00 Cycles TS3.20 2344.00 Cycles TS3.21 2399.00 Cycles TS3.22 2390.00 Cycles TS3.23 2439.00 Cycles TS3.24 2389.00 Cycles TS3.25 2290.00 Cycles TS3.26 2246.00 Cycles TS3.27 2550.00 Cycles TS3.28 2350.00 Cycles TS3.29 2444.00 Cycles TS3.30 2290.00 Cycles TS3.31 2514.00 Cycles TS3.32 2302.00 Cycles TS3.33 2469.00 Cycles TS3.33 2469.00 Cycles TS3.32 2302.00 Cycles
TS3.33 2469.00 Cycles
TS3.35 2336.00 Cycles
TS3.35 2336.00 Cycles
TS3.37 2457.00 Cycles
TS3.37 2457.00 Cycles
TS3.39 2412.00 Cycles
TS3.39 2308.00 Cycles
TS3.40 2426.00 Cycles
TS3.41 2308.00 Cycles
TS3.42 2224.00 Cycles
TS3.42 2224.00 Cycles
TS3.44 2254.00 Cycles
TS3.45 2248.00 Cycles
TS3.46 2456.00 Cycles
TS3.46 2456.00 Cycles
TS3.48 2506.00 Cycles
TS3.49 2303.00 Cycles
TS3.50 2324.00 Cycles
TS3.51 2293.00 Cycles
TS3.51 2293.00 Cycles
TS3.52 2281.00 Cycles
TS3.53 2221.00 Cycles TS3.52 2281.00 Cycles TS3.53 2241.00 Cycles TS3.55 2217.00 Cycles TS3.55 2207.00 Cycles TS3.56 2307.00 Cycles TS3.57 2343.00 Cycles TS3.58 2333.00 Cycles TS3.60 2236.00 Cycles TS3.60 2236.00 Cycles TS3.61 2238.00 Cycles TS3.62 2245.00 Cycles TS3.63 2238.00 Cycles TS3.63 2238.00 Cycles TS3.63 2238.00 Cycles TS3.64 2170.00 Cycles TS3.65 2266.00 Cycles TS3.66 2202.00 Cycles TS3.67 2305.00 Cycles TS3.68 2599.00 Cycles TS3.69 2292.00 Cycles TS3.70 2235.00 Cycles TS3.71 2264.00 Cycles TS3.72 2390.00 Cycles TS3.72 2390.00 Cycles TS3.73 2187.00 Cycles TS3.74 2449.00 Cycles TS3.75 2409.00 Cycles TS3.76 2229.00 Cycles TS3.77 2228.00 Cycles TS3.78 2370.00 Cycles TS3.79 2207.00 Cycles TS3.80 2307.00 Cycles TS3.81 2373.00 Cycles TS3.82 2339.00 Cycles TS3.83 2193.00 Cycles TS3.83 2193.00 Cycles TS3.83 2193.00 Cycles TS3.84 2198.00 Cycles TS3.85 2347.00 Cycles TS3.86 2216.00 Cycles TS3.87 2498.00 Cycles TS3.89 2375.00 Cycles TS3.90 2255.00 Cycles TS3.91 2245.00 Cycles TS3.92 2256.00 Cycles TS3.93 2191.00 Cycles TS3.94 2255.00 Cycles TS3.95 2306.00 Cycles TS3.95 2177.00 Cycles TS3.96 2306.00 Cycles TS3.96 2177.00 Cycles TS3.97 2215.00 Cycles TS3.99 2274.00 Cycles TS3.99 2231.00 Cycles TS3.100 2184.00 Cycles TS3.101 2338.00 Cycles TS3.102 2215.00 Cycles TS3.103 2318.00 Cycles





Description Vector Description:

TS3.1LKACmd_HwNm_f32 = Min TS3.2LKACmd_HwNm_f32 = Max TS3.3LKACmd_HwNm_f32 = Zero TS3.4LKACmd_HwNm_f32 = Pos TS3.5LKACmd_HwNm_f32 = Neg TS3.6SecureVehicleSpeed_Kph_f32 = Min TS3.7SecureVehicleSpeed_Kph_f32 = Max TS3.8SecureVehicleSpeed_Kph_f32 = Pos TS3.9PosServEnable_Cnt_lgc = False TS3.10PosServEnable_Cnt_lgc = True TS3.11PosSrvoCmd_MtrNm_f32 = Min TS3.12PosSrvoCmd_MtrNm_f32 = Max TS3.13PosSrvoCmd_MtrNm_f32 = Zero TS3.14PosSrvoCmd_MtrNm_f32 = Pos TS3.15PosSrvoCmd_MtrNm_f32= Neg TS3.16HwTorque_HwNm_f32 = Min TS3.17HwTorque_HwNm_f32 = Max TS3.18HwTorque_HwNm_f32 = Zero TS3.19HwTorque_HwNm_f32 = Pos
TS3.20HwTorque_HwNm_f32 = Neg
TS3.20HwTorque_HwNm_f32 = Neg
TS3.21TrqOscCmd_MtrNm_f32 = Min
TS3.22TrqOscCmd_MtrNm_f32 = Zero
TS3.23TrqOscCmd_MtrNm_f32 = Zero
TS3.24TrqOscCmd_MtrNm_f32 = Pos TS3.25TrqOscCmd_MtrNm_f32 = Neg
TS3.26LKAState_State_enum = LKA_Inactive
TS3.27LKAState_State_enum = LKA_Active TS3.27LKAState_State_enum = LKA_Active
TS3.28LKAState_State_enum = LKA_TemporarilyLimited
TS3.29LKAState_State_enum = LKA_PermanentlyFailed
TS3.39LKAState_State_enum = LKA_ManufacturingDisable
TS3.31GMOSHOscillate_State_enum = Haptic_Inactive
TS3.31GMOSHOscillate_State_enum = Haptic_Active
TS3.33GMOSHOscillate_State_enum = Haptic_Vaiting
TS3.34TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Min
TS3.35TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Zero
TS3.37TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Pos
TS3.38TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Neg
TS3.38TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Neg
TS3.39TrqArblim_HwTorqueSV_HwNm_M_Str.SV = Ros
TS3.40TrqArblim_HwTorqueSV_HwNm_M_Str.K = Min
TS3.40TrqArblim_HwTorqueSV_HwNm_M_Str.K = Pos
TS3.42TrqArblim_APADisableRate_Uls_M_f32 = Min
TS3.43TrqArblim_APADisableRate_Uls_M_f32 = Pos TS3.44TrqArblim_APADisableRate_Uls_M_f32 = Pos TS3.45TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Min TS3.46TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Max TS3.47TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Zero TS3.48TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Pos TS3.49TrqArblim_PosSrvoCmd_MtrNm_M_f32 = Neg TS3.50TrqArblim_PreLKATrqDel_HwNm_M_f32 = Min TS3.51TrqArblim_PreLKATrqDel_HwNm_M_f32 = Min TS3.52TrqArblim_PreLKATrqDel_HwNm_M_f32 = Zero TS3.53TrgArblim PreLKATrgDel HwNm M f32 = Pos TS3.54TrqArblim_PreLKATrqDel_HwNm_M_f32 = Neg TS3.55k_APAEnableRate_pSec_f32 = Min TS3.56k_APAEnableRate_pSec_f32 = Max TS3.57k_APAEnableRate_pSec_f32 = Pos TS3.58k_APAUseAsstScale_lgc = False TS3.59k_APAUseAsstScale_lgc = True TS3.60k_APAUseRetScale_lgc = False TS3.61k_APAUseRetScale_lgc = True TS3.62k_APAUseADmpScale_lgc = False TS3.63k_APAUseADmpScale_lgc = True TS3.68k_LKAUSeSlewCal_lgc = False
TS3.64k_LKAUseSlewCal_lgc = False
TS3.65k_LKAUSeSlewCal_lgc = True
TS3.66t_LKASlewX_Kph_u8p8[6] = Min
TS3.67t_LKASlewX_Kph_u8p8[6] = Max
TS3.68t_LKASlewX_Kph_u8p8[6] = Pos
TS3.69t_LKASlewY_NmpSec_u4p12[6] = Min IS3.69f_LKASlewY_NmpSec_u4p12[6] = Min
TS3.70t_LKASlewY_NmpSec_u4p12[6] = Max
TS3.71t_LKASlewY_NmpSec_u4p12[6] = Pos
TS3.72t_APADisableRateX_HwNm_u4p12[6] = Min
TS3.73t_APADisableRateX_HwNm_u4p12[6] = Max
TS3.74t_APADisableRateX_HwNm_u4p12[6] = Pos
TS3.75t_APADisableRateY_pSec_u7p9[6] = Min
TS3.76t_APADisableRateY_pSec_u7p9[6] = Max
TS3.77t_APADisableRateY_pSec_u7p9[6] = Pos
TS3.78t_APASmoothX_UIs_u2p14[10] = Min
TS3.79t_APASmoothX_UIs_u2p14[10] = Min TS3.781_APASmoothX_UIs_u2p14[10] = Min TS3.79t_APASmoothX_UIs_u2p14[10] = Max TS3.801_APASmoothX_UIs_u2p14[10] = Pos TS3.81t_APASmoothY_UIs_u2p14[10] = Min TS3.82t_APASmoothY_UIs_u2p14[10] = Max TS3.83t_APASmoothY_UIs_u2p14[10] = Pos TS3.84ESCCmd_HwNm_f32=Min TS3.85ESCCmd_HwNm_f32 = Max TS3.85ESCCmd_Hwnm_f32 TS3.86ESCCmd_HwNm_f32 = Zero TS3.87ESCCmd_HwNm_f32 = Pos TS3.88ESCCmd_HwNm_f32 = Neg TS3.89ESCState_State_enum=ESC_Inactive TS3.90ESCState_State_enum=ESC_Active
TS3.91ESCState_State_enum=ESC_TemporarilyLimited
TS3.92ESCState_State_enum=ESC_PermanentlyFailed TS3.93ESCState_State_enum=ESC_ManufacturingDisable TS3.94k_ESCMax_HwNm_f32=Min





TS3.95k_ESCMax_HwNm_f32=Max
TS3.96k_ESCMax_HwNm_f32=Pos
TS3.97TrqArblim_ESC_State_Cnt_M_enum=ESC_Inactive
TS3.98TrqArblim_ESC_State_Cnt_M_enum=ESC_Active
TS3.99TrqArblim_ESC_State_Cnt_M_enum=ESC_TemporarilyLimited
TS3.100TrqArblim_ESC_State_Cnt_M_enum=ESC_PermanentlyFailed
TS3.101TrqArblim_ESC_State_Cnt_M_enum=ESC_ManufacturingDisable
TS3.102All Min
TS3.103All Max

Test Step 3.1 (Repeat Count = 1) Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0099999978
TrqArblim_ESC_State_Cnt_M_enum	0
FrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.85999966
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00200000009
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.60000038
<pre><_APAEnableRate_pSec_f32</pre>	1.5
<_APAUseADmpScale_lgc	0
<pre><_APAUseAsstScale_lgc</pre>	0
<pre><_APAUseRetScale_lgc</pre>	0
CESCMax_HwNm_f32 CLKALlesSlowCol. less	2.24000001
<pre><_LKAUseSlewCal_lgc :_APADisableRateX_HwNm_u4p12[0]</pre>	0
:_APADisableRateX_HwNm_u4p12[1]	410
:_APADisableRateX_HwNm_u4p12[2]	819
	1229
	1638
 _APADisableRateX_HwNm_u4p12[5]	2048
APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	512
:_APADisableRateY_pSec_u7p9[2]	1024
t_APADisableRateY_pSec_u7p9[3]	1536
t_APADisableRateY_pSec_u7p9[4]	2048
t_APADisableRateY_pSec_u7p9[5]	2560
t_APASmoothX_Uls_u2p14[0]	0
APASmoothX_Uls_u2p14[1]	164
:_APASmoothX_Uls_u2p14[2]	328
t_APASmoothX_UIs_u2p14[3]	492 655
t_APASmoothX_Uls_u2p14[4] t_APASmoothX_Uls_u2p14[5]	819
t_APASmoothX_UIs_u2p14[6]	983
t_APASmoothX_UIs_u2p14[7]	1147
t_APASmoothX_Uls_u2p14[8]	1311
t_APASmoothX_Uls_u2p14[9]	1475
t_APASmoothY_Uls_u2p14[0]	12780
t_APASmoothY_Uls_u2p14[1]	12943
t_APASmoothY_Uls_u2p14[2]	13107
t_APASmoothY_Uls_u2p14[3]	13271
t_APASmoothY_Uls_u2p14[4]	13435
t_APASmoothY_Uls_u2p14[5]	13599
t_APASmoothY_Uls_u2p14[6]	13763
t_APASmoothY_Uls_u2p14[7]	13926
:_APASmoothY_Uls_u2p14[8]	14090
:_APASmoothY_Uls_u2p14[9]	14254
t_LKASlewX_Kph_u8p8[0]	0 512
t_LKASlewX_Kph_u8p8[1] t_LKASlewX_Kph_u8p8[2]	1024
t_LKASlewX_Kph_u8p8[3]	1536
t_LKASlewX_Kph_u8p8[4]	2048
trvolewrpii_uopo[4] trvolewx_rpii_uopo[4]	2560
t_LKASlewY_NmpSec_u4p12[0]	0
LKASlewY_NmpSec_u4p12[1]	410
 :_LKASlewY_NmpSec_u4p12[2]	819
LKASlewY_NmpSec_u4p12[3]	1229
_LKASlewY_NmpSec_u4p12[4]	1638
_LKASlewY_NmpSec_u4p12[5]	2048
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	ium	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.82000017		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.64000034		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	2		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.209999993		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	5.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85956001	-9.85956001 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2200003	-10.2200003 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.5732336	-8.57322979 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.2 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.023
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038
k_APAEnableRate_pSec_f32	6
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.8499999
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	410
t_APADisableRateX_HwNm_u4p12[1]	819
t_APADisableRateX_HwNm_u4p12[2]	1229
t_APADisableRateX_HwNm_u4p12[3]	1638
t_APADisableRateX_HwNm_u4p12[4]	2048
t_APADisableRateX_HwNm_u4p12[5]	2458
t_APADisableRateY_pSec_u7p9[0]	512
t_APADisableRateY_pSec_u7p9[1]	1024
t_APADisableRateY_pSec_u7p9[2]	1536
t_APADisableRateY_pSec_u7p9[3]	2048
t_APADisableRateY_pSec_u7p9[4]	2560
t_APADisableRateY_pSec_u7p9[5]	3072
t_APASmoothX_Uls_u2p14[0]	164





Name	Input Value		
t_APASmoothX_Uls_u2p14[1]	328		
t_APASmoothX_Uls_u2p14[2]	492		
t_APASmoothX_Uls_u2p14[3]	655		
t_APASmoothX_Uls_u2p14[4]	819		
t_APASmoothX_Uls_u2p14[5]	983		
t_APASmoothX_Uls_u2p14[6]	1147		
t_APASmoothX_UIs_u2p14[7]	1311		
t_APASmoothX_Uls_u2p14[8]	1475 1638		
t_APASmoothX_Uls_u2p14[9] t_APASmoothY_Uls_u2p14[0]	12943		
t_APASmoothY_Uls_u2p14[1]	13107		
t APASmoothY Uls u2p14[2]	13271		
t_APASmoothY_Uls_u2p14[3]	13435		
t_APASmoothY_Uls_u2p14[4]	13599		
t_APASmoothY_Uls_u2p14[5]	13763		
t_APASmoothY_Uls_u2p14[6]	13926		
t_APASmoothY_Uls_u2p14[7]	14090		
t_APASmoothY_Uls_u2p14[8]	14254		
t_APASmoothY_Uls_u2p14[9]	14418		
t_LKASlewX_Kph_u8p8[0]	512		
t_LKASlewX_Kph_u8p8[1]	1024		
t_LKASlewX_Kph_u8p8[2]	1536		
t_LKASlewX_Kph_u8p8[3]	2048		
t_LKASlewX_Kph_u8p8[4] t LKASlewX Kph u8p8[5]	3072		
t_LKASiewX_kpn_usp8[5] t_LKASiewY_NmpSec_u4p12[0]	410		
t_LKASlewY_NmpSec_u4p12[1]	819		
t_LKASlewY_NmpSec_u4p12[2]	1229		
t LKASlewY NmpSec u4p12[3]	1638		
t_LKASlewY_NmpSec_u4p12[4]	2048		
t_LKASlewY_NmpSec_u4p12[5]	2458		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.22999954 10.2299995		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1		
target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	5.86000013		
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	0.560000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0109999999	0.0109999999 ± 0000390625	· · · · · · · · · · · · · · · · · · ·
TrqArblim_ESC_State_Cnt_M_enum	1	1	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.24571609	-9.24571991 ± 0.03125	-
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.02000046	-9.02000046 ± 0.000244140625	•
TrgArblim PreLKATrgReg HwNm M f32	-8.09400082	-8.09399986 ± 0.03125	~
	0.99129951	0.991299987 ± 0000390625	- − •
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value			
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.99129951	0.991299987 ± 0000390625	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0.99129951	1	•
target_TrqArblim_Per1_AssistDDFactor_UIs_f32.value target_TrqArblim_Per1_DampingDDFactor_UIs_f32.value target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.99129951 1 -4.8499999	1 -4.8499999 ± 0000390625	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.99129951 1 -4.8499999 -3	1 -4.8499999 ± 0000390625 -3 ± 0.0000390625	~
target_TrqArblim_Per1_AssistDDFactor_UIs_f32.value target_TrqArblim_Per1_DampingDDFactor_UIs_f32.value target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.99129951 1 -4.8499999	1 -4.8499999 ± 0000390625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0359999985
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.64000034
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.82000017
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.5999999
APAEnableRate pSec f32	10.5
APAUseADmpScale lgc	1
APAUseAsstScale Igc	1
<_APAUseRetScale_lgc	1
c ESCMax HwNm f32	3.3099994
<pre>comes_mmm_es_</pre> <pre>(_LKAUseSlewCal_lgc</pre>	1
i_broossonou_ge :_APADisableRateX_HwNm_u4p12[0]	819
: APADisableRateX HwNm u4p12[1]	1229
:_APADisableRateX_HwNm_u4p12[2]	1638
: APADisableRateX_HwNm_u4p12[2]	2048
	2458
_APADisableRateX_HwNm_u4p12[4]	2867
:_APADisableRateX_HwNm_u4p12[5]	1024
t_APADisableRateY_pSec_u7p9[0]	
t_APADisableRateY_pSec_u7p9[1]	1536
t_APADisableRateY_pSec_u7p9[2]	2048
t_APADisableRateY_pSec_u7p9[3]	2560
t_APADisableRateY_pSec_u7p9[4]	3072
_APADisableRateY_pSec_u7p9[5]	3584
:_APASmoothX_Uls_u2p14[0]	328
:_APASmoothX_Uls_u2p14[1]	492
t_APASmoothX_Uls_u2p14[2]	655
t_APASmoothX_Uls_u2p14[3]	819
t_APASmoothX_Uls_u2p14[4]	983
t_APASmoothX_Uls_u2p14[5]	1147
t_APASmoothX_Uls_u2p14[6]	1311
t_APASmoothX_Uls_u2p14[7]	1475
t_APASmoothX_Uls_u2p14[8]	1638
t_APASmoothX_Uls_u2p14[9]	1802
t_APASmoothY_Uls_u2p14[0]	13107
_APASmoothY_Uls_u2p14[1]	13271
_APASmoothY_Uls_u2p14[2]	13435
_APASmoothY_Uls_u2p14[3]	13599
:_APASmoothY_Uls_u2p14[4]	13763
: APASmoothY Uls u2p14[5]	13926
	14090
t_APASmoothY_Uls_u2p14[7]	14254
APASmoothY_Uls_u2p14[8]	14418
	14582
	1024
LKASlewX_Kph_u8p8[1]	1536
:_LKASlewX_Kph_u8p8[2]	2048
:_LKASlewX_Kph_u8p8[3]	2560
:_LKASlewX_Kph_u8p8[4]	3072
:_LKASlewX_Kph_u8p8[5]	3584
:_LKASlewY_NmpSec_u4p12[0]	819
	1229
:_LKASlewY_NmpSec_u4p12[1]	
:_LKASlewY_NmpSec_u4p12[2]	1638
:_LKASlewY_NmpSec_u4p12[3]	2048
LKASlewY_NmpSec_u4p12[4]	2458
LKASlewY_NmpSec_u4p12[5]	2867
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	I_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.42000008		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.82000017		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	9.43000031		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.910000026		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.057	0.057 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.62835217	-8.62835026 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.910000026	0.910000026 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.59399986	-7.59399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.30999994	-3.30999994 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	6.34341431	6.34341002 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.952292442	0.952292025 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	•

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0489999987
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.61999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.0999999
k_APAEnableRate_pSec_f32	15
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.20000005
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	1229
t_APADisableRateX_HwNm_u4p12[1]	1638
t_APADisableRateX_HwNm_u4p12[2]	2048
t_APADisableRateX_HwNm_u4p12[3]	2458
t_APADisableRateX_HwNm_u4p12[4]	2867
t_APADisableRateX_HwNm_u4p12[5]	3277
t_APADisableRateY_pSec_u7p9[0]	1536
t_APADisableRateY_pSec_u7p9[1]	2048
t_APADisableRateY_pSec_u7p9[2]	2560
t_APADisableRateY_pSec_u7p9[3]	3072
t_APADisableRateY_pSec_u7p9[4]	3584

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TrqArblim_Per1



Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	4096		
t APASmoothX Uls u2p14[0]	492		
t_APASmoothX_Uls_u2p14[1]	655		
t_APASmoothX_Uls_u2p14[2]	819		
t_APASmoothX_Uls_u2p14[3]	983		
t_APASmoothX_Uls_u2p14[4]	1147		
t_APASmoothX_Uls_u2p14[5]	1311		
t_APASmoothX_Uls_u2p14[6]	1475		
t_APASmoothX_Uls_u2p14[7]	1638		
t_APASmoothX_Uls_u2p14[8]	1802		
t_APASmoothX_Uls_u2p14[9]	1966		
t_APASmoothY_Uls_u2p14[0]	13271		
t_APASmoothY_Uls_u2p14[1]	13435		
t_APASmoothY_Uls_u2p14[2]	13599		
t_APASmoothY_Uls_u2p14[3]	13763		
t_APASmoothY_Uls_u2p14[4]	13926		
t_APASmoothY_Uls_u2p14[5]	14090		
t_APASmoothY_Uls_u2p14[6]	14254		
t_APASmoothY_Uls_u2p14[7]	14418		
t_APASmoothY_Uls_u2p14[8]	14582		
t_APASmoothY_Uls_u2p14[9]	14746		
t_LKASlewX_Kph_u8p8[0]	1536		
t_LKASlewX_Kph_u8p8[1]	2048		
t_LKASlewX_Kph_u8p8[2]	2560		
t_LKASlewX_Kph_u8p8[3]	3072		
t_LKASlewX_Kph_u8p8[4]	3584		
t_LKASlewX_Kph_u8p8[5]	4096		
t_LKASlewY_NmpSec_u4p12[0]	1229		
t_LKASlewY_NmpSec_u4p12[1]	1638		
t_LKASlewY_NmpSec_u4p12[2]	2048		
t_LKASlewY_NmpSec_u4p12[3]	2458		
t_LKASlewY_NmpSec_u4p12[4]	2867		
t_LKASlewY_NmpSec_u4p12[5]	3277		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
	target_TrqArblim_Per1_IqTrqOv_HwNm_f3.		
target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f3 target_TrqArblim_Per1_LKAState_state_er		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleS		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target TrqArblim Per1 OpTrqOv MtrNm f		
target Rte Inst Ap TrgArblim.TrgArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor UIs f32	target_TrqArblim_Per1_ReturnDDFactor_U		
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	-4.21999979		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target TrqArblim Per1 HwTorque HwNm f32.value	-5.40999985		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.61999989		
target TrqArblim Per1 LKAState State enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	13.1933002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.25999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0789999962	0.0790000036 ± 0000390625	✓
TrqArblim ESC State Cnt M enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.00746727	-8.00747013 ± 0.03125	
TrqArblim PosSrvoCmd MtrNm M f32	1.25999999	1.25999999 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.07453918	-7.07454014 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	·
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000330025	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
<u> </u>		3 + 0 0000300635	

-3

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

-3 ± 0.0000390625





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.0854996592	0.0854996964 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

est Step 3.5 (Repeat Count = 1)	Innuit Value
lame	Input Value
tte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.061999999
rqArblim_ESC_State_Cnt_M_enum	3
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-7.42000008
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
rqArblim_PosSrvoCmd_MtrNm_M_f32	-5.42000008
rqArblim_PreLKATrqReq_HwNm_M_f32	-6.5999999
_APAEnableRate_pSec_f32	19.5
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
_ESCMax_HwNm_f32	1.73500001
_LKAUseSlewCal_lgc	1
APADisableRateX_HwNm_u4p12[0]	1638
APADisableRateX_HwNm_u4p12[1]	2048
_APADisableRateX_HwNm_u4p12[2]	2458
APADisableRateX_HwNm_u4p12[3]	2867
APADisableRateX_HwNm_u4p12[4]	3277
_APADisableRateX_HwNm_u4p12[5]	3686
APADisableRateY_pSec_u7p9[0]	2048
APADisableRateY_pSec_u7p9[1]	2560
_APADisableRateY_pSec_u7p9[2]	3072
_APADisableRateY_pSec_u7p9[3]	3584
_APADisableRateY_pSec_u7p9[4]	4096
APADisableRateY_pSec_u7p9[5]	4608
APASmoothX_Uls_u2p14[0]	655
_APASmoothX_Uls_u2p14[1]	819
_APASmoothX_Uls_u2p14[2]	983
_APASmoothX_Uls_u2p14[3]	1147
_APASmoothX_Uls_u2p14[4]	1311
APASmoothX_Uls_u2p14[5]	1475
_APASmoothX_Uls_u2p14[6]	1638
APASmoothX_Uls_u2p14[7]	1802
APASmoothX_Uls_u2p14[8]	1966
_APASmoothX_Uls_u2p14[9]	2130
APASmoothY_Uls_u2p14[0]	13435
APASmoothY_Uls_u2p14[1]	13599
APASmoothY_Uls_u2p14[2]	13763
APASmoothY_Uls_u2p14[3]	13926
APASmoothY_Uls_u2p14[4]	14090
APASmoothY_Uls_u2p14[5]	14254
APASmoothY_Uls_u2p14[6]	14418
APASmoothY_Uls_u2p14[7]	14582
APASmoothY Uls u2p14[8]	14746
APASmoothY Uls u2p14[9]	14909
LKASlewX_Kph_u8p8[0]	2048
LKASlewX_Kph_u8p8[1]	2560
LKASlewX_Kph_u8p8[2]	3072
LKASlewX_Kph_u8p8[3]	3584
LKASlewX_Kph_u8p8[4]	4096
LKASlewX_Kph_u8p8[5]	4608
LKASlewY_NmpSec_u4p12[0]	1638
LKASlewY_NmpSec_u4p12[1]	2048
LKASlewY NmpSec u4p12[1]	2458
_LKASlewY_NmpSec_u4p12[3]	2867
_LKASlewY_NmpSec_u4p12[4]	3277
_LKASlewY_NmpSec_u4p12[5]	3686
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32 target_TrqArblim_Per1_DampingDDFactor_Uls_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNn	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_C	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State	_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliv	ered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate	_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwN	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_	enum		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f3222222222222222222222222222222222222$	target_TrqArblim_Per1_LKATorqueDelive	ered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3	2 target_TrqArblim_Per1_MinSecureVehicl	eSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_	Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mt	rNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_Mtr	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.01999998			
target_TrqArblim_Per1_ESCState_State_enum.value	4			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-4			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.42000008			
target_TrqArblim_Per1_LKAState_State_enum.value	4			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	16.9083004			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.61000001			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	6.69999981			
Name	Actual Value	Expected Value	Resul	
TrqArblim_APADisableRate_Uls_M_f32	0.0439999998	0.0439999998 ± 0000390625	•	
TrqArblim_ESC_State_Cnt_M_enum	4	4	•	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-7.38306427	-7.38305998 ± 0.03125	•	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.42000008	-5.42000008 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.59399986	-6.59399986 ± 0.03125	•	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.963745117	0.963744998 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.963745117	0.963744998 ± 0000390625	•	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	,	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TroArblim Per1 CP1 CheckpointReached	1	_

6.50349855

0.963745117

-3 ± 0.0000390625

6.50349998 ± 0000390625

0.963744998 ± 0000390625

Test Step 3.6 (Repeat Count = 1)		~
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.075000003	
TrqArblim_ESC_State_Cnt_M_enum	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.21999979	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.0999999	
k_APAEnableRate_pSec_f32	24	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	1.26900005	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	2048	
t_APADisableRateX_HwNm_u4p12[1]	2458	
t_APADisableRateX_HwNm_u4p12[2]	2867	
t_APADisableRateX_HwNm_u4p12[3]	3277	
t_APADisableRateX_HwNm_u4p12[4]	3686	
t_APADisableRateX_HwNm_u4p12[5]	4096	
t_APADisableRateY_pSec_u7p9[0]	2560	
t_APADisableRateY_pSec_u7p9[1]	3072	

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value$

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value\\ target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	3584		
t_APADisableRateY_pSec_u7p9[3]	4096		
t_APADisableRateY_pSec_u7p9[4]	4608		
t_APADisableRateY_pSec_u7p9[5]	5120		
t_APASmoothX_Uls_u2p14[0] t APASmoothX Uls u2p14[1]	983		
t_APASmoothX_Uls_u2p14[2]	1147		
t_APASmoothX_Uls_u2p14[3]	1311		
t_APASmoothX_Uls_u2p14[4]	1475		
t_APASmoothX_Uls_u2p14[5]	1638		
t_APASmoothX_Uls_u2p14[6]	1802		
t_APASmoothX_Uls_u2p14[7]	1966		
t_APASmoothX_Uls_u2p14[8]	2130		
t_APASmoothX_Uls_u2p14[9]	2294		
t_APASmoothY_Uls_u2p14[0]	13599		
t_APASmoothY_Uls_u2p14[1]	13763 13926		
t_APASmoothY_Uls_u2p14[2] t_APASmoothY_Uls_u2p14[3]	14090		
t_APASmoothY_Uls_u2p14[4]	14254		
t_APASmoothY_UIs_u2p14[5]	14418		
t APASmoothY Uls u2p14[6]	14582		
t_APASmoothY_Uls_u2p14[7]	14746		
t_APASmoothY_Uls_u2p14[8]	14909		
t_APASmoothY_Uls_u2p14[9]	15073		
t_LKASlewX_Kph_u8p8[0]	2560		
t_LKASlewX_Kph_u8p8[1]	3072		
t_LKASlewX_Kph_u8p8[2]	3584		
t_LKASlewX_Kph_u8p8[3]	4096		
t_LKASlewX_Kph_u8p8[4]	4608		
t_LKASlewX_Kph_u8p8[5]	5120		
t_LKASlewY_NmpSec_u4p12[0]	2048 2458		
t_LKASlewY_NmpSec_u4p12[1] t_LKASlewY_NmpSec_u4p12[2]	2867		
t_LKASlewY_NmpSec_u4p12[3]	3277		
t_LKASlewY_NmpSec_u4p12[4]	3686		
t_LKASlewY_NmpSec_u4p12[5]	4096		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_GMOSHOscillate_S target TrqArblim Per1 HwTorque HwNm	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target Rte Inst Ap TrgArblim.TrgArblim Per1 LKATorqueDelivered HwNm f32	target TrqArblim Per1 LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target TrqArblim Per1 HwTorque HwNm f32.value	2 -2.58999991		
target_rrqArblim_Per1_hw1orque_hw10ff_isz.value	-9.10000038		
target TrqArblim Perl LKAState State enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0 0 1.96000004	Expected Value	Resu
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0 0 1.96000004 6.9000001	Expected Value 0.0549999997 ± 0000390625	
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value Name TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum	0 0 1.96000004 6.9000001 Actual Value 0.0549999997 0	0.0549999997 ± 0000390625 0	•
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value Name TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0 0 1.96000004 6.9000001 Actual Value 0.0549999997 0 -6.75513983	0.0549999997 ± 0000390625 0 -6.75513983 ± 0.03125	
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value Name TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32	0 0 1.96000004 6.9000001 Actual Value 0.0549999997 0 -6.75513983 -4.21999979	0.0549999997 ± 0000390625 0 -6.75513983 ± 0.03125 -4.21999979 ± 0.000244140625	•
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value Name TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32 TrqArblim_PreLKATrqReq_HwNm_M_f32	0 0 1.96000004 6.9000001 Actual Value 0.0549999997 0 -6.75513983 -4.21999979 -6.07299995	0.054999997 ± 0000390625 0 -6.75513983 ± 0.03125 -4.21999979 ± 0.000244140625 -6.07299995 ± 0.03125	Resul
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value Name TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32	0 0 1.96000004 6.9000001 Actual Value 0.0549999997 0 -6.75513983 -4.21999979	0.0549999997 ± 0000390625 0 -6.75513983 ± 0.03125 -4.21999979 ± 0.000244140625	•

0

0





Name	Actual Value	Expected Value	Result
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.1938086	-0.193809003 ± 0000390625	~
target TrqArblim Per1 ReturnDDFactor Uls f32.value	0.954073787	0.954074025 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0879999995
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0152000003
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.01999998
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.5999999
<_APAEnableRate_pSec_f32	28.5
c_APAUseADmpScale_lgc	1
<_APAUseAsstScale_lgc	1
c_APAUseRetScale_lgc	1
c_ESCMax_HwNm_f32	0.802999973
c_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	2458
_APADisableRateX_HwNm_u4p12[1]	2867
_APADisableRateX_HwNm_u4p12[2]	3277
_APADisableRateX_HwNm_u4p12[3]	3686
_APADisableRateX_HwNm_u4p12[4]	4096
_APADisableRateX_HwNm_u4p12[5]	4506
_APADisableRateY_pSec_u7p9[0]	3072
_APADisableRateY_pSec_u7p9[1]	3584
_APADisableRateY_pSec_u7p9[2]	4096
_APADisableRateY_pSec_u7p9[3]	4608
_APADisableRateY_pSec_u7p9[4]	5120
_APADisableRateY_pSec_u7p9[5]	5632
t_APASmoothX_Uls_u2p14[0]	983
_APASmoothX_Uls_u2p14[1]	1147
t_APASmoothX_UIs_u2p14[2]	1311
t_APASmoothX_Uls_u2p14[3]	1475
t_APASmoothX_Uls_u2p14[4]	1638
_APASmoothX_Uls_u2p14[5]	1802
_APASmoothX_Uls_u2p14[6]	1966
_APASmoothX_UIs_u2p14[7]	2130
_APASmoothX_Uls_u2p14[8]	2294
_APASmoothX_UIs_u2p14[9]	2458
_APASmoothY_Uls_u2p14[0]	13763
_APASmoothY_Uls_u2p14[1]	13926
_APASmoothY_Uls_u2p14[2]	14090
_APASmoothY_Uls_u2p14[3]	14254
_APASmoothY_Uls_u2p14[4]	14418
_APASmoothY_Uls_u2p14[5]	14582
_APASmoothY_Uls_u2p14[6]	14746
_APASmoothY_Uls_u2p14[7]	14909
_APASmoothY_Uls_u2p14[8]	15073
_APASmoothY_Uls_u2p14[9]	15237
_LKASlewX_Kph_u8p8[0]	3072
_LKASlewX_Kph_u8p8[1]	3584
_LKASlewX_Kph_u8p8[2]	4096
_LKASlewX_Kph_u8p8[3]	4608
LKASlewX_Kph_u8p8[4]	5120
_LKASlewX_Kph_u8p8[5]	5632
_LKASlewY_NmpSec_u4p12[0]	2458
t_LKASlewY_NmpSec_u4p12[1]	2867
t_LKASlewY_NmpSec_u4p12[2]	3277
t_LKASlewY_NmpSec_u4p12[3]	3686
t_LKASlewY_NmpSec_u4p12[4]	4096





Name	Input Value		
t_LKASlewY_NmpSec_u4p12[5]	4506		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	state_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3:	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-0.620000005		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-1.17999995		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.60999966		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	511		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.30999994		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.0999999		
Name	Actual Value	Expected Value	Result
TrgArblim APADisableRate Uls M f32	0.145000011	0.144999996 ± 0000390625	
TrgArblim ESC State Cnt M enum	1	1	•
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	-6.12369585	-6.12370014 ± 0.03125	•
TrgArblim PosSrvoCmd MtrNm M f32	2.30999994	2.30999994 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.59399986	-5.59399986 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.865885615	0.865885973 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.865885615	0.865885973 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.620000005	-0.620000005 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.620000005	-0.620000005 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target TrqArblim Per1 OpTrqOv MtrNm f32.value	7.40980434	7.40980005 ± 0000390625	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	-

0.865885615

0.865885973 ± 0000390625

Test Step 3.8 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.101000004
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.59000015
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0174000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.82000005
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.0999999
k_APAEnableRate_pSec_f32	33
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.337000012
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	2867
t_APADisableRateX_HwNm_u4p12[1]	3277
t_APADisableRateX_HwNm_u4p12[2]	3686
t_APADisableRateX_HwNm_u4p12[3]	4096
t_APADisableRateX_HwNm_u4p12[4]	4506

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value





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Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	4915		
t_APADisableRateY_pSec_u7p9[0]	3584		
t_APADisableRateY_pSec_u7p9[1]	4096		
t_APADisableRateY_pSec_u7p9[2]	4608		
t_APADisableRateY_pSec_u7p9[3]	5120		
t_APADisableRateY_pSec_u7p9[4]	5632		
t_APADisableRateY_pSec_u7p9[5]	6144		
t_APASmoothX_Uls_u2p14[0]	1147		
t_APASmoothX_Uls_u2p14[1]	1311		
t_APASmoothX_Uls_u2p14[2]	1475		
t_APASmoothX_UIs_u2p14[3]	1638		
t_APASmoothX_UIs_u2p14[4]	1802		
t_APASmoothX_UIs_u2p14[5]	1966		
t_APASmoothX_UIs_u2p14[6]	2130 2294		
t_APASmoothX_Uls_u2p14[7] t_APASmoothX_Uls_u2p14[8]	2458		
t_APASmoothX_Uls_u2p14[9]	2621		
t_APASmoothY_UIs_u2p14[0]	13926		
t_APASmoothY_Uls_u2p14[1]	14090		
t_APASmoothY_Uls_u2p14[2]	14254		
t APASmoothY Uls u2p14[3]	14418		
t_APASmoothY_UIs_u2p14[4]	14582		
t_APASmoothY_Uls_u2p14[5]	14746		
t_APASmoothY_Uls_u2p14[6]	14909		
t_APASmoothY_Uls_u2p14[7]	15073		
t_APASmoothY_Uls_u2p14[8]	15237		
t_APASmoothY_Uls_u2p14[9]	15401		
t_LKASlewX_Kph_u8p8[0]	3584		
t_LKASlewX_Kph_u8p8[1]	4096		
t_LKASlewX_Kph_u8p8[2]	4608		
t_LKASlewX_Kph_u8p8[3]	5120		
t_LKASlewX_Kph_u8p8[4]	5632		
t_LKASlewX_Kph_u8p8[5]	6144		
t_LKASlewY_NmpSec_u4p12[0]	2867		
t_LKASlewY_NmpSec_u4p12[1]	3277		
t_LKASlewY_NmpSec_u4p12[2]	3686		
t_LKASlewY_NmpSec_u4p12[3]	4096		
t_LKASlewY_NmpSec_u4p12[4]	4506		
t_LKASlewY_NmpSec_u4p12[5]	4915	100	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3 target TrqArblim Per1_ESCIsLimited Cnt I		
target_Rte_Inst_Ap_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	•	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_HwTorque_HwNm_f	_	
target Rte Inst Ap TrgArblim.TrgArblim Per1 IgTrgOv HwNm f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target TrqArblim Per1 LKACmd HwNm f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.230000004		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.11999989		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	256.339996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.66000009		
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	7.30000019	I=	1_
	Actual Value	Expected Value	Resul
Name			
Name TrqArblim_APADisableRate_Uls_M_f32	0.166999996	0.166999996 ± 0000390625	•
Name TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum	0.166999996	2	
Name TrqArblim_APADisableRate_Uls_M_f32	0.166999996		

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Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.337000012	0.337000012 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.337000012	0.337000012 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.417567909	0.417567998 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.9 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.114
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.98000002
TrgArblim HwTorqueSV HwNm M Str.K UIs f32	0.0196000002
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.620000005
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.5999999
k_APAEnableRate_pSec_f32	37.5
k_APAUseADmpScale_lgc	1
k APAUseAsstScale Igc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	3277
t_APADisableRateX_HwNm_u4p12[1]	3686
t_APADisableRateX_HwNm_u4p12[2]	4096
t_APADisableRateX_HwNm_u4p12[3]	4506
t_APADisableRateX_HwNm_u4p12[4]	4915
t APADisableRateX HwNm u4p12[5]	5325
t_APADisableRateY_pSec_u7p9[0]	4096
t APADisableRateY pSec u7p9[1]	4608
t_APADisableRateY_pSec_u7p9[2]	5120
t APADisableRateY pSec u7p9[3]	5632
t_APADisableRateY_pSec_u7p9[4]	6144
t_APADisableRateY_pSec_u7p9[5]	6656
t_APASmoothX_UIs_u2p14[0]	1311
t_APASmoothX_Uls_u2p14[1]	1475
t_APASmoothX_UIs_u2p14[2]	1638
t_APASmoothX_Uls_u2p14[3]	1802
t_APASmoothX_Uls_u2p14[4]	1966
t_APASmoothX_Uls_u2p14[5]	2130
t_APASmoothX_Uls_u2p14[6]	2294
t_APASmoothX_Uls_u2p14[7]	2458
t_APASmoothX_Uls_u2p14[8]	2621
t_APASmoothX_Uls_u2p14[9]	2785
t_APASmoothY_Uls_u2p14[0]	14090
t_APASmoothY_Uls_u2p14[1]	14254
t_APASmoothY_Uls_u2p14[1]	14418
t_APASmoothY_Uls_u2p14[3]	14582
t APASmoothY Uls u2p14[4]	14746
t_APASmoothY_Uls_u2p14[5]	14909
t_APASmoothY_Uls_u2p14[6]	15073
t_APASmoothY_Uls_u2p14[7]	15237
t_APASmoothY_Uls_u2p14[8]	15401
	15565
t_APASmoothY_Uls_u2p14[9]	4096
t_LKASlewX_Kph_u8p8[0]	4608
t_LKASlewX_Kph_u8p8[1]	5120
t_LKASlewX_Kph_u8p8[2]	
t_LKASlewX_Kph_u8p8[3]	5632
t_LKASlewX_Kph_u8p8[4]	6144
t_LKASlewX_Kph_u8p8[5]	6656
t_LKASlewY_NmpSec_u4p12[0]	3277
t_LKASlewY_NmpSec_u4p12[1]	3686

TrqArblim_Per1

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Name	Input Value		
t_LKASlewY_NmpSec_u4p12[2]	4096		
t_LKASlewY_NmpSec_u4p12[3]	4506		
t_LKASlewY_NmpSec_u4p12[4]	4915		
t_LKASlewY_NmpSec_u4p12[5]	5325		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target TrqArblim Per1 LKACmd HwNm f3	32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State en	um	
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target TrqArblim Per1 LKATorqueDelivere	d HwNm f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target TrqArblim Per1 MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cn		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosSrvoCmd MtrNm f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor Uls f32	target TrqArblim Per1 ReturnDDFactor UI		
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target TrqArblim Per1 TrqOscCmd MtrNm	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	1.77999997	_	
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.63999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.63000011		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	19.6399994		
target TrqArblim Per1 PosServEnable Cnt Igc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.00999999		
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	7.5		
Name	Actual Value	Expected Value	Result
TrgArblim APADisableRate Uls M f32	0.0879999995	0.0879999995 ± 0000390625	~
TrgArblim ESC State Cnt M enum	3	3	✓
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	-4.85024786	-4.85024977 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.620000005	-0.620000005 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.59399986	-4.59399986 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.923623025	0.923623025 ± 0000390625	_
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.923623025	0.923623025 ± 0000330025	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0.923023023 1 0000390023	-
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	
target_frqmbilin_r err_trottorqueDelivereu_riwinin_loz.value	-v	-0 ± 0.0000030020	

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

7.45264626

0.923623025

7.45265007 ± 0000390625

0.923623025 ± 0000390625

Test Step 3.10 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.127000004	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.579999983	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.0999999	
k_APAEnableRate_pSec_f32	42	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	4.8499999	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	3686	
t_APADisableRateX_HwNm_u4p12[1]	4096	

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$





, · = ·			
Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	4506		
t_APADisableRateX_HwNm_u4p12[3]	4915		
t_APADisableRateX_HwNm_u4p12[4]	5325		
t_APADisableRateX_HwNm_u4p12[5] t_APADisableRateY_pSec_u7p9[0]	5734 4608		
t_APADisableRateY_pSec_u7p9[1]	5120		
t_APADisableRateY_pSec_u7p9[2]	5632		
t_APADisableRateY_pSec_u7p9[3]	6144		
t_APADisableRateY_pSec_u7p9[4]	6656		
t_APADisableRateY_pSec_u7p9[5]	7168		
t_APASmoothX_Uls_u2p14[0]	1475		
t_APASmoothX_Uls_u2p14[1]	1638		
t_APASmoothX_Uls_u2p14[2]	1802		
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	1966 2130		
t_APASmoothX_Uls_u2p14[5]	2294		
t_APASmoothX_Uls_u2p14[6]	2458		
t_APASmoothX_Uls_u2p14[7]	2621		
t_APASmoothX_Uls_u2p14[8]	2785		
t_APASmoothX_Uls_u2p14[9]	2949		
t_APASmoothY_Uls_u2p14[0]	14254		
t_APASmoothY_Uls_u2p14[1]	14418		
t_APASmoothY_Uls_u2p14[2]	14582		
t_APASmoothY_Uls_u2p14[3]	14746 14909		
t_APASmoothY_Uls_u2p14[4] t_APASmoothY_Uls_u2p14[5]	15073		
t_APASmoothY_Uls_u2p14[6]	15237		
t_APASmoothY_Uls_u2p14[7]	15401		
t_APASmoothY_Uls_u2p14[8]	15565		
t_APASmoothY_Uls_u2p14[9]	15729		
t_LKASlewX_Kph_u8p8[0]	4608		
t_LKASlewX_Kph_u8p8[1]	5120		
t_LKASlewX_Kph_u8p8[2]	5632 6144		
t_LKASlewX_Kph_u8p8[3] t_LKASlewX_Kph_u8p8[4]	6656		
t_LKASlewX_Kph_u8p8[5]	7168		
t_LKASlewY_NmpSec_u4p12[0]	3686		
t_LKASlewY_NmpSec_u4p12[1]	4096		
t_LKASlewY_NmpSec_u4p12[2]	4506		
t_LKASlewY_NmpSec_u4p12[3]	4915		
t_LKASlewY_NmpSec_u4p12[4]	5325		
t_LKASlewY_NmpSec_u4p12[5]	5734	f22	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls target TrqArblim Per1 DampingDDFactor		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f3 target_TrqArblim_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LRAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	2.98000002 4		
target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.04999995		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.13999987		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	24		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.3599999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.69999981		
Name	Actual Value	Expected Value	Resul
		•	rtoour
TrqArblim_APADisableRate_Uls_M_f32 TrqArblim ESC State Cnt M enum	0.21100001 4	0.210999995 ± 0000390625	Ttoda.





Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20824385	-4.20824003 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.3599999	3.3599999 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.07579994	-4.07579994 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.680617154	0.680616975 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
FrqArblim_APADisableRate_Uls_M_f32	0.140000001	
FrqArblim_ESC_State_Cnt_M_enum	4	
FrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.75999999	
FrgArblim HwTorqueSV HwNm M Str.K Uls f32	0.0240000002	
FrgArblim_PosSrvoCmd_MtrNm_M_f32	1.77999997	
FrqArblim_PreLKATrqReq_HwNm_M_f32	-3.599999	
<pre>x_APAEnableRate_pSec_f32</pre>	46.5	
APAUseADmpScale Igc	1	
	1	
_APAUseAsstScale_lgc : APAUseRetScale_lgc	1	
	3.3099994	
ESCMax_HwNm_f32	3.30999994	
LKAUseSlewCal_lgc		
_APADisableRateX_HwNm_u4p12[0]	4096	
_APADisableRateX_HwNm_u4p12[1]	4506	
_APADisableRateX_HwNm_u4p12[2]	4915	
_APADisableRateX_HwNm_u4p12[3]	5325	
_APADisableRateX_HwNm_u4p12[4]	5734	
_APADisableRateX_HwNm_u4p12[5]	6144	
_APADisableRateY_pSec_u7p9[0]	5120	
_APADisableRateY_pSec_u7p9[1]	5632	
_APADisableRateY_pSec_u7p9[2]	6144	
_APADisableRateY_pSec_u7p9[3]	6656	
_APADisableRateY_pSec_u7p9[4]	7168	
_APADisableRateY_pSec_u7p9[5]	7680	
_APASmoothX_Uls_u2p14[0]	1638	
_APASmoothX_Uls_u2p14[1]	1802	
_APASmoothX_Uls_u2p14[2]	1966	
_APASmoothX_Uls_u2p14[3]	2130	
_APASmoothX_Uls_u2p14[4]	2294	
_APASmoothX_Uls_u2p14[5]	2458	
_APASmoothX_Uls_u2p14[6]	2621	
_APASmoothX_Uls_u2p14[7]	2785	
_APASmoothX_Uls_u2p14[8]	2949	
_APASmoothX_Uls_u2p14[9]	3113	
_APASmoothY_Uls_u2p14[0]	14418	
_APASmoothY_Uls_u2p14[1]	14582	
_APASmoothY_Uls_u2p14[2]	14746	
_APASmoothY_Uls_u2p14[3]	14909	
_APASmoothY_Uls_u2p14[4]	15073	
_APASmoothY_Uls_u2p14[5]	15237	
_APASmoothY_Uls_u2p14[6]	15401	
_APASmoothY_Uls_u2p14[7]	15565	
_APASmoothY_Uls_u2p14[8]	15729	
_APASmoothY_Uls_u2p14[9]	15892	
LKASlewX_Kph_u8p8[0]	5120	
LKASlewX_Kph_u8p8[1]	5632	
LKASlewX_Kph_u8p8[2]	6144	
LKASlewX Kph u8p8[3]	6656	
_LKASlewX_Kph_u8p8[4]	7168	

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TrqArblim_Per1

TrqArbiim_Per1			MACICAL
Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	7680		
t_LKASlewY_NmpSec_u4p12[0]	4096		
t_LKASlewY_NmpSec_u4p12[1]	4506		
t_LKASlewY_NmpSec_u4p12[2]	4915		
t_LKASlewY_NmpSec_u4p12[3]	5325		
t_LKASlewY_NmpSec_u4p12[4]	5734		
t_LKASlewY_NmpSec_u4p12[5]	6144		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	4.17999983		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.46000004		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.6500001		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	32.5400009		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.80000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.23300001	0.232999995 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.56272006	-3.56272006 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019 ± 0.0002441406	25
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.59399986	-3.5940001 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.77399683	0.773997009 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.77399683	0.773997009 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-1.98882806	-1.98882997 ± 0000390625	✓
torget TraArblim Bort BeturnDDEcetor IIIe f22 value	0.77200602	0.772007000 + 0000200625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TroArblim Per1 CP1 CheckpointReached	1	/

0.77399683

0.773997009 ± 0000390625

Test Step 3.12 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.152999997	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.98000002	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.0999999	
k_APAEnableRate_pSec_f32	51	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	1.20000005	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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TIQATDIIII_FETT	
Name	Input Value
_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	4506
_APADisableRateX_HwNm_u4p12[1]	4915
_APADisableRateX_HwNm_u4p12[2]	5325
_APADisableRateX_HwNm_u4p12[3]	5734
_APADisableRateX_HwNm_u4p12[4]	6144
_APADisableRateX_HwNm_u4p12[5]	6554
_APADisableRateY_pSec_u7p9[0]	5632
_APADisableRateY_pSec_u7p9[1]	6144
_APADisableRateY_pSec_u7p9[2]	6656
_APADisableRateY_pSec_u7p9[3]	7168
_APADisableRateY_pSec_u7p9[4]	7680
_APADisableRateY_pSec_u7p9[5]	8192
_APASmoothX_Uls_u2p14[0]	1802
_APASmoothX_Uls_u2p14[1]	1966
_APASmoothX_Uls_u2p14[1]	2130
_APASmoothX_Uls_u2p14[3]	2294
	2458
_APASmoothX_Uls_u2p14[4]	2621
_APASmoothX_Uls_u2p14[5]	
_APASmoothX_Uls_u2p14[6]	2785
_APASmoothX_Uls_u2p14[7]	2949
_APASmoothX_Uls_u2p14[8]	3113
_APASmoothX_Uls_u2p14[9]	3277
_APASmoothY_Uls_u2p14[0]	14582
_APASmoothY_UIs_u2p14[1]	14746
_APASmoothY_Uls_u2p14[2]	14909
_APASmoothY_Uls_u2p14[3]	15073
_APASmoothY_Uls_u2p14[4]	15237
_APASmoothY_Uls_u2p14[5]	15401
_APASmoothY_Uls_u2p14[6]	15565
_APASmoothY_Uls_u2p14[7]	15729
_APASmoothY_Uls_u2p14[8]	15892
_APASmoothY_Uls_u2p14[9]	16056
_LKASlewX_Kph_u8p8[0]	5632
_LKASlewX_Kph_u8p8[1]	6144
_LKASlewX_Kph_u8p8[2]	6656
LKASlewX_Kph_u8p8[3]	7168
_LKASlewX_Kph_u8p8[4]	7680
LKASlewX_Kph_u8p8[5]	8192
_LKASlewY_NmpSec_u4p12[0]	4506
_LKASlewY_NmpSec_u4p12[1]	4915
_LKASlewY_NmpSec_u4p12[2]	5325
_transcer_uap12[3]	5734
_LKASlewY_NmpSec_u4p12[4]	6144
_LKASlewY_NmpSec_u4p12[5]	6554
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
arget_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011
arget_TrqArblim_Per1_ESCState_State_enum.value	1
arget_TrqArblim_Per1_ESCState_state_enum.value	1
arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.8699989
arget_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.15999985
arget_TrqArblim_Per1_LKAState_State_enum.value	0
arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	38.2933006
	1
arget_TrqArblim_Per1_PosServEnable_Cnt_lgc.value arget_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019

TrqArblim_Per1



Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.254999995	0.254999995 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.91367602	-2.91368008 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.09399986	-3.0940001 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.750104964	0.750105023 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.20000005	1.20000005 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.20000005	1.20000005 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.80000019 ± 0000390625	~
target TrgArblim Per1 ReturnDDFactor Uls f32.value	0.750104964	0.750105023 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
FrgArblim_APADisableRate_Uls_M_f32	0.165999994	
TrqArblim_ESC_State_Cnt_M_enum	1	
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.53999996	
FrgArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0284000002	
rqArblim_PosSrvoCmd_MtrNm_M_f32	4.17999983	
rqArblim_PreLKATrqReq_HwNm_M_f32	-2.5999999	
: APAEnableRate pSec f32	55.5	
APAUseADmpScale Igc	0	
_APAUseAsstScale_lgc	0	
APAUseRetScale Igc	0	
ESCMax HwNm f32	1.73500001	
_ESCMAX_HWNII_IS2 LKAUseSlewCal lgc	0	
	4915	
_APADisableRateX_HwNm_u4p12[0]	5325	
_APADisableRateX_HwNm_u4p12[1]	5734	
_APADisableRateX_HwNm_u4p12[2] _APADisableRateX_HwNm_u4p12[3]	6144	
	6554	
_APADisableRateX_HwNm_u4p12[4]	6963	
_APADisableRateX_HwNm_u4p12[5]	6144	
_APADisableRateY_pSec_u7p9[0]	6656	
APADisableRateY_pSec_u7p9[1]	7168	
_APADisableRateY_pSec_u7p9[2]		
_APADisableRateY_pSec_u7p9[3]	7680	
_APADisableRateY_pSec_u7p9[4]	8192	
_APADisableRateY_pSec_u7p9[5]	8704	
_APASmoothX_UIs_u2p14[0]	1966	
_APASmoothX_UIs_u2p14[1]	2130	
_APASmoothX_UIs_u2p14[2]	2294	
APASmoothX_Uls_u2p14[3]	2458	
_APASmoothX_Uls_u2p14[4]	2621	
_APASmoothX_UIs_u2p14[5]	2785	
_APASmoothX_Uls_u2p14[6]	2949	
_APASmoothX_Uls_u2p14[7]	3113	
_APASmoothX_Uls_u2p14[8]	3277	
_APASmoothX_Uls_u2p14[9]	3441	
_APASmoothY_Uls_u2p14[0]	14746	
_APASmoothY_Uls_u2p14[1]	14909	
_APASmoothY_Uls_u2p14[2]	15073	
_APASmoothY_Uls_u2p14[3]	15237	
_APASmoothY_Uls_u2p14[4]	15401	
_APASmoothY_Uls_u2p14[5]	15565	
_APASmoothY_Uls_u2p14[6]	15729	
_APASmoothY_Uls_u2p14[7]	15892	
_APASmoothY_Uls_u2p14[8]	16056	
_APASmoothY_Uls_u2p14[9]	16220	
_LKASlewX_Kph_u8p8[0]	6144	
_LKASlewX_Kph_u8p8[1]	6656	
_LKASlewX_Kph_u8p8[2]	7168	





Name	Input Value			
t_LKASlewX_Kph_u8p8[3]	7680	7680		
t_LKASlewX_Kph_u8p8[4]	8192			
t_LKASlewX_Kph_u8p8[5]	8704			
t_LKASlewY_NmpSec_u4p12[0]	4915			
t_LKASlewY_NmpSec_u4p12[1]	5325			
t_LKASlewY_NmpSec_u4p12[2]	5734			
t_LKASlewY_NmpSec_u4p12[3]	6144			
t_LKASlewY_NmpSec_u4p12[4]	6554			
t_LKASlewY_NmpSec_u4p12[5]	6963			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Ul	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	<u>lgc</u>		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	<u>f</u> 32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	2		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_er	num		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3:	2 target_TrqArblim_Per1_MinSecureVehicleS	speed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cr	ıt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	n_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992			
target_TrqArblim_Per1_ESCState_State_enum.value	2			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.28000021			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	44.7433014			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.30000019			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.27700001	0.27700001 ± 0000390625	•	
TrgArblim ESC State Cnt M enum	2	2	•	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.26111197	-2.26111007 ± 0.03125		
TrgArblim PosSrvoCmd MtrNm M f32	0	0 ± 0.000244140625	•	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.57821965	-2.57821989 ± 0.03125		
target TrgArblim Per1 AssistDDFactor Uls f32.value	1	1 ± 0000390625		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625		
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1		
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	1.73500001	1.73500001 ± 0000390625		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.73500001	1.73500001 ± 0.0000390625		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625		
target TrqArblim_Per1_DTrqOv MtrNm f32.value	8.30000019	8.30000019 ± 0000390625		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.14 (Repeat Count = 1)		<u> </u>
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.179000005	
TrqArblim_ESC_State_Cnt_M_enum	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.92999995	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.38000011	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.0999999	
k_APAEnableRate_pSec_f32	60	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	





Name	Input Value
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.26900005
k_LKAUseSlewCal_lgc t_APADisableRateX_HwNm_u4p12[0]	1 5325
t_APADisableRateX_HwNm_u4p12[1]	5734
t_APADisableRateX_HwNm_u4p12[2]	6144
t_APADisableRateX_HwNm_u4p12[3]	6554
t_APADisableRateX_HwNm_u4p12[4]	6963
t_APADisableRateX_HwNm_u4p12[5]	7373
t_APADisableRateY_pSec_u7p9[0]	6656
t_APADisableRateY_pSec_u7p9[1]	7168
t_APADisableRateY_pSec_u7p9[2]	7680 8192
t_APADisableRateY_pSec_u7p9[3] t_APADisableRateY_pSec_u7p9[4]	8704
t_APADisableRateY_pSec_u7p9[5]	9216
t_APASmoothX_Uls_u2p14[0]	2130
t_APASmoothX_Uls_u2p14[1]	2294
t_APASmoothX_Uls_u2p14[2]	2458
t_APASmoothX_Uls_u2p14[3]	2621
t_APASmoothX_Uls_u2p14[4]	2785
t_APASmoothX_Uls_u2p14[5]	2949 3113
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	3277
t_APASmoothX_Uls_u2p14[7]	3441
t_APASmoothX_UIs_u2p14[9]	3604
t_APASmoothY_UIs_u2p14[0]	14909
t_APASmoothY_Uls_u2p14[1]	15073
t_APASmoothY_Uls_u2p14[2]	15237
t_APASmoothY_Uls_u2p14[3]	15401
t_APASmoothY_Uls_u2p14[4]	15565
t_APASmoothY_UIs_u2p14[5]	15729 15892
t_APASmoothY_Uls_u2p14[6] t_APASmoothY_Uls_u2p14[7]	16056
t_APASmoothY_UIs_u2p14[8]	16220
t_APASmoothY_Uls_u2p14[9]	16384
t_LKASlewX_Kph_u8p8[0]	6656
t_LKASlewX_Kph_u8p8[1]	7168
t_LKASlewX_Kph_u8p8[2]	7680
t_LKASlewX_Kph_u8p8[3]	8192
t_LKASlewX_Kph_u8p8[4]	8704 9216
t_LKASlewX_Kph_u8p8[5] t LKASlewY NmpSec u4p12[0]	5325
t LKASlewY NmpSec u4p12[1]	5734
t_LKASlewY_NmpSec_u4p12[2]	6144
t_LKASlewY_NmpSec_u4p12[3]	6554
t_LKASlewY_NmpSec_u4p12[4]	6963
t_LKASlewY_NmpSec_u4p12[5]	7373
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021
target_TrqArblim_Per1_ESCState_State_enum.value	3
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.68999958
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.17999983
	4
target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	1 51.1932983

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Name	Input Value		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.4000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.299000025	0.298999995 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.60502791	-1.60502994 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.4000001	4.4000001 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-2.09399986	-2.0940001 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-2.09399986	-2.0940001 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-2.09399986	-2.0940001 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.31560016	1.31560004 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.700999975	0.700999975 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value
	·
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.192000002
TrqArblim_ESC_State_Cnt_M_enum	3
FrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.32000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0328000002
FrqArblim_PosSrvoCmd_MtrNm_M_f32	6.57999992
FrqArblim_PreLKATrqReq_HwNm_M_f32	-1.60000002
<pre>c_APAEnableRate_pSec_f32</pre>	64.5
<pre>x_APAUseADmpScale_lgc</pre>	0
<pre><_APAUseAsstScale_lgc</pre>	0
a_APAUseRetScale_lgc	0
c_ESCMax_HwNm_f32	0.802999973
<pre>c_LKAUseSlewCal_lgc</pre>	0
_APADisableRateX_HwNm_u4p12[0]	5734
_APADisableRateX_HwNm_u4p12[1]	6144
_APADisableRateX_HwNm_u4p12[2]	6554
_APADisableRateX_HwNm_u4p12[3]	6963
_APADisableRateX_HwNm_u4p12[4]	7373
_APADisableRateX_HwNm_u4p12[5]	7782
_APADisableRateY_pSec_u7p9[0]	7168
_APADisableRateY_pSec_u7p9[1]	7680
_APADisableRateY_pSec_u7p9[2]	8192
_APADisableRateY_pSec_u7p9[3]	8704
_APADisableRateY_pSec_u7p9[4]	9216
_APADisableRateY_pSec_u7p9[5]	9728
_APASmoothX_Uls_u2p14[0]	2294
_APASmoothX_Uls_u2p14[1]	2458
_APASmoothX_Uls_u2p14[2]	2621
_APASmoothX_Uls_u2p14[3]	2785
_APASmoothX_Uls_u2p14[4]	2949
_APASmoothX_Uls_u2p14[5]	3113
_APASmoothX_Uls_u2p14[6]	3277
_APASmoothX_Uls_u2p14[7]	3441
_APASmoothX_Uls_u2p14[8]	3604
_APASmoothX_Uls_u2p14[9]	3768
_APASmoothY_Uls_u2p14[0]	6226
_APASmoothY_Uls_u2p14[1]	6390
_APASmoothY_Uls_u2p14[2]	6554
_APASmoothY_Uls_u2p14[3]	6717
_APASmoothY_Uls_u2p14[4]	6881
_APASmoothY_Uls_u2p14[5]	7045
_APASmoothY_Uls_u2p14[6]	7209
APASmoothY Uls u2p14[7]	7373
_APASmoothY_Uls_u2p14[8]	7537
APASmoothY_Uls_u2p14[9]	7700

TrqArblim_Per1

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Name	Input Value		
t_LKASlewX_Kph_u8p8[0]	7168		
LKASlewX_Kph_u8p8[1]	7680		
LKASlewX_Kph_u8p8[2]	8192		
t_LKASlewX_Kph_u8p8[3]	8704		
t LKASlewX Kph u8p8[4]	9216		
t_LKASlewX_Kph_u8p8[5]	9728		
t LKASlewY NmpSec u4p12[0]	5734		
t_LKASlewY_NmpSec_u4p12[1]	6144		
t_LKASlewY_NmpSec_u4p12[2]	6554		
t_LKASlewY_NmpSec_u4p12[3]	6963		
t_LKASlewY_NmpSec_u4p12[4]	7373		
t_LKASlewY_NmpSec_u4p12[5]	7782		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target TrqArblim Per1 AssistDDFactor UI	s f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliver		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_HwTorque_HwNm_	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target_TrqArblim_Per1_LKACmd_HwNm_f		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target_TrqArblim_Per1_LKAState_State_er		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target TrqArblim Per1 LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target Rte Inst Ap TrgArblim.TrgArblim Per1 OpTrgOv MtrNm f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_t		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target TrqArblim Per1 PosSrvoCmd MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNr	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target TrgArblim Per1 GMOSHOscillate State enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.69000006		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	57.6432991		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target TrgArblim Per1 PosSrvoCmd MtrNm f32.value	-4.4000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.6999981		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.32100001	0.32100001 ± 0000390625	rtoou
TrqArblim_ESC_State_Cnt_M_enum	4	4	
TrgArblim HwTorqueSV HwNm M Str.SV Uls f32	-0.981504083	-0.981504023 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	-4.4000001	-4.4000001 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.57972467	-1.57972002 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target TrgArblim Per1 ESCIsLimited Cnt Igc.value	0	0	
target_TrqArblim_Fer1_ESCIsElfilited_Crit_igc.value target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
arget_TrqArblim_Per1_lqTrqOv_HwNm_f32.value	-1.57972467	-1.57972467 ± 0.0000390625	
target_rrqArblim_Per1_iq1rqOv_nwnin_is2.value target_rrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-1.57972467	-1.57972467 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_Hwitin_is2.value target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.03621292	8.03621006 ± 0000390625	
target_rrqArblim_Per1_Op1rqOv_MthNin_i32.value	1	1 ± 0000390625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.16 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.204999998	
TrqArblim_ESC_State_Cnt_M_enum	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.709999979	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0350000001	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.78000021	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002	





	Input Value
	69
	1
	1
	0.337000012
	1
t_APADisableRateX_HwNm_u4p12[0]	6144
t_APADisableRateX_HwNm_u4p12[1]	6554
t_APADisableRateX_HwNm_u4p12[2]	6963
	7373
t_APADisableRateX_HwNm_u4p12[4]	7782
t_APADisableRateX_HwNm_u4p12[5]	8192
	7680 8192
	8704
t_APADisableRateY_pSec_u7p9[3]	9216
t_APADisableRateY_pSec_u7p9[4]	9728
t_APADisableRateY_pSec_u7p9[5]	10240
t_APASmoothX_Uls_u2p14[0]	2458
t_APASmoothX_Uls_u2p14[1]	2621
t_APASmoothX_UIs_u2p14[2]	2785
t_APASmoothX_UIs_u2p14[3]	2949
	3113
t_APASmoothX_Uls_u2p14[5] t_APASmoothX_Uls_u2p14[6]	3277 3441
	3604
t APASmoothX Uls u2p14[8]	3768
t_APASmoothX_UIs_u2p14[9]	3932
t_APASmoothY_Uls_u2p14[0]	6390
t_APASmoothY_Uls_u2p14[1]	6554
t_APASmoothY_Uls_u2p14[2]	6717
t_APASmoothY_Uls_u2p14[3]	6881
t_APASmoothY_Uls_u2p14[4]	7045
t_APASmoothY_UIs_u2p14[5]	7209
t_APASmoothY_Uls_u2p14[6] t_APASmoothY_Uls_u2p14[7]	7373 7537
t_APASmoothY_UIs_u2p14[8]	7700
t_APASmoothY_Uls_u2p14[9]	7864
	7680
t_LKASlewX_Kph_u8p8[1]	8192
t_LKASlewX_Kph_u8p8[2]	8704
t_LKASlewX_Kph_u8p8[3]	9216
t_LKASlewX_Kph_u8p8[4]	9728
t_LKASlewX_Kph_u8p8[5]	10240
t_LKASlewY_NmpSec_u4p12[0] t_LKASlewY_NmpSec_u4p12[1]	6144 6554
t_LKASlewY_NmpSec_u4p12[2]	6963
t_LKASlewY_NmpSec_u4p12[3]	7373
t_LKASlewY_NmpSec_u4p12[4]	7782
t_LKASlewY_NmpSec_u4p12[5]	8192
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_rrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target TrqArblim Per1 ESCCmd HwNm f32.value	10.1800003
· - ·	0
0 - 1	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10

target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value\\ target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

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-1.09399998 ± 0.0000390625

-1.09399998 ± 0.0000390625

0.564993024 ± 0000390625

0.927379012 ± 0000390625

TrqArblim_Per1	077 00 70, 72.22.07 0000	Razo	orcat
Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	64.0932999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-4.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.60000038		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.174999997	0.174999997 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.03515005	-1.03515005 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.78000021	7.78000021 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.09399998	-1.09399998 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.927378833	0.927379012 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

-1.09399998

-1.09399998

0.564992547

0.927378833

Name	Input Value	
Rte Inst Ap TrgArblim	target_Rte_Inst_Ap_TrqArblim	
TrgArblim APADisableRate Uls M f32	0.217999995	
TrgArblim_APADIsableRate_Ois_IM_IS2 TrgArblim ESC State Cnt M enum	0.217999995	
TrgArblim_ESC_State_Crit_M_enum TrgArblim HwTorqueSV HwNm M Str.SV Uls f32	-0.100000001	
	0.0372000001	
FrgArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	8.97999954	
FrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.600000024	
rqArblim_PreLKATrqReq_HwNm_M_f32	73.5	
x_APALeaAPmaScale_las	1	
<_APAUseApartCale_lgc	1	
<pre>APAUseAsstScale_lgc</pre>		
«_APAUseRetScale_lgc	1	
C_ESCMax_HwNm_f32	1.25	

@ Report created by TESSY V3.1.7, report template V2.1

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Name	Input Value		
t_APASmoothY_Uls_u2p14[7]	7700		
t APASmoothY Uls u2p14[8]	7864		
t_APASmoothY_Uls_u2p14[9]	8028		
t LKASlewX Kph u8p8[0]	8192		
t_LKASlewX_Kph_u8p8[1]	8704		
t_LKASlewX_Kph_u8p8[2]	9216		
t_LKASlewX_Kph_u8p8[3]	9728		
t_LKASlewX_Kph_u8p8[4]	10240		
t LKASlewX Kph u8p8[5]	10752		
t LKASlewY NmpSec u4p12[0]	6554		
t_LKASlewY_NmpSec_u4p12[1]	6963		
t_LKASlewY_NmpSec_u4p12[1]	7373		
t LKASlewY NmpSec u4p12[3]	7782		
t_LKASlewY_NmpSec_u4p12[4]	8192		
	8602		
t_LKASlewY_NmpSec_u4p12[5]		. 622	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_DampingDDFactor_ target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCEInt_rwinin_is2 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCISLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target TrqArblim Per1 ESCTorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target TrqArblim Per1 MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	· - ·		
target_rte_inst_ap_frqArblim.frqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10	_102	
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	10		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.71000004		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	70.5432968		
target TrqArblim Per1 PosServEnable Cnt Igc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-4.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.10000038		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32	0.36500001	0.36500001 ± 0000390625	Kesuit
TrqArblim_ESC_State_Cnt_M_enum	1	1	_
TrgArblim HwTorqueSV HwNm M Str.SV Uls f32	0.27572003	0.27572 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.0999999	-4.0999999 ± 0.000244140625	
TrqArblim PreLKATrqReq HwNm M f32	-0.594000041	-0.593999992 ± 0.00125	,
· - ·			
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.821153581 0.821153581	0.821153998 ± 0000390625 0.821153998 ± 0000390625	
target_rqArblim_Per1_DampingDDFactor_Uis_t32.value target_rrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_rqArblim_Per1_ESCISLimited_Cnt_igc.value target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value		1 25 + 0000300625	Ž
	-1.25 -1.25	-1.25 ± 0000390625	Ž
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value target TrqArblim Per1 LKATorqueDelivered HwNm f32.value	0	-1.25 ± 0.0000390625 0 ± 0.0000390625	.4
target_IrqArblim_Per1_LKATorqueDelivered_Hwnm_t32.value target TrqArblim Per1 OpTrqOv MtrNm f32.value	-0.733270347	-0.733269989 ± 0000390625	Ž
target_rrqArblim_Per1_oprrqov_withNin_isz.value	0.821153581		
target_rryArbiliti_Fett_ReturnDDFactor_OIS_132.Value	0.02 1000	0.821153998 ± 0000390625	

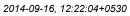
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.18 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.231000006	
TrqArblim_ESC_State_Cnt_M_enum	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.50999999	

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TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	Input Value
1147 1151111_11W1014466 V_11W1111_W_611.11_616_162	0.0394000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.1800003
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.100000001
k_APAEnableRate_pSec_f32	78
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc k_ESCMax_HwNm_f32	1 3.24000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	6963
t_APADisableRateX_HwNm_u4p12[1]	7373
t_APADisableRateX_HwNm_u4p12[2]	7782
t_APADisableRateX_HwNm_u4p12[3]	8192
t_APADisableRateX_HwNm_u4p12[4]	8602
t_APADisableRateX_HwNm_u4p12[5]	9011
t_APADisableRateY_pSec_u7p9[0]	8704 9216
t_APADisableRateY_pSec_u7p9[1] t_APADisableRateY_pSec_u7p9[2]	9728
t APADisableRateY pSec u7p9[3]	10240
t_APADisableRateY_pSec_u7p9[4]	10752
t_APADisableRateY_pSec_u7p9[5]	11264
t_APASmoothX_Uls_u2p14[0]	2785
t_APASmoothX_Uls_u2p14[1]	2949
t_APASmoothX_Uls_u2p14[2]	3113
t_APASmoothX_Uls_u2p14[3]	3277
t_APASmoothX_UIs_u2p14[4]	3441
t_APASmoothX_Uls_u2p14[5] t_APASmoothX_Uls_u2p14[6]	3604 3768
t_APASmoothX_Uls_u2p14[7]	3932
t_APASmoothX_Uls_u2p14[8]	4096
t_APASmoothX_Uls_u2p14[9]	4260
t_APASmoothY_Uls_u2p14[0]	6717
t_APASmoothY_Uls_u2p14[1]	6881
t_APASmoothY_Uls_u2p14[2]	7045
t_APASmoothY_Uls_u2p14[3]	7209
t_APASmoothY_Uls_u2p14[4]	7373
t_APASmoothY_Uls_u2p14[5] t_APASmoothY_Uls_u2p14[6]	7537 7700
t APASmoothY Uls u2p14[7]	7864
t_APASmoothY_Uls_u2p14[8]	8028
t_APASmoothY_Uls_u2p14[9]	8192
t_LKASlewX_Kph_u8p8[0]	8704
t_LKASlewX_Kph_u8p8[1]	9216
t_LKASlewX_Kph_u8p8[2]	9728
t_LKASlewX_Kph_u8p8[3]	10240
t_LKASlewX_Kph_u8p8[4]	10752
t_LKASlewX_Kph_u8p8[5] t_LKASlewY_NmpSec_u4p12[0]	11264 6963
t_LKASlewY_NmpSec_u4p12[1]	7373
t LKASlewY NmpSec u4p12[2]	7782
t_LKASlewY_NmpSec_u4p12[3]	8192
t_LKASlewY_NmpSec_u4p12[4]	8602
t_LKASlewY_NmpSec_u4p12[5]	9011
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target TrqArblim Per1 ESCState State enum
target_Rte_Inst_Ap_IrqArblim.IrqArblim_PerI_ESCState_State_enum target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target_IrqArbiim_Peri_ESCState_state_enum target TrqArbiim_Peri_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwiNin_S2 target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate State enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServEnable_Cnt_lgc target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_IrqArblim.IrqArblim_Peri_PosSrvoCmd_MtrNm_132 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_IrqArblim_Per1_PossrvoCmd_intrNim_r32 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
	target TrqArblim Per1 TrqOscCmd MtrNm f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 -9.52000046





Name	Input Value		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	76.9933014		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.196999997	0.196999997 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.489905983	0.489906013 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.1800003	10.1800003 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.0940000042	-0.0939999968 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.24000001	-3.24000001 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.876287162	0.876286983 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.913920701	0.913920999 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrgArblim APADisableRate Uls M f32	0.24400003
TrqArblim_ESC_State_Cnt_M_enum	2
TrgArblim HwTorqueSV HwNm M Str.SV Uls f32	1.12
TrgArblim HwTorqueSV HwNm M Str.K Uls f32	0.0416000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.40000006
APAEnableRate pSec f32	82.5
APAUseADmpScale lgc	0
APAUseAsstScale lgc	0
k APAUseRetScale Igc	0
k_ESCMax_HwNm_f32	1.58000004
<pre><_LKAUseSlewCal_lgc</pre>	0
_APADisableRateX_HwNm_u4p12[0]	7373
	7782
_APADisableRateX_HwNm_u4p12[2]	8192
_APADisableRateX_HwNm_u4p12[3]	8602
_APADisableRateX_HwNm_u4p12[4]	9011
APADisableRateX_HwNm_u4p12[5]	9421
_APADisableRateY_pSec_u7p9[0]	9216
_APADisableRateY_pSec_u7p9[1]	9728
_APADisableRateY_pSec_u7p9[2]	10240
_APADisableRateY_pSec_u7p9[3]	10752
_APADisableRateY_pSec_u7p9[4]	11264
_APADisableRateY_pSec_u7p9[5]	11776
_APASmoothX_Uls_u2p14[0]	2949
_APASmoothX_Uls_u2p14[1]	3113
_APASmoothX_Uls_u2p14[2]	3277
_APASmoothX_Uls_u2p14[3]	3441
:_APASmoothX_Uls_u2p14[4]	3604
_APASmoothX_Uls_u2p14[5]	3768
_APASmoothX_Uls_u2p14[6]	3932
_APASmoothX_Uls_u2p14[7]	4096
_APASmoothX_Uls_u2p14[8]	4260
_APASmoothX_Uls_u2p14[9]	4424
_APASmoothY_Uls_u2p14[0]	0
APASmoothY_Uls_u2p14[1]	164
t_APASmoothY_Uls_u2p14[2]	328
t APASmoothY Uls u2p14[3]	492

TrqArblim_Per1

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			-12 - 10010
Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	655		
t_APASmoothY_Uls_u2p14[5]	819		
t_APASmoothY_Uls_u2p14[6]	983		
t_APASmoothY_Uls_u2p14[7]	1147		
t_APASmoothY_Uls_u2p14[8]	1311		
t_APASmoothY_Uls_u2p14[9]	1475		
t_KASlewX_Kph_u8p8[0]	9216		
t_LKASlewX_Kph_u8p8[1]	9728		
t_LKASlewX_Kph_u8p8[2]	10240		
t_LKASlewX_Kph_u8p8[3]	10752		
t_LKASlewX_Kph_u8p8[4]	11264		
t_LKASlewX_Kph_u8p8[5]	11776		
t LKASlewY NmpSec u4p12[0]	7373		
t_LKASlewY_NmpSec_u4p12[1]	7782		
t LKASlewY NmpSec u4p12[2]	8192		
t_LKASlewY_NmpSec_u4p12[3]	8602		
t_LKASlewY_NmpSec_u4p12[4]	9011		
t_LKASlewY_NmpSec_u4p12[5]	9421		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target TrqArblim Per1 ESCCmd HwNm f3		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State en		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target Rte Inst Ap TrgArblim.TrgArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.34000015		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-2.73000002		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	83.4432983		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.409000009	0.409000009 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	3	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.29555202	1.29554999 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.70000005	-3.70000005 ± 0.000244140625	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.382734507	0.382735014 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.382734507	0.382734507 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.382734507	0.382734507 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.86376238	1.86375999 ± 0000390625	
target TrgArblim Per1 ReturnDDFactor Uls f32 value	1	1 + 0000390625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

1 ± 0000390625

1

Test Step 3.20 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value





Name	Input Value
TrqArblim_APADisableRate_Uls_M_f32	0.256999999
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.73000002 0.0438000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.52000046
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.89999976
k_APAEnableRate_pSec_f32	87
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	7782
t_APADisableRateX_HwNm_u4p12[1]	8192
t_APADisableRateX_HwNm_u4p12[2]	8602
t_APADisableRateX_HwNm_u4p12[3]	9011
t_APADisableRateX_HwNm_u4p12[4]	9421 9830
t_APADisableRateX_HwNm_u4p12[5] t_APADisableRateY_pSec_u7p9[0]	9728
t APADisableRateY pSec u7p9[1]	10240
t_APADisableRateY_pSec_u7p9[2]	10752
t_APADisableRateY_pSec_u7p9[3]	11264
t_APADisableRateY_pSec_u7p9[4]	11776
t_APADisableRateY_pSec_u7p9[5]	12288
t_APASmoothX_UIs_u2p14[0]	3113
t_APASmoothX_Uls_u2p14[1]	3277
t_APASmoothX_Uls_u2p14[2]	3441
t_APASmoothX_Uls_u2p14[3]	3604
t_APASmoothX_Uls_u2p14[4]	3768
t_APASmoothX_Uls_u2p14[5]	3932
t_APASmoothX_UIs_u2p14[6]	4096
t_APASmoothX_UIs_u2p14[7]	4260 4424
t_APASmoothX_Uls_u2p14[8] t_APASmoothX_Uls_u2p14[9]	4588
t_APASmoothY_Uls_u2p14[0]	164
t_APASmoothY_Uls_u2p14[1]	328
t_APASmoothY_Uls_u2p14[2]	492
t_APASmoothY_Uls_u2p14[3]	655
t_APASmoothY_Uls_u2p14[4]	819
t_APASmoothY_Uls_u2p14[5]	983
t_APASmoothY_Uls_u2p14[6]	1147
t_APASmoothY_Uls_u2p14[7]	1311
t_APASmoothY_Uls_u2p14[8]	1475
t_APASmoothY_Uls_u2p14[9]	1638
t_KASlewX_Kph_u8p8[0]	9728 10240
t_LKASlewX_Kph_u8p8[1] t_LKASlewX_Kph_u8p8[2]	10752
t_LKASlewX_Kph_u8p8[3]	11264
t_LKASlewX_Kph_u8p8[4]	11776
t_LKASlewX_Kph_u8p8[5]	12288
t_LKASlewY_NmpSec_u4p12[0]	7782
t_LKASlewY_NmpSec_u4p12[1]	8192
t_LKASlewY_NmpSec_u4p12[2]	8602
t_LKASlewY_NmpSec_u4p12[3]	9011
t_LKASlewY_NmpSec_u4p12[4]	9421
t_LKASlewY_NmpSec_u4p12[5]	9830
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_state_entitli target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOs	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.86000013		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-2.24000001		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	89.8933029		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.430999994	0.430999994 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.39755797	1.39756 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.5	-3.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.893999994	0.893999994 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.150813162	-0.150812998 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.956910551	0.956911027 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.21 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.270000011
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-9.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.39999998
k_APAEnableRate_pSec_f32	91.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	8192
t_APADisableRateX_HwNm_u4p12[1]	8602
t_APADisableRateX_HwNm_u4p12[2]	9011
t_APADisableRateX_HwNm_u4p12[3]	9421
t_APADisableRateX_HwNm_u4p12[4]	9830
t_APADisableRateX_HwNm_u4p12[5]	10240
t_APADisableRateY_pSec_u7p9[0]	10240
t_APADisableRateY_pSec_u7p9[1]	10752
t_APADisableRateY_pSec_u7p9[2]	11264
t_APADisableRateY_pSec_u7p9[3]	11776
t_APADisableRateY_pSec_u7p9[4]	12288
t_APADisableRateY_pSec_u7p9[5]	12800
t_APASmoothX_Uls_u2p14[0]	3277
t_APASmoothX_Uls_u2p14[1]	3441
t_APASmoothX_Uls_u2p14[2]	3604
t_APASmoothX_Uls_u2p14[3]	3768
t_APASmoothX_Uls_u2p14[4]	3932
t_APASmoothX_Uls_u2p14[5]	4096
t_APASmoothX_Uls_u2p14[6]	4260
t_APASmoothX_Uls_u2p14[7]	4424
t_APASmoothX_Uls_u2p14[8]	4588
t_APASmoothX_Uls_u2p14[9]	4751
t_APASmoothY_Uls_u2p14[0]	328

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Name	Input Value	
t_APASmoothY_Uls_u2p14[1]	492	
t_APASmoothY_Uls_u2p14[2]	655	
t_APASmoothY_Uls_u2p14[3]	819	
t_APASmoothY_Uls_u2p14[4]	983	
t_APASmoothY_Uls_u2p14[5]	1147	
t_APASmoothY_Uls_u2p14[6]	1311	
t_APASmoothY_Uls_u2p14[7]	1475	
t_APASmoothY_Uls_u2p14[8]	1638	
t_APASmoothY_Uls_u2p14[9]	1802	
t_LKASlewX_Kph_u8p8[0]	10240	
t_LKASlewX_Kph_u8p8[1]	10752	
t_LKASlewX_Kph_u8p8[2]	11264	
t_LKASlewX_Kph_u8p8[3]	11776	
t_LKASlewX_Kph_u8p8[4]	12288	
t_LKASlewX_Kph_u8p8[5]	12800	
t_LKASlewY_NmpSec_u4p12[0]	8192	
t_LKASlewY_NmpSec_u4p12[1]	8602	
t_LKASlewY_NmpSec_u4p12[2]	9011	
t_LKASlewY_NmpSec_u4p12[3]	9421	
t_LKASlewY_NmpSec_u4p12[4]	9830	
t_LKASlewY_NmpSec_u4p12[5]	10240	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3: target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	! target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992	
target TrqArblim Per1 ESCState State enum.value	0	
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1	
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.85999966	
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.5	
target_TrqArblim_Per1_LKAState_State_enum.value	0	
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	96.3432999	
target TrqArblim Per1 PosServEnable Cnt Igc.value	0	
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.29999995	
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	-8.80000019	
Name	Actual Value Expected Value	Resul
TrgArblim APADisableRate Uls M f32	0.230000004	
TrgArblim ESC State Cnt M enum	0 0	
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	1.77880001 1.77880001 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	-9.03999996 -9.03999996 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.39399993 1.39400005 ± 0.03125	
target TrqArblim Per1 AssistDDFactor Uls f32.value	0.9885028 0.990368009 ± 0000390625	
target TrqArblim Per1 DampingDDFactor Uls f32.value	0.9885028 0.990368009 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0 0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0 0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.39399993 1.39400005 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.39399993 1.39400005 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-8.80000019 -8.80000019 ± 0000390625	
	0.9885028 0.990368009 ± 0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓



Test Step 3.22 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.282999992
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.95000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0482000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.56000042
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.8999998
k_APAEnableRate_pSec_f32	96
k_APAUseADmpScale_lgc k_APAUseAsstScale_lqc	0
k_APAUseRetScale_lgc	0
k ESCMax HwNm f32	3.1400001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	8602
t_APADisableRateX_HwNm_u4p12[1]	9011
t_APADisableRateX_HwNm_u4p12[2]	9421
t_APADisableRateX_HwNm_u4p12[3]	9830
t_APADisableRateX_HwNm_u4p12[4]	10240
t_APADisableRateX_HwNm_u4p12[5]	10650
t_APADisableRateY_pSec_u7p9[0]	10752
t_APADisableRateY_pSec_u7p9[1] t APADisableRateY pSec u7p9[2]	11264 11776
t_APADisableRateY_pSec_u7p9[2]	12288
t_APADisableRateY_pSec_u7p9[4]	12800
t_APADisableRateY_pSec_u7p9[5]	13312
t_APASmoothX_Uls_u2p14[0]	3441
t_APASmoothX_Uls_u2p14[1]	3604
t_APASmoothX_Uls_u2p14[2]	3768
t_APASmoothX_Uls_u2p14[3]	3932
t_APASmoothX_Uls_u2p14[4]	4096
t_APASmoothX_Uls_u2p14[5]	4260
t_APASmoothX_Uls_u2p14[6]	4424
t_APASmoothX_UIs_u2p14[7]	4588
t_APASmoothX_Uls_u2p14[8] t_APASmoothX_Uls_u2p14[9]	4751 4915
t_APASmoothY_Uls_u2p14[0]	492
t_APASmoothY_Uls_u2p14[1]	655
t_APASmoothY_Uls_u2p14[2]	819
t_APASmoothY_Uls_u2p14[3]	983
t_APASmoothY_Uls_u2p14[4]	1147
t_APASmoothY_Uls_u2p14[5]	1311
t_APASmoothY_Uls_u2p14[6]	1475
t_APASmoothY_Uls_u2p14[7]	1638
t_APASmoothY_Uls_u2p14[8]	1802
t_APASmoothY_UIs_u2p14[9]	1966
t_LKASlewX_Kph_u8p8[0] t_LKASlewX_Kph_u8p8[1]	10752 11264
t_LKASlewX_Kph_u8p8[2]	11776
t_LKASlewX_Kph_u8p8[3]	12288
t_LKASlewX_Kph_u8p8[4]	12800
t_LKASlewX_Kph_u8p8[5]	13312
t_LKASlewY_NmpSec_u4p12[0]	8602
t_LKASlewY_NmpSec_u4p12[1]	9011
t_LKASlewY_NmpSec_u4p12[2]	9421
t_LKASlewY_NmpSec_u4p12[3]	9830
t_LKASlewY_NmpSec_u4p12[4]	10240
t_LKASlewY_NmpSec_u4p12[5]	10650
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32 target_TrqArblim_Per1_DampingDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	target_TrqArblim_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9.25		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.850000024		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	102.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-3.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.474999994	0.474999994 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.36195993	2.36195993 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.0999999	-3.0999999 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.88499177	1.88498998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.1400001	-3.1400001 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.62330723	8.62331009 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.23 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.296000004
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.55999994
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0504000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.07999992
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.4000001
k_APAEnableRate_pSec_f32	100.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	9011
t_APADisableRateX_HwNm_u4p12[1]	9421
t_APADisableRateX_HwNm_u4p12[2]	9830
t_APADisableRateX_HwNm_u4p12[3]	10240
t_APADisableRateX_HwNm_u4p12[4]	10650
t_APADisableRateX_HwNm_u4p12[5]	11059
t_APADisableRateY_pSec_u7p9[0]	11264
t_APADisableRateY_pSec_u7p9[1]	11776
t_APADisableRateY_pSec_u7p9[2]	12288
t_APADisableRateY_pSec_u7p9[3]	12800
t_APADisableRateY_pSec_u7p9[4]	13312
t_APADisableRateY_pSec_u7p9[5]	13824
t_APASmoothX_Uls_u2p14[0]	3604
t_APASmoothX_Uls_u2p14[1]	3768
t_APASmoothX_Uls_u2p14[2]	3932
t_APASmoothX_Uls_u2p14[3]	4096
t_APASmoothX_Uls_u2p14[4]	4260
t_APASmoothX_Uls_u2p14[5]	4424
t_APASmoothX_Uls_u2p14[6]	4588
t_APASmoothX_Uls_u2p14[7]	4751

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TrqArblim_Per1

TIQAIDIIII_I EIT			
Name	Input Value		
t_APASmoothX_Uls_u2p14[8]	4915		
t_APASmoothX_Uls_u2p14[9]	5079		
t_APASmoothY_Uls_u2p14[0]	655		
t_APASmoothY_Uls_u2p14[1]	819		
t_APASmoothY_Uls_u2p14[2]	983		
t_APASmoothY_Uls_u2p14[3]	1147		
t_APASmoothY_Uls_u2p14[4]	1311		
t_APASmoothY_Uls_u2p14[5]	1475		
t_APASmoothY_Uls_u2p14[6]	1638		
t_APASmoothY_Uls_u2p14[7]	1802		
t_APASmoothY_Uls_u2p14[8]	1966		
t_APASmoothY_Uls_u2p14[9]	2130		
t_LKASlewX_Kph_u8p8[0]	11264		
t_LKASlewX_Kph_u8p8[1]	11776		
t_LKASlewX_Kph_u8p8[2]	12288		
t_LKASlewX_Kph_u8p8[3]	12800		
t_LKASlewX_Kph_u8p8[4]	13312		
t_LKASlewX_Kph_u8p8[5]	13824		
t_LKASlewY_NmpSec_u4p12[0]	9011		
t_LKASlewY_NmpSec_u4p12[1]	9421		
t_LKASlewY_NmpSec_u4p12[2]	9830		
t_LKASlewY_NmpSec_u4p12[3]	10240		
t_LKASlewY_NmpSec_u4p12[4]	10650		
t_LKASlewY_NmpSec_u4p12[5]	11059		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	T32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.11999989		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64000034		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.20000005		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	109.242996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	2 2000001		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.9000001 0		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value		Esmanted Malus	
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.241999999	0.241999999 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum TraArblim_Hu/TorqueS(V_Hu/Alm_M_Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M_Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_Alm_M)Sta_S(V_Hu_	2 04544000	2 0454204 + 0.02425	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.94511986	2.9451201 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.07999992	-8.07999992 ± 0.00024414062	
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.39400005	2.39400005 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.985007942	0.985008001 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.985007942	0.985008001 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	2.57000003	2 57000002 + 0000200625	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-2.57999992 -2.57999992	-2.57999992 ± 0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

-2.57999992

-0.12113589

0.985007942

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value$

 $target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value$

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

0 ± 0.0000390625

-2.57999992 ± 0.0000390625

-0.121136002 ± 0000390625

0.985008001 ± 0000390625



Fest Step 3.24 (Repeat Count = 1)	Input Value
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32 rqArblim_ESC_State_Cnt_M_enum	0.308999985
rqArblim HwTorqueSV HwNm M Str.SV UIs f32	4.17000008
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
rqArblim_PosSrvoCmd_MtrNm_M_f32	-7.5999999
rqArblim_PreLKATrqReq_HwNm_M_f32	2.9000001
APAEnableRate_pSec_f32	105
	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
z_ESCMax_HwNm_f32	0.239999995
x_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	9421
_APADisableRateX_HwNm_u4p12[1]	9830
_APADisableRateX_HwNm_u4p12[2]	10240
_APADisableRateX_HwNm_u4p12[3]	10650
_APADisableRateX_HwNm_u4p12[4] _APADisableRateX_HwNm_u4p12[5]	11059 11469
APADisableRateY_PSec_u7p9[0]	11776
APADisableRateY pSec u7p9[1]	12288
_APADisableRateY_pSec_u7p9[2]	12800
_APADisableRateY_pSec_u7p9[3]	13312
_APADisableRateY_pSec_u7p9[4]	13824
_APADisableRateY_pSec_u7p9[5]	14336
_APASmoothX_Uls_u2p14[0]	3768
_APASmoothX_Uls_u2p14[1]	3932
_APASmoothX_Uls_u2p14[2]	4096
_APASmoothX_Uls_u2p14[3]	4260
_APASmoothX_Uls_u2p14[4]	4424
_APASmoothX_UIs_u2p14[5]	4588
_APASmoothX_UIs_u2p14[6]	4751
_APASmoothX_Uls_u2p14[7]	4915 5079
_APASmoothX_UIs_u2p14[8] _APASmoothX_UIs_u2p14[9]	5243
_APASmoothY_Uls_u2p14[0]	819
_APASmoothY_Uls_u2p14[1]	983
_APASmoothY_Uls_u2p14[2]	1147
_APASmoothY_Uls_u2p14[3]	1311
_APASmoothY_Uls_u2p14[4]	1475
_APASmoothY_Uls_u2p14[5]	1638
_APASmoothY_Uls_u2p14[6]	1802
_APASmoothY_Uls_u2p14[7]	1966
_APASmoothY_Uls_u2p14[8]	2130
_APASmoothY_Uls_u2p14[9]	2294
_LKASlewX_Kph_u8p8[0]	11776
_LKASlewX_Kph_u8p8[1]	12288
_LKASlewX_Kph_u8p8[2]	12800
_LKASlewX_Kph_u8p8[3] LKASlewX Kph u8p8[4]	13312 13824
_tkASiewx_kpn_u8p8[4] _tKASiewx_kph_u8p8[5]	14336
LKASlewY NmpSec u4p12[0]	9421
_LKASlewY_NmpSec_u4p12[1]	9830
_LKASlewY_NmpSec_u4p12[2]	10240
LKASlewY_NmpSec_u4p12[3]	10650
LKASlewY_NmpSec_u4p12[4]	11059
_LKASlewY_NmpSec_u4p12[5]	11469
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	terret traduline Dard CMOCHOscillate Ctate anum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_lqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph	_f32 target_TrqArblim_Per1_MinSec	ureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqC	0v_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosSer	vEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrv	oCmd_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnE	DDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOsc	Cmd_MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.02999973		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.54999995		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	115.693001		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.4000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.252999991	0.252999991 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.52828002	3.52828002 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-7.5999999	-7.5999999 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.88649678	2.88649988 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.88649678	2.88649678 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.88649678	2.88649678 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.25963926	4.25964022 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.25 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap TrqArblim	target Rte Inst Ap TrqArblim
TrgArblim APADisableRate Uls M f32	0.321999997
TrqArblim_ESC_State_Cnt_M_enum	3
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	4.78000021
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrgArblim PosSrvoCmd MtrNm M f32	-7.11999989
TrgArblim PreLKATrgReg HwNm M f32	3.400001
k APAEnableRate pSec f32	109.5
k_APAUseADmpScale_lgc	1
k APAUseAsstScale lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	9830
t_APADisableRateX_HwNm_u4p12[1]	10240
t_APADisableRateX_HwNm_u4p12[2]	10650
t_APADisableRateX_HwNm_u4p12[3]	11059
t_APADisableRateX_HwNm_u4p12[4]	11469
t_APADisableRateX_HwNm_u4p12[5]	11878
t_APADisableRateY_pSec_u7p9[0]	12288
t_APADisableRateY_pSec_u7p9[1]	12800
t_APADisableRateY_pSec_u7p9[2]	13312
t_APADisableRateY_pSec_u7p9[3]	13824
t_APADisableRateY_pSec_u7p9[4]	14336
t_APADisableRateY_pSec_u7p9[5]	14848
t_APASmoothX_Uls_u2p14[0]	3932
t_APASmoothX_Uls_u2p14[1]	4096
t_APASmoothX_Uls_u2p14[2]	4260
t_APASmoothX_Uls_u2p14[3]	4424
t_APASmoothX_Uls_u2p14[4]	4588
t_APASmoothX_Uls_u2p14[5]	4751

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TIQAIDIIII_FeIT			TOLECTION.
Name	Input Value		
t_APASmoothX_Uls_u2p14[6]	4915		
t_APASmoothX_Uls_u2p14[7]	5079		
t_APASmoothX_Uls_u2p14[8]	5243		
t_APASmoothX_Uls_u2p14[9]	5407		
t_APASmoothY_Uls_u2p14[0]	983		
t_APASmoothY_Uls_u2p14[1]	1147		
t_APASmoothY_Uls_u2p14[2]	1311		
t_APASmoothY_UIs_u2p14[3]	1475		
t_APASmoothY_UIs_u2p14[4]	1638 1802		
t_APASmoothY_Uls_u2p14[5] t_APASmoothY_Uls_u2p14[6]	1966		
t_APASmoothY_Uls_u2p14[7]	2130		
t_APASmoothY_Uls_u2p14[8]	2294		
t_APASmoothY_Uls_u2p14[9]	2458		
t_LKASlewX_Kph_u8p8[0]	12288		
t_LKASlewX_Kph_u8p8[1]	12800		
t_LKASlewX_Kph_u8p8[2]	13312		
t_LKASlewX_Kph_u8p8[3]	13824		
t_LKASlewX_Kph_u8p8[4]	14336		
t_LKASlewX_Kph_u8p8[5]	14848		
t_LKASlewY_NmpSec_u4p12[0]	9830		
t_LKASlewY_NmpSec_u4p12[1]	10240		
t_LKASlewY_NmpSec_u4p12[2]	10650		
t_LKASlewY_NmpSec_u4p12[3]	11059		
t_LKASlewY_NmpSec_u4p12[4]	11469		
t_LKASlewY_NmpSec_u4p12[5]	11878		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_er		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_U target_TrqArblim_Per1_TrqOscCmd_MtrNr	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	-6.15999985	11_132	
target TrqArblim Per1 ESCState State enum.value	4		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target TrgArblim Per1 HwTorque HwNm f32.value	-7.42000008		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.8999998		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target TrgArblim Per1 MinSecureVehicleSpeed Kph f32.value	122.142998		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	-2.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.4000001		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.541000009	0.541000009 ± 0000390625	
TrgArblim ESC State Cnt M enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.11144018	4.11144018 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.5	-2.5 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.39400005	3.39400005 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.918836772	0.918837011 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.918836772	0.918837011 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
	3	3 ± 0.0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value			
	3	3 ± 0.0000390625	
target_TrqArblim_Perl_IqTrqOv_HWNIn_I32.value target_TrqArblim_Perl_LKATorqueDelivered_HwNm_f32.value target_TrqArblim_Perl_OpTrqOv_MtrNm_f32.value	3 -4.60290813	3 ± 0.0000390625 -4.60291004 ± 0000390625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	~

Test Step 3.26 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.335000008
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.38999987
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.63999987
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.9000001
k_APAEnableRate_pSec_f32	114
<_APAUseADmpScale_lgc	0
<_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.75
<_LKAUseSlewCal_lgc	0
APADisableRateX_HwNm_u4p12[0]	10240
APADisableRateX HwNm u4p12[1]	10650
	11059
t_APADisableRateX_HwNm_u4p12[3]	11469
	11878
	12288
t_APADisableRateY_pSec_u7p9[0]	12800
t APADisableRateY pSec u7p9[1]	13312
t_APADisableRateY_pSec_u7p9[2]	13824
t APADisableRateY pSec u7p9[3]	14336
t_APADisableRateY_pSec_u7p9[4]	14848
t APADisableRateY pSec u7p9[5]	15360
t_APASmoothX_Uls_u2p14[0]	4096
APASmoothX_UIs_u2p14[1]	4260
t_APASmoothX_Uls_u2p14[2]	4424
t_APASmoothX_Uls_u2p14[3]	4588
	4751
t_APASmoothX_Uls_u2p14[4] t_APASmoothX_Uls_u2p14[5]	4915
	5079
t_APASmoothX_Uls_u2p14[6]	5243
t_APASmoothX_Uls_u2p14[7]	5407
t_APASmoothX_Uls_u2p14[8]	
t_APASmoothX_Uls_u2p14[9]	5571
t_APASmoothY_Uls_u2p14[0]	1147
_APASmoothY_Uls_u2p14[1]	1311
t_APASmoothY_Uls_u2p14[2]	1475
t_APASmoothY_Uls_u2p14[3]	1638
t_APASmoothY_Uls_u2p14[4]	1802
t_APASmoothY_Uls_u2p14[5]	1966
t_APASmoothY_Uls_u2p14[6]	2130
t_APASmoothY_Uls_u2p14[7]	2294
t_APASmoothY_Uls_u2p14[8]	2458
_APASmoothY_Uls_u2p14[9]	2621
_LKASlewX_Kph_u8p8[0]	12800
:_LKASlewX_Kph_u8p8[1]	13312
LKASlewX_Kph_u8p8[2]	13824
:_LKASlewX_Kph_u8p8[3]	14336
:_LKASlewX_Kph_u8p8[4]	14848
LKASlewX_Kph_u8p8[5]	15360
:_LKASlewY_NmpSec_u4p12[0]	10240
_LKASlewY_NmpSec_u4p12[1]	10650
LKASlewY_NmpSec_u4p12[2]	11059
_LKASlewY_NmpSec_u4p12[3]	11469
t_LKASlewY_NmpSec_u4p12[4]	11878
t_LKASlewY_NmpSec_u4p12[5]	12288
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
$target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.67999983		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.80999994		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.25		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.593002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-2.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.209999993		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.563000023	0.563000023 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.69460011	4.69460011 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.29999995	-2.29999995 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.88800168	3.88800001 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.00285077095	0.00285077002 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.27 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.34799999
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.4000001
k_APAEnableRate_pSec_f32	118.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	10650
t_APADisableRateX_HwNm_u4p12[1]	11059
t_APADisableRateX_HwNm_u4p12[2]	11469
t_APADisableRateX_HwNm_u4p12[3]	11878
t_APADisableRateX_HwNm_u4p12[4]	12288
t_APADisableRateX_HwNm_u4p12[5]	12698
t_APADisableRateY_pSec_u7p9[0]	13312
t_APADisableRateY_pSec_u7p9[1]	13824
t_APADisableRateY_pSec_u7p9[2]	14336
t_APADisableRateY_pSec_u7p9[3]	14848
t_APADisableRateY_pSec_u7p9[4]	15360

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	15872		
t_APASmoothX_Uls_u2p14[0]	4260		
t_APASmoothX_Uls_u2p14[1]	4424		
t_APASmoothX_Uls_u2p14[2]	4588		
t_APASmoothX_Uls_u2p14[3]	4751		
t_APASmoothX_Uls_u2p14[4]	4915		
t_APASmoothX_Uls_u2p14[5]	5079		
t_APASmoothX_Uls_u2p14[6]	5243		
t_APASmoothX_Uls_u2p14[7]	5407		
t_APASmoothX_Uls_u2p14[8]	5571		
t_APASmoothX_Uls_u2p14[9]	5734		
t_APASmoothY_Uls_u2p14[0]	1311		
t_APASmoothY_Uls_u2p14[1]	1475 1638		
t_APASmoothY_Uls_u2p14[2]	1802		
t_APASmoothY_Uls_u2p14[3] t_APASmoothY_Uls_u2p14[4]	1966		
t_APASmoothY_UIs_u2p14[5]	2130		
t_APASmoothY_Uls_u2p14[6]	2294		
t_APASmoothY_Uls_u2p14[7]	2458		
t_APASmoothY_Uls_u2p14[8]	2621		
t_APASmoothY_Uls_u2p14[9]	2785		
t LKASlewX Kph u8p8[0]	13312		
t_LKASlewX_Kph_u8p8[1]	13824		
t_LKASlewX_Kph_u8p8[2]	14336		
t_LKASlewX_Kph_u8p8[3]	14848		
t_LKASlewX_Kph_u8p8[4]	15360		
t_LKASlewX_Kph_u8p8[5]	15872		
t_LKASlewY_NmpSec_u4p12[0]	10650		
t_LKASlewY_NmpSec_u4p12[1]	11059		
t_LKASlewY_NmpSec_u4p12[2]	11469		
t_LKASlewY_NmpSec_u4p12[3]	11878		
t_LKASlewY_NmpSec_u4p12[4]	12288		
t_LKASlewY_NmpSec_u4p12[5]	12698		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32$			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3:	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.19999981		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.5999999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	135.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.0999999 0.560000002		
		l=	l
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.285999984	0.286000013 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV/_HwMm_M_Str_SV/_Hip_f23	1 5 27776002	1 5 27776002 + 0 02125	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.27776003	5.27776003 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.15999985	-6.15999985 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.39379978	4.39379978 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_UIs_f32.value	0.969713748	0.969713986 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	1 -1.27999997 ± 0000390625	
target_frqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.000390625	
targot_rightbillit_r or i_iq riqOv_riwiviii_i32.value	-1.21 333331	-1.2133331 I U.UUUU33U023	

0

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value$

0 ± 0.0000390625





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.186563313	-0.186563 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.969713748	0.969713986 ± 0000390625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.28 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.361000001
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6.61000013
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0614
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.67999983
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.9000001
<_APAEnableRate_pSec_f32	123
<_APAUseADmpScale_lgc	0
c_APAUseAsstScale_lgc	0
<pre>c_APAUseRetScale_lgc</pre>	0
c_ESCMax_HwNm_f32	3.69000006
_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	11059
_APADisableRateX_HwNm_u4p12[1]	11469
_APADisableRateX_HwNm_u4p12[2]	11878
_APADisableRateX_HwNm_u4p12[3]	12288
_APADisableRateX_HwNm_u4p12[4]	12698
_APADisableRateX_HwNm_u4p12[5]	13107
_APADisableRateY_pSec_u7p9[0]	13824
_APADisableRateY_pSec_u7p9[1]	14336
_APADisableRateY_pSec_u7p9[2]	14848
_APADisableRateY_pSec_u7p9[3]	15360
_APADisableRateY_pSec_u7p9[4]	15872
_APADisableRateY_pSec_u7p9[5]	16384
_APASmoothX_Uls_u2p14[0]	4424
_APASmoothX_Uls_u2p14[1]	4588
_APASmoothX_Uls_u2p14[2]	4751
_APASmoothX_Uls_u2p14[3]	4915
_APASmoothX_Uls_u2p14[4]	5079
_APASmoothX_Uls_u2p14[5]	5243
_APASmoothX_Uls_u2p14[6]	5407
_APASmoothX_Uls_u2p14[7]	5571
_APASmoothX_Uls_u2p14[8]	5734
_APASmoothX_Uls_u2p14[9]	5898
_APASmoothY_Uls_u2p14[0]	1475
_APASmoothY_Uls_u2p14[1]	1638
_APASmoothY_Uls_u2p14[2]	1802
APASmoothY Uls u2p14[3]	1966
_APASmoothY_Uls_u2p14[4]	2130
_APASmoothY_Uls_u2p14[5]	2294
_APASmoothY_Uls_u2p14[6]	2458
APASmoothY Uls u2p14[7]	2621
_APASmoothY_Uls_u2p14[8]	2785
_APASmoothY_Uls_u2p14[9]	2949
LKASlewX_Kph_u8p8[0]	13824
_LKASlewX_Kph_u8p8[1]	14336
_LKASlewX_Kph_u8p8[2]	14848
_LKASlewX_Kph_u8p8[3]	15360
_LKASlewX_Kph_u8p8[4]	15872
_LKASlewX_Kph_u8p8[5]	16384
_LKASlewY_NmpSec_u4p12[0]	11059
_LKASlewY_NmpSec_u4p12[1]	11469
	11878
_LKASlewY_NmpSec_u4p12[2]	
:_LKASlewY_NmpSec_u4p12[3] : LKASlewY_NmpSec_u4p12[4]	12288 12698
t_LKASlewY_NmpSec_u4p12[5]	13107
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32 target_TrqArblim_Per1_DampingDDFactor_UIs_f32





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_	enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelive	ered_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicle	eSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm	_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_0	Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtr	rNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_	Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	Nm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979			
target_TrqArblim_Per1_ESCState_State_enum.value	2			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.59000015			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.95000005			
target_TrqArblim_Per1_LKAState_State_enum.value	2			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	141.492996			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.89999998			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.910000026			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.606999993	0.606999993 ± 0000390625	•	
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.86091995	5.86091995 ± 0.03125	•	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.89999998	-1.89999998 ± 0.000244140625	·	
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.88950682	4.88951015 ± 0.03125	•	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•	
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	•	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.69000006	-3.69000006 ± 0000390625	-	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	-	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	•

-0.207585543

-0.207586005 ± 0000390625

1 ± 0000390625

Test Step 3.29 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.374000013
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.21999979
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0636000037
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.19999981
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.4000001
k_APAEnableRate_pSec_f32	127.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	11469
t_APADisableRateX_HwNm_u4p12[1]	11878
t_APADisableRateX_HwNm_u4p12[2]	12288
t_APADisableRateX_HwNm_u4p12[3]	12698
t_APADisableRateX_HwNm_u4p12[4]	13107
t_APADisableRateX_HwNm_u4p12[5]	13517
t_APADisableRateY_pSec_u7p9[0]	14336
t_APADisableRateY_pSec_u7p9[1]	14848

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	15360		
t_APADisableRateY_pSec_u7p9[3]	15872		
t_APADisableRateY_pSec_u7p9[4]	16384		
t_APADisableRateY_pSec_u7p9[5]	16896		
t_APASmoothX_Uls_u2p14[0]	4588		
t_APASmoothX_Uls_u2p14[1]	4751		
t_APASmoothX_Uls_u2p14[2]	4915		
t_APASmoothX_Uls_u2p14[3]	5079		
t_APASmoothX_Uls_u2p14[4]	5243		
t_APASmoothX_Uls_u2p14[5] t_APASmoothX_Uls_u2p14[6]	5407 5571		
t_APASmoothX_Uls_u2p14[7]	5734		
t_APASmoothX_Uls_u2p14[8]	5898		
t_APASmoothX_Uls_u2p14[9]	6062		
t_APASmoothY_Uls_u2p14[0]	1638		
t_APASmoothY_Uls_u2p14[1]	1802		
t_APASmoothY_Uls_u2p14[2]	1966		
t_APASmoothY_Uls_u2p14[3]	2130		
t_APASmoothY_Uls_u2p14[4]	2294		
t_APASmoothY_Uls_u2p14[5]	2458		
t_APASmoothY_Uls_u2p14[6]	2621		
t_APASmoothY_Uls_u2p14[7] t_APASmoothY_Uls_u2p14[8]	2785 2949		
t_APASmoothY_Uls_u2p14[9]	3113		
t_LKASlewX_Kph_u8p8[0]	14336		
t_LKASlewX_Kph_u8p8[1]	14848		
t_LKASlewX_Kph_u8p8[2]	15360		
t_LKASlewX_Kph_u8p8[3]	15872		
t_LKASlewX_Kph_u8p8[4]	16384		
t_LKASlewX_Kph_u8p8[5]	16896		
t_LKASlewY_NmpSec_u4p12[0]	11469		
t_LKASlewY_NmpSec_u4p12[1]	11878		
t_LKASlewY_NmpSec_u4p12[2]	12288		
t_LKASlewY_NmpSec_u4p12[3] t_LKASlewY_NmpSec_u4p12[4]	12698 13107		
t_LKASlewY_NmpSec_u4p12[5]	13517		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_en target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target Rte Inst Ap TrgArblim.TrgArblim Per1 OpTrgOv MtrNm f32	target TrgArblim Per1 OpTrgOv MtrNm f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	4 0000000		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-4.98000002 3.29999995		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value	3.29999995		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.942993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.25999999		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.308000028	0.307999998 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	3	3	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6.44407988	6.44407988 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-5.19999981	-5.19999981 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.39340019	5.39340019 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.960578859	0.960578978 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.960578859	0.960578978 ± 0000390625	•

0

0

TrqArblim_Per1



Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.20498994	-0.20499 ± 0000390625	~
target TrgArblim Per1 ReturnDDFactor Uls f32.value	0.960578859	0.960578978 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.30 (Repeat Count = 1)	Input Value
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.386999995
rqArblim_ESC_State_Cnt_M_enum	3
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.82999992
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0658000037
rqArblim_PosSrvoCmd_MtrNm_M_f32	-4.71999979
FrqArblim_PreLKATrqReq_HwNm_M_f32	5.9000001
x_APAEnableRate_pSec_f32	132
x_APAUseADmpScale_lgc	0
<pre>x_APAUseAsstScale_lgc</pre>	0
x_APAUseRetScale_lgc	0
ESCMax_HwNm_f32	1.23000002
x_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	11878
_APADisableRateX_HwNm_u4p12[1]	12288
_APADisableRateX_HwNm_u4p12[2]	12698
_APADisableRateX_HwNm_u4p12[3]	13107
_APADisableRateX_HwNm_u4p12[4]	13517
_APADisableRateX_HwNm_u4p12[5]	13926
_APADisableRateY_pSec_u7p9[0]	14848
_APADisableRateY_pSec_u7p9[1]	15360
_APADisableRateY_pSec_u7p9[2]	15872
_APADisableRateY_pSec_u7p9[3]	16384
_APADisableRateY_pSec_u7p9[4]	16896
_APADisableRateY_pSec_u7p9[5]	17408
_APASmoothX_Uls_u2p14[0]	4751
_APASmoothX_Uls_u2p14[1]	4915
_APASmoothX_Uls_u2p14[2]	5079
_APASmoothX_Uls_u2p14[3]	5243
_APASmoothX_Uls_u2p14[4]	5407
_APASmoothX_Uls_u2p14[5]	5571
_APASmoothX_Uls_u2p14[6]	5734
_APASmoothX_Uls_u2p14[7]	5898
_APASmoothX_Uls_u2p14[8]	6062
_APASmoothX_Uls_u2p14[9]	6226
_APASmoothY_Uls_u2p14[0]	1802
_APASmoothY_Uls_u2p14[1]	1966
_APASmoothY_Uls_u2p14[2]	2130
_APASmoothY_Uls_u2p14[3]	2294
_APASmoothY_Uls_u2p14[4]	2458
_APASmoothY_Uls_u2p14[5]	2621
_APASmoothY_Uls_u2p14[6]	2785
_APASmoothY_Uls_u2p14[7]	2949
_APASmoothY_Uls_u2p14[8]	3113
_APASmoothY_Uls_u2p14[9]	3277
_LKASlewX_Kph_u8p8[0]	14848
_LKASlewX_Kph_u8p8[1]	15360
_LKASlewX_Kph_u8p8[2]	15872
_LKASlewX_Kph_u8p8[3]	16384
_LKASlewX_Kph_u8p8[4]	16896
_LKASlewX_Kph_u8p8[5]	17408
_LKASlewY_NmpSec_u4p12[0]	11878
_LKASlewY_NmpSec_u4p12[1]	12288
_LKASlewY_NmpSec_u4p12[2]	12698
_LKASlewY_NmpSec_u4p12[3]	13107
_LKASlewY_NmpSec_u4p12[4]	13517

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Name	Input Value		
t_LKASlewY_NmpSec_u4p12[5]	13926		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_Si	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	!	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	n_f32	
target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-4.36999989		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	3.6500001		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	154.393005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.61000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.651000023	0.651000023 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.0272398	7.0272398 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.5	-1.5 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.89101171	5.89100981 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.41468811	1.41469002 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.31 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.40000006	
TrqArblim_ESC_State_Cnt_M_enum	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.43999958	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0680000037	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-4.23999977	
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.4000001	
k_APAEnableRate_pSec_f32	136.5	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	3.24000001	
k_LKAUseSlewCal_lgc	1	
t_APADisableRateX_HwNm_u4p12[0]	12288	
t_APADisableRateX_HwNm_u4p12[1]	12698	
t_APADisableRateX_HwNm_u4p12[2]	13107	
t_APADisableRateX_HwNm_u4p12[3]	13517	
t APADisableRateX HwNm u4p12[4]	13926	





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	14336		
t_APADisableRateY_pSec_u7p9[0]	15360		
t_APADisableRateY_pSec_u7p9[1]	15872		
t_APADisableRateY_pSec_u7p9[2]	16384		
t_APADisableRateY_pSec_u7p9[3]	16896		
t_APADisableRateY_pSec_u7p9[4]	17408 17920		
t_APADisableRateY_pSec_u7p9[5] t_APASmoothX_Uls_u2p14[0]	4915		
t_APASmoothX_Uls_u2p14[1]	5079		
t_APASmoothX_Uls_u2p14[1]	5243		
t_APASmoothX_Uls_u2p14[3]	5407		
t_APASmoothX_Uls_u2p14[4]	5571		
t APASmoothX Uls u2p14[5]	5734		
t_APASmoothX_Uls_u2p14[6]	5898		
t_APASmoothX_Uls_u2p14[7]	6062		
t APASmoothX Uls u2p14[8]	6226		
t_APASmoothX_UIs_u2p14[9]	6390		
t_APASmoothY_Uls_u2p14[0]	1966		
t_APASmoothY_Uls_u2p14[1]	2130		
t_APASmoothY_Uls_u2p14[2]	2294		
t_APASmoothY_Uls_u2p14[3]	2458		
t_APASmoothY_Uls_u2p14[4]	2621		
t_APASmoothY_Uls_u2p14[5]	2785		
t_APASmoothY_Uls_u2p14[6]	2949		
t_APASmoothY_Uls_u2p14[7]	3113		
t_APASmoothY_Uls_u2p14[8]	3277		
t_APASmoothY_Uls_u2p14[9]	3441		
t_LKASlewX_Kph_u8p8[0]	15360		
t_LKASlewX_Kph_u8p8[1]	15872		
t_LKASlewX_Kph_u8p8[2]	16384		
t_LKASlewX_Kph_u8p8[3]	16896		
t_LKASlewX_Kph_u8p8[4]	17408		
t_LKASlewX_Kph_u8p8[5]	17920		
t_LKASlewY_NmpSec_u4p12[0]	12288		
t_LKASlewY_NmpSec_u4p12[1]	12698		
t_LKASlewY_NmpSec_u4p12[2]	13107		
t_LKASlewY_NmpSec_u4p12[3]	13517		
t_LKASlewY_NmpSec_u4p12[4]	13926		
t_LKASlewY_NmpSec_u4p12[5]	14336		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target TrqArblim Per1 DampingDDFactor	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.82000017		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-3.75999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	160.843002		
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.96000004		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.330000013	0.330000013 ± 0000390625	~
	0.0000000		
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_ESC_State_Cnt_M_enum TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32		0 7.6104002 ± 0.03125	
	0		, , , , , , , , , , , , , , , , , , ,





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.950512052	0.950511992 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.950512052	0.950511992 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.209828764	-0.209829003 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.950512052	0.950511992 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Test Step 3.32 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.412999988
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrgArblim HwTorqueSV HwNm M Str.K Uls f32	0.0702000037
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.75999999
TrgArblim_PreLKATrgReg_HwNm_M_f32	6.9000001
k_APAEnableRate_pSec_f32	141
 k_APAUseADmpScale_lgc	0
k APAUseAsstScale lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	12698
t_APADisableRateX_HwNm_u4p12[1]	13107
t_APADisableRateX_HwNm_u4p12[2]	13517
t_APADisableRateX_HwNm_u4p12[3]	13926
t_APADisableRateX_HwNm_u4p12[4]	14336
t APADisableRateX HwNm u4p12[5]	14746
t_APADisableRateY_pSec_u7p9[0]	15872
t_APADisableRateY_pSec_u7p9[1]	16384
t_APADisableRateY_pSec_u7p9[2]	16896
t_APADisableRateY_pSec_u7p9[3]	17408
t_APADisableRateY_pSec_u7p9[4]	17920
t_APADisableRateY_pSec_u7p9[5]	18432
t_APASmoothX_Uls_u2p14[0]	5079
t_APASmoothX_Uls_u2p14[1]	5243
t_APASmoothX_Uls_u2p14[2]	5407
t_APASmoothX_Uls_u2p14[3]	5571
t_APASmoothX_Uls_u2p14[4]	5734
t_APASmoothX_Uls_u2p14[5]	5898
t_APASmoothX_Uls_u2p14[6]	6062
t_APASmoothX_Uls_u2p14[7]	6226
t_APASmoothX_Uls_u2p14[8]	6390
t_APASmoothX_Uls_u2p14[9]	6554
t_APASmoothY_Uls_u2p14[0]	2130
t_APASmoothY_Uls_u2p14[1]	2294
t_APASmoothY_Uls_u2p14[2]	2458
t_APASmoothY_Uls_u2p14[3]	2621
t APASmoothY Uls u2p14[4]	2785
t_APASmoothY_Uls_u2p14[5]	2949
t_APASmoothY_Uls_u2p14[6]	3113
t APASmoothY Uls u2p14[7]	3277
t_APASmoothY_Uls_u2p14[8]	3441
t_APASmoothY_Uls_u2p14[9]	3604
t_LKASlewX_Kph_u8p8[0]	15872
t_LKASlewX_Kph_u8p8[1]	16384
t_LKASlewX_Kph_u8p8[2]	16896
t LKASlewX Kph u8p8[3]	17408
t_LKASlewX_Kph_u8p8[4]	17920
t_LKASlewX_Kph_u8p8[5]	18432
t_LKASlewY_NmpSec_u4p12[0]	12698
t_LKASlewY_NmpSec_u4p12[1]	13107
	10101

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Name	Input Value		
t_LKASlewY_NmpSec_u4p12[2]	13517		
t_LKASlewY_NmpSec_u4p12[3]	13926		
t_LKASlewY_NmpSec_u4p12[4]	14336		
t_LKASlewY_NmpSec_u4p12[5]	14746		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_L	lls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFacto	r_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cn	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_6	enum	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_ESCTorqueDelive	red_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDeliver	red_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtr	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_I	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	m_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.61999989		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-3.1500001		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.3499999		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-1.10000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.30999994		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.694999993	0.694999993 ± 0000390625	-
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.19355965	8.19355965 ± 0.03125	-
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.10000002	-1.10000002 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.89251661	6.89251995 ± 0.03125	-
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	-
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.58000004	-1.58000004 ± 0000390625	-
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.58000004	-1.58000004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	-
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.14183235	2.14182997 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.33 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.425999999	
TrqArblim_ESC_State_Cnt_M_enum	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.65999985	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0724000037	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.27999997	
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.4000001	
k_APAEnableRate_pSec_f32	145.5	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	4.28000021	
k_LKAUseSlewCal_lgc	1	
t_APADisableRateX_HwNm_u4p12[0]	13107	
t_APADisableRateX_HwNm_u4p12[1]	13517	





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	13926		
t_APADisableRateX_HwNm_u4p12[3]	14336		
t_APADisableRateX_HwNm_u4p12[4]	14746		
t_APADisableRateX_HwNm_u4p12[5]	15155		
t_APADisableRateY_pSec_u7p9[0]	16384		
t_APADisableRateY_pSec_u7p9[1]	16896		
t_APADisableRateY_pSec_u7p9[2]	17408 17920		
t_APADisableRateY_pSec_u7p9[3] t_APADisableRateY_pSec_u7p9[4]	18432		
t_APADisableRateY_pSec_u7p9[5]	18944		
t_APASmoothX_UIs_u2p14[0]	5243		
t_APASmoothX_Uls_u2p14[1]	5407		
t APASmoothX Uls u2p14[2]	5571		
t_APASmoothX_Uls_u2p14[3]	5734		
t_APASmoothX_Uls_u2p14[4]	5898		
t_APASmoothX_Uls_u2p14[5]	6062		
t_APASmoothX_Uls_u2p14[6]	6226		
t_APASmoothX_Uls_u2p14[7]	6390		
t_APASmoothX_Uls_u2p14[8]	6554		
t_APASmoothX_Uls_u2p14[9]	6717		
t_APASmoothY_Uls_u2p14[0]	2294		
t_APASmoothY_Uls_u2p14[1]	2458		
t_APASmoothY_Uls_u2p14[2]	2621		
t_APASmoothY_Uls_u2p14[3]	2785		
t_APASmoothY_Uls_u2p14[4]	2949		
t_APASmoothY_Uls_u2p14[5]	3113		
t_APASmoothY_UIs_u2p14[6]	3277		
t_APASmoothY_UIs_u2p14[7]	3441		
t_APASmoothY_UIs_u2p14[8]	3604 3768		
t_APASmoothY_Uls_u2p14[9] t_LKASlewX_Kph_u8p8[0]	16384		
t_LKASlewX_Kph_u8p8[1]	16896		
t_LKASlewX_Kph_u8p8[2]	17408		
t_LKASlewX_Kph_u8p8[3]	17920		
t_LKASlewX_Kph_u8p8[4]	18432		
t_LKASlewX_Kph_u8p8[5]	18944		
t_LKASlewY_NmpSec_u4p12[0]	13107		
t_LKASlewY_NmpSec_u4p12[1]	13517		
t_LKASlewY_NmpSec_u4p12[2]	13926		
t_LKASlewY_NmpSec_u4p12[3]	14336		
t_LKASlewY_NmpSec_u4p12[4]	14746		
t_LKASlewY_NmpSec_u4p12[5]	15155		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lo		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enutarget TrqArblim Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_Sta		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm f3	_	
target_Rte_inst_Ap_1rqArbiim.1rqArbiim_Per1_mw1orque_mwnin_is2 target_Rte_inst_Ap_1rqArbiim.TrqArbiim_Per1_lqTrqOv_HwNm_f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.42000008		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-2.53999996		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.6999981		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	173.742996		
	0		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.899999976		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976 2.66000009		-
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.899999976	Expected Value 0.351999998 ± 0000390625	Resu





Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.77672005	8.77672005 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-3.27999997	-3.27999997 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.39260006	7.39260006 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.939457059	0.939456999 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.939457059	0.939456999 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4.28000021	-4.28000021 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.198580936	-0.198580995 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.939457059	0.939456999 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim APADisableRate Uls M f32	0.43900001
FrqArblim_ESC_State_Cnt_M_enum	2
rgArblim HwTorqueSV HwNm M Str.SV Uls f32	-10
	0.0746000037
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-2.79999995
FrqArblim_PosSrvoCmd_MtrNm_M_f32	7.9000001
rqArblim_PreLKATrqReq_HwNm_M_f32	150
APALlas Appresals Jas	0
APAUseADmpScale_lgc	
_APAUseAsstScale_lgc	0
APAUseRetScale_lgc	0
ESCMax_HwNm_f32	3.25
x_LKAUseSlewCal_lgc ABABisable BataY, Llochtra and 4000.	0
_APADisableRateX_HwNm_u4p12[0]	13517
_APADisableRateX_HwNm_u4p12[1]	13926
_APADisableRateX_HwNm_u4p12[2]	14336
_APADisableRateX_HwNm_u4p12[3]	14746
_APADisableRateX_HwNm_u4p12[4]	15155
_APADisableRateX_HwNm_u4p12[5]	15565
_APADisableRateY_pSec_u7p9[0]	16896
_APADisableRateY_pSec_u7p9[1]	17408
_APADisableRateY_pSec_u7p9[2]	17920
_APADisableRateY_pSec_u7p9[3]	18432
_APADisableRateY_pSec_u7p9[4]	18944
_APADisableRateY_pSec_u7p9[5]	19456
_APASmoothX_Uls_u2p14[0]	5407
_APASmoothX_Uls_u2p14[1]	5571
_APASmoothX_Uls_u2p14[2]	5734
_APASmoothX_Uls_u2p14[3]	5898
_APASmoothX_Uls_u2p14[4]	6062
_APASmoothX_Uls_u2p14[5]	6226
_APASmoothX_Uls_u2p14[6]	6390
_APASmoothX_Uls_u2p14[7]	6554
_APASmoothX_Uls_u2p14[8]	6717
_APASmoothX_Uls_u2p14[9]	6881
_APASmoothY_Uls_u2p14[0]	2458
_APASmoothY_Uls_u2p14[1]	2621
_APASmoothY_Uls_u2p14[2]	2785
_APASmoothY_Uls_u2p14[3]	2949
_APASmoothY_Uls_u2p14[4]	3113
_APASmoothY_Uls_u2p14[5]	3277
_APASmoothY_Uls_u2p14[6]	3441
_APASmoothY_Uls_u2p14[7]	3604
_APASmoothY_Uls_u2p14[8]	3768
_APASmoothY_Uls_u2p14[9]	3932
_ LKASlewX_Kph_u8p8[0]	16896
LKASlewX_Kph_u8p8[1]	17408
_LKASlewX_Kph_u8p8[2]	17920
LKASlewX_Kph_u8p8[3]	18432
LKASlewX_Kph_u8p8[4]	18944

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Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	19456		
t_LKASlewY_NmpSec_u4p12[0]	13517		
t_LKASlewY_NmpSec_u4p12[1]	13926		
t_LKASlewY_NmpSec_u4p12[2]	14336		
t_LKASlewY_NmpSec_u4p12[3]	14746		
t_LKASlewY_NmpSec_u4p12[4]	15155		
t_LKASlewY_NmpSec_u4p12[5]	15565		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.21999979		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-1.92999995		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.05000019		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	180.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.69999988		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.00999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.363000005	0.363000005 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9.39797783	-9.39797974 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.79999995	-2.79999995 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.89400005	7.89400005 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.185984328	-0.185984001 ± 0000390625	✓
	T.	1	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

1 ± 0000390625

Test Step 3.35 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.451999992	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0768000036	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-2.31999993	
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.39999962	
k_APAEnableRate_pSec_f32	154.5	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	3.1400001	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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Name	Input Value
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	13926
t_APADisableRateX_HwNm_u4p12[1]	14336
t_APADisableRateX_HwNm_u4p12[2]	14746
t_APADisableRateX_HwNm_u4p12[3]	15155
:_APADisableRateX_HwNm_u4p12[4]	15565
_APADisableRateX_HwNm_u4p12[5]	15974
_APADisableRateY_pSec_u7p9[0]	17408
_APADisableRateY_pSec_u7p9[1]	17920
_APADisableRateY_pSec_u7p9[2]	18432
_APADisableRateY_pSec_u7p9[3]	18944 19456
_APADisableRateY_pSec_u7p9[4]	19968
_APADisableRateY_pSec_u7p9[5] _APASmoothX_Uls_u2p14[0]	5571
_APASmoothX_Uls_u2p14[0]	5734
_APASmoothX_Uls_u2p14[1]	5898
_APASmoothX_Uls_u2p14[3]	6062
_APASmoothX_Uls_u2p14[4]	6226
_APASmoothX_UIs_u2p14[5]	6390
APASmoothX Uls u2p14[6]	6554
_APASmoothX_Uls_u2p14[7]	6717
_APASmoothX_Uls_u2p14[8]	6881
APASmoothX_Uls_u2p14[9]	7045
_APASmoothY_Uls_u2p14[0]	2621
_APASmoothY_Uls_u2p14[1]	2785
_APASmoothY_Uls_u2p14[2]	2949
_APASmoothY_Uls_u2p14[3]	3113
_APASmoothY_Uls_u2p14[4]	3277
_APASmoothY_Uls_u2p14[5]	3441
_APASmoothY_Uls_u2p14[6]	3604
_APASmoothY_Uls_u2p14[7]	3768
_APASmoothY_Uls_u2p14[8]	3932
_APASmoothY_Uls_u2p14[9]	4096
_LKASlewX_Kph_u8p8[0]	17408
:_LKASlewX_Kph_u8p8[1]	17920 18432
:_LKASlewX_Kph_u8p8[2]	18944
:_LKASlewX_Kph_u8p8[3] :_LKASlewX_Kph_u8p8[4]	19456
_LKASlewX_Kph_u8p8[5]	19968
_LKASlewY_NmpSec_u4p12[0]	13926
traceurnmpees_u4p12[1]	14336
: LKASlewY NmpSec u4p12[2]	14746
_LKASlewY_NmpSec_u4p12[3]	15155
_LKASlewY_NmpSec_u4p12[4]	15565
LKASlewY_NmpSec_u4p12[5]	15974
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 -3.01999998
arget_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.01999998 4
arget_TrqArblim_Per1_ESCState_State_enum.value arget_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
arget_I rqArbiim_Per1_GMOSHOscillate_State_enum.value arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	-1.32000005
arget_IrqArbiim_Per1_HW1orque_HWNm_f32.value arget_TrqArblim_Per1_LKACmd_HwNm_f32.value	-1.32000005 5.4000001
target_TrqArblim_Per1_LKACtrid_rwivin_isz.value target_TrqArblim_Per1_LKAState_State_enum.value	1
target_TrqArblim_Per1_EKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	186.643005
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1
target_TrqArblim_Per1_PosServeTrable_Ont_igc.value	-0.5
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.3599999

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Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.761000037	0.760999978 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.13062382	9.13062 ± 0.03125	_
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.5	-0.5 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.39219952	8.39220047 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	_
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0951250046	-0.0951249972 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.809749961	0.809750021 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
FrqArblim_APADisableRate_Uls_M_f32	0.465000004	
FrgArblim_ESC_State_Cnt_M_enum	4	
FrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	
FrgArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0790000036	
rqArblim_PosSrvoCmd_MtrNm_M_f32	-1.84000003	
rqArblim_PreLKATrqReq_HwNm_M_f32	8.69999981	
APAEnableRate pSec f32	159	
APAUseADmpScale Igc	0	
_APAUseAsstScale_lgc	0	
APAUseRetScale Igc	0	
ESCMax HwNm f32	2.57999992	
_ESCMAX_HWNII_IS2 LKAUseSlewCal lgc	0	
	· ·	
_APADisableRateX_HwNm_u4p12[0]	14336	
APADisableRateX_HwNm_u4p12[1] APADisableRateX_HwNm_u4p12[2]	14746 15155	
_APADisableRateX_HwNm_u4p12[3]	15565	
_APADisableRateX_HwNm_u4p12[4]	15974	
_APADisableRateX_HwNm_u4p12[5]	16384	
APADisableRateY_pSec_u7p9[0]	17920	
_APADisableRateY_pSec_u7p9[1]	18432	
_APADisableRateY_pSec_u7p9[2]	18944	
_APADisableRateY_pSec_u7p9[3]	19456	
_APADisableRateY_pSec_u7p9[4]	19968	
_APADisableRateY_pSec_u7p9[5]	20480	
_APASmoothX_UIs_u2p14[0]	5734	
_APASmoothX_Uls_u2p14[1]	5898	
_APASmoothX_Uls_u2p14[2]	6062	
_APASmoothX_Uls_u2p14[3]	6226	
_APASmoothX_Uls_u2p14[4]	6390	
_APASmoothX_Uls_u2p14[5]	6554	
_APASmoothX_Uls_u2p14[6]	6717	
_APASmoothX_Uls_u2p14[7]	6881	
_APASmoothX_Uls_u2p14[8]	7045	
_APASmoothX_Uls_u2p14[9]	7209	
_APASmoothY_Uls_u2p14[0]	2785	
_APASmoothY_Uls_u2p14[1]	2949	
_APASmoothY_Uls_u2p14[2]	3113	
_APASmoothY_Uls_u2p14[3]	3277	
_APASmoothY_Uls_u2p14[4]	3441	
_APASmoothY_Uls_u2p14[5]	3604	
_APASmoothY_Uls_u2p14[6]	3768	
_APASmoothY_Uls_u2p14[7]	3932	
_APASmoothY_Uls_u2p14[8]	4096	
_APASmoothY_Uls_u2p14[9]	4260	
_LKASlewX_Kph_u8p8[0]	17920	
_LKASlewX_Kph_u8p8[1]	18432	
_LKASlewX_Kph_u8p8[2]	18944	

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TrqArblim_Per1		•	VALCITAGE
Name	Input Value		
t_LKASlewX_Kph_u8p8[3]	19456		
t_LKASlewX_Kph_u8p8[4]	19968		
t LKASlewX Kph u8p8[5]	20480		
t_LKASlewY_NmpSec_u4p12[0]	14336		
t_LKASlewY_NmpSec_u4p12[1]	14746		
t_LKASlewY_NmpSec_u4p12[2]	15155		
t LKASlewY NmpSec u4p12[3]	15565		
t_LKASlewY_NmpSec_u4p12[4]	15974		
t_LKASlewY_NmpSec_u4p12[5]	16384		
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor UIs f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target TrqArblim Per1 ESCCmd HwNm f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target Rte Inst Ap TrgArblim.TrgArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target Rte Inst Ap TrgArblim.TrgArblim Per1 LKAState State enum	target_TrqArblim_Per1_LKAState_State_en		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrgArblim Per1 PosServEnable Cn		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosSrvoCmd MtrNm f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor Uls f32	target_TrqArblim_Per1_ReturnDDFactor_UI		
target Rte Inst Ap TrgArblim.TrgArblim Per1 TrgOscCmd MtrNm f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-0.709999979		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.75		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	193.093002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.300000012		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.4000001		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.782999992	0.782999992 ± 0000390625	
TrgArblim ESC State Cnt M enum	0	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.056090001	-0.056090001 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.30000012	-0.300000012 ± 0.00024414062	25
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.69400024	8.69400024 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target TrgArblim Per1 DampingDDFactor Uls f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	0	0 ± 0000390625	
target TrqArblim Per1 IqTrqOv HwNm f32.value	3	3 ± 0.0000390625	
target TrgArblim Per1 LKATorqueDelivered HwNm f32.value	3	3 ± 0.0000390625	
target TrgArblim Per1 OpTrgOv MtrNm f32.value	-4.46107626	-4.46108007 ± 0000390625	
	1	1 ± 0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.37 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.477999985	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0812000036	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.36000001	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8	
k_APAEnableRate_pSec_f32	163.5	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	





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Name	Input Value
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	14746
t_APADisableRateX_HwNm_u4p12[1]	15155
t_APADisableRateX_HwNm_u4p12[2]	15565
t_APADisableRateX_HwNm_u4p12[3] t_APADisableRateX_HwNm_u4p12[4]	15974 16384
_APADisableRateX_HwNm_u4p12[4] :_APADisableRateX_HwNm_u4p12[5]	16794
_Ar AbisableRateY_pSec_u7p9[0]	18432
_APADisableRateY_pSec_u7p9[1]	18944
_APADisableRateY_pSec_u7p9[2]	19456
_APADisableRateY_pSec_u7p9[3]	19968
_APADisableRateY_pSec_u7p9[4]	20480
	20992
_APASmoothX_Uls_u2p14[0]	5898
_APASmoothX_Uls_u2p14[1]	6062
_APASmoothX_Uls_u2p14[2]	6226
_APASmoothX_Uls_u2p14[3]	6390
_APASmoothX_Uls_u2p14[4]	6554
_APASmoothX_Uls_u2p14[5]	6717
_APASmoothX_Uls_u2p14[6]	6881
_APASmoothX_Uls_u2p14[7]	7045
_APASmoothX_Uls_u2p14[8]	7209
_APASmoothX_Uls_u2p14[9]	7373
_APASmoothY_Uls_u2p14[0]	2949
_APASmoothY_Uls_u2p14[1]	3113
_APASmoothY_Uls_u2p14[2]	3277
_APASmoothY_Uls_u2p14[3]	3441
_APASmoothY_UIs_u2p14[4]	3604 3768
_APASmoothY_UIs_u2p14[5]	3932
_APASmoothY_Uls_u2p14[6] _APASmoothY_Uls_u2p14[7]	4096
_APASmoothY_Uls_u2p14[8]	4260
_APASmoothY_UIs_u2p14[9]	4424
LKASlewX_Kph_u8p8[0]	18432
LKASlewX_Kph_u8p8[1]	18944
LKASlewX_Kph_u8p8[2]	19456
LKASlewX_Kph_u8p8[3]	19968
_LKASlewX_Kph_u8p8[4]	20480
_LKASlewX_Kph_u8p8[5]	20992
_LKASlewY_NmpSec_u4p12[0]	14746
_LKASlewY_NmpSec_u4p12[1]	15155
_LKASlewY_NmpSec_u4p12[2]	15565
_LKASlewY_NmpSec_u4p12[3]	15974
_LKASlewY_NmpSec_u4p12[4]	16384
_LKASlewY_NmpSec_u4p12[5]	16794
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 arget Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target_TrqArblim_Per1_HwTorque_HwNm_f32 target TrqArblim Per1_lqTrqOv HwNm f32
arget Rte Inst Ap TrqArbiim.TrqArbiim Per1 LKACmd HwNm f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
arget Rte Inst Ap TrgArblim.TrgArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
arget Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleSpeed Kph f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
arget_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-0.620000005
arget_TrqArblim_Per1_ESCState_State_enum.value	1
arget_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2
arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	-0.100000001
arget_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.0999999
arget_frqArbilitt_Ferr_LtActifit_frwittit_f32.value	To the second se
	1
target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	1 199.542999

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Name	Input Value		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-0.100000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.30000019		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.395999968	0.395999998 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.81707191	5.81707001 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-1.36000001	-1.36000001 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.99179983	-7.99179983 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.239999995	-0.239999995 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.239999995	-0.239999995 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.116297916	-0.116297998 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.914486825	0.914487004 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.38 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap TrgArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.490999997
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.86000013
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0834000036
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.87999995
TrqArblim PreLKATrqReq HwNm M f32	-7
k APAEnableRate pSec f32	168
k_APAUseADmpScale_lgc	0
k APAUseAsstScale Igc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.14000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	15155
t_APADisableRateX_HwNm_u4p12[1]	15565
t APADisableRateX HwNm u4p12[2]	15974
t APADisableRateX HwNm u4p12[3]	16384
t APADisableRateX HwNm u4p12[4]	16794
t APADisableRateX HwNm u4p12[5]	17203
t_APADisableRateY_pSec_u7p9[0]	18944
t APADisableRateY pSec u7p9[1]	19456
t_APADisableRateY_pSec_u7p9[2]	19968
t APADisableRateY pSec u7p9[3]	20480
t_APADisableRateY_pSec_u7p9[4]	20992
t APADisableRateY pSec u7p9[5]	21504
t_APASmoothX_Uls_u2p14[0]	6062
t_APASmoothX_Uls_u2p14[1]	6226
t APASmoothX Uls u2p14[2]	6390
t_APASmoothX_Uls_u2p14[3]	6554
t_APASmoothX_Uls_u2p14[4]	6717
t_APASmoothX_Uls_u2p14[5]	6881
t APASmoothX Uls u2p14[6]	7045
t_APASmoothX_Uls_u2p14[7]	7209
t_APASmoothX_Uls_u2p14[8]	7373
t_APASmoothX_Uls_u2p14[9]	7537
t_APASmoothY_Uls_u2p14[0]	3113
t_APASmoothY_Uls_u2p14[1]	3277
t_APASmoothY_Uls_u2p14[2]	3441
t_APASmoothY_Uls_u2p14[3]	3604
t_APASmoothY_Uls_u2p14[4]	3768
t_APASmoothY_Uls_u2p14[5]	3932
t_APASmoothY_Uls_u2p14[6]	4096
t_APASmoothY_Uls_u2p14[7]	4260
t_APASmoothY_Uls_u2p14[8]	4424
t_APASmoothY_Uls_u2p14[9]	4588
	,





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Name	Input Value		
t_LKASlewX_Kph_u8p8[0]	18944		
t_LKASlewX_Kph_u8p8[1]	19456		
t_LKASlewX_Kph_u8p8[2]	19968		
t_LKASlewX_Kph_u8p8[3]	20480		
t_LKASlewX_Kph_u8p8[4]	20992		
t_LKASlewX_Kph_u8p8[5]	21504		
LKASlewY_NmpSec_u4p12[0]	15155		
t_LKASlewY_NmpSec_u4p12[1]	15565		
t_LKASlewY_NmpSec_u4p12[2]	15974		
_LKASlewY_NmpSec_u4p12[3]	16384		
t_LKASlewY_NmpSec_u4p12[4]	16794		
t_LKASlewY_NmpSec_u4p12[5]	17203		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	_	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target TrqArblim Per1 IqTrqOv HwNm f32		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
arget Rte Inst Ap TrgArblim.TrgArblim Per1 LKAState State enum	target_TrqArblim_Per1_LKAState_State_en		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target TrqArblim Per1 LKATorqueDelivere		
arget Rte Inst Ap TrgArblim.TrgArblim Per1 MinSecureVehicleSpeed Kph f32			
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target TrqArblim Per1 OpTrqOv MtrNm f3		
rarget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI		
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
arget_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983	_	
arget_TrqArblim_Per1_ESCState_State_enum.value	2		
arget_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
arget_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.50999999		
arget_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.4499981		
arget_TrqArblim_Per1_LKAState_State_enum.value	4		
arget_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	205.992996		
arget_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
arget_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.100000001		
arget_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-4.0999999		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.407000005	0.407000005 ± 0000390625	11000
TrqArblim_ESC_State_Cnt_M_enum	2	2	
FrgArblim HwTorqueSV HwNm M Str.SV Uls f32	-5.32874203	-5.32874012 ± 0.03125	
rqArblim PosSrvoCmd MtrNm M f32	-0.879999995	-0.879999995 ± 0.00024414062	5
rgArblim_Possivocina_mitinin_m_isz rgArblim PreLKATrgReg HwNm M f32	-6.99399996	-6.99399996 ± 0.03125	3
arget TrgArblim Per1 AssistDDFactor Uls f32.value	1	1 ± 0000390625	
arget_TrqArblim_Per1_AssistDDFactor_Ols_is2.value arget_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625 1 ± 0000390625	
	1	1 ± 0000390625	
arget_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0.140000001		
arget_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.140000001	0.140000001 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value		0.140000001 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-4.1812768	-4.18128014 ± 0000390625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

1 ± 0000390625

Test Step 3.39 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.504000008
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.649999976
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.40000006
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value





Name	Input Value
k_APAEnableRate_pSec_f32	172.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc k_APAUseRetScale_lgc	1
k ESCMax HwNm f32	0.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	15565
t_APADisableRateX_HwNm_u4p12[1]	15974
t_APADisableRateX_HwNm_u4p12[2]	16384
t_APADisableRateX_HwNm_u4p12[3]	16794
t_APADisableRateX_HwNm_u4p12[4]	17203
t_APADisableRateX_HwNm_u4p12[5]	17613
t_APADisableRateY_pSec_u7p9[0]	19456
t_APADisableRateY_pSec_u7p9[1]	19968
t_APADisableRateY_pSec_u7p9[2] t_APADisableRateY_pSec_u7p9[3]	20480 20992
t_APADisableRateY_pSec_u7p9[4]	21504
t_APADisableRateY_pSec_u7p9[5]	22016
t_APASmoothX_Uls_u2p14[0]	6226
t_APASmoothX_Uls_u2p14[1]	6390
t_APASmoothX_Uls_u2p14[2]	6554
t_APASmoothX_Uls_u2p14[3]	6717
t_APASmoothX_Uls_u2p14[4]	6881
t_APASmoothX_Uls_u2p14[5]	7045
t_APASmoothX_UIs_u2p14[6]	7209
t_APASmoothX_Uls_u2p14[7]	7373
t_APASmoothX_UIs_u2p14[8]	7537
t_APASmoothX_UIs_u2p14[9]	7700
t_APASmoothY_UIs_u2p14[0]	3277 3441
t_APASmoothY_Uls_u2p14[1] t_APASmoothY_Uls_u2p14[2]	3604
t_APASmoothY_Uls_u2p14[3]	3768
t_APASmoothY_Uls_u2p14[4]	3932
t_APASmoothY_Uls_u2p14[5]	4096
t_APASmoothY_Uls_u2p14[6]	4260
t_APASmoothY_Uls_u2p14[7]	4424
t_APASmoothY_Uls_u2p14[8]	4588
t_APASmoothY_Uls_u2p14[9]	4751
t_LKASlewX_Kph_u8p8[0]	19456
t_LKASlewX_Kph_u8p8[1]	19968
t_LKASlewX_Kph_u8p8[2]	20480
t_LKASlewX_Kph_u8p8[3] t_LKASlewX_Kph_u8p8[4]	20992 21504
t_LKASlewX_Kph_u8p8[5]	22016
t_LKASlewY_NmpSec_u4p12[0]	15565
t LKASlewY NmpSec u4p12[1]	15974
t_LKASlewY_NmpSec_u4p12[2]	16384
t_LKASlewY_NmpSec_u4p12[3]	16794
t_LKASlewY_NmpSec_u4p12[4]	17203
t_LKASlewY_NmpSec_u4p12[5]	17613
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_State_enum target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCTorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_IrqArblim_Per1_ESC1orqueDelivered_Hwnm_132 target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	1.77999997
target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	3 2
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.12
ca.goq. abiiii_i or i_i in roiduo_i iwinii_ioz.valuo	···-
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	6.80000019

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 Name
 Input Value

 target_TrqArblim_Per1_LKAState_State_enum.value
 0

 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value
 212.442993

 target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value
 1

 target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value
 0.300000012

 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value
 -3.9000001

 Name
 Actual Value
 Expected Value
 Resul

 TrqArblim_APADisableRate_Uls_M_f32
 0.849000037
 0.848999977 ± 0000390625

 TrqArblim_ESC State Cnt M_enum
 3
 3

3.2 1 2 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2			
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.849000037	0.848999977 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.650590241	0.650590003 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.300000012	0.300000012 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.99139977	-5.99139977 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.0738574117	0.0738573968 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.753808618	0.753808975 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
FrgArblim APADisableRate Uls M f32	0.51700002	
FrqArblim_ESC_State_Cnt_M_enum	3	
FrgArblim HwTorqueSV HwNm M Str.SV Uls f32	1.21000004	
FrgArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390027	
rqArblim_PosSrvoCmd_MtrNm_M_f32	0.079999982	
rqArblim_PreLKATrqReq_HwNm_M_f32	-5	
APAEnableRate pSec f32	177	
_APAUseADmpScale_lgc	0	
APAUseAsstScale Igc	0	
APAUseRetScale lgc	0	
ESCMax HwNm f32	1.27999997	
LKAUseSlewCal lgc	0	
APADisableRateX HwNm u4p12[0]	15974	
_APADisableRateX_HwNm_u4p12[1]	16384	
APADisableRateX HwNm u4p12[2]	16794	
APADisableRateX HwNm u4p12[3]	17203	
_APADisableRateX_HwNm_u4p12[4]	17613	
_APADisableRateX_HwNm_u4p12[5]	18022	
_APADisableRateY_pSec_u7p9[0]	19968	
APADisableRateY pSec u7p9[1]	20480	
APADisableRateY pSec u7p9[2]	20992	
_APADisableRateY_pSec_u7p9[3]	21504	
_APADisableRateY_pSec_u7p9[4]	22016	
_APADisableRateY_pSec_u7p9[5]	22528	
APASmoothX Uls u2p14[0]	6390	
APASmoothX Uls u2p14[1]	6554	
_APASmoothX_Uls_u2p14[2]	6717	
_APASmoothX_UIs_u2p14[3]	6881	
APASmoothX Uls u2p14[4]	7045	
APASmoothX Uls u2p14[5]	7209	
_APASmoothX_UIs_u2p14[6]	7373	
_APASmoothX_UIs_u2p14[7]	7537	
_APASmoothX_Uls_u2p14[8]	7700	
_APASmoothX_Uls_u2p14[9]	7864	
_APASmoothY_Uls_u2p14[0]	3441	
_APASmoothY_Uls_u2p14[1]	3604	
_APASmoothY_Uls_u2p14[2]	3768	
_APASmoothY_Uls_u2p14[3]	3932	
APASmoothY Uls u2p14[4]	4096	
_APASmoothY_Uls_u2p14[5]	4260	
_APASmoothY_Uls_u2p14[6]	4424	

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Name	Input Value		
t APASmoothY Uls u2p14[7]	4588		
t APASmoothY Uls u2p14[8]	4751		
t_APASmoothY_Uls_u2p14[9]	4915		
t_LKASlewX_Kph_u8p8[0]	19968		
t_LKASlewX_Kph_u8p8[1]	20480		
t_LKASlewX_Kph_u8p8[2]	20992		
t_LKASlewX_Kph_u8p8[3]	21504		
t_LKASlewX_Kph_u8p8[4]	22016		
t_LKASlewX_Kph_u8p8[5]	22528		
t LKASlewY NmpSec u4p12[0]	15974		
t_LKASlewY_NmpSec_u4p12[1]	16384		
t_LKASlewY_NmpSec_u4p12[2]	16794		
t LKASlewY NmpSec u4p12[3]	17203		
t_LKASlewY_NmpSec_u4p12[4]	17613		
	18022		
t_LKASlewY_NmpSec_u4p12[5]		, f22	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Ulstarget_TrqArblim_Per1_DampingDDFactor_	_	
target_rte_inst_Ap_frqArblim.frqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCISLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_S target_TrqArblim_Per1_HwTorque_HwNm_	_	
target Rte Inst Ap TrgArblim.TrgArblim Per1 IgTrgOv HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3.		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target TrqArblim Per1 MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	2.98000002	1_102	
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.73000002		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.1500001		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	218.893005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32	0.43900001	0.43900001 ± 0000390625	ixesuit
TrqArblim ESC State Cnt M enum	4	4	
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	1.58200288	1.58200002 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.079999982	0.0799999982 ± 0.00024414062	
TrgArblim PreLKATrgReg HwNm M f32	-4.99399996	-4.99399996 ± 0.03125	.5
target TrgArblim Per1 AssistDDFactor Uls f32.value	1	1 ± 0000390625	
target TrgArblim Per1 DampingDDFactor Uls f32.value	1	1 ± 0000330625	
target_rrqArblim_Per1_DampingDDFactor_ors_rsz.value	0	0	
target_frqArblim_Per1_ESCIsLimited_Cnt_igc.value target_frqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_ESC10rqueDelivered_HwNm_is2.value	-3	-3 ± 0.000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3 -3	-3 ± 0.0000390625	
target_TrqArblim_Per1_DpTrqOv_MtrNm_f32.value	-3 -3.69090486	-3 ± 0.0000390625	
target_rrqArblim_Per1_oprrqov_witnNm_i3z.value			
target_rrqArbilitt_Perr_ReturnDDFactor_OIS_132.Value	1	1 ± 0000390625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TroArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.41 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.529999971	
TrqArblim_ESC_State_Cnt_M_enum	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.76999998	

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Name	Input Value
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0500000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10.2299995
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4
k_APALenABerraCords Inc.	181.5
k_APAUseADmpScale_lgc k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	16384
t_APADisableRateX_HwNm_u4p12[1]	16794
t_APADisableRateX_HwNm_u4p12[2]	17203
t_APADisableRateX_HwNm_u4p12[3]	17613
t_APADisableRateX_HwNm_u4p12[4]	18022
t_APADisableRateX_HwNm_u4p12[5] t APADisableRateY pSec u7p9[0]	18432 20480
t_APADisableNateY_pSec_u7p9[0]	20992
t_APADisableRateY_pSec_u7p9[2]	21504
t_APADisableRateY_pSec_u7p9[3]	22016
t_APADisableRateY_pSec_u7p9[4]	22528
t_APADisableRateY_pSec_u7p9[5]	23040
t_APASmoothX_Uls_u2p14[0]	6554
t_APASmoothX_Uls_u2p14[1]	6717
t_APASmoothX_Uls_u2p14[2]	6881 7045
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	7209
t_APASmoothX_Uls_u2p14[5]	7373
t_APASmoothX_Uls_u2p14[6]	7537
t_APASmoothX_Uls_u2p14[7]	7700
t_APASmoothX_Uls_u2p14[8]	7864
t_APASmoothX_Uls_u2p14[9]	8028
t_APASmoothY_Uls_u2p14[0]	3604
t_APASmoothY_Uls_u2p14[1]	3768
t_APASmoothY_Uls_u2p14[2]	3932
t_APASmoothY_Uls_u2p14[3] t_APASmoothY_Uls_u2p14[4]	4096 4260
t_APASmoothY_Uls_u2p14[5]	4424
t_APASmoothY_Uls_u2p14[6]	4588
t_APASmoothY_Uls_u2p14[7]	4751
t_APASmoothY_Uls_u2p14[8]	4915
t_APASmoothY_Uls_u2p14[9]	5079
t_LKASlewX_Kph_u8p8[0]	20480
t_LKASlewX_Kph_u8p8[1]	20992
t_LKASlewX_Kph_u8p8[2]	21504
t_LKASlewX_Kph_u8p8[3] t_LKASlewX_Kph_u8p8[4]	22016 22528
t_LKASlewX_Kph_u8p8[5]	23040
t_LKASlewY_NmpSec_u4p12[0]	16384
t_LKASlewY_NmpSec_u4p12[1]	16794
t_LKASlewY_NmpSec_u4p12[2]	17203
t_LKASlewY_NmpSec_u4p12[3]	17613
t_LKASlewY_NmpSec_u4p12[4]	18022
t_LKASlewY_NmpSec_u4p12[5]	18432
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32 target TrqArblim Per1_ESCCmd HwNm f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCInd_mwnin_isz target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCGIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIst_Inited_Cnt_igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target TrqArblim Per1 ESCState State enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_inst_Ap_frqArblim.frqArblim_Per1_OpfrqOv_intiNin_isz target_Rte_inst_Ap_frqArblim.frqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_MtiNin_isz target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosSrvoCmd MtrNm f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32
tanget_1 tto_1110_1 tp_1114 tb	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 4.17999983 0

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Name	Input Value		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.33999991		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.5		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	225.343002		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.69999988		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.893000007	0.893000007 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.79849994	1.79849994 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.69999988	0.69999988 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.99099994	-3.99099994 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.193779469	0.193779007 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.42 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrgArblim APADisableRate UIs M f32	0
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.32999992
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.023
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.03999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3
k_APAEnableRate_pSec_f32	186
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	2.75
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	16794
t_APADisableRateX_HwNm_u4p12[1]	17203
t_APADisableRateX_HwNm_u4p12[2]	17613
t_APADisableRateX_HwNm_u4p12[3]	18022
t_APADisableRateX_HwNm_u4p12[4]	18432
t_APADisableRateX_HwNm_u4p12[5]	18842
t_APADisableRateY_pSec_u7p9[0]	20992
t_APADisableRateY_pSec_u7p9[1]	21504
t_APADisableRateY_pSec_u7p9[2]	22016
t_APADisableRateY_pSec_u7p9[3]	22528
t_APADisableRateY_pSec_u7p9[4]	23040
t_APADisableRateY_pSec_u7p9[5]	23552
t_APASmoothX_Uls_u2p14[0]	6717
t_APASmoothX_Uls_u2p14[1]	6881
t_APASmoothX_Uls_u2p14[2]	7045
t_APASmoothX_Uls_u2p14[3]	7209
t_APASmoothX_Uls_u2p14[4]	7373
t_APASmoothX_Uls_u2p14[5]	7537
t_APASmoothX_Uls_u2p14[6]	7700
t_APASmoothX_Uls_u2p14[7]	7864
t_APASmoothX_Uls_u2p14[8]	8028
t_APASmoothX_Uls_u2p14[9]	8192
t_APASmoothY_Uls_u2p14[0]	3768
t_APASmoothY_Uls_u2p14[1]	3932
t_APASmoothY_Uls_u2p14[2]	4096
t_APASmoothY_Uls_u2p14[3]	4260

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TrqArblim_Per1

TrqArbiim_PerT			MACILAN
Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	4424		
t_APASmoothY_Uls_u2p14[5]	4588		
t_APASmoothY_Uls_u2p14[6]	4751		
t_APASmoothY_Uls_u2p14[7]	4915		
t APASmoothY Uls u2p14[8]	5079		
	5243		
t_APASmoothY_UIs_u2p14[9]	20992		
t_LKASlewX_Kph_u8p8[0] t_LKASlewX_Kph_u8p8[1]	21504		
	22016		
t_LKASlewX_Kph_u8p8[2] t_LKASlewX_Kph_u8p8[3]	22528		
	23040		
t_KASlewX_Kph_u8p8[4]	23552		
t_LKASlewX_Kph_u8p8[5]	16794		
t_LKASlewY_NmpSec_u4p12[0]	17203		
t_LKASlewY_NmpSec_u4p12[1]			
t_LKASlewY_NmpSec_u4p12[2]	17613		
t_LKASlewY_NmpSec_u4p12[3]	18022		
t_LKASlewY_NmpSec_u4p12[4]	18432		
t_LKASlewY_NmpSec_u4p12[5]	18842	100	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_Si	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.95000005		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.23000002		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.89999976		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.29999995		
Name	Actual Value	Expected Value	Result
TrgArblim APADisableRate Uls M f32	0.372000009	0.372000009 ± 0000390625	~
TrgArblim ESC State Cnt M enum	1	1	✓
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.34425998	2.34425998 ± 0.03125	_
TrgArblim PosSrvoCmd MtrNm M f32	0.899999976	0.899999976 ± 0.0002441406	25
TrgArblim PreLKATrgReg HwNm M f32	-2.99399996	-2.99399996 ± 0.03125	
target TrgArblim Per1 AssistDDFactor UIs f32.value	1	1 ± 0000390625	<u> </u>
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target TrgArblim Per1 ESCIsLimited Cnt Igc.value	1	1	·
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	2.75	2.75 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.75	2.75 ± 0.000390625	_
target_rrqArblim_Per1_iq1rqOv_nwnin_i32.value	0	0 ± 0.0000390625	
target_TrqArblim_Per1_cxArbriquebeilvereu_nwini1_isz.value	-3.22300243	-3.22300005 ± 0000390625	
target_frqArbiliti_r et i_Op frqOv_withviii_loz.value	-0.22000240	-5.22500005 ± 0000390025	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	-

1

1 ± 0000390625

Test Step 3.43 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value





Name	Input Value
TrqArblim_APADisableRate_Uls_M_f32	1
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.8900001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32 TrqArblim_PosSryoCmd_MtrNm_M_f32	0.0340000018 1.51999998
TrqArblim_PosSrvoCmd_MtrNm_M_f32 TrqArblim_PreLKATrqReq_HwNm_M_f32	-2
k_APAEnableRate_pSec_f32	190.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k APAUseRetScale Igc	1
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	17203
t_APADisableRateX_HwNm_u4p12[1]	17613
t_APADisableRateX_HwNm_u4p12[2]	18022
t_APADisableRateX_HwNm_u4p12[3]	18432
t_APADisableRateX_HwNm_u4p12[4]	18842
t_APADisableRateX_HwNm_u4p12[5]	19251
t_APADisableRateY_pSec_u7p9[0]	21504
t_APADisableRateY_pSec_u7p9[1]	22016 22528
t_APADisableRateY_pSec_u7p9[2] t_APADisableRateY_pSec_u7p9[3]	23040
t_APADisableRateY_pSec_u7p9[4]	23552
t_APADisableRateY_pSec_u7p9[5]	24064
t_APASmoothX_UIs_u2p14[0]	6881
t_APASmoothX_Uls_u2p14[1]	7045
t_APASmoothX_UIs_u2p14[2]	7209
t_APASmoothX_Uls_u2p14[3]	7373
t_APASmoothX_Uls_u2p14[4]	7537
t_APASmoothX_Uls_u2p14[5]	7700
t_APASmoothX_Uls_u2p14[6]	7864
t_APASmoothX_Uls_u2p14[7]	8028
t_APASmoothX_Uls_u2p14[8]	8192
t_APASmoothX_Uls_u2p14[9]	8356
t_APASmoothY_Uls_u2p14[0]	3932
t_APASmoothY_UIs_u2p14[1]	4096
t_APASmoothY_Uls_u2p14[2] t_APASmoothY_Uls_u2p14[3]	4260 4424
t_APASmoothY_Uls_u2p14[4]	4588
t_APASmoothY_Uls_u2p14[5]	4751
t_APASmoothY_Uls_u2p14[6]	4915
t_APASmoothY_Uls_u2p14[7]	5079
t_APASmoothY_Uls_u2p14[8]	5243
t_APASmoothY_Uls_u2p14[9]	5407
t_LKASlewX_Kph_u8p8[0]	21504
t_LKASlewX_Kph_u8p8[1]	22016
t_LKASlewX_Kph_u8p8[2]	22528
t_LKASlewX_Kph_u8p8[3]	23040
t_LKASlewX_Kph_u8p8[4]	23552
t_LKASlewX_Kph_u8p8[5]	24064
t_LKASlewY_NmpSec_u4p12[0]	17203
t_KASlewY_NmpSec_u4p12[1]	17613 18022
t_KASlewY_NmpSec_u4p12[2]	18432
t_LKASlewY_NmpSec_u4p12[3] t_LKASlewY_NmpSec_u4p12[4]	1842 18842
t LKASlewY NmpSec u4p12[5]	19251
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
TAMES RIE INSLAN TROBUNIU FRANKUI PELI RETURNIUEZATOR LUS 137	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32





Name	Input Value		
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	· ·	target TrgArblim Per1 TrgOscCmd MtrNm f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.55999994		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.19999981		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	238.242996		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.10000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.91278005	2.91278005 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.10000002	1.10000002 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.99060011	-1.99059999 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.66998291	0.669983029 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.66998291	0.669983029 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	1.23000002	1.23000002 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.23000002	1.23000002 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.363018811	0.36301899 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.66998291	0.669983029 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.44 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.300000012
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.45000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0450000018
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1
k_APAEnableRate_pSec_f32	195
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	17613
t_APADisableRateX_HwNm_u4p12[1]	18022
t_APADisableRateX_HwNm_u4p12[2]	18432
t_APADisableRateX_HwNm_u4p12[3]	18842
t_APADisableRateX_HwNm_u4p12[4]	19251
t_APADisableRateX_HwNm_u4p12[5]	19661
t_APADisableRateY_pSec_u7p9[0]	22016
t_APADisableRateY_pSec_u7p9[1]	22528
t_APADisableRateY_pSec_u7p9[2]	23040
t_APADisableRateY_pSec_u7p9[3]	23552
t_APADisableRateY_pSec_u7p9[4]	24064
t_APADisableRateY_pSec_u7p9[5]	24576
t_APASmoothX_Uls_u2p14[0]	7045
t_APASmoothX_Uls_u2p14[1]	7209
t_APASmoothX_Uls_u2p14[2]	7373
t_APASmoothX_Uls_u2p14[3]	7537
t_APASmoothX_Uls_u2p14[4]	7700
t_APASmoothX_Uls_u2p14[5]	7864
t_APASmoothX_Uls_u2p14[6]	8028
t_APASmoothX_Uls_u2p14[7]	8192
t_APASmoothX_Uls_u2p14[8]	8356
t_APASmoothX_Uls_u2p14[9]	8520
t_APASmoothY_Uls_u2p14[0]	4096

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Name	Input Value		
t_APASmoothY_Uls_u2p14[1]	4260		
t_APASmoothY_Uls_u2p14[2]	4424		
t_APASmoothY_Uls_u2p14[3]	4588		
t_APASmoothY_Uls_u2p14[4]	4751		
t_APASmoothY_Uls_u2p14[5]	4915		
t_APASmoothY_Uls_u2p14[6]	5079		
t_APASmoothY_Uls_u2p14[7]	5243		
t_APASmoothY_Uls_u2p14[8]	5407		
t_APASmoothY_Uls_u2p14[9]	5571		
t_LKASlewX_Kph_u8p8[0]	22016		
t_LKASlewX_Kph_u8p8[1]	22528		
t_LKASlewX_Kph_u8p8[2]	23040		
t_LKASlewX_Kph_u8p8[3]	23552 24064		
t_LKASlewX_Kph_u8p8[4] t_LKASlewX_Kph_u8p8[5]	24576		
t_LKASlewY_NmpSec_u4p12[0]	17613		
t_LKASlewY_NmpSec_u4p12[0]	18022		
t_LKASlewY_NmpSec_u4p12[2]	18432		
t_LKASlewY_NmpSec_u4p12[3]	18842		
t_LKASlewY_NmpSec_u4p12[4]	19251		
t_LKASlewY_NmpSec_u4p12[5]	19661		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target TrqArblim Per1 AssistDDFactor Uls f3	32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target_TrqArblim_Per1_DampingDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum	L	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_I	HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State	e_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_H	HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSpee	ed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lg		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f3	32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.17000008 8.55000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value	0		
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	244.692993		
target TrqArblim Per1 PosServEnable Cnt Igc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.2999995		
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	-2.9000001		
Name		Expected Value	Result
TrqArblim APADisableRate Uls M f32		.689999998 ± 0000390625	\ ✓
TrqArblim ESC State Cnt M enum	3 3		
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32		3.48239994 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32		.29999995 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32		0.994000018 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value		± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value		± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0 0		✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value		± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value		0.994000018 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value		0.994000018 ± 0.0000390625	-
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.30500409	.305004001 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓



Test Step 3.45 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.0250000004
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.01000023
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0560000017
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998
TrqArblim_PreLKATrqReq_HwNm_M_f32	0
k_APAEnableRate_pSec_f32	199.5
k_APAUseADmpScale_lgc k_APAUseAsstScale_lgc	0
k APAUseRetScale Igc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	18022
t_APADisableRateX_HwNm_u4p12[1]	18432
t_APADisableRateX_HwNm_u4p12[2]	18842
t_APADisableRateX_HwNm_u4p12[3]	19251
t_APADisableRateX_HwNm_u4p12[4]	19661
t_APADisableRateX_HwNm_u4p12[5]	20070
t_APADisableRateY_pSec_u7p9[0]	22528 23040
t_APADisableRateY_pSec_u7p9[1] t_APADisableRateY_pSec_u7p9[2]	23552
t_APADisableRateY_pSec_u7p9[3]	24064
t_APADisableRateY_pSec_u7p9[4]	24576
t_APADisableRateY_pSec_u7p9[5]	25088
t_APASmoothX_Uls_u2p14[0]	7209
t_APASmoothX_Uls_u2p14[1]	7373
t_APASmoothX_Uls_u2p14[2]	7537
t_APASmoothX_Uls_u2p14[3]	7700
t_APASmoothX_Uls_u2p14[4]	7864
t_APASmoothX_UIs_u2p14[5]	8028 8192
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	8356
t_APASmoothX_Uls_u2p14[8]	8520
t_APASmoothX_Uls_u2p14[9]	8684
t_APASmoothY_Uls_u2p14[0]	4260
t_APASmoothY_Uls_u2p14[1]	4424
t_APASmoothY_Uls_u2p14[2]	4588
t_APASmoothY_Uls_u2p14[3]	4751
t_APASmoothY_Uls_u2p14[4]	4915
t_APASmoothY_Uls_u2p14[5]	5079
t_APASmoothY_Uls_u2p14[6] t_APASmoothY_Uls_u2p14[7]	5243 5407
t_APASmoothY_Uls_u2p14[8]	5571
t_APASmoothY_Uls_u2p14[9]	5734
t_LKASlewX_Kph_u8p8[0]	22528
t_LKASlewX_Kph_u8p8[1]	23040
t_LKASlewX_Kph_u8p8[2]	23552
t_LKASlewX_Kph_u8p8[3]	24064
t_LKASlewX_Kph_u8p8[4]	24576
t_LKASlewX_Kph_u8p8[5]	25088
t_LKASlewY_NmpSec_u4p12[0]	18022
t_KASlewY_NmpSec_u4p12[1]	18432 18842
t_LKASlewY_NmpSec_u4p12[2] t_LKASlewY_NmpSec_u4p12[3]	19251
t_LKASlewY_NmpSec_u4p12[4]	19661
t_LKASlewY_NmpSec_u4p12[5]	20070
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32
target Rte Inst An TraArhlim TraArhlim Pert I KACmd HuMm f22	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_	Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_M	trNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_Mtr	Nm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	4.78000021		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	8.89999962		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	251.143005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0	0 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.05312014	4.05312014 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998	-10.2399998 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	0	0 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-2.70000005	-2.70000005 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.46 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.529999971
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.76999998
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.050000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.560000002
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4
k_APAEnableRate_pSec_f32	181.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	16384
t_APADisableRateX_HwNm_u4p12[1]	16794
t_APADisableRateX_HwNm_u4p12[2]	17203
t_APADisableRateX_HwNm_u4p12[3]	17613
t_APADisableRateX_HwNm_u4p12[4]	18022
t_APADisableRateX_HwNm_u4p12[5]	18432
t_APADisableRateY_pSec_u7p9[0]	20480
t_APADisableRateY_pSec_u7p9[1]	20992
t_APADisableRateY_pSec_u7p9[2]	21504
t_APADisableRateY_pSec_u7p9[3]	22016
t_APADisableRateY_pSec_u7p9[4]	22528
t_APADisableRateY_pSec_u7p9[5]	23040
t_APASmoothX_Uls_u2p14[0]	6554
t_APASmoothX_Uls_u2p14[1]	6717
t_APASmoothX_Uls_u2p14[2]	6881
t_APASmoothX_Uls_u2p14[3]	7045
t_APASmoothX_Uls_u2p14[4]	7209
t_APASmoothX_Uls_u2p14[5]	7373
t_APASmoothX_Uls_u2p14[6]	7537
t_APASmoothX_Uls_u2p14[7]	7700

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Name	Input Value		
t_APASmoothX_Uls_u2p14[8]	7864		
t_APASmoothX_Uls_u2p14[9]	8028		
t_APASmoothY_Uls_u2p14[0]	3604		
t_APASmoothY_Uls_u2p14[1]	3768		
t_APASmoothY_Uls_u2p14[2]	3932		
t_APASmoothY_Uls_u2p14[3]	4096		
t_APASmoothY_Uls_u2p14[4]	4260		
t_APASmoothY_Uls_u2p14[5]	4424		
t_APASmoothY_Uls_u2p14[6]	4588		
t_APASmoothY_Uls_u2p14[7]	4751		
t_APASmoothY_Uls_u2p14[8]	4915		
t_APASmoothY_Uls_u2p14[9]	5079		
t_LKASlewX_Kph_u8p8[0]	20480		
t_LKASlewX_Kph_u8p8[1]	20992		
t_LKASlewX_Kph_u8p8[2]	21504		
t_LKASlewX_Kph_u8p8[3]	22016		
t_LKASlewX_Kph_u8p8[4]	22528		
t_LKASlewX_Kph_u8p8[5]	23040		
t_LKASlewY_NmpSec_u4p12[0]	16384		
t_LKASlewY_NmpSec_u4p12[1]	16794		
t_LKASlewY_NmpSec_u4p12[2]	17203		
t_LKASlewY_NmpSec_u4p12[3]	17613		
t_LKASlewY_NmpSec_u4p12[4]	18022		
t_LKASlewY_NmpSec_u4p12[5]	18432		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	ı_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	4.17999983		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.33999991		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.5		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	225.343002		
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	0.69999988		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.893000007	0.893000007 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.79849994	1.79849994 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.69999988	0.69999988 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.99099994	-3.99099994 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.723172188	0.723172009 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.193779469	0.193779007 ± 0000390625	~
target TrgArblim Per1 ReturnDDFactor Uls f32.value	0.723172188	0.723172009 ± 0000390625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0.723172188

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

0.723172009 ± 0000390625



Fest Step 3.47 (Repeat Count = 1)	Input Value
	Input Value
Rte_Inst_Ap_TrqArblim rqArblim APADisableRate Uls M f32	target_Rte_Inst_Ap_TrqArblim 0.075000003
rqArblim_AFADIsableRate_0is_ini_isz rqArblim_ESC_State_Cnt_M_enum	0.0750000005
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.13000011
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0780000016
rqArblim_PosSrvoCmd_MtrNm_M_f32	0
rqArblim_PreLKATrqReq_HwNm_M_f32	2
_APAEnableRate_pSec_f32	208.5
<pre>a_APAUseADmpScale_lgc</pre>	1
z_APAUseAsstScale_lgc	1
a_APAUseRetScale_lgc	1
ESCMax_HwNm_f32	3.25
x_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	18842
_APADisableRateX_HwNm_u4p12[1]	19251
_APADisableRateX_HwNm_u4p12[2]	19661 20070
_APADisableRateX_HwNm_u4p12[3]	20480
_APADisableRateX_HwNm_u4p12[4] _APADisableRateX_HwNm_u4p12[5]	20890
APADisableRateY pSec u7p9[0]	23552
_APADisableRateY_pSec_u7p9[1]	24064
_APADisableRateY_pSec_u7p9[2]	24576
APADisableRateY_pSec_u7p9[3]	25088
_APADisableRateY_pSec_u7p9[4]	25600
_APADisableRateY_pSec_u7p9[5]	26112
_APASmoothX_Uls_u2p14[0]	7537
_APASmoothX_Uls_u2p14[1]	7700
_APASmoothX_Uls_u2p14[2]	7864
_APASmoothX_UIs_u2p14[3]	8028
_APASmoothX_Uls_u2p14[4]	8192
_APASmoothX_UIs_u2p14[5]	8356
_APASmoothX_Uls_u2p14[6]	8520 8684
_APASmoothX_UIs_u2p14[7] _APASmoothX_UIs_u2p14[8]	8847
_APASmoothX_Uls_u2p14[9]	9011
_APASmoothY_Uls_u2p14[0]	4588
_APASmoothY_Uls_u2p14[1]	4751
_APASmoothY_Uls_u2p14[2]	4915
_APASmoothY_Uls_u2p14[3]	5079
_APASmoothY_Uls_u2p14[4]	5243
_APASmoothY_Uls_u2p14[5]	5407
_APASmoothY_Uls_u2p14[6]	5571
_APASmoothY_Uls_u2p14[7]	5734
_APASmoothY_Uls_u2p14[8]	5898
_APASmoothY_Uls_u2p14[9]	6062
_LKASlewX_Kph_u8p8[0]	23552
_LKASlewX_Kph_u8p8[1]	24064
LKASlewX_Kph_u8p8[2]	24576 25088
_LKASlewX_Kph_u8p8[3] LKASlewX Kph u8p8[4]	25008
_LKASlewX_Kph_u8p8[5]	26112
LKASlewY_NmpSec_u4p12[0]	18842
_LKASlewY_NmpSec_u4p12[1]	19251
LKASlewY_NmpSec_u4p12[2]	19661
LKASlewY_NmpSec_u4p12[3]	20070
_LKASlewY_NmpSec_u4p12[4]	20480
_LKASlewY_NmpSec_u4p12[5]	20890
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
	target_TrqArblim_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_State_enum target_TrqArblim_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_State_enum target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrqArblim_Per1_IqTrqOv_HwNm_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_GMOSHOscillate_State_enum target_TrqArblim_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3:	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	9.60000038		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	264.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	1.8999998		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.492000043	0.492000014 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.19786024	5.19785976 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.8999998	1.89999998 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.99079978	1.98979998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.846520245	0.846520007 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.846520245	0.846520007 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.25	-3.25 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.291611522	0.291610986 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.846520245	0.846520007 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Took Ston 2 40 (Bonnet Count = 4)	
Test Step 3.48 (Repeat Count = 1) Name	Input Value
Rte Inst Ap TrqArblim	target Rte Inst Ap TrgArblim
	0.10000001
TrqArblim_APADisableRate_Uls_M_f32	
TrqArblim_ESC_State_Cnt_M_enum	1 5.69000006
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.0890000015
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.3499999
TrqArblim_PreLKATrqReq_HwNm_M_f32	3
k_APAEnableRate_pSec_f32	213
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.1400001
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	19251
t_APADisableRateX_HwNm_u4p12[1]	19661
t_APADisableRateX_HwNm_u4p12[2]	20070
t_APADisableRateX_HwNm_u4p12[3]	20480
t_APADisableRateX_HwNm_u4p12[4]	20890
t_APADisableRateX_HwNm_u4p12[5]	21299
t_APADisableRateY_pSec_u7p9[0]	24064
t_APADisableRateY_pSec_u7p9[1]	24576
t_APADisableRateY_pSec_u7p9[2]	25088
t_APADisableRateY_pSec_u7p9[3]	25600
t_APADisableRateY_pSec_u7p9[4]	26112
t_APADisableRateY_pSec_u7p9[5]	26624
t_APASmoothX_Uls_u2p14[0]	7700
t_APASmoothX_Uls_u2p14[1]	7864
t_APASmoothX_Uls_u2p14[2]	8028
t_APASmoothX_Uls_u2p14[3]	8192
t_APASmoothX_Uls_u2p14[4]	8356
t_APASmoothX_Uls_u2p14[5]	8520





Name	Input Value		
t_APASmoothX_Uls_u2p14[6]	8684		
t_APASmoothX_Uls_u2p14[7]	8847		
t_APASmoothX_Uls_u2p14[8]	9011		
t_APASmoothX_Uls_u2p14[9]	9175		
t_APASmoothY_Uls_u2p14[0]	4751		
t_APASmoothY_Uls_u2p14[1]	4915		
t_APASmoothY_Uls_u2p14[2]	5079		
t_APASmoothY_UIs_u2p14[3]	5243		
t_APASmoothY_Uls_u2p14[4]	5407 5571		
t_APASmoothY_Uls_u2p14[5] t_APASmoothY_Uls_u2p14[6]	5734		
t_APASmoothY_Uls_u2p14[7]	5898		
t_APASmoothY_UIs_u2p14[8]	6062		
t_APASmoothY_Uls_u2p14[9]	6226		
t_LKASlewX_Kph_u8p8[0]	24064		
t_LKASlewX_Kph_u8p8[1]	24576		
t_LKASlewX_Kph_u8p8[2]	25088		
t_LKASlewX_Kph_u8p8[3]	25600		
t_LKASlewX_Kph_u8p8[4]	26112		
t_LKASlewX_Kph_u8p8[5]	26624		
t_LKASlewY_NmpSec_u4p12[0]	19251		
t_LKASlewY_NmpSec_u4p12[1]	19661		
t_LKASlewY_NmpSec_u4p12[2]	20070		
t_LKASlewY_NmpSec_u4p12[3]	20480		
t_LKASlewY_NmpSec_u4p12[4]	20890		
t_LKASlewY_NmpSec_u4p12[5]	21299		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ReturnDDFactor Uls f32	target TrqArblim Per1 ReturnDDFactor UI		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6.61000013		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	9.94999981		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	270.493011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-2.0999999		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.526000023	0.526000023 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	2	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.77188015	5.77188015 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.0999999	2.0999999 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.99060011	2.98959994 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.818063855	0.818063974 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.818063855	0.818063974 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.1400001	-3.1400001 ± 0000390625	
	2	2 + 0 0000300035	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	
	-3 0 -1.71793401	-3 ± 0.0000390625 0 ± 0.0000390625 -1.71792996 ± 0000390625	





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.49 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.125
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.100000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.53999996
TrqArblim_PreLKATrqReq_HwNm_M_f32	4
k APAEnableRate pSec f32	217.5
k APAUseADmpScale Igc	1
APAUseAsstScale lqc	1
	1
c ESCMax HwNm f32	2.57999992
<pre>C_CLKAUseSlewCal_lgc</pre>	1
rolesselevicut_ge :_APADisableRateX_HwNm_u4p12[0]	19661
APADisableRateX HwNm u4p12[1]	20070
:_APADisableRateX_HwNm_u4p12[2]	20480
: APADisableRateX_HwNm_u4p12[3]	20890
: APADisableRateX_HwNm_u4p12[3]	21299
	21709
:_APADisableRateX_HwNm_u4p12[5]	21709 24576
t_APADisableRateY_pSec_u7p9[0]	
t_APADisableRateY_pSec_u7p9[1]	25088
t_APADisableRateY_pSec_u7p9[2]	25600
t_APADisableRateY_pSec_u7p9[3]	26112
t_APADisableRateY_pSec_u7p9[4]	26624
t_APADisableRateY_pSec_u7p9[5]	27136
t_APASmoothX_Uls_u2p14[0]	7864
t_APASmoothX_Uls_u2p14[1]	8028
t_APASmoothX_Uls_u2p14[2]	8192
t_APASmoothX_Uls_u2p14[3]	8356
t_APASmoothX_Uls_u2p14[4]	8520
t_APASmoothX_Uls_u2p14[5]	8684
t_APASmoothX_Uls_u2p14[6]	8847
t_APASmoothX_Uls_u2p14[7]	9011
t_APASmoothX_Uls_u2p14[8]	9175
t_APASmoothX_Uls_u2p14[9]	9339
t_APASmoothY_Uls_u2p14[0]	4915
t_APASmoothY_Uls_u2p14[1]	5079
t_APASmoothY_Uls_u2p14[2]	5243
t_APASmoothY_Uls_u2p14[3]	5407
t_APASmoothY_Uls_u2p14[4]	5571
t_APASmoothY_Uls_u2p14[5]	5734
t_APASmoothY_Uls_u2p14[6]	5898
t_APASmoothY_Uls_u2p14[7]	6062
t_APASmoothY_Uls_u2p14[8]	6226
t_APASmoothY_Uls_u2p14[9]	6390
t LKASlewX Kph u8p8[0]	24576
	25088
totolomypn_usps[1] :_LKASlewX_Kph_u8p8[2]	25600
:_LKASlewX_Kph_u8p8[3]	26112
trace.wx_qpi_dopo(o) :_LKASlewX_Kph_u8p8[4]	26624
:_LKASlewX_Kph_u8p8[5]	27136
_LKASlewY_NmpSec_u4p12[0]	19661
LKASlewY_NmpSec_u4p12[1]	20070
:_LKASlewY_NmpSec_u4p12[1]	20480
:_LKASlewY_NmpSec_u4p12[3]	20890
:_LKASlewY_NmpSec_u4p12[4]	21299
:_LKASlewY_NmpSec_u4p12[5]	21709
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.21999979		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.0500002		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	276.942993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.8999998		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0189999938	0.0189999994 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34700012	6.34700012 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-6.53999996	-6.53999996 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.99039984	3.98939991 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.994300246	0.994300008 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.994300246	0.994300008 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.0372764729	-0.037276499 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.994300246	0.994300008 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.50 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.150000006
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6.80999994
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.111000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.46000004
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.80000019
k_APAEnableRate_pSec_f32	222
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	20070
t_APADisableRateX_HwNm_u4p12[1]	20480
t_APADisableRateX_HwNm_u4p12[2]	20890
t_APADisableRateX_HwNm_u4p12[3]	21299
t_APADisableRateX_HwNm_u4p12[4]	21709
t_APADisableRateX_HwNm_u4p12[5]	22118
t_APADisableRateY_pSec_u7p9[0]	25088
t_APADisableRateY_pSec_u7p9[1]	25600
t_APADisableRateY_pSec_u7p9[2]	26112
t_APADisableRateY_pSec_u7p9[3]	26624
t_APADisableRateY_pSec_u7p9[4]	27136

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	27648		
t_APASmoothX_Uls_u2p14[0]	8028		
t_APASmoothX_Uls_u2p14[1]	8192		
t_APASmoothX_Uls_u2p14[2]	8356		
t_APASmoothX_Uls_u2p14[3]	8520		
t_APASmoothX_Uls_u2p14[4]	8684 8847		
t_APASmoothX_Uls_u2p14[5] t_APASmoothX_Uls_u2p14[6]	9011		
t_APASmoothX_Uls_u2p14[7]	9175		
t_APASmoothX_Uls_u2p14[8]	9339		
t_APASmoothX_Uls_u2p14[9]	9503		
t_APASmoothY_Uls_u2p14[0]	5079		
t_APASmoothY_Uls_u2p14[1]	5243		
t_APASmoothY_Uls_u2p14[2]	5407		
t_APASmoothY_Uls_u2p14[3]	5571		
t_APASmoothY_Uls_u2p14[4]	5734		
t_APASmoothY_Uls_u2p14[5]	5898		
t_APASmoothY_Uls_u2p14[6] t_APASmoothY_Uls_u2p14[7]	6062 6226		
t_APASmoothY_Uls_u2p14[8]	6390		
t APASmoothY Uls u2p14[9]	6554		
t LKASlewX Kph u8p8[0]	25088		
t_LKASlewX_Kph_u8p8[1]	25600		
t_LKASlewX_Kph_u8p8[2]	26112		
t_LKASlewX_Kph_u8p8[3]	26624		
t_LKASlewX_Kph_u8p8[4]	27136		
t_LKASlewX_Kph_u8p8[5]	27648		
t_LKASlewY_NmpSec_u4p12[0]	20070		
t_LKASlewY_NmpSec_u4p12[1]	20480		
t_LKASlewY_NmpSec_u4p12[2] t_LKASlewY_NmpSec_u4p12[3]	20890 21299		
t_LKASlewY_NmpSec_u4p12[4]	21709		
t_LKASlewY_NmpSec_u4p12[5]	22118		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	22118 target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uis_132 target_TrqArblim_Per1_DampingDDFactor_Uls_132		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESClsLimited_Cnt_lgc$	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32 target_TrgArblim_Per1_lgTrgOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_lqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_lqTrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_IrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_frqArblim_Per1_minSecureveniciespeed_kpn_f32 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_trqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7 82999992		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.82999992		
target_TrqArblim_Per1_LKAState_State_enum.value	10.21		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	283.393005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.70000005		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.594000041	0.593999982 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.92322016	6.92322016 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.5	2.5 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.79400063	-8.79399967 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	1 ± 0000390625	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.000390625	
terret Transhim Dari LKATerrus Delivered Livibles 622 value	2	3 + 0 0000300635	

-3

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

-3 ± 0.0000390625





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.594036281	0.594035983 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

est Step 3.51 (Repeat Count = 1)	Innut Value
lame	Input Value
tte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.174999997
rqArblim_ESC_State_Cnt_M_enum	4
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.36999989
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.122000001
rqArblim_PosSrvoCmd_MtrNm_M_f32	2.75
rqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019
_APALIca APauro Cook - Inc.	226.5
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1 0.140000001
_ESCMax_HwNm_f32	
_LKAUseSlewCal_lgc	1
APADisableRateX_HwNm_u4p12[0]	20480
APADisableRateX_HwNm_u4p12[1]	20890
APADisableRateX_HwNm_u4p12[2]	21299
APADisableRateX_HwNm_u4p12[3]	21709
APADisableRateX_HwNm_u4p12[4]	22118
_APADisableRateX_HwNm_u4p12[5]	22528
APADisableRateY_pSec_u7p9[0]	25600
APADisableRateY_pSec_u7p9[1]	26112
APADisableRateY_pSec_u7p9[2]	26624
_APADisableRateY_pSec_u7p9[3]	27136
_APADisableRateY_pSec_u7p9[4]	27648
APADisableRateY_pSec_u7p9[5]	28160
_APASmoothX_UIs_u2p14[0]	8192
APASmoothX_UIs_u2p14[1]	8356
_APASmoothX_UIs_u2p14[2]	8520
APASmoothX_Uls_u2p14[3]	8684
APASmoothX_Uls_u2p14[4]	8847
APASmoothX_Uls_u2p14[5]	9011
_APASmoothX_Uls_u2p14[6]	9175
APASmoothX_Uls_u2p14[7]	9339
_APASmoothX_Uls_u2p14[8]	9503
APASmoothX_Uls_u2p14[9]	9667
_APASmoothY_Uls_u2p14[0]	5243
_APASmoothY_Uls_u2p14[1]	5407
_APASmoothY_Uls_u2p14[2]	5571
_APASmoothY_Uls_u2p14[3]	5734
APASmoothY_Uls_u2p14[4]	5898
APASmoothY_Uls_u2p14[5]	6062
_APASmoothY_UIs_u2p14[6]	6226
_APASmoothY_Uls_u2p14[7]	6390
_APASmoothY_Uls_u2p14[8]	6554
_APASmoothY_UIs_u2p14[9]	6717
_LKASlewX_Kph_u8p8[0]	25600
_LKASlewX_Kph_u8p8[1]	26112
_LKASlewX_Kph_u8p8[2]	26624
_LKASlewX_Kph_u8p8[3]	27136
_LKASlewX_Kph_u8p8[4]	27648
LKASlewX_Kph_u8p8[5]	28160
_LKASlewY_NmpSec_u4p12[0]	20480
LKASlewY_NmpSec_u4p12[1]	20890
LKASlewY_NmpSec_u4p12[2]	21299
LKASlewY_NmpSec_u4p12[3]	21709
LKASlewY_NmpSec_u4p12[4]	22118
_LKASlewY_NmpSec_u4p12[5]	22528
	target_TrqArblim_Per1_AssistDDFactor_Uls_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_6	enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliver	red_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_L	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	m_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.07999992		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.43999958		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	0.230000004		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	289.842987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.0649999902	0.0649999976 ± 0000390625	-
TrqArblim_ESC_State_Cnt_M_enum	0	0	-
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.50053978	7.50053978 ± 0.03125	-
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.75	2.75 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	8.78999996	8.78899956 ± 0.03125	-
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.979199529	0.979200006 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.979199529	0.979200006 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	•
	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0.0572012998

0.979199529

0.0572012998 ± 0000390625

0.979200006 ± 0000390625

Test Step 3.52 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.200000003	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.92999983	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133000001	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.03999996	
TrqArblim_PreLKATrqReq_HwNm_M_f32	0	
k_APAEnableRate_pSec_f32	231	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	0.75	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	20890	
t_APADisableRateX_HwNm_u4p12[1]	21299	
t_APADisableRateX_HwNm_u4p12[2]	21709	
t_APADisableRateX_HwNm_u4p12[3]	22118	
t_APADisableRateX_HwNm_u4p12[4]	22528	
t_APADisableRateX_HwNm_u4p12[5]	22938	
t_APADisableRateY_pSec_u7p9[0]	26112	
t_APADisableRateY_pSec_u7p9[1]	26624	

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	27136		
t_APADisableRateY_pSec_u7p9[3]	27648		
t_APADisableRateY_pSec_u7p9[4]	28160		
t_APADisableRateY_pSec_u7p9[5]	28672		
t_APASmoothX_Uls_u2p14[0]	8356		
t_APASmoothX_Uls_u2p14[1]	8520		
t_APASmoothX_Uls_u2p14[2]	8684		
t_APASmoothX_UIs_u2p14[3]	8847		
t_APASmoothX_Uls_u2p14[4] t_APASmoothX_Uls_u2p14[5]	9011 9175		
t_APASmoothX_Uls_u2p14[6]	9339		
t_APASmoothX_Uls_u2p14[7]	9503		
t_APASmoothX_Uls_u2p14[8]	9667		
t_APASmoothX_Uls_u2p14[9]	9830		
t_APASmoothY_Uls_u2p14[0]	5407		
t_APASmoothY_Uls_u2p14[1]	5571		
t_APASmoothY_Uls_u2p14[2]	5734		
t_APASmoothY_Uls_u2p14[3]	5898		
t_APASmoothY_Uls_u2p14[4]	6062		
t_APASmoothY_UIs_u2p14[5]	6226		
t_APASmoothY_Uls_u2p14[6] t_APASmoothY_Uls_u2p14[7]	6390 6554		
t_APASmoothY_Uls_u2p14[8]	6717		
t APASmoothY Uls u2p14[9]	6881		
t_LKASlewX_Kph_u8p8[0]	26112		
t_LKASlewX_Kph_u8p8[1]	26624		
t_LKASlewX_Kph_u8p8[2]	27136		
t_LKASlewX_Kph_u8p8[3]	27648		
t_LKASlewX_Kph_u8p8[4]	28160		
t_LKASlewX_Kph_u8p8[5]	28672		
t_LKASlewY_NmpSec_u4p12[0]	20890		
t_LKASlewY_NmpSec_u4p12[1] t_LKASlewY_NmpSec_u4p12[2]	21299 21709		
t_LKASlewY_NmpSec_u4p12[3]	22118		
t_LKASlewY_NmpSec_u4p12[4]	22528		
t_LKASlewY_NmpSec_u4p12[5]	22938		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt	_*	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_6		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDeliver		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_target_TrqArblim_Per1_IqTrqOv_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target TrqArblim Per1 LKACmd HwNm		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target TrgArblim Per1 LKATorqueDeliver		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicle		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_C		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_Mtrf		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrN	m_t32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.5999999 1		
target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.05000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	1.12		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	296.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	2.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.29999995		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_UIs_M_f32	0.662	0.662 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	1	1	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.07895947	8.07896042 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2.9000001	2.9000001 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32 target TrqArblim Per1 AssistDDFactor UIs f32.value	1	0 ± 0.03125 1 ± 0000390625	
target_TrqArblim_Per1_AssistDbPactor_Uls_f32.value	1	1 ± 0000390625	
		. = 555555525	





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.75	-0.75 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.75	-0.75 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.493716717	-0.493717015 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.53 (Repeat Count = 1)	Imput Voluce
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
FrqArblim_APADisableRate_Uls_M_f32	0.224999994
rqArblim_ESC_State_Cnt_M_enum	1
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.48999977
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.143999994
rqArblim_PosSrvoCmd_MtrNm_M_f32	3.32999992
FrqArblim_PreLKATrqReq_HwNm_M_f32	4.5999999
CAPAEnableRate_pSec_f32	235.5
x_APAUseADmpScale_lgc	0
<pre>x_APAUseAsstScale_lgc</pre>	0
x_APAUseRetScale_lgc	0
ESCMax_HwNm_f32	1.27999997
x_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	21299
_APADisableRateX_HwNm_u4p12[1]	21709
_APADisableRateX_HwNm_u4p12[2]	22118
_APADisableRateX_HwNm_u4p12[3]	22528
_APADisableRateX_HwNm_u4p12[4]	22938
_APADisableRateX_HwNm_u4p12[5]	23347
_APADisableRateY_pSec_u7p9[0]	26624
_APADisableRateY_pSec_u7p9[1]	27136
_APADisableRateY_pSec_u7p9[2]	27648
_APADisableRateY_pSec_u7p9[3]	28160
_APADisableRateY_pSec_u7p9[4]	28672
_APADisableRateY_pSec_u7p9[5]	29184
_APASmoothX_Uls_u2p14[0]	8520
_APASmoothX_Uls_u2p14[1]	8684
_APASmoothX_Uls_u2p14[2]	8847
_APASmoothX_Uls_u2p14[3]	9011
_APASmoothX_Uls_u2p14[4]	9175
_APASmoothX_Uls_u2p14[5]	9339
_APASmoothX_Uls_u2p14[6]	9503
_APASmoothX_Uls_u2p14[7]	9667
_APASmoothX_Uls_u2p14[8]	9830
_APASmoothX_Uls_u2p14[9]	9994
_APASmoothY_Uls_u2p14[0]	5571
_APASmoothY_Uls_u2p14[1]	5734
_APASmoothY_Uls_u2p14[2]	5898
_APASmoothY_Uls_u2p14[3]	6062
_APASmoothY_Uls_u2p14[4]	6226
_APASmoothY_Uls_u2p14[5]	6390
_APASmoothY_Uls_u2p14[6]	6554
_APASmoothY_Uls_u2p14[7]	6717
_APASmoothY_Uls_u2p14[8]	6881
_APASmoothY_Uls_u2p14[9]	7045
_LKASlewX_Kph_u8p8[0]	26624
_LKASlewX_Kph_u8p8[1]	27136
_LKASlewX_Kph_u8p8[2]	27648
_LKASlewX_Kph_u8p8[3]	28160
_LKASlewX_Kph_u8p8[4]	28672
_LKASlewX_Kph_u8p8[5]	29184
_LKASlewY_NmpSec_u4p12[0]	21299
_LKASlewY_NmpSec_u4p12[1]	21709
_LKASlewY_NmpSec_u4p12[2]	22118
_LKASlewY_NmpSec_u4p12[3]	22528
_LKASlewY_NmpSec_u4p12[4]	22938





Name	Input Value		
t_LKASlewY_NmpSec_u4p12[5]	23347		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f3:	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3	2 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-7.11999989		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.65999985		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.00999999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	302.743011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-1.10000002		
Name	Actual Value Expected	Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.110999987 0.11100000	01 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	2 2		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.65847969 8.65847969	9 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.32999992 3.32999992	2 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.59399986 4.59399986	3 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1 1 ± 000039	0625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1 1 ± 000039	0625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1 1		
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997 -1.2799999	7 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997 -1.2799999	7 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0 0 ± 0.00003	390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.125684112 0.12568399	93 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1 1 ± 000039	0625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.54 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.25	
TrqArblim_ESC_State_Cnt_M_enum	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.61999989	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.5999999	
k_APAEnableRate_pSec_f32	240	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	3.69000006	
k_LKAUseSlewCal_lgc	1	
t_APADisableRateX_HwNm_u4p12[0]	21709	
t_APADisableRateX_HwNm_u4p12[1]	22118	
t_APADisableRateX_HwNm_u4p12[2]	22528	
t_APADisableRateX_HwNm_u4p12[3]	22938	
t APADisableRateX HwNm u4p12[4]	23347	





1 · = ·			
Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	23757		
t_APADisableRateY_pSec_u7p9[0]	27136		
t_APADisableRateY_pSec_u7p9[1]	27648		
t_APADisableRateY_pSec_u7p9[2]	28160		
t_APADisableRateY_pSec_u7p9[3]	28672		
t_APADisableRateY_pSec_u7p9[4]	29184		
t_APADisableRateY_pSec_u7p9[5]	29696		
t_APASmoothX_UIs_u2p14[0]	8684		
t_APASmoothX_UIs_u2p14[1]	8847		
t_APASmoothX_UIs_u2p14[2]	9011		
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	9175 9339		
t APASmoothX Uls u2p14[5]	9503		
t_APASmoothX_UIs_u2p14[6]	9667		
t_APASmoothX_UIs_u2p14[7]	9830		
t_APASmoothX_Uls_u2p14[8]	9994		
t_APASmoothX_Uls_u2p14[9]	10158		
t_APASmoothY_Uls_u2p14[0]	5734		
t_APASmoothY_Uls_u2p14[1]	5898		
t_APASmoothY_Uls_u2p14[2]	6062		
t_APASmoothY_Uls_u2p14[3]	6226		
t_APASmoothY_Uls_u2p14[4]	6390		
t_APASmoothY_Uls_u2p14[5]	6554		
t_APASmoothY_Uls_u2p14[6]	6717		
t_APASmoothY_Uls_u2p14[7]	6881		
t_APASmoothY_Uls_u2p14[8]	7045		
t_APASmoothY_Uls_u2p14[9]	7209		
t_LKASlewX_Kph_u8p8[0]	27136		
t_LKASlewX_Kph_u8p8[1]	27648		
t_LKASlewX_Kph_u8p8[2]	28160		
t_LKASlewX_Kph_u8p8[3]	28672		
t_LKASlewX_Kph_u8p8[4]	29184		
t_LKASlewX_Kph_u8p8[5]	29696		
t_LKASlewY_NmpSec_u4p12[0] t_LKASlewY_NmpSec_u4p12[1]	21709 22118		
t_LKASlewY_NmpSec_u4p12[1]	22528		
t_LKASlewY_NmpSec_u4p12[3]	22938		
t_LKASlewY_NmpSec_u4p12[4]	23347		
t_LKASlewY_NmpSec_u4p12[5]	23757		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target_TrqArblim_Per1_DampingDDFactor_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_le	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_ -6.63999987	_102	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987		
target_TrqArblim_Per1_ESCState_State_enum.value target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.9000001		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	309.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.89999976		
Name	Actual Value	Expected Value	Resu
		0.730000019 ± 0000390625	
TrgArblim APADisableRate Uls M f32	0.730000019		
TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum	0.730000019	3	•
		3 7.74800014 ± 0.03125	
TrqArblim_ESC_State_Cnt_M_enum	3		





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.159965992	0.159966007 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	✓

Manage Control of the	Insura Value
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.275000006
rqArblim_ESC_State_Cnt_M_enum	3
rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.60999966
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.165999994
rqArblim_PosSrvoCmd_MtrNm_M_f32	3.91000009
rqArblim_PreLKATrqReq_HwNm_M_f32	-8.10000038
_APAEnableRate_pSec_f32	0
_APAUseADmpScale_lgc	0
_APAUseAsstScale_lgc	0
_APAUseRetScale_lgc	0
z_ESCMax_HwNm_f32	2.75
_LKAUseSlewCal_lgc	0
_APADisableRateX_HwNm_u4p12[0]	22118
_APADisableRateX_HwNm_u4p12[1]	22528
_APADisableRateX_HwNm_u4p12[2]	22938
_APADisableRateX_HwNm_u4p12[3]	23347
_APADisableRateX_HwNm_u4p12[4]	23757
APADisableRateX HwNm u4p12[5]	24166
_APADisableRateY_pSec_u7p9[0]	27648
_APADisableRateY_pSec_u7p9[1]	28160
	28672
_APADisableRateY_pSec_u7p9[2]	
_APADisableRateY_pSec_u7p9[3]	29184
_APADisableRateY_pSec_u7p9[4]	29696
_APADisableRateY_pSec_u7p9[5]	30208
_APASmoothX_Uls_u2p14[0]	8847
_APASmoothX_Uls_u2p14[1]	9011
_APASmoothX_Uls_u2p14[2]	9175
_APASmoothX_Uls_u2p14[3]	9339
_APASmoothX_Uls_u2p14[4]	9503
_APASmoothX_Uls_u2p14[5]	9667
_APASmoothX_Uls_u2p14[6]	9830
_APASmoothX_Uls_u2p14[7]	9994
_APASmoothX_Uls_u2p14[8]	10158
_APASmoothX_Uls_u2p14[9]	10322
_APASmoothY_Uls_u2p14[0]	5898
_APASmoothY_Uls_u2p14[1]	6062
_APASmoothY_Uls_u2p14[2]	6226
_APASmoothY_Uls_u2p14[3]	6390
_APASmoothY_Uls_u2p14[4]	6554
_APASmoothY_Uls_u2p14[5]	6717
APASmoothY Uls u2p14[6]	6881
APASmoothY Uls u2p14[7]	7045
_APASmoothY_Uls_u2p14[8]	7209
APASmoothY Uls u2p14[9]	7373
_KKASIlewX_Kph_u8p8[0]	27648
_LKASlewX_Kph_u8p8[1]	28160
_LKASlewX_Kph_u8p8[2]	28672
_LKASlewX_Kph_u8p8[3]	29184
_LKASlewX_Kph_u8p8[4]	29696
_LKASlewX_Kph_u8p8[5]	30208
_LKASlewY_NmpSec_u4p12[0]	22118
_LKASlewY_NmpSec_u4p12[1]	22528





Name	Input Value		
t_LKASlewY_NmpSec_u4p12[2]	22938		
t_LKASlewY_NmpSec_u4p12[3]	23347		
t_LKASlewY_NmpSec_u4p12[4]	23757		
t_LKASlewY_NmpSec_u4p12[5]	24166		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor	_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFac	ctor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_0	Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State	e_enum	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_ESCTorqueDeli	vered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate	te_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwN	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNr	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State	e_enum	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_LKATorqueDeli	vered_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehic	cleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable	_Cnt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_N	/ltrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFacto	r_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_Mt	trNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.15999985		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.21000004		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	3.78999996		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	315.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.69999988		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.157000005	0.157000005 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.21560001	8.21560001 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.91000009	3.91000009 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.09400082	-8.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0.479015768	-0.479016006 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.56 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.300000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.35000038
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.177000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.19999981
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.0999999
k_APAEnableRate_pSec_f32	500
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938

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Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	23347		
t_APADisableRateX_HwNm_u4p12[3]	23757		
t_APADisableRateX_HwNm_u4p12[4]	24166		
t_APADisableRateX_HwNm_u4p12[5]	24576		
t APADisableRateY pSec u7p9[0]	28160		
t_APADisableRateY_pSec_u7p9[1]	28672		
t_APADisableRateY_pSec_u7p9[2]	29184		
	29696		
t_APADisableRateY_pSec_u7p9[3]	30208		
t_APADisableRateY_pSec_u7p9[4]			
t_APADisableRateY_pSec_u7p9[5]	30720		
t_APASmoothX_Uls_u2p14[0]	9011		
t_APASmoothX_Uls_u2p14[1]	9175		
t_APASmoothX_Uls_u2p14[2]	9339		
t_APASmoothX_Uls_u2p14[3]	9503		
t_APASmoothX_Uls_u2p14[4]	9667		
t_APASmoothX_Uls_u2p14[5]	9830		
t_APASmoothX_Uls_u2p14[6]	9994		
t_APASmoothX_Uls_u2p14[7]	10158		
t_APASmoothX_Uls_u2p14[8]	10322		
t_APASmoothX_UIs_u2p14[9]	10486		
t_APASmoothY_Uls_u2p14[0]	6062		
t_APASmoothY_Uls_u2p14[1]	6226		
t_APASmoothY_Uls_u2p14[2]	6390		
t_APASmoothY_Uls_u2p14[3]	6554		
t_APASmoothY_Uls_u2p14[4]	6717		
t_APASmoothY_Uls_u2p14[5]	6881		
t_APASmoothY_Uls_u2p14[6]	7045		
t_APASmoothY_Uls_u2p14[7]	7209		
t_APASmoothY_Uls_u2p14[8]	7373		
t_APASmoothY_Uls_u2p14[9]	7537		
t_LKASlewX_Kph_u8p8[0]	28160		
t_LKASlewX_Kph_u8p8[1]	28672		
t_LKASlewX_Kph_u8p8[2]	29184		
t_LKASlewX_Kph_u8p8[3]	29696 30208		
t_LKASlewX_Kph_u8p8[4]			
t_LKASlewX_Kph_u8p8[5]	30720		
t_LKASlewY_NmpSec_u4p12[0]	22528		
t_LKASlewY_NmpSec_u4p12[1]	22938		
t_LKASlewY_NmpSec_u4p12[2]	23347		
t_LKASlewY_NmpSec_u4p12[3]	23757		
t_LKASlewY_NmpSec_u4p12[4]	24166		
t_LKASlewY_NmpSec_u4p12[5]	24576		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target TrqArblim Per1 OpTrqOv MtrNm f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target TrqArblim Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.67999983		
	0		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	1.76999998		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.67999983		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	322.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.70000005		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.180000007	0.180000007 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓

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Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.38176012	-7.38176012 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.19999981	4.19999981 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-7.08899975	-7.08799982 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.933400869	0.933400989 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.933400869	0.933400989 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.279716313	0.279716015 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.933400869	0.933400989 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
FrgArblim APADisableRate Uls M f32	0.324999988	
FrqArblim_ESC_State_Cnt_M_enum	0.324939900	
FrqArblim_E3C_3tate_Gft_W_effulfi FrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-9	
	0.187999994	
FrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	4.48999977	
rqArblim_PosSrvoCmd_MtrNm_M_f32	-6.099999	
rqArblim_PreLKATrqReq_HwNm_M_f32		
APALICA Proposition of the second of the sec	250.339996	
_APAUseADmpScale_lgc	0	
_APAUseAsstScale_lgc	0	
APAUseRetScale_lgc	0	
ESCMax_HwNm_f32	3.24000001	
_LKAUseSlewCal_lgc	0	
_APADisableRateX_HwNm_u4p12[0]	22938	
_APADisableRateX_HwNm_u4p12[1]	23347	
_APADisableRateX_HwNm_u4p12[2]	23757	
_APADisableRateX_HwNm_u4p12[3]	24166	
_APADisableRateX_HwNm_u4p12[4]	24576	
_APADisableRateX_HwNm_u4p12[5]	24986	
_APADisableRateY_pSec_u7p9[0]	28672	
_APADisableRateY_pSec_u7p9[1]	29184	
_APADisableRateY_pSec_u7p9[2]	29696	
_APADisableRateY_pSec_u7p9[3]	30208	
_APADisableRateY_pSec_u7p9[4]	30720	
_APADisableRateY_pSec_u7p9[5]	31232	
_APASmoothX_Uls_u2p14[0]	9175	
_APASmoothX_Uls_u2p14[1]	9339	
_APASmoothX_Uls_u2p14[2]	9503	
_APASmoothX_Uls_u2p14[3]	9667	
_APASmoothX_Uls_u2p14[4]	9830	
_APASmoothX_Uls_u2p14[5]	9994	
_APASmoothX_Uls_u2p14[6]	10158	
_APASmoothX_Uls_u2p14[7]	10322	
_APASmoothX_Uls_u2p14[8]	10486	
_APASmoothX_Uls_u2p14[9]	10650	
APASmoothY_Uls_u2p14[0]	6226	
_APASmoothY_Uls_u2p14[1]	6390	
_APASmoothY_Uls_u2p14[2]	6554	
_APASmoothY_Uls_u2p14[3]	6717	
_APASmoothY_Uls_u2p14[4]	6881	
_APASmoothY_Uls_u2p14[5]	7045	
_APASmoothY_Uls_u2p14[6]	7209	
_APASmoothY_Uls_u2p14[7]	7373	
_APASmoothY_Uls_u2p14[8]	7537	
APASmoothY Uls u2p14[9]	7700	
LKASlewX Kph u8p8[0]	28672	
_LKASlewX_Kph_u8p8[1]	29184	
_LKASlewX_Kph_u8p8[2]	29696	
_LKASlewX_Kph_u8p8[3]	30208	
_LKASlewX_Kph_u8p8[4]	30720	

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TrqArblim_Per1

TrqArbiim_Per1			MACILAB
Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	31232		
t LKASlewY NmpSec u4p12[0]	22938		
t_LKASlewY_NmpSec_u4p12[1]	23347		
t LKASlewY NmpSec u4p12[2]	23757		
t_LKASlewY_NmpSec_u4p12[3]	24166		
t_LKASlewY_NmpSec_u4p12[4]	24576		
t_LKASlewY_NmpSec_u4p12[5]	24986		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	1_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.19999981		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.32999992		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	5.57000017		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	328.542999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.300000012		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.825680017	0.825680017 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.86995983	-6.86995983 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.9000001	3.9000001 ± 0.00024414062	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-6.09399986	-6.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.24000001	-3.24000001 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.51337719	1.51338005 ± 0000390625	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~	
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~	

1 ± 0000390625

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Test Step 3.58 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.349999994	
TrqArblim_ESC_State_Cnt_M_enum	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.64999962	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.199000001	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.78000021	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.0999999	
k_APAEnableRate_pSec_f32	246	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	1.58000004	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value





Name	Input Value
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	23347
t_APADisableRateX_HwNm_u4p12[1]	23757 24166
t_APADisableRateX_HwNm_u4p12[2] t_APADisableRateX_HwNm_u4p12[3]	24576
t_APADisableRateX_HwNm_u4p12[4]	24986
t_APADisableRateX_HwNm_u4p12[5]	25395
t_APADisableRateY_pSec_u7p9[0]	29184
t_APADisableRateY_pSec_u7p9[1]	29696
t_APADisableRateY_pSec_u7p9[2]	30208
t_APADisableRateY_pSec_u7p9[3]	30720
t_APADisableRateY_pSec_u7p9[4]	31232
t_APADisableRateY_pSec_u7p9[5] t_APASmoothX_Uls_u2p14[0]	31744 9339
t_APASmoothX_UIs_u2p14[1]	9503
t_APASmoothX_Uls_u2p14[2]	9667
t_APASmoothX_Uls_u2p14[3]	9830
t_APASmoothX_Uls_u2p14[4]	9994
t_APASmoothX_Uls_u2p14[5]	10158
t_APASmoothX_UIs_u2p14[6]	10322
t_APASmoothX_UIs_u2p14[7]	10486
t_APASmoothX_Uls_u2p14[8] t_APASmoothX_Uls_u2p14[9]	10650 10813
t APASmoothY Uls u2p14[9]	6390
t_APASmoothY_Uls_u2p14[1]	6554
t_APASmoothY_Uls_u2p14[2]	6717
t_APASmoothY_Uls_u2p14[3]	6881
t_APASmoothY_Uls_u2p14[4]	7045
t_APASmoothY_Uls_u2p14[5]	7209
t_APASmoothY_UIs_u2p14[6]	7373
t_APASmoothY_UIs_u2p14[7]	7537 7700
t_APASmoothY_Uls_u2p14[8] t_APASmoothY_Uls_u2p14[9]	7864
t_LKASlewX_Kph_u8p8[0]	29184
t_LKASlewX_Kph_u8p8[1]	29696
t_LKASlewX_Kph_u8p8[2]	30208
t_LKASlewX_Kph_u8p8[3]	30720
t_LKASlewX_Kph_u8p8[4]	31232
t_LKASlewX_Kph_u8p8[5]	31744
t_LKASlewY_NmpSec_u4p12[0]	23347 23757
t_LKASlewY_NmpSec_u4p12[1] t_LKASlewY_NmpSec_u4p12[2]	24166
t LKASlewY NmpSec u4p12[3]	24576
t_LKASlewY_NmpSec_u4p12[4]	24986
t_LKASlewY_NmpSec_u4p12[5]	25395
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_State_enum target TrqArblim Per1 ESCTorqueDelivered HwNm f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_E3CT0fquebelivered_nwinin_isz	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_trqArblim.TrqArblim_Per1_PosServEnable_Cnt_igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_IrqArblim_Per1_PosServEnable_Cnt_gc target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	2.8900001
	6.46000004
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	
target_TrqArblim_Per1_LKAState_State_enum.value	0
target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	334.993011
target_TrqArblim_Per1_LKAState_State_enum.value	





Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.842000008	0.842000008 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.35353947	-6.35353994 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	4.0999999	4.0999999 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-5.09399986	-5.09399986 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.58000004	-1.58000004 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.58000004	-1.58000004 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.5569886	1.55699003 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
FrqArblim_APADisableRate_Uls_M_f32	0.375	
FrqArblim_ESC_State_Cnt_M_enum	2	
FrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.3000019	
FrgArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.20999993	
rqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017	
rqArblim_PreLKATrqReq_HwNm_M_f32	-4.099999	
APAEnableRate pSec f32	255.75	
APAUseADmpScale Igc	1	
_APAUseAsstScale_lgc	1	
APAUseRetScale Igc	1	
ESCMax HwNm f32	4.28000021	
_ESCMAX_HWNII_IS2 LKAUseSlewCal lgc	1	
	23757	
_APADisableRateX_HwNm_u4p12[0]	24166	
_APADisableRateX_HwNm_u4p12[1]	24576	
_APADisableRateX_HwNm_u4p12[2]	24986	
_APADisableRateX_HwNm_u4p12[3]		
_APADisableRateX_HwNm_u4p12[4]	25395 25805	
_APADisableRateX_HwNm_u4p12[5]	29696	
_APADisableRateY_pSec_u7p9[0]	30208	
APADisableRateY_pSec_u7p9[1]	30720	
_APADisableRateY_pSec_u7p9[2]		
_APADisableRateY_pSec_u7p9[3]	31232	
_APADisableRateY_pSec_u7p9[4]	31744	
_APADisableRateY_pSec_u7p9[5]	32256	
_APASmoothX_UIs_u2p14[0]	9503	
APASmoothX_UIs_u2p14[1]	9667	
_APASmoothX_UIs_u2p14[2]	9830	
_APASmoothX_Uls_u2p14[3]	9994	
_APASmoothX_Uls_u2p14[4]	10158	
_APASmoothX_UIs_u2p14[5]	10322	
_APASmoothX_Uls_u2p14[6]	10486	
_APASmoothX_Uls_u2p14[7]	10650	
_APASmoothX_Uls_u2p14[8]	10813	
_APASmoothX_Uls_u2p14[9]	10977	
_APASmoothY_Uls_u2p14[0]	6554	
_APASmoothY_Uls_u2p14[1]	6717	
_APASmoothY_Uls_u2p14[2]	6881	
_APASmoothY_Uls_u2p14[3]	7045	
_APASmoothY_Uls_u2p14[4]	7209	
_APASmoothY_Uls_u2p14[5]	7373	
_APASmoothY_Uls_u2p14[6]	7537	
_APASmoothY_Uls_u2p14[7]	7700	
_APASmoothY_Uls_u2p14[8]	7864	
_APASmoothY_Uls_u2p14[9]	8028	
_LKASlewX_Kph_u8p8[0]	29696	
_LKASlewX_Kph_u8p8[1]	30208	
_LKASlewX_Kph_u8p8[2]	30720	

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Name	Input Value		
t_LKASlewX_Kph_u8p8[3]	31232		
t_LKASlewX_Kph_u8p8[4]	31744		
t_LKASlewX_Kph_u8p8[5]	32256		
t_LKASlewY_NmpSec_u4p12[0]	23757		
t_LKASlewY_NmpSec_u4p12[1]	24166		
t_LKASlewY_NmpSec_u4p12[2]	24576		
t_LKASlewY_NmpSec_u4p12[3]	24986		
t_LKASlewY_NmpSec_u4p12[4]	25395		
t_LKASlewY_NmpSec_u4p12[5]	25805		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	.lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3:	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	3.45000005		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	7.3499999		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	341.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.100000001		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	0.258355469	0.248999998 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	3	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.83250046	-5.83249998 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.07000017	5.07000017 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.08839989	-4.08739996 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.523976862	0.505002975 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.896651506	0.900394022 ± 0000390625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	•

Test Step 3.60 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.40000006	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.94999981	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.221000001	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.36000013	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-3.0999999	
k_APAEnableRate_pSec_f32	265.5	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	1	





Namo	Input Value
Name k_APAUseRetScale_lgc	Input Value
k ESCMax HwNm f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	24166
t_APADisableRateX_HwNm_u4p12[1]	24576
t_APADisableRateX_HwNm_u4p12[2]	24986
t_APADisableRateX_HwNm_u4p12[3]	25395
t_APADisableRateX_HwNm_u4p12[4]	25805
t_APADisableRateX_HwNm_u4p12[5]	26214 30208
t_APADisableRateY_pSec_u7p9[0] t APADisableRateY pSec u7p9[1]	30720
t_APADisableRateY_pSec_u7p9[2]	31232
t_APADisableRateY_pSec_u7p9[3]	31744
t_APADisableRateY_pSec_u7p9[4]	32256
t_APADisableRateY_pSec_u7p9[5]	32768
t_APASmoothX_Uls_u2p14[0]	9667
t_APASmoothX_Uls_u2p14[1]	9830
t_APASmoothX_UIs_u2p14[2]	9994
t_APASmoothX_UIs_u2p14[3]	10158 10322
t_APASmoothX_Uls_u2p14[4] t APASmoothX Uls u2p14[5]	10486
t_APASmoothX_Uls_u2p14[6]	10650
t_APASmoothX_UIs_u2p14[7]	10813
t_APASmoothX_Uls_u2p14[8]	10977
t_APASmoothX_UIs_u2p14[9]	11141
t_APASmoothY_Uls_u2p14[0]	6717
t_APASmoothY_Uls_u2p14[1]	6881
t_APASmoothY_UIs_u2p14[2]	7045 7209
t_APASmoothY_Uls_u2p14[3] t_APASmoothY_Uls_u2p14[4]	7373
t_APASmoothY_Uls_u2p14[5]	7537
t_APASmoothY_Uls_u2p14[6]	7700
t_APASmoothY_Uls_u2p14[7]	7864
t_APASmoothY_Uls_u2p14[8]	8028
t_APASmoothY_Uls_u2p14[9]	8192
t_LKASlewX_Kph_u8p8[0]	30208
t_LKASlewX_Kph_u8p8[1]	30720
t_LKASlewX_Kph_u8p8[2]	31232 31744
t_LKASlewX_Kph_u8p8[3] t_LKASlewX_Kph_u8p8[4]	32256
t_LKASlewX_Kph_u8p8[5]	32768
t LKASlewY_NmpSec_u4p12[0]	24166
t_LKASlewY_NmpSec_u4p12[1]	24576
t_LKASlewY_NmpSec_u4p12[2]	24986
t_LKASlewY_NmpSec_u4p12[3]	25395
t_LKASlewY_NmpSec_u4p12[4]	25805
t_LKASlewY_NmpSec_u4p12[5]	26214
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32 target_TrqArblim_Per1_ESCCmd_HwNm_f32
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCIsLimited Cnt Igc	target TrgArblim Per1 ESCIsLimited Cnt Igc
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LRATorqueDelivered_Hwinin_is2 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target TrgArblim Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	4.04000023
target_TrqArblim_Per1_HwTorque_HwNm_f32.value target_TrqArblim_Per1_LKACmd_HwNm_f32.value	4.01000023 8.23999977
target_IrqArblim_Per1_LKACmd_HwNm_r3z.value target_TrqArblim_Per1_LKAState_State_enum.value	8.23999977
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	347.893005
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0
	!

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

TrqArblim_Per1

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1 ± 0000390625

Input Value target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value 4.5 $target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value$ 0.30000012 **Actual Value Expected Value** Result TrqArblim_APADisableRate_Uls_M_f32 0.272000015 ± 0000390625 0.282000005 TrqArblim_ESC_State_Cnt_M_enum -5.30683994 TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -5.30683994 ± 0.03125 TrqArblim_PosSrvoCmd_MtrNm_M_f32 5.36000013 5.36000013 ± 0.000244140625 TrqArblim_PreLKATrqReq_HwNm_M_f32 -3.09399986 -3.0940001 ± 0.03125 target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value 0.884387553 0.888486981 ± 0000390625 target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value 1 ± 0000390625 $target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value$ 0 0 target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value 0 0 ± 0000390625 -3 ± 0.0000390625 target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value -3 target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value -3 -3 ± 0.0000390625 target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value 0.61968267 0.597707987 ± 0000390625

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

1

Test Step 3.61 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.25
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.05000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.61999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.5999999
k_APAEnableRate_pSec_f32	240
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	21709
t_APADisableRateX_HwNm_u4p12[1]	22118
t_APADisableRateX_HwNm_u4p12[2]	22528
t_APADisableRateX_HwNm_u4p12[3]	22938
t_APADisableRateX_HwNm_u4p12[4]	23347
t_APADisableRateX_HwNm_u4p12[5]	23757
t_APADisableRateY_pSec_u7p9[0]	27136
t_APADisableRateY_pSec_u7p9[1]	27648
t_APADisableRateY_pSec_u7p9[2]	28160
t_APADisableRateY_pSec_u7p9[3]	28672
t_APADisableRateY_pSec_u7p9[4]	29184
t_APADisableRateY_pSec_u7p9[5]	29696
t_APASmoothX_Uls_u2p14[0]	8684
t_APASmoothX_Uls_u2p14[1]	8847
t_APASmoothX_Uls_u2p14[2]	9011
t_APASmoothX_Uls_u2p14[3]	9175
t_APASmoothX_Uls_u2p14[4]	9339
t_APASmoothX_Uls_u2p14[5]	9503
t_APASmoothX_Uls_u2p14[6]	9667
t_APASmoothX_Uls_u2p14[7]	9830
t_APASmoothX_Uls_u2p14[8]	9994
t_APASmoothX_Uls_u2p14[9]	10158
t_APASmoothY_Uls_u2p14[0]	5734
t_APASmoothY_Uls_u2p14[1]	5898
t_APASmoothY_Uls_u2p14[2]	6062
t_APASmoothY_Uls_u2p14[3]	6226
t_APASmoothY_Uls_u2p14[4]	6390
t_APASmoothY_Uls_u2p14[5]	6554
t_APASmoothY_Uls_u2p14[6]	6717
t_APASmoothY_Uls_u2p14[7]	6881
t_APASmoothY_Uls_u2p14[8]	7045
t_APASmoothY_Uls_u2p14[9]	7209

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TrqArblim_Per1

Name	Input Value		
t_LKASlewX_Kph_u8p8[0]	27136		
t_LKASlewX_Kph_u8p8[1]	27648		
t_LKASlewX_Kph_u8p8[2]	28160		
t_LKASlewX_Kph_u8p8[3]	28672		
t_LKASlewX_Kph_u8p8[4]	29184		
t_LKASlewX_Kph_u8p8[5]	29696		
t_LKASlewY_NmpSec_u4p12[0]	21709		
t_LKASlewY_NmpSec_u4p12[1]	22118		
t_LKASlewY_NmpSec_u4p12[2]	22528		
t_LKASlewY_NmpSec_u4p12[3]	22938		
t_LKASlewY_NmpSec_u4p12[4]	23347		
t_LKASlewY_NmpSec_u4p12[5]	23757		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target_TrqArblim_Per1_GMOSHOscillate_S		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_HwTorque_HwNm_f	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target TrqArblim Per1 LKACmd HwNm f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State en		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target TrqArblim Per1 LKATorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987	_102	
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.900001		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.2999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32	0.730000019	0.730000019 ± 0000390625	ricount
TrqArblim ESC State Cnt M enum	3	3	✓
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	7.74800014	7.74800014 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	3.2999995	3.29999995 ± 0.00024414062	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.58939981	-4.58839989 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_UIs_f32.value	0.678798199	0.67879802 ± 0000390625	<u> </u>
target TrqArblim Per1 DampingDDFactor UIs f32.value	0.678798199	0.67879802 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	-
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target TrqArblim Per1 IqTrqOv HwNm f32.value	-3	-3 ± 0.000390625	-
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	
target TrqArblim Per1 OpTrqOv MtrNm f32.value	0.159965992	0.159966007 ± 0000390625	
targot_rryrabilit_r or r_opridov_ivitiviti_loz.value	0.10000002	5.155500007 I 0000590025	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

0.678798199

0.67879802 ± 0000390625

Test Step 3.62 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.44999988	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-7.25	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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Input Value
285
1
1
2.57999992
1
24986
25395
25805
26214
26624
27034 31232
31744
32256
32768
33280
33792
9994
10158
10322
10486 10650
10813
10977
11141
11305
11469
7045
7209
7373
7537 7700
7864
8028
8192
8356
8520
31232
31744
32256 32768
33280
33792
24986
25395
25805
26214
26624
27034
target_TrqArblim_Per1_AssistDDFactor_Uls_f32 target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_TrqArblim_Per1_ESCState_State_enum
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_TrqArblim_Per1_HwTorque_HwNm_f32
target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrqArblim_Per1_LKAState_State_enum
target_TrqArblim_Per1_LKAState_State_enum target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_TrqArblim_Per1_ReturnDDFactor_UIs_f32
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
-6.61999989
-6.61999989 1
-6.61999989

target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value\\ target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

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-2.57999992 ± 0000390625

0 ± 0.0000390625

-2.57999992 ± 0.0000390625

0.812222004 ± 0000390625

0.863261998 ± 0000390625

	Razon	at
Input Value		
2		
360.792999		
0		
4.9000001		
0.69999988		
Actual Value	Expected Value	Result
0.327999979	0.317999989 ± 0000390625	~
1	1	✓
-4.24166012	-4.24166012 ± 0.03125	~
5.94000006	5.94000006 ± 0.000244140625	✓
-1.08779979	-1.08679998 ± 0.03125	✓
0.858962417	0.863261998 ± 0000390625	•
1	1 ± 0000390625	~
1	1	✓
	2 360.792999 0 4.9000001 0.699999988 Actual Value 0.327999979 1 -4.24166012 5.94000006 -1.08779979	Input Value 2 360.792999 0 4.9000001 0.699999988 Actual Value 0.327999979 1 1 4.24166012 4.24166012 5.94000006 5.94000006 -1.08779979 -1.08679998 ± 0.00390625 0.858962417 0.863261998 ± 0000390625

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

-2.57999992

-2.57999992

0.83776325

0.858962417

Name	Input Value	
Rte Inst Ap TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrgArblim APADisableRate Uls M f32	0.474999994	
TrgArblim_ArAbisableRate_Ois_im_is2 TrgArblim ESC State Cnt M enum	1	
FrgArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.9000001 0.25400008	
FrgArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	6.23000002	
FrqArblim_PosSrvoCmd_MtrNm_M_f32	-0.10000001	
FrqArblim_PreLKATrqReq_HwNm_M_f32	294.75	
x_APALeaAPmpSeele_lea	1	
<_APAUseADmpScale_lgc	0	
<pre><_APAUseAsstScale_lgc</pre>		
«_APAUseRetScale_lgc	0	
x_ESCMax_HwNm_f32	0.239999995	
<pre>(_LKAUseSlewCal_lgc ABADiaphlaDataX_Llublar_u4p13(0)</pre>	0	
_APADisableRateX_HwNm_u4p12[0]	25395	
_APADisableRateX_HwNm_u4p12[1]	25805	
_APADisableRateX_HwNm_u4p12[2]	26214	
_APADisableRateX_HwNm_u4p12[3]	26624	
_APADisableRateX_HwNm_u4p12[4]	27034	
_APADisableRateX_HwNm_u4p12[5]	27443	
_APADisableRateY_pSec_u7p9[0]	31744	
_APADisableRateY_pSec_u7p9[1]	32256	
_APADisableRateY_pSec_u7p9[2]	32768	
_APADisableRateY_pSec_u7p9[3]	33280	
_APADisableRateY_pSec_u7p9[4]	33792	
_APADisableRateY_pSec_u7p9[5]	34304	
_APASmoothX_Uls_u2p14[0]	10158	
_APASmoothX_Uls_u2p14[1]	10322	
_APASmoothX_Uls_u2p14[2]	10486	
_APASmoothX_Uls_u2p14[3]	10650	
_APASmoothX_Uls_u2p14[4]	10813	
_APASmoothX_Uls_u2p14[5]	10977	
_APASmoothX_Uls_u2p14[6]	11141	
_APASmoothX_Uls_u2p14[7]	11305	
_APASmoothX_Uls_u2p14[8]	11469	
_APASmoothX_Uls_u2p14[9]	11633	
_APASmoothY_Uls_u2p14[0]	7209	
_APASmoothY_Uls_u2p14[1]	7373	
_APASmoothY_Uls_u2p14[2]	7537	
_APASmoothY_Uls_u2p14[3]	7700	
_APASmoothY_Uls_u2p14[4]	7864	
_APASmoothY_Uls_u2p14[5]	8028	
_APASmoothY_Uls_u2p14[6]	8192	





Name	Input Value		
t_APASmoothY_Uls_u2p14[7]	8356		
t_APASmoothY_Uls_u2p14[8]	8520		
t_APASmoothY_Uls_u2p14[9]	8684		
t LKASlewX Kph u8p8[0]	31744		
t LKASlewX Kph u8p8[1]	32256		
t_LKASlewX_Kph_u8p8[2]	32768		
t_LKASlewX_Kph_u8p8[3]	33280		
t_LKASlewX_Kph_u8p8[4]	33792		
t LKASlewX Kph u8p8[5]	34304		
t_LKASlewY_NmpSec_u4p12[0]	25395		
t_LKASlewY_NmpSec_u4p12[1]	25805		
t_LKASlewY_NmpSec_u4p12[2]	26214		
t_LKASlewY_NmpSec_u4p12[3]	26624		
t_LKASlewY_NmpSec_u4p12[4]	27034		
t_LKASlewY_NmpSec_u4p12[5]	27443		
target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor UIs f32	target TrqArblim Per1 AssistDDFactor Uls	: f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrgArblim Per1 GMOSHOscillate S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target TrqArblim Per1 HwTorque HwNm	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_Per1_LKACmd_HwNm_f32	target TrqArblim Per1 LKACmd HwNm f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi		
target_Rte_Inst_Ap_TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_UI		
target_Rte_Inst_Ap_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-5.42000008	1_132	
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.69000006		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2200003		
target TrqArblim Per1 LKAState State enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	367.243011		
target TrqArblim Per1 PosServEnable Cnt Igc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	5.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.899999976		
		Even at ad Value	Desul
Name Tandahira ADADirahlaData IIIa M 600	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	2	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.70213985	-3.70214009 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.0999999	5.0999999 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-0.0940000042	-0.0939999968 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.469970703	0.469971001 ± 0000390625	\ '
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.239999995	-0.239999995 ± 0000390625	,
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.239999995	-0.239999995 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.60314941	3.60314989 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	-

Test Step 3.64 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.5	
TrqArblim_ESC_State_Cnt_M_enum	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.55000019	

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Name	Input Value
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.264999986
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.51999998
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.89999976
k_APAEnableRate_pSec_f32	304.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc k_ESCMax_HwNm_f32	1 0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t_APADisableRateY_pSec_u7p9[0] t_APADisableRateY_pSec_u7p9[1]	32256 32768
t_APADisableRateY_pSec_u7p9[1]	33280
t_APADisableRateY_pSec_u7p9[3]	33792
t_APADisableRateY_pSec_u7p9[4]	34304
t_APADisableRateY_pSec_u7p9[5]	34816
t_APASmoothX_Uls_u2p14[0]	10322
t_APASmoothX_Uls_u2p14[1]	10486
t_APASmoothX_Uls_u2p14[2]	10650
t_APASmoothX_Uls_u2p14[3]	10813
t_APASmoothX_UIs_u2p14[4]	10977
t_APASmoothX_UIs_u2p14[5] t_APASmoothX_UIs_u2p14[6]	11305
t_APASmoothX_UIs_u2p14[7]	11469
t_APASmoothX_Uls_u2p14[8]	11633
t_APASmoothX_Uls_u2p14[9]	11796
t_APASmoothY_Uls_u2p14[0]	7373
t_APASmoothY_Uls_u2p14[1]	7537
t_APASmoothY_Uls_u2p14[2]	7700
t_APASmoothY_Uls_u2p14[3]	7864
t_APASmoothY_Uls_u2p14[4]	8028 8192
t_APASmoothY_Uls_u2p14[5] t_APASmoothY_Uls_u2p14[6]	8356
t APASmoothY Uls u2p14[7]	8520
t_APASmoothY_Uls_u2p14[8]	8684
t_APASmoothY_Uls_u2p14[9]	8847
t_LKASlewX_Kph_u8p8[0]	32256
t_LKASlewX_Kph_u8p8[1]	32768
t_LKASlewX_Kph_u8p8[2]	33280
t_LKASlewX_Kph_u8p8[3]	33792
t_LKASlewX_Kph_u8p8[4]	34304
t_LKASlewX_Kph_u8p8[5] t_LKASlewY_NmpSec_u4p12[0]	34816 25805
t_LKASlewY_NmpSec_u4p12[1]	26214
t LKASlewY NmpSec u4p12[2]	26624
t_LKASlewY_NmpSec_u4p12[3]	27034
t_LKASlewY_NmpSec_u4p12[4]	27443
t_LKASlewY_NmpSec_u4p12[5]	27853
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_inst_Ap_IrqArbiim.IrqArbiim_Per1_ESCIsLimited_Cnt_igc target_Rte_inst_Ap_TrqArbiim.TrqArbiim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target TrqArblim Per1 ESCState State enum
target Rte Inst Ap TrqArblim.TrqArblim Per ESCState_State_enum target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target TrqArblim Per1 ESCState_State_enum target TrqArblim Per1 ESCTorqueDelivered HwNm f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate State enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_inst_Ap_irqArbiim.irqArbiim_Peri_PosServEnable_Cnt_igc target_Rte_inst_Ap_irqArbiim.TrqArbiim_Peri_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServEnable_Cnt_Igc target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
gotclot_/ ip_11q/ ubiiii. 11q/ ubiiii_1 of 1_1 odo1vooiid_ivii1vii1_102	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor Uls f32	target TrqArblim Per1 ReturnDDFactor Uls f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32 target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32



Name	Input Value		
target TrgArblim Per1 GMOSHOscillate State enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	6.25		
target TrqArblim Per1 LKACmd HwNm f32.value	-9.86999989		
target TrqArblim Per1 LKAState State enum.value	2		
target TrgArblim Per1 MinSecureVehicleSpeed Kph f32.value	373.692993		
target TrqArblim Per1 PosServEnable Cnt Igc.value	1		
target TrqArblim Per1 PosSrvoCmd MtrNm f32.value	5.30000019		
	1.10000002		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value		I	1
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.15800023	-3.15799999 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.30000019	5.30000019 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	0.893999994	0.893999994 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.460021973	0.460022002 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.460021973	0.460022002 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0.893999994	0.893999994 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.86188364	2.86188006 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.460021973	0.460022002 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.65 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrgArblim APADisableRate Uls M f32	0.25
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.05000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.61999989
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.5999999
k_APAEnableRate_pSec_f32	240
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	21709
t_APADisableRateX_HwNm_u4p12[1]	22118
t_APADisableRateX_HwNm_u4p12[2]	22528
t_APADisableRateX_HwNm_u4p12[3]	22938
t_APADisableRateX_HwNm_u4p12[4]	23347
t_APADisableRateX_HwNm_u4p12[5]	23757
t_APADisableRateY_pSec_u7p9[0]	27136
t_APADisableRateY_pSec_u7p9[1]	27648
t_APADisableRateY_pSec_u7p9[2]	28160
t_APADisableRateY_pSec_u7p9[3]	28672
t_APADisableRateY_pSec_u7p9[4]	29184
t_APADisableRateY_pSec_u7p9[5]	29696
t_APASmoothX_Uls_u2p14[0]	8684
t_APASmoothX_Uls_u2p14[1]	8847
t_APASmoothX_Uls_u2p14[2]	9011
t_APASmoothX_Uls_u2p14[3]	9175
t_APASmoothX_Uls_u2p14[4]	9339
t_APASmoothX_Uls_u2p14[5]	9503
t_APASmoothX_Uls_u2p14[6]	9667
t_APASmoothX_Uls_u2p14[7]	9830
t_APASmoothX_Uls_u2p14[8]	9994
t_APASmoothX_Uls_u2p14[9]	10158
t_APASmoothY_Uls_u2p14[0]	5734
t_APASmoothY_Uls_u2p14[1]	5898
t_APASmoothY_Uls_u2p14[2]	6062
t_APASmoothY_Uls_u2p14[3]	6226

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Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	6390		
t APASmoothY Uls u2p14[5]	6554		
t_APASmoothY_Uls_u2p14[6]	6717		
t_APASmoothY_Uls_u2p14[7]	6881		
t_APASmoothY_Uls_u2p14[8]	7045		
t_APASmoothY_Uls_u2p14[9]	7209		
t_LKASlewX_Kph_u8p8[0]	27136		
t_LKASlewX_Kph_u8p8[1]	27648		
t_LKASlewX_Kph_u8p8[2]	28160		
t LKASlewX Kph u8p8[3]	28672		
t_LKASlewX_Kph_u8p8[4]	29184		
t_LKASlewX_Kph_u8p8[5]	29696		
t LKASlewY NmpSec u4p12[0]	21709		
t_LKASlewY_NmpSec_u4p12[1]	22118		
t_LKASlewY_NmpSec_u4p12[2]	22528		
t_LKASlewY_NmpSec_u4p12[3]	22938		
t_LKASlewY_NmpSec_u4p12[4]	23347		
t_LKASlewY_NmpSec_u4p12[5]	23757		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target TrqArblim Per1 AssistDDFactor UI	: f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistbDPactor_Ois_is2 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target TrqArblim Per1 ESCCmd HwNm f		
· · · ·	target TrqArblim Per1 ESCIsLimited Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		. •	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target_TrqArblim_Per1_ESCState_State_er target_TrqArblim_Per1_ESCTorqueDelivere		
	target TrqArblim Per1 GMOSHOscillate S		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum		_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32 target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3		
	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_er		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f3;			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	1_132	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-6.63999987		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	0.649999976		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	2.9000001		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	3.29999995		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-0.899999976		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.730000019	0.730000019 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	3	3	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.74800014	7.74800014 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	3.2999995	3.29999995 ± 0.00024414062	.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.58939981	-4.58839989 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.159965992	0.159966007 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.678798199	0.67879802 ± 0000390625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	-

Test Step 3.66 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim





Name	Input Value
TrqArblim_APADisableRate_Uls_M_f32	0.550000012
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.8499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.287
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.099999
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.9000001
k_APAEnableRate_pSec_f32	324
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	26624
t_APADisableRateX_HwNm_u4p12[1]	27034
t_APADisableRateX_HwNm_u4p12[2]	27443
t_APADisableRateX_HwNm_u4p12[3]	27853
t_APADisableRateX_HwNm_u4p12[4]	28262
t_APADisableRateX_HwNm_u4p12[5]	28672
t_APADisableRateY_pSec_u7p9[0]	33280
t_APADisableRateY_pSec_u7p9[1]	33792
t_APADisableRateY_pSec_u7p9[2]	34304
t_APADisableRateY_pSec_u7p9[3]	34816
t_APADisableRateY_pSec_u7p9[4]	35328
t_APADisableRateY_pSec_u7p9[5]	35840
t_APASmoothX_Uls_u2p14[0]	10650
t_APASmoothX_UIs_u2p14[1]	10813
t_APASmoothX_UIs_u2p14[2]	10977
t_APASmoothX_Uls_u2p14[3]	11141
t_APASmoothX_Uls_u2p14[4]	11305
t_APASmoothX_UIs_u2p14[5]	11469
t_APASmoothX_UIs_u2p14[6]	11633
t_APASmoothX_UIs_u2p14[7]	11796 11960
t_APASmoothX_Uls_u2p14[8] t_APASmoothX_Uls_u2p14[9]	12124
t_APASmoothY_Uls_u2p14[9]	7700
t_APASmoothY_Uls_u2p14[1]	7864
t_APASmoothY_Uls_u2p14[2]	8028
t_APASmoothY_Uls_u2p14[3]	8192
t_APASmoothY_Uls_u2p14[4]	8356
t_APASmoothY_Uls_u2p14[5]	8520
t_APASmoothY_Uls_u2p14[6]	8684
t_APASmoothY_Uls_u2p14[7]	8847
t_APASmoothY_Uls_u2p14[8]	9011
t_APASmoothY_Uls_u2p14[9]	9175
t_LKASlewX_Kph_u8p8[0]	0
t_LKASlewX_Kph_u8p8[1]	0
t_LKASlewX_Kph_u8p8[2]	0
t_LKASlewX_Kph_u8p8[3]	0
t_LKASlewX_Kph_u8p8[4]	0
t_LKASlewX_Kph_u8p8[5]	0
t_LKASlewY_NmpSec_u4p12[0]	26624
t_LKASlewY_NmpSec_u4p12[1]	27034
t_LKASlewY_NmpSec_u4p12[2]	27443
t_KASlewY_NmpSec_u4p12[3]	27853
t_KASlewY_NmpSec_u4p12[4]	28262
t_LKASlewY_NmpSec_u4p12[5] target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor Uls f32	28672
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uis_132 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32 target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_inst_Ap_rtqArblim.TrqArblim_Peri_bampingbbractor_ois_isz target_Rte_Inst_Ap_rtqArblim.TrqArblim_Peri_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_rrqArblim.TrqArblim_Per1_ESCCInd_nwnin_is2 target_Rte_Inst_Ap_rrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target TrqArblim Per1 ESCIsLimited Cnt Igc
target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_rite_inst_Ap_rrqArbiini_rerr_rerr_resorveenta_witrivin_isz	



Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscC	md_MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-1.82000005		
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.36999989		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-9.17000008		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	386.592987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	5.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.05586004	-2.05586004 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.69999981	5.69999981 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.88600016	2.88599992 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.440002441	0.440001994 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.440002441	0.440001994 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.88600016	2.88599992 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.88600016	2.88599992 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.19198608	3.1919899 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.440002441	0.440001994 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.67 (Repeat Count = 1)	🗸
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.574999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.5
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.298000008
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.38999987
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.9000001
k_APAEnableRate_pSec_f32	333.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	27034
t_APADisableRateX_HwNm_u4p12[1]	27443
t_APADisableRateX_HwNm_u4p12[2]	27853
t_APADisableRateX_HwNm_u4p12[3]	28262
t_APADisableRateX_HwNm_u4p12[4]	28672
t_APADisableRateX_HwNm_u4p12[5]	29082
t_APADisableRateY_pSec_u7p9[0]	33792
t_APADisableRateY_pSec_u7p9[1]	34304
t_APADisableRateY_pSec_u7p9[2]	34816
t_APADisableRateY_pSec_u7p9[3]	35328
t_APADisableRateY_pSec_u7p9[4]	35840
t_APADisableRateY_pSec_u7p9[5]	36352
t_APASmoothX_Uls_u2p14[0]	10813
t_APASmoothX_Uls_u2p14[1]	10977
t_APASmoothX_Uls_u2p14[2]	11141
t_APASmoothX_Uls_u2p14[3]	11305
t_APASmoothX_Uls_u2p14[4]	11469
t_APASmoothX_Uls_u2p14[5]	11633
t_APASmoothX_Uls_u2p14[6]	11796
t_APASmoothX_Uls_u2p14[7]	11960
t_APASmoothX_Uls_u2p14[8]	12124
t_APASmoothX_Uls_u2p14[9]	12288
t_APASmoothY_Uls_u2p14[0]	7864

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TrqArblim_Per1

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Name	Input Value		
t_APASmoothY_Uls_u2p14[1]	8028		
t_APASmoothY_Uls_u2p14[2]	8192		
t_APASmoothY_Uls_u2p14[3]	8356		
t_APASmoothY_Uls_u2p14[4]	8520		
t_APASmoothY_Uls_u2p14[5]	8684		
t_APASmoothY_Uls_u2p14[6]	8847		
t_APASmoothY_Uls_u2p14[7]	9011		
t_APASmoothY_Uls_u2p14[8]	9175		
t_APASmoothY_Uls_u2p14[9]	9339		
t_LKASlewX_Kph_u8p8[0]	56320		
t_LKASlewX_Kph_u8p8[1]	56320		
t_LKASlewX_Kph_u8p8[2]	56320		
t_LKASlewX_Kph_u8p8[3]	56320		
t_LKASlewX_Kph_u8p8[4]	56320		
t_LKASlewX_Kph_u8p8[5]	56320		
t_LKASlewY_NmpSec_u4p12[0]	27034		
t_LKASlewY_NmpSec_u4p12[1]	27443		
t_LKASlewY_NmpSec_u4p12[2]	27853		
t_LKASlewY_NmpSec_u4p12[3]	28262		
t_LKASlewY_NmpSec_u4p12[4]	28672		
t_LKASlewY_NmpSec_u4p12[5]	29082		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	- · -	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-0.620000005		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	7.92999983		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.81999969		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	393.042999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	5.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.442999989	0.442999989 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.49785995	-1.49785995 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.38999987	7.38999987 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.88679981	3.88579988 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.620000005	-0.620000005 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.620000005	-0.620000005 ± 0.0000390625	<u> </u>
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3 27134562	0 ± 0.0000390625	
target TrgArblim Per1 OnTrgOv MtrNm f32 value	3.2/134562	3 27134991 + 0000390625	V

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

3.27134562

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

3.27134991 ± 0000390625

1 ± 0000390625



Test Step 3.68 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target Rte Inst Ap TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.600000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.1500001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.308999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.67999983
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.900001
k_APALlarAPavaSallar	343.5
k_APAUseADmpScale_lgc k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k ESCMax HwNm f32	2.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	27443
t_APADisableRateX_HwNm_u4p12[1]	27853
t_APADisableRateX_HwNm_u4p12[2]	28262
t_APADisableRateX_HwNm_u4p12[3]	28672
t_APADisableRateX_HwNm_u4p12[4]	29082
t_APADisableRateX_HwNm_u4p12[5]	29491
t_APADisableRateY_pSec_u7p9[0]	34304
t_APADisableRateY_pSec_u7p9[1] t APADisableRateY pSec u7p9[2]	34816 35328
t_APADisableRateY_pSec_u7p9[2]	35328 35840
t_APADisableRateY_pSec_u7p9[4]	36352
t_APADisableRateY_pSec_u7p9[5]	36864
t_APASmoothX_UIs_u2p14[0]	10977
t_APASmoothX_Uls_u2p14[1]	11141
t_APASmoothX_Uls_u2p14[2]	11305
t_APASmoothX_Uls_u2p14[3]	11469
t_APASmoothX_Uls_u2p14[4]	11633
t_APASmoothX_Uls_u2p14[5]	11796
t_APASmoothX_Uls_u2p14[6]	11960
t_APASmoothX_Uls_u2p14[7] t_APASmoothX_Uls_u2p14[8]	12124 12288
t_APASmoothX_Uls_u2p14[9]	12452
t_APASmoothY_Uls_u2p14[0]	8028
t_APASmoothY_Uls_u2p14[1]	8192
t_APASmoothY_Uls_u2p14[2]	8356
t_APASmoothY_Uls_u2p14[3]	8520
t_APASmoothY_Uls_u2p14[4]	8684
t_APASmoothY_Uls_u2p14[5]	8847
t_APASmoothY_Uls_u2p14[6]	9011
t_APASmoothY_Uls_u2p14[7]	9175
t_APASmoothY_Uls_u2p14[8]	9339
t_APASmoothY_Uls_u2p14[9] t_LKASlewX_Kph_u8p8[0]	9503 34304
t LKASlewX Kph u8p8[1]	34816
t_LKASlewX_Kph_u8p8[2]	35328
t_LKASlewX_Kph_u8p8[3]	35840
t_LKASlewX_Kph_u8p8[4]	36352
t_LKASlewX_Kph_u8p8[5]	36864
t_LKASlewY_NmpSec_u4p12[0]	27443
t_LKASlewY_NmpSec_u4p12[1]	27853
t_LKASlewY_NmpSec_u4p12[2]	28262
t_LKASlewY_NmpSec_u4p12[3]	28672
t_LKASlewY_NmpSec_u4p12[4]	29082
t_LKASlewY_NmpSec_u4p12[5] target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor Uls f32	29491 target TrqArblim Per1 AssistDDFactor Uls f32
target_Rte_Inst_Ap_IrqArblim.IrqArblim_Peri_AssistDDFactor_Uis_132 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Peri_DampingDDFactor_Uis_132	target_IrqArblim_Per1_AssistbDFactor_UIs_f32 target_TrqArblim_Per1_DampingDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_opTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
rangor_rate_rate_rate_rate_rate_rate_rate_rat	targot_riqrabalit_i or i_opriqov_murani_toz



Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServE	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvo0	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDE	Factor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCi	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0.579999983			
target_TrqArblim_Per1_ESCState_State_enum.value	2			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	8.48999977			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-8.47000027			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	399.493011			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.0999999			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	1.89999998			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.466000021	0.465999991 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	2	2	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.935240269	-0.935239971 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.67999983	7.67999983 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.88565111	4.88560009 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.77166456	0.771664977 ± 0000390625	~	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.77166456	0.771664977 ± 0000390625	~	
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.579999983	0.579999983 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.579999983	0.579999983 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.75361621	1.75362003 ± 0000390625	~	
target TrgArblim Per1 ReturnDDFactor Uls f32.value	0.77166456	0.771664977 ± 0000390625	_	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.69 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.625
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.319999993
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.96999979
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.9000001
k_APAEnableRate_pSec_f32	353.25
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.23000002
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	27853
t_APADisableRateX_HwNm_u4p12[1]	28262
t_APADisableRateX_HwNm_u4p12[2]	28672
t_APADisableRateX_HwNm_u4p12[3]	29082
t_APADisableRateX_HwNm_u4p12[4]	29491
t_APADisableRateX_HwNm_u4p12[5]	29901
t_APADisableRateY_pSec_u7p9[0]	34816
t_APADisableRateY_pSec_u7p9[1]	35328
t_APADisableRateY_pSec_u7p9[2]	35840
t_APADisableRateY_pSec_u7p9[3]	36352
t_APADisableRateY_pSec_u7p9[4]	36864
t_APADisableRateY_pSec_u7p9[5]	37376
t_APASmoothX_Uls_u2p14[0]	11141
t_APASmoothX_Uls_u2p14[1]	11305
t_APASmoothX_Uls_u2p14[2]	11469
t_APASmoothX_Uls_u2p14[3]	11633
t_APASmoothX_Uls_u2p14[4]	11796
t_APASmoothX_Uls_u2p14[5]	11960
t_APASmoothX_Uls_u2p14[6]	12124
t_APASmoothX_UIs_u2p14[7]	12288

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Name	Input Value		
t_APASmoothX_Uls_u2p14[8]	12452		
t_APASmoothX_Uls_u2p14[9]	12616		
t_APASmoothY_Uls_u2p14[0]	8192		
t_APASmoothY_Uls_u2p14[1]	8356		
t_APASmoothY_Uls_u2p14[2]	8520		
t_APASmoothY_Uls_u2p14[3]	8684		
t_APASmoothY_Uls_u2p14[4]	8847		
t_APASmoothY_Uls_u2p14[5]	9011		
t_APASmoothY_Uls_u2p14[6]	9175		
t_APASmoothY_Uls_u2p14[7]	9339		
t_APASmoothY_Uls_u2p14[8]	9503		
t_APASmoothY_Uls_u2p14[9]	9667		
t_LKASlewX_Kph_u8p8[0]	33280		
t_LKASlewX_Kph_u8p8[1]	33792		
t_LKASlewX_Kph_u8p8[2]	34304		
t_LKASlewX_Kph_u8p8[3]	34816		
t_LKASlewX_Kph_u8p8[4]	35328		
t_LKASlewX_Kph_u8p8[5]	35840		
t_LKASlewY_NmpSec_u4p12[0]	0		
t LKASlewY NmpSec u4p12[1]	0		
t_LKASlewY_NmpSec_u4p12[2]	0		
t_LKASlewY_NmpSec_u4p12[3]	0		
t LKASlewY NmpSec u4p12[4]	0		
t_LKASlewY_NmpSec_u4p12[5]	0		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target TrqArblim Per1 AssistDDFactor Uls	f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target_TrqArblim_Per1_DampingDDFactor_		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCIsLimited Cnt Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target TrgArblim Per1 GMOSHOscillate S		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	· -	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target TrqArblim Per1 ReturnDDFactor Ul		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	1.77999997	_102	
target TrqArblim Per1 ESCState State enum.value	3		
target TrqArblim Per1 GMOSHOscillate State enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.05000019		
target TrqArblim Per1 LKACmd HwNm f32.value	-8.11999989		
target TrqArblim Per1 LKAState State enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	405.942993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.0999999		
Name	Actual Value	Expected Value	Popult
		•	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum TraArblim_HuTorqueSV Hubbr M. Str SV His #22	3	3	*
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.368000031	-0.368000001 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.3000019	6.30000019 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.89400005	5.89400005 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.368000031	-0.368000001 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.30000019	6.30000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.89400005	5.89400005 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.71716928	3.71717 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	•



Test Step 3.70 (Repeat Count = 1)	🗸
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrgArblim APADisableRate Uls M f32	0.649999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.44999981
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.331
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.26000023
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.9000001
k_APAEnableRate_pSec_f32	363
k_APAUseADmpScale_lgc	
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	3.24000001
k_LKAUseSlewCal_lgc t_APADisableRateX_HwNm_u4p12[0]	28262
t_APADisableRateX_HwNm_u4p12[1]	28672
t_APADisableRateX_HwNm_u4p12[2]	29082
t_APADisableRateX_HwNm_u4p12[3]	29491
t_APADisableRateX_HwNm_u4p12[4]	29901
t_APADisableRateX_HwNm_u4p12[5]	30310
t_APADisableRateY_pSec_u7p9[0]	35328
t_APADisableRateY_pSec_u7p9[1]	35840
t_APADisableRateY_pSec_u7p9[2]	36352
t_APADisableRateY_pSec_u7p9[3]	36864
t_APADisableRateY_pSec_u7p9[4]	37376
t_APADisableRateY_pSec_u7p9[5]	37888
t_APASmoothX_UIs_u2p14[0]	11305
t_APASmoothX_UIs_u2p14[1]	11469
t_APASmoothX_UIs_u2p14[2]	11633 11796
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	11960
t_APASmoothX_Uls_u2p14[5]	12124
t_APASmoothX_UIs_u2p14[6]	12288
t_APASmoothX_Uls_u2p14[7]	12452
t_APASmoothX_Uls_u2p14[8]	12616
t_APASmoothX_Uls_u2p14[9]	12780
t_APASmoothY_Uls_u2p14[0]	8356
t_APASmoothY_Uls_u2p14[1]	8520
t_APASmoothY_Uls_u2p14[2]	8684
t_APASmoothY_Uls_u2p14[3]	8847
t_APASmoothY_Uls_u2p14[4]	9011
t_APASmoothY_Uls_u2p14[5]	9175
t_APASmoothY_Uls_u2p14[6]	9339
t_APASmoothY_Uls_u2p14[7]	9503
t_APASmoothY_UIs_u2p14[8]	9667
t_APASmoothY_Uls_u2p14[9] t LKASlewX Kph u8p8[0]	9830 33792
t_LKASlewX_Kph_u8p8[1]	34304
t_LKASlewX_Kph_u8p8[2]	34816
t_LKASlewX_Kph_u8p8[3]	35328
t_LKASlewX_Kph_u8p8[4]	35840
t_LKASlewX_Kph_u8p8[5]	36352
t_LKASlewY_NmpSec_u4p12[0]	55296
t_LKASlewY_NmpSec_u4p12[1]	55296
t_LKASlewY_NmpSec_u4p12[2]	55296
t_LKASlewY_NmpSec_u4p12[3]	55296
t_LKASlewY_NmpSec_u4p12[4]	55296
t_LKASlewY_NmpSec_u4p12[5]	55296
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_UIs_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32
target_Rte_Inst_Ap_IrqArblim.IrqArblim_Per1_ESCCmd_HwNm_132 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCCmd_HwNm_f32 target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCState State enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate State enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum

target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value$

TrqArblim_Per1



3 ± 0.0000390625 3 ± 0.0000390625

6.19984007 ± 0000390625

0.400023997 ± 0000390625



Name	Input Value		
target Rte Inst Ap TrgArblim.TrgArblim Per1 MinSecureVehicleSpeed Kph f32	target TrgArblim Per1 MinSecureVehicleSp	and Kab 600	
	0 - 1 1	- · -	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3.		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	-v	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	2.98000002		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	9.60999966		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.76999998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	412.393005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	4	4	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.203859806	0.20386 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.5	6.5 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.87300014	6.87300014 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.400024414	0.400023997 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.400024414	0.400023997 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

6.1998415

0.400024414

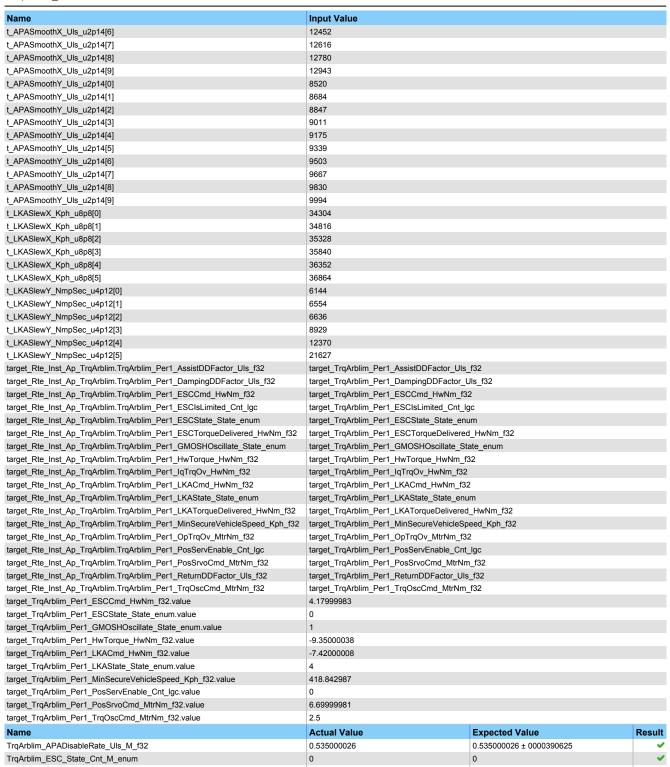
3

Test Step 3.71 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.675000012
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342000008
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.55000019
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.9000001
k_APAEnableRate_pSec_f32	372.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	28672
t_APADisableRateX_HwNm_u4p12[1]	29082
t_APADisableRateX_HwNm_u4p12[2]	29491
t_APADisableRateX_HwNm_u4p12[3]	29901
t_APADisableRateX_HwNm_u4p12[4]	30310
t_APADisableRateX_HwNm_u4p12[5]	30720
t_APADisableRateY_pSec_u7p9[0]	35840
t_APADisableRateY_pSec_u7p9[1]	36352
t_APADisableRateY_pSec_u7p9[2]	36864
t_APADisableRateY_pSec_u7p9[3]	37376
t_APADisableRateY_pSec_u7p9[4]	37888
t_APADisableRateY_pSec_u7p9[5]	38400
t_APASmoothX_Uls_u2p14[0]	11469
t_APASmoothX_Uls_u2p14[1]	11633
t_APASmoothX_Uls_u2p14[2]	11796
t_APASmoothX_Uls_u2p14[3]	11960
t_APASmoothX_Uls_u2p14[4]	12124
t_APASmoothX_Uls_u2p14[5]	12288

TrgArblim Per1

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Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.535000026	0.535000026 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	0	0	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.89550018	-5.89550018 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.55000019	8.55000019 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	7.88944006	7.88944006 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.8786993	4.87869978 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

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Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	~

Test Step 3.72 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.69999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.75
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.352999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.84000015
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.10000002
<_APAEnableRate_pSec_f32	382.5
<_APAUseADmpScale_lgc	1
APAUseAsstScale lgc	1
<_APAUseRetScale_lgc	1
C ESCMax HwNm f32	4.28000021
t_K_LKAUseSlewCal_lgc	1
	0
	0
	0
:_APADisableRateX_HwNm_u4p12[3]	0
:_APADisableRateX_HwNm_u4p12[4]	0
:_APADisableRateX_HwNm_u4p12[5]	0
_APADisableRateY_pSec_u7p9[0]	36352
_APADisableRateY_pSec_u7p9[t] :_APADisableRateY_pSec_u7p9[1]	36864
_APADisableRateY_pSec_u7p9[1] :_APADisableRateY_pSec_u7p9[2]	37376
: APADisableRateY pSec u7p9[3]	37888
: : :	38400
:_APADisableRateY_pSec_u7p9[4]	38912
:_APADisableRateY_pSec_u7p9[5]	
:_APASmoothX_Uls_u2p14[0]	11633
APASmoothX_Uls_u2p14[1]	11796
:_APASmoothX_Uls_u2p14[2]	11960
_APASmoothX_Uls_u2p14[3]	12124
_APASmoothX_Uls_u2p14[4]	12288
_APASmoothX_Uls_u2p14[5]	12452
t_APASmoothX_Uls_u2p14[6]	12616
:_APASmoothX_Uls_u2p14[7]	12780
:_APASmoothX_Uls_u2p14[8]	12943
_APASmoothX_Uls_u2p14[9]	13107
:_APASmoothY_Uls_u2p14[0]	8684
:_APASmoothY_Uls_u2p14[1]	8847
:_APASmoothY_Uls_u2p14[2]	9011
_APASmoothY_Uls_u2p14[3]	9175
:_APASmoothY_Uls_u2p14[4]	9339
_APASmoothY_Uls_u2p14[5]	9503
:_APASmoothY_Uls_u2p14[6]	9667
_APASmoothY_Uls_u2p14[7]	9830
:_APASmoothY_Uls_u2p14[8]	9994
_APASmoothY_Uls_u2p14[9]	10158
_LKASlewX_Kph_u8p8[0]	34816
_LKASlewX_Kph_u8p8[1]	35328
LKASlewX_Kph_u8p8[2]	35840
LKASlewX_Kph_u8p8[3]	36352
LKASlewX_Kph_u8p8[4]	36864
	37376
LKASlewY_NmpSec_u4p12[0]	27853
LKASlewY_NmpSec_u4p12[1]	28262
 LKASlewY_NmpSec_u4p12[2]	28672
 LKASlewY_NmpSec_u4p12[3]	29082
LKASlewY_NmpSec_u4p12[4]	29491
:_LKASlewY_NmpSec_u4p12[5]	29901
rarget_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_UIs_f32
arget_Rte_inst_Ap_frqArblim.frqArblim_Per1_ESCCmd_HwNm_f32	target_rrqArblim_rer1_barnpingbbractor_ors_rs2
arget_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3:	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	5.38000011		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-9		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.07000017		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	425.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.70000005		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	1	1	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.60325003	-5.60325003 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	6.9000001	6.9000001 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.08539987	1.08539999 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	4.28000021	4.28000021 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.2779665	4.27796984 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.380004883	0.380005002 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.73 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.725000024
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.363999993
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.60000002
k_APAEnableRate_pSec_f32	392.25
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	40960
t_APADisableRateX_HwNm_u4p12[1]	40960
t_APADisableRateX_HwNm_u4p12[2]	40960
t_APADisableRateX_HwNm_u4p12[3]	40960
t_APADisableRateX_HwNm_u4p12[4]	40960
t_APADisableRateX_HwNm_u4p12[5]	40960
t_APADisableRateY_pSec_u7p9[0]	36864
t_APADisableRateY_pSec_u7p9[1]	37376
t_APADisableRateY_pSec_u7p9[2]	37888
t_APADisableRateY_pSec_u7p9[3]	38400
t_APADisableRateY_pSec_u7p9[4]	38912

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TrqArblim_Per1



Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	39424		
t_APASmoothX_Uls_u2p14[0]	11796		
t_APASmoothX_Uls_u2p14[1]	11960		
t_APASmoothX_Uls_u2p14[2]	12124		
t_APASmoothX_UIs_u2p14[3]	12288		
t_APASmoothX_UIs_u2p14[4]	12452		
t_APASmoothX_UIs_u2p14[5]	12616 12780		
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	12943		
t_APASmoothX_UIs_u2p14[8]	13107		
t_APASmoothX_Uls_u2p14[9]	13271		
t_APASmoothY_Uls_u2p14[0]	8847		
t_APASmoothY_Uls_u2p14[1]	9011		
t_APASmoothY_Uls_u2p14[2]	9175		
t_APASmoothY_Uls_u2p14[3]	9339		
t_APASmoothY_Uls_u2p14[4]	9503		
t_APASmoothY_Uls_u2p14[5]	9667		
t_APASmoothY_Uls_u2p14[6]	9830		
t_APASmoothY_Uls_u2p14[7]	9994		
t_APASmoothY_Uls_u2p14[8]	10158		
t_APASmoothY_Uls_u2p14[9]	10322		
t_LKASlewX_Kph_u8p8[0]	35328		
t_LKASlewX_Kph_u8p8[1]	35840		
t_LKASlewX_Kph_u8p8[2]	36352		
t_LKASlewX_Kph_u8p8[3]	36864		
t_LKASlewX_Kph_u8p8[4]	37376		
t_LKASlewX_Kph_u8p8[5]	37888		
t_LKASlewY_NmpSec_u4p12[0]	28262		
t_LKASlewY_NmpSec_u4p12[1]	28672		
t_LKASlewY_NmpSec_u4p12[2]	29082		
t_LKASlewY_NmpSec_u4p12[3]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901 30310		
t_LKASlewY_NmpSec_u4p12[5]		. 122	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target TrqArblim Per1 ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target TrqArblim Per1 GMOSHOscillate S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64999962		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.71999979		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	431.743011		
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	7,000000		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	7.0999999 2.9000001		
Name		Expected Value	Poord
	Actual Value 0.58100003	Expected Value 0.58099997 ± 0000390625	Resul
TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum	0.58100003	0.58099997 ± 0000390625	
TrqArblim HwTorqueSV HwNm M Str.SV UIs f32	-5.31099987	-5.31099987 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011	9.13000011 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.59399998	1.59399998 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1.59599996	1.59399996 ± 0.03125 1 ± 0000390625	
	1	1 ± 0000390625	
target Irgarblim Peri Damping Disector His 132 value	1.1	1 - 0000000020	
target_TrqArblim_Per1_DampingDDFactor_UIs_f32.value target_TroArblim_Per1_ESCIsLimited_Cnt_loc.value	1	1	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1 3.25	1 3.25 ± 0000390625	
	1 3.25 3	1 3.25 ± 0000390625 3 ± 0.0000390625	





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	5.76432991	5.76432991 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

est Step 3.74 (Repeat Count = 1)	Invest Walter
ame	Input Value
te_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.75
rqArblim_ESC_State_Cnt_M_enum	1
rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.375
rqArblim_PosSrvoCmd_MtrNm_M_f32	9.42000008
rqArblim_PreLKATrqReq_HwNm_M_f32	2.099999
_APAEnableRate_pSec_f32	402
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
_ESCMax_HwNm_f32	3.1400001
_LKAUseSlewCal_lgc	1
APADisableRateX_HwNm_u4p12[0]	4096
APADisableRateX_HwNm_u4p12[1]	8192
APADisableRateX_HwNm_u4p12[2]	12288
APADisableRateX_HwNm_u4p12[3]	16384
APADisableRateX_HwNm_u4p12[4]	20480
APADisableRateX_HwNm_u4p12[5]	24576
APADisableRateY_pSec_u7p9[0]	37376
APADisableRateY_pSec_u7p9[1]	37888
APADisableRateY_pSec_u7p9[2]	38400
APADisableRateY pSec u7p9[3]	38912
APADisableRateY_pSec_u7p9[4]	39424
APADisableRateY_pSec_u7p9[5]	39936
APASmoothX_Uls_u2p14[0]	11960
APASmoothX Uls u2p14[1]	12124
APASmoothX_UIs_u2p14[2]	12288
APASmoothX_Uls_u2p14[3]	12452
APASmoothX_Uls_u2p14[4]	12616
APASmoothX_Uls_u2p14[5]	12780
APASmoothX_Uls_u2p14[6]	12943
APASmoothX_Uls_u2p14[7]	13107
APASmoothX_Uls_u2p14[8]	13271
APASmoothX_Uls_u2p14[9]	13435
APASmoothY_Uls_u2p14[0]	9011
APASmoothY Uls u2p14[1]	9175
: ::	9339
APASmoothY_Uls_u2p14[2]	
APAS month Y_UIs_u2p14[3]	9503
APAS month Y_UIs_u2p14[4]	9667
APAS month Y_UIs_u2p14[5]	9830
APAS month Y_UIs_u2p14[6]	9994
APASmoothY_UIs_u2p14[7]	10158
APASmoothY_UIs_u2p14[8]	10322
APASmoothY_UIs_u2p14[9]	10486
LKASlewX_Kph_u8p8[0]	35840
LKASlewX_Kph_u8p8[1]	36352
LKASlewX_Kph_u8p8[2]	36864
LKASlewX_Kph_u8p8[3]	37376
LKASlewX_Kph_u8p8[4]	37888
LKASlewX_Kph_u8p8[5]	38400
LKASlewY_NmpSec_u4p12[0]	28672
LKASlewY_NmpSec_u4p12[1]	29082
LKASlewY_NmpSec_u4p12[2]	29491
LKASlewY_NmpSec_u4p12[3]	29901
LKASlewY_NmpSec_u4p12[4]	30310
LKASlewY_NmpSec_u4p12[5]	30720





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32	arget_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	arget_TrqArblim_Per1_ESClsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_Hw	Nm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_e	enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_Hwl	Nm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_	Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32			
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021			
target_TrqArblim_Per1_ESCState_State_enum.value	3			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.30000019			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.0999999			
Name	Actual Value Exp	pected Value	Resu	
TrqArblim_APADisableRate_Uls_M_f32	1 1±	0000390625		
TrqArblim_ESC_State_Cnt_M_enum	3			
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.01875019 -5.0	1875019 ± 0.03125		
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.30000019 7.30	0000019 ± 0.000244140625		
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0849998 2.08	3500004 ± 0.03125		
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.359985352 0.35	59984994 ± 0000390625		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.359985352 0.35	59984994 ± 0000390625		
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0 0			
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0 0 ±	0000390625		
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998 2.08	3500004 ± 0.0000390625		
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998 2.08	3500004 ± 0.0000390625		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

4.67210722

0.359985352

4.67211008 ± 0000390625

0.359984994 ± 0000390625

Test Step 3.75 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.774999976
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.386000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.5999999
k_APAEnableRate_pSec_f32	411.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	29491
t_APADisableRateX_HwNm_u4p12[1]	29901
t_APADisableRateX_HwNm_u4p12[2]	30310
t_APADisableRateX_HwNm_u4p12[3]	30720
t_APADisableRateX_HwNm_u4p12[4]	31130
t_APADisableRateX_HwNm_u4p12[5]	31539
t_APADisableRateY_pSec_u7p9[0]	0
t_APADisableRateY_pSec_u7p9[1]	0

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	0		
t_APADisableRateY_pSec_u7p9[3]	0		
t_APADisableRateY_pSec_u7p9[4]	0		
t_APADisableRateY_pSec_u7p9[5]	0		
t_APASmoothX_Uls_u2p14[0]	12124		
t_APASmoothX_Uls_u2p14[1]	12288		
t_APASmoothX_Uls_u2p14[2]	12452		
t_APASmoothX_Uls_u2p14[3] t_APASmoothX_Uls_u2p14[4]	12616 12780		
t_APASmoothX_UIs_u2p14[5]	12943		
t_APASmoothX_Uls_u2p14[6]	13107		
t_APASmoothX_Uls_u2p14[7]	13271		
t_APASmoothX_Uls_u2p14[8]	13435		
t_APASmoothX_Uls_u2p14[9]	13599		
t_APASmoothY_Uls_u2p14[0]	9175		
t_APASmoothY_Uls_u2p14[1]	9339		
t_APASmoothY_Uls_u2p14[2]	9503		
t_APASmoothY_Uls_u2p14[3]	9667		
t_APASmoothY_Uls_u2p14[4]	9830		
t_APASmoothY_Uls_u2p14[5] t APASmoothY Uls u2p14[6]	9994		
t_APASmoothY_Uls_u2p14[7]	10322		
t_APASmoothY_UIs_u2p14[8]	10486		
t_APASmoothY_Uls_u2p14[9]	10650		
t_LKASlewX_Kph_u8p8[0]	36352		
t_LKASlewX_Kph_u8p8[1]	36864		
t_LKASlewX_Kph_u8p8[2]	37376		
t_LKASlewX_Kph_u8p8[3]	37888		
t_LKASlewX_Kph_u8p8[4]	38400		
t_LKASlewX_Kph_u8p8[5]	38912		
t_LKASlewY_NmpSec_u4p12[0]	29082		
t_LKASlewY_NmpSec_u4p12[1]	29491		
t_LKASlewY_NmpSec_u4p12[2]	29901		
t_LKASlewY_NmpSec_u4p12[3] t_LKASlewY_NmpSec_u4p12[4]	30310 30720		
t_LKASlewY_NmpSec_u4p12[5]	31130		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_er target TrqArblim Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target Rte Inst Ap TrqArblim.TrqArblim Per1 OpTrqOv MtrNm f32	target TrqArblim Per1 OpTrqOv MtrNm f		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.94999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998		
target_TrqArblim_Per1_LKAState_State_enum.value target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0 444.643005		
target_TrqArblim_Per1_MinSecure venicleSpeed_Rpn_fs2.value target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.29999995		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.774599969	0.774600029 ± 0000390625	1.000
TrqArblim_ESC_State_Cnt_M_enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.72650003	-4.72650003 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59399986	2.5940001 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target TrgArblim Per1 ESCIsLimited Cnt Igc.value	0	0	₩

0

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Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	4.47177887	4.47177982 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.76 (Repeat Count = 1)	
Name	Input Value
	·
Rte_Inst_Ap_TrqArblim TrqArblim_APADisableRate_Uls_M_f32	target_Rte_Inst_Ap_TrqArblim 0.800000012
	3
TrqArblim_ESC_State_Cnt_M_enum TraArblim_Llu:TorqueSV_Llu:blm_M_Sta_SV_Llip_622	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.099999
k_APALIs a AParis Carla Jar	421.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	29901
t_APADisableRateX_HwNm_u4p12[1]	30310
t_APADisableRateX_HwNm_u4p12[2]	30720
t_APADisableRateX_HwNm_u4p12[3]	31130
t_APADisableRateX_HwNm_u4p12[4]	31539
t_APADisableRateX_HwNm_u4p12[5]	31949
t_APADisableRateY_pSec_u7p9[0]	65531
t_APADisableRateY_pSec_u7p9[1]	65531
t_APADisableRateY_pSec_u7p9[2]	65531
t_APADisableRateY_pSec_u7p9[3]	65531
t_APADisableRateY_pSec_u7p9[4]	65531
t_APADisableRateY_pSec_u7p9[5]	65531
t_APASmoothX_UIs_u2p14[0]	12288
t_APASmoothX_Uls_u2p14[1]	12452
t_APASmoothX_Uls_u2p14[2]	12616
t_APASmoothX_Uls_u2p14[3]	12780
t_APASmoothX_Uls_u2p14[4]	12943
t_APASmoothX_Uls_u2p14[5]	13107
t_APASmoothX_Uls_u2p14[6]	13271
t_APASmoothX_UIs_u2p14[7]	13435
t_APASmoothX_UIs_u2p14[8]	13599
t_APASmoothX_Uls_u2p14[9]	13763
t_APASmoothY_Uls_u2p14[0]	6226
t_APASmoothY_Uls_u2p14[1]	6390
t_APASmoothY_Uls_u2p14[2]	6554
t_APASmoothY_Uls_u2p14[3]	6717
t_APASmoothY_Uls_u2p14[4]	6881
	7045
t_APASmoothY_Uls_u2p14[5] t_APASmoothY_Uls_u2p14[6]	7209
t_APASmoothY_Uls_u2p14[7]	7373
t_APASmoothY_Uls_u2p14[8]	7537 7700
t_APASmoothY_Uls_u2p14[9]	
t_LKASlewX_Kph_u8p8[0]	36864
t_LKASlewX_Kph_u8p8[1]	37376
t_LKASlewX_Kph_u8p8[2]	37888
t_LKASlewX_Kph_u8p8[3]	38400
t_LKASlewX_Kph_u8p8[4]	38912
t_LKASlewX_Kph_u8p8[5]	39424
t_LKASlewY_NmpSec_u4p12[0]	29491
t_LKASlewY_NmpSec_u4p12[1]	29901
t_LKASlewY_NmpSec_u4p12[2]	30310
t_LKASlewY_NmpSec_u4p12[3]	30720
t_LKASlewY_NmpSec_u4p12[4]	31130





Name	Input Value		
t_LKASlewY_NmpSec_u4p12[5]	31539		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.1800003		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	-
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.43424988	-4.43424988 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.530029297	0.530028999 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.530029297	0.530028999 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.61877441	3.61876988 ± 0000390625	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

0.530029297

0.530028999 ± 0000390625

Test Step 3.77 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.824999988
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.5999999
k_APAEnableRate_pSec_f32	431.25
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	30310
t_APADisableRateX_HwNm_u4p12[1]	30720
t_APADisableRateX_HwNm_u4p12[2]	31130
t_APADisableRateX_HwNm_u4p12[3]	31539
t_APADisableRateX_HwNm_u4p12[4]	31949

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value





<u>' </u>			
Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	32358		
t_APADisableRateY_pSec_u7p9[0]	37888		
t_APADisableRateY_pSec_u7p9[1]	38400		
t_APADisableRateY_pSec_u7p9[2]	38912		
t_APADisableRateY_pSec_u7p9[3]	39424		
t_APADisableRateY_pSec_u7p9[4]	39936		
t_APADisableRateY_pSec_u7p9[5]	40448		
t_APASmoothX_UIs_u2p14[0]	12452		
t_APASmoothX_UIs_u2p14[1]	12616 12780		
t_APASmoothX_Uls_u2p14[2] t_APASmoothX_Uls_u2p14[3]	12943		
t_APASmoothX_Uls_u2p14[4]	13107		
t APASmoothX Uls u2p14[5]	13271		
t_APASmoothX_Uls_u2p14[6]	13435		
t_APASmoothX_Uls_u2p14[7]	13599		
t_APASmoothX_Uls_u2p14[8]	13763		
t_APASmoothX_Uls_u2p14[9]	13926		
t_APASmoothY_Uls_u2p14[0]	6390		
t_APASmoothY_Uls_u2p14[1]	6554		
t_APASmoothY_Uls_u2p14[2]	6717		
t_APASmoothY_Uls_u2p14[3]	6881		
t_APASmoothY_Uls_u2p14[4]	7045		
t_APASmoothY_UIs_u2p14[5]	7209		
t_APASmoothY_Uls_u2p14[6]	7373		
t_APASmoothY_Uls_u2p14[7] t_APASmoothY_Uls_u2p14[8]	7537 7700		
t_APASmoothY_Uls_u2p14[9]	7864		
t_LKASlewX_Kph_u8p8[0]	37376		
t_LKASlewX_Kph_u8p8[1]	37888		
t_LKASlewX_Kph_u8p8[2]	38400		
t_LKASlewX_Kph_u8p8[3]	38912		
t_LKASlewX_Kph_u8p8[4]	39424		
t_LKASlewX_Kph_u8p8[5]	39936		
t_LKASlewY_NmpSec_u4p12[0]	29901		
t_LKASlewY_NmpSec_u4p12[1]	30310		
t_LKASlewY_NmpSec_u4p12[2]	30720		
t_LKASlewY_NmpSec_u4p12[3]	31130		
t_LKASlewY_NmpSec_u4p12[4]	31539		
t_LKASlewY_NmpSec_u4p12[5]	31949	. 400	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Ulstarget_TrqArblim_Per1_DampingDDFactor_	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target_TrqArblim_Per1_ESCCmd_HwNm_f		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3.	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_U target TrqArblim Per1 TrqOscCmd MtrNn	_	
target TrqArblim Per1 ESCCmd HwNm f32.value	-10	1_102	
target_TrqArblim_Per1_ESCState_State_enum.value	0		
target TrqArblim Per1 GMOSHOscillate State enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.25		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.32000017		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	457.542999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.70000005		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.676999986	0.676999986 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	0	0	•
Total Anti-Circ. 1 In Tours and OV 1 In Alex Ad. Ota OV 1 III 400	4.4400000	-4.1420002 ± 0.03125	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.1420002		
TrqArblim_PosSrvoCmd_MtrNm_M_5tr.5v_Dis_132 TrqArblim_PosSrvoCmd_MtrNm_M_f32 TrqArblim_PreLKATrqReq_HwNm_M_f32	-4.1420002 0.5 3.59399986	0.5 ± 0.000244140625 3.5940001 ± 0.03125	





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.83202004	3.83202004 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.78 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.85000024
TrgArblim ESC State Cnt M enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
TrgArblim HwTorqueSV HwNm M Str.K Uls f32	0.419
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.0999999
k_APAEnableRate_pSec_f32	441
k APAUseADmpScale Igc	1
k_APAUseAsstScale_lgc	1
k APAUseRetScale Igc	1
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	30720
t_APADisableRateX_HwNm_u4p12[1]	31130
t_APADisableRateX_HwNm_u4p12[2]	31539
t_APADisableRateX_HwNm_u4p12[3]	31949
t_APADisableRateX_HwNm_u4p12[4]	32358
t APADisableRateX_HwNm_u4p12[5]	32768
t_APADisableRateY_pSec_u7p9[0]	38400
t_APADisableRateY_pSec_u7p9[1]	38912
t_APADisableRateY_pSec_u7p9[1]	39424
t APADisableRateY pSec u7p9[3]	39936
t_APADisableRateY_pSec_u7p9[3]	40448
t_APADisableRateY_pSec_u7p9[5]	40960
t APASmoothX Uls u2p14[0]	0
t_APASmoothX_Uls_u2p14[1]	0
t_APASmoothX_UIs_u2p14[2]	0
t_APASmoothX_Uls_u2p14[3]	0
t_APASmoothX_Uls_u2p14[4]	0
t_APASmoothX_Uls_u2p14[5]	0
t_APASmoothX_Uls_u2p14[6]	0
t_APASmoothX_Uls_u2p14[7]	0
t_APASmoothX_Uls_u2p14[8]	0
t_APASmoothX_Uls_u2p14[9]	0
t_APASmoothY_Uls_u2p14[0]	6554
t_APASmoothY_Uls_u2p14[1]	6717
t_APASmoothY_Uls_u2p14[2]	6881
t_APASmoothY_Uls_u2p14[3]	7045
t_APASmoothY_Uls_u2p14[4]	7209
t_APASmoothY_Uls_u2p14[5]	7373
t APASmoothY Uls u2p14[6]	7537
t_APASmoothY_Uls_u2p14[7]	7700
t_APASmoothY_Uls_u2p14[8]	7864
t_APASmoothY_Uls_u2p14[9]	8028
t LKASlewX Kph u8p8[0]	37888
t_LKASlewX_Kph_u8p8[1]	38400
t_LKASlewX_Kph_u8p8[2]	38912
t LKASlewX Kph u8p8[3]	39424
t_LKASlewX_Kph_u8p8[4]	39936
t_LKASlewX_Kph_u8p8[5]	40448
t_LKASlewY_NmpSec_u4p12[0]	30310
t_LKASlewY_NmpSec_u4p12[1]	30720
	1

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Name	Input Value		
t_LKASlewY_NmpSec_u4p12[2]	31130		
t_LKASlewY_NmpSec_u4p12[3]	31539		
t_LKASlewY_NmpSec_u4p12[4]	31949		
t_LKASlewY_NmpSec_u4p12[5]	32358		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e	num	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_ESCTorqueDeliver	red_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_t	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicles	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	Nm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	m_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.9000001		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.96999979		
target_TrqArblim_Per1_LKAState_State_enum.value	3		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	463.993011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.10000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.700000048	0.699999988 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	1	1	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.84975004	-3.84975004 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75	0.75 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.08419991	4.08419991 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.6570068	0.657006979 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.6570068	0.657006979 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.75	-0.75 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.75	-0.75 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.257244885	0.257245004 ± 0000390625	•
target TrqArblim Per1 ReturnDDFactor UIs f32.value	0.6570068	0.657006979 ± 0000390625	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.79 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.875
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.29999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.5999999
k_APAEnableRate_pSec_f32	450.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	31130
t_APADisableRateX_HwNm_u4p12[1]	31539





Name	Input Value		
t_APADisableRateX_HwNm_u4p12[2]	31949		
t_APADisableRateX_HwNm_u4p12[3]	32358		
t_APADisableRateX_HwNm_u4p12[4]	32768		
t_APADisableRateX_HwNm_u4p12[5]	33178		
t_APADisableRateY_pSec_u7p9[0]	38912		
t_APADisableRateY_pSec_u7p9[1]	39424		
t_APADisableRateY_pSec_u7p9[2]	39936		
t_APADisableRateY_pSec_u7p9[3]	40448		
t_APADisableRateY_pSec_u7p9[4]	40960		
t_APADisableRateY_pSec_u7p9[5] t_APASmoothX_Uls_u2p14[0]	41472 16384		
t_APASmoothX_Uls_u2p14[1]	16384		
t_APASmoothX_Uls_u2p14[2]	16384		
t_APASmoothX_UIs_u2p14[3]	16384		
t_APASmoothX_Uls_u2p14[4]	16384		
t_APASmoothX_Uls_u2p14[5]	16384		
t_APASmoothX_Uls_u2p14[6]	16384		
t_APASmoothX_Uls_u2p14[7]	16384		
t_APASmoothX_Uls_u2p14[8]	16384		
t_APASmoothX_Uls_u2p14[9]	16384		
t_APASmoothY_Uls_u2p14[0]	6717		
t_APASmoothY_Uls_u2p14[1]	6881		
t_APASmoothY_Uls_u2p14[2]	7045		
t_APASmoothY_Uls_u2p14[3]	7209		
t_APASmoothY_Uls_u2p14[4]	7373		
t_APASmoothY_Uls_u2p14[5]	7537		
t_APASmoothY_Uls_u2p14[6]	7700		
t_APASmoothY_UIs_u2p14[7]	7864 8028		
t_APASmoothY_UIs_u2p14[8]	8192		
t_APASmoothY_Uls_u2p14[9] t_LKASlewX_Kph_u8p8[0]	38400		
t_LKASlewX_Kph_u8p8[1]	38912		
t_LKASlewX_Kph_u8p8[2]	39424		
t_LKASlewX_Kph_u8p8[3]	39936		
t_LKASlewX_Kph_u8p8[4]	40448		
t_LKASlewX_Kph_u8p8[5]	40960		
t_LKASlewY_NmpSec_u4p12[0]	30720		
t_LKASlewY_NmpSec_u4p12[1]	31130		
t_LKASlewY_NmpSec_u4p12[2]	31539		
t_LKASlewY_NmpSec_u4p12[3]	31949		
t_LKASlewY_NmpSec_u4p12[4]	32358		
t_LKASlewY_NmpSec_u4p12[5]	32768		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_UIs_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3:		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lo		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enutarget TrqArblim Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate Sta		
target_Rte_inst_Ap_rrqArbiim.rrqArbiim_Per1_GWOSHOSdilate_state_enum target_Rte_Inst_Ap_rrqArbiim.rrqArbiim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f3	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target TrgArblim Per1 IgTrgOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm	1_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.03999996		
target_TrqArblim_Per1_ESCState_State_enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.55000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.61999989		
target_TrqArblim_Per1_LKAState_State_enum.value	4		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	470.442993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	8.30000019		
torrect Transfelling Dard Deachar Cond Mitching 500 and	O SUBBURING		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.0999999	Francisco d Victoria	le.
		Expected Value 1±0000390625	Resul

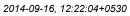




Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.55749989	-3.55749989 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.30000019	8.30000019 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.59399986	4.59399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-1.27999997	-1.27999997 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-1.27999997	-1.27999997 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	7.5027771	7.50277996 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

lame	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
rqArblim_APADisableRate_Uls_M_f32	0.89999976
rqArblim_ESC_State_Cnt_M_enum	2
rqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.94999988
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
rqArblim_PosSrvoCmd_MtrNm_M_f32	1.25
rqArblim_PreLKATrqReq_HwNm_M_f32	5.0999999
_APAEnableRate_pSec_f32	460.5
APAUseADmpScale Igc	1
	1
_APAUseAsstScale_lgc APAUseRetScale_lgc	1
	3.69000006
_ESCMax_HwNm_f32	1
_LKAUseSlewCal_lgc	
_APADisableRateX_HwNm_u4p12[0]	31539
APADisableRateX_HwNm_u4p12[1]	31949
APADisableRateX_HwNm_u4p12[2]	32358
APADisableRateX_HwNm_u4p12[3]	32768
_APADisableRateX_HwNm_u4p12[4]	33178
_APADisableRateX_HwNm_u4p12[5]	33587
_APADisableRateY_pSec_u7p9[0]	39424
APADisableRateY_pSec_u7p9[1]	39936
APADisableRateY_pSec_u7p9[2]	40448
_APADisableRateY_pSec_u7p9[3]	40960
_APADisableRateY_pSec_u7p9[4]	41472
_APADisableRateY_pSec_u7p9[5]	41984
_APASmoothX_Uls_u2p14[0]	12616
_APASmoothX_Uls_u2p14[1]	12780
_APASmoothX_Uls_u2p14[2]	12943
APASmoothX_Uls_u2p14[3]	13107
APASmoothX_Uls_u2p14[4]	13271
APASmoothX_Uls_u2p14[5]	13435
_APASmoothX_Uls_u2p14[6]	13599
APASmoothX_Uls_u2p14[7]	13763
_APASmoothX_Uls_u2p14[8]	13926
_APASmoothX_Uls_u2p14[9]	14090
_APASmoothY_Uls_u2p14[0]	9175
_APASmoothY_Uls_u2p14[1]	9339
APASmoothY_Uls_u2p14[2]	9503
_APASmoothY_Uls_u2p14[3]	9667
_APASmoothY_Uls_u2p14[4]	9830
APASmoothY_Uls_u2p14[5]	9994
APASmoothY_Uls_u2p14[6]	10158
APASmoothY_Uls_u2p14[7]	10322
APASmoothY_Uls_u2p14[8]	10486
APASmoothY_Uls_u2p14[9]	10650
LKASlewX_Kph_u8p8[0]	38912
LKASlewX_Kph_u8p8[1]	39424
LKASlewX_Kph_u8p8[2]	39936
LKASlewX_Kph_u8p8[3]	40448
_LKASlewX_Kph_u8p8[4]	40960





TrqArblim_Per1	16, 12:22:04+0530		Razoncat
Name	Input Value		
t_LKASlewX_Kph_u8p8[5]	41472		
t_LKASlewY_NmpSec_u4p12[0]	31130		
t LKASlewY NmpSec u4p12[1]	31539		
t LKASlewY NmpSec u4p12[2]	31949		
t_LKASlewY_NmpSec_u4p12[3]	32358		
t LKASlewY NmpSec u4p12[4]	32768		
t LKASlewY NmpSec u4p12[5]	33178		
target Rte Inst Ap TrgArblim.TrgArblim Per1 AssistDDFactor UIs f32	target TrqArblim Per1 AssistDDFactor Uls	f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target TrqArblim Per1 DampingDDFactor U		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3.	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lg		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCState State enum	target TrqArblim Per1 ESCState State enu		
target Rte Inst Ap TrgArblim.TrgArblim Per1 ESCTorqueDelivered HwNm f32	target TrqArblim Per1 ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f3	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 IqTrqOv HwNm f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target TrqArblim Per1 LKACmd HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu		
	target TrqArblim Per1 LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3:		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_132	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.19999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.26999998		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	476.893005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.30000019		
Name	Actual Value	Expected Value	Resi
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	
TrqArblim_ESC_State_Cnt_M_enum	3	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.26525021	-3.26524997 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.5	8.5 ± 0.000244140625	
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.08379984	5.08379984 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.349975586	0.349976003 ± 000039062	5
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.349975586	0.349976003 ± 000039062	5
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	
target TrqArblim Per1 LKATorqueDelivered HwNm f32.value	3	3 ± 0.0000390625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

8.80000019

0.349975586

8.80000019 ± 0000390625

0.349976003 ± 0000390625

Test Step 3.81 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.925000012	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.5999999	
k_APAEnableRate_pSec_f32	470.25	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	
k_APAUseRetScale_lgc	1	
k_ESCMax_HwNm_f32	2.75	

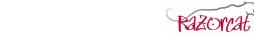
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value





lls_f32
r_Uls_f32
<u>f</u> 32
_lgc
enum
red_HwNm_f32 State_enum
_f32
32
f32
enum
red_HwNm_f32
Speed_Kph_f32
f32
int_lgc
Nm_f32 Jls_f32
m_f32
_

TrqArblim_Per1



Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.768999994	0.768999994 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.97300005	-2.97300005 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58360004	5.58360004 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-2.75	-2.75 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-2.75	-2.75 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
FrqArblim_APADisableRate_Uls_M_f32	0.94999988	
FrgArblim_ESC_State_Cnt_M_enum	4	
FrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.25	
FrgArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.463	
rqArblim_PosSrvoCmd_MtrNm_M_f32	1.75	
rqArblim_PreLKATrqReq_HwNm_M_f32	6.099999	
APAEnableRate pSec f32	480	
APAUseADmpScale Igc	0	
_APAUseAsstScale_lgc	0	
	0	
_APAUseRetScale_lgc	1.23000002	
_ESCMax_HwNm_f32	0	
_LKAUseSlewCal_lgc	· · · · · · · · · · · · · · · · · · ·	
_APADisableRateX_HwNm_u4p12[0]	32358	
_APADisableRateX_HwNm_u4p12[1]	32768	
_APADisableRateX_HwNm_u4p12[2]	33178	
_APADisableRateX_HwNm_u4p12[3]	33587	
_APADisableRateX_HwNm_u4p12[4]	33997	
_APADisableRateX_HwNm_u4p12[5]	34406	
_APADisableRateY_pSec_u7p9[0]	40448	
_APADisableRateY_pSec_u7p9[1]	40960	
_APADisableRateY_pSec_u7p9[2]	41472	
APADisableRateY_pSec_u7p9[3]	41984	
_APADisableRateY_pSec_u7p9[4]	42496	
_APADisableRateY_pSec_u7p9[5]	43008	
_APASmoothX_Uls_u2p14[0]	12943	
_APASmoothX_Uls_u2p14[1]	13107	
_APASmoothX_Uls_u2p14[2]	13271	
_APASmoothX_Uls_u2p14[3]	13435	
_APASmoothX_Uls_u2p14[4]	13599	
_APASmoothX_Uls_u2p14[5]	13763	
_APASmoothX_Uls_u2p14[6]	13926	
_APASmoothX_Uls_u2p14[7]	14090	
_APASmoothX_Uls_u2p14[8]	14254	
_APASmoothX_Uls_u2p14[9]	14418	
_APASmoothY_Uls_u2p14[0]	16384	
_APASmoothY_Uls_u2p14[1]	16384	
_APASmoothY_Uls_u2p14[2]	16384	
_APASmoothY_Uls_u2p14[3]	16384	
_APASmoothY_Uls_u2p14[4]	16384	
_APASmoothY_Uls_u2p14[5]	16384	
_APASmoothY_Uls_u2p14[6]	16384	
APASmoothY Uls u2p14[7]	16384	
APASmoothY Uls u2p14[8]	16384	
_APASmoothY_Uls_u2p14[9]	16384	
LKASlewX Kph u8p8[0]	39936	
_LKASlewX_Kph_u8p8[1]	40448	
_LKASlewX_Kph_u8p8[2]	40960	

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TIQAIDIIII_FEIT			1000
Name	Input Value		
t_LKASlewX_Kph_u8p8[3]	41472		
t_LKASlewX_Kph_u8p8[4]	41984		
t_LKASlewX_Kph_u8p8[5]	42496		
t_LKASlewY_NmpSec_u4p12[0]	31949		
t_LKASlewY_NmpSec_u4p12[1]	32358		
t_LKASlewY_NmpSec_u4p12[2]	32768		
t_LKASlewY_NmpSec_u4p12[3]	33178		
t_LKASlewY_NmpSec_u4p12[4]	33587		
t_LKASlewY_NmpSec_u4p12[5]	33997		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_I	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target TrqArblim Per1 ESCState State en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d HwNm f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate St		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKACmd HwNm f32	target TrqArblim Per1 LKACmd HwNm f3.	2	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_end	um	
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target TrqArblim Per1 LKATorqueDelivered		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target TrqArblim Per1 OpTrqOv MtrNm f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosSrvoCmd MtrNm f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target TrgArblim Per1 ESCCmd HwNm f32.value	-7.5999999	_	
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target TrgArblim Per1 GMOSHOscillate State enum.value	2		
target TrgArblim Per1 HwTorque HwNm f32.value	-5.5		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.56999993		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target TrgArblim Per1 MinSecureVehicleSpeed Kph f32.value	489.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.60000038		
target TrgArblim Per1 TrgOscCmd MtrNm f32.value	4.69999981		
Name	Actual Value	Expected Value	Resul
TrgArblim APADisableRate Uls M f32	0.791999996	0.791999996 ± 0000390625	rtoou
TrqArblim_ESC_State_Cnt_M_enum	3	3	
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	-2.68074989	-2.68074989 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	1.75	1.75 ± 0.000244140625	
TrgArblim PreLKATrgReg HwNm M f32	6.09399986	6.09399986 ± 0.03125	
target TrqArblim Per1 AssistDDFactor Uls f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	
target TrqArblim Per1 ESCTorqueDelivered HwNm f32.value	0	0 ± 0000390625	
target_rrqArblim_Per1_ESCTorqueDelivered_HwNm_isz.value	3	3 ± 0.000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivereu_Hwnin_isz.value target TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	1.38600004	1.38600004 ± 0000390625	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1.58600004 ± 0000590025 1 ± 0000390625	
target_rrynthilli_rei i_returiiDDFactor_OIS_132.Value	1	1 1 0000390025	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.83 (Repeat Count = 1)		<u> </u>
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.975000024	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2	
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.5999999	
k_APAEnableRate_pSec_f32	489.75	
k_APAUseADmpScale_lgc	1	
k_APAUseAsstScale_lgc	1	





	Input Value
	1
	3.2400001
	1 32768
	33178
	33587
	33997
t_APADisableRateX_HwNm_u4p12[4]	34406
t_APADisableRateX_HwNm_u4p12[5]	34816
	40960
	41472
	41984 42496
	43008
	43520
t_APASmoothX_Uls_u2p14[0]	13107
t_APASmoothX_UIs_u2p14[1]	13271
t_APASmoothX_Uls_u2p14[2]	13435
t_APASmoothX_Uls_u2p14[3]	13599
	13763
	13926
t_APASmoothX_Uls_u2p14[6]	14090 14254
t_APASmoothX_Uls_u2p14[7] t_APASmoothX_Uls_u2p14[8]	14418
t_APASmoothX_UIs_u2p14[9]	14582
	12616
t_APASmoothY_Uls_u2p14[1]	12780
t_APASmoothY_Uls_u2p14[2]	12943
	13107
	13271
	13435
t_APASmoothY_Uls_u2p14[6] t_APASmoothY_Uls_u2p14[7]	13599 13763
	13926
	14090
	40448
t_LKASlewX_Kph_u8p8[1]	40960
	41472
	41984
	42496
	43008 32358
	32768
	33178
t_LKASlewY_NmpSec_u4p12[3]	33587
t_LKASlewY_NmpSec_u4p12[4]	33997
t_LKASlewY_NmpSec_u4p12[5]	34406
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
	target_TrqArblim_Per1_ESCCmd_HwNm_f32
	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESC5tate_state_enuml target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_frqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
	target_TrqArblim_Per1_LKAState_State_enum
	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrqArblim_Per1_PosServEnable_Cnt_lgc
	target_TrqArblim_Per1_PosServEnable_Crit_igc target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
	-7.11999989
target_TrqArblim_Per1_ESCState_State_enum.value	4
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
	-5.1500001
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003
target_TrqArblim_Per1_LKACmd_HwNm_f32.value target_TrqArblim_Per1_LKAState_State_enum.value	-3.22000003 1 496.243011

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Name	Input Value		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.10000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.38850021	-2.38849998 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.10000038	-8.10000038 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.58319998	6.58319998 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-2.06588173	-2.06588006 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.140014648	0.140015006 ± 0000390625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.84 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap TrgArblim	target_Rte_Inst_Ap_TrqArblim
TrgArblim APADisableRate Uls M f32	0.69999988
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.352999985
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.84000015
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.10000002
k_APAEnableRate_pSec_f32	382.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.58000004
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	24166
t_APADisableRateX_HwNm_u4p12[1]	24576
t_APADisableRateX_HwNm_u4p12[2]	24986
t_APADisableRateX_HwNm_u4p12[3]	25395
t_APADisableRateX_HwNm_u4p12[4]	25805
t_APADisableRateX_HwNm_u4p12[5]	26214
t_APADisableRateY_pSec_u7p9[0]	43008
t_APADisableRateY_pSec_u7p9[1]	43520
t_APADisableRateY_pSec_u7p9[2]	44032
t_APADisableRateY_pSec_u7p9[3]	44544
t_APADisableRateY_pSec_u7p9[4]	45056
t_APADisableRateY_pSec_u7p9[5]	45568
t_APASmoothX_Uls_u2p14[0]	8356
t_APASmoothX_Uls_u2p14[1]	8520
t_APASmoothX_Uls_u2p14[2]	8684
t_APASmoothX_Uls_u2p14[3]	8847
t_APASmoothX_Uls_u2p14[4]	9011
t_APASmoothX_Uls_u2p14[5]	9175
t_APASmoothX_Uls_u2p14[6]	9339
t_APASmoothX_Uls_u2p14[7]	9503
t_APASmoothX_Uls_u2p14[8]	9667
t_APASmoothX_Uls_u2p14[9]	9830
t_APASmoothY_Uls_u2p14[0]	5407
t_APASmoothY_Uls_u2p14[1]	5571
t_APASmoothY_Uls_u2p14[2]	5734
t_APASmoothY_Uls_u2p14[3]	5898
t_APASmoothY_Uls_u2p14[4]	6062
t_APASmoothY_Uls_u2p14[5]	6226
t_APASmoothY_Uls_u2p14[6]	6390
t_APASmoothY_Uls_u2p14[7]	6554
t_APASmoothY_Uls_u2p14[8]	6717
t_APASmoothY_Uls_u2p14[9]	6881

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TraArblim Per1

TrqArblim_Per1			MAZUICAL
Name	Input Value		
t_LKASlewX_Kph_u8p8[0]	34816		
t_LKASlewX_Kph_u8p8[1]	35328		
t_LKASlewX_Kph_u8p8[2]	35840		
t_LKASlewX_Kph_u8p8[3]	36352		
t_LKASlewX_Kph_u8p8[4]	36864		
t_LKASlewX_Kph_u8p8[5]	37376		
t LKASlewY NmpSec u4p12[0]	27853		
t_LKASlewY_NmpSec_u4p12[1]	28262		
LKASlewY_NmpSec_u4p12[2]	28672		
t_LKASlewY_NmpSec_u4p12[3]	29082		
	29491		
t_LKASlewY_NmpSec_u4p12[5]	29901		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor	Uls f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCTorqueDelivered HwNm f32	target TrqArblim Per1 ESCTorqueDeliver		
target Rte Inst Ap TrgArblim.TrgArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_1		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_el		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKATorqueDelivered HwNm f32	target_TrqArblim_Per1_LKATorqueDeliver		
target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target TrqArblim Per1 MinSecureVehicles		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Ci		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_L		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-10.2299995	-	
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target TrqArblim Per1 GMOSHOscillate State enum.value	2		
target TrgArblim Per1 HwTorque HwNm f32.value	-9		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-7.07000017		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	425.292999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	6.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.70000005		
Name	Actual Value	Expected Value	Resu
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	
TrgArblim ESC State Cnt M enum	4	4	
TrgArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.60325003	-5.60325003 ± 0.03125	
TrgArblim PosSrvoCmd MtrNm M f32	6.9000001	6.9000001 ± 0.00024414062	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.08539987	1.08539999 ± 0.03125	
target TrgArblim Per1 AssistDDFactor Uls f32.value	0.58001709	0.140015006 ± 0000390625	
target TrgArblim Per1 DampingDDFactor Uls f32.value	0.58001709	0.140015006 ± 0000390625	
target TrgArblim Per1 ESCIsLimited Cnt Igc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	1.08539987	1.08539987 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	1.08539987	1.08539987 ± 0.0000390625	
target TrgArblim Per1 OpTrgOv MtrNm f32.value	2.89788222	-2.06588006 ± 0000390625	
target_TraArblim_Bord_BeturnDDEceter_Ula_f22.value	0.59001700	0.140015006 ± 0000330025	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

0.58001709

Test Step 3.85 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.725000024	
TrqArblim_ESC_State_Cnt_M_enum	2	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.363999993	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011	
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.60000002	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

0.140015006 ± 0000390625





Name	Input Value
k_APAEnableRate_pSec_f32	392.25
k_APAUseApatScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc k_ESCMax_HwNm_f32	4.28000021
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	24576
t_APADisableRateX_HwNm_u4p12[1]	24986
t_APADisableRateX_HwNm_u4p12[2]	25395
t_APADisableRateX_HwNm_u4p12[3]	25805
t_APADisableRateX_HwNm_u4p12[4]	26214
t_APADisableRateX_HwNm_u4p12[5]	26624
t_APADisableRateY_pSec_u7p9[0]	43520
t_APADisableRateY_pSec_u7p9[1] t_APADisableRateY_pSec_u7p9[2]	44032 44544
t_APADisableRateY_pSec_u7p9[3]	45056
t_APADisableRateY_pSec_u7p9[4]	45568
t_APADisableRateY_pSec_u7p9[5]	46080
t_APASmoothX_Uls_u2p14[0]	8520
t_APASmoothX_Uls_u2p14[1]	8684
t_APASmoothX_Uls_u2p14[2]	8847
t_APASmoothX_Uls_u2p14[3]	9011
t_APASmoothX_UIs_u2p14[4]	9175
t_APASmoothX_UIs_u2p14[5]	9339
t_APASmoothX_UIs_u2p14[6]	9503 9667
t_APASmoothX_Uls_u2p14[7] t APASmoothX Uls u2p14[8]	9830
t APASmoothX Uls u2p14[9]	9994
t_APASmoothY_Uls_u2p14[0]	5571
t_APASmoothY_Uls_u2p14[1]	5734
t_APASmoothY_Uls_u2p14[2]	5898
t_APASmoothY_Uls_u2p14[3]	6062
t_APASmoothY_Uls_u2p14[4]	6226
t_APASmoothY_UIs_u2p14[5]	6390
t_APASmoothY_Uls_u2p14[6] t_APASmoothY_Uls_u2p14[7]	6554 6717
t_APASmoothY_Uls_u2p14[8]	6881
t_APASmoothY_Uls_u2p14[9]	7045
t_LKASlewX_Kph_u8p8[0]	35328
t_LKASlewX_Kph_u8p8[1]	35840
t_LKASlewX_Kph_u8p8[2]	36352
t_LKASlewX_Kph_u8p8[3]	36864
t_LKASlewX_Kph_u8p8[4]	37376
t_LKASlewX_Kph_u8p8[5]	37888
t_LKASlewY_NmpSec_u4p12[0] t_LKASlewY_NmpSec_u4p12[1]	28262 28672
t_LKASlewY_NmpSec_u4p12[2]	29082
t LKASlewY NmpSec u4p12[3]	29491
t_LKASlewY_NmpSec_u4p12[4]	29901
t_LKASlewY_NmpSec_u4p12[5]	30310
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32$	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_UIs_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServEnable_Crit_igc target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PossivoCritd_MtiNffi_32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_PossivoCritd_MitNint_132
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995
target_TrqArblim_Per1_ESCState_State_enum.value	2
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.64999962
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.71999979

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

TraArblim Per1

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3 ± 0.0000390625

0 ± 0.0000390625

1 ± 0000390625

4.80017996 ± 0000390625

77q7 (13/117 <u>-</u>			10-10
Name	Input Value		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	431.743011		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.0999999		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	2.9000001		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.555000007	0.555000007 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	2	2	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.31099987	-5.31099987 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.13000011	9.13000011 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	1.59399998	1.59399998 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	4.28000021	4.28000021 ± 0000390625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

4.80018139

0

Name	Input Value	
Rte Inst Ap TrgArblim	target_Rte_Inst_Ap_TrqArblim	
FrgArblim APADisableRate Uls M f32	0.75	
FrgArblim ESC State Cnt M enum	3	
FrgArblim HwTorqueSV HwNm M Str.SV Uls f32	-3.04999995	
FrgArblim HwTorqueSV HwNm M Str.K Uls f32	0.375	
FrgArblim PosSrvoCmd MtrNm M f32	9.42000008	
FrgArblim_PreLKATrqReq_HwNm_M_f32	2.0999999	
APAEnableRate pSec f32	402	
APAUseADmpScale lgc	1	
APAUseAsstScale Igc	1	
APAUseRetScale lgc	1	
ESCMax HwNm f32	3.25	
LKAUseSlewCal lgc	1	
APADisableRateX HwNm u4p12[0]	24986	
APADisableRateX_HwNm_u4p12[1]	25395	
APADisableRateX HwNm u4p12[2]	25805	
_APADisableRateX_HwNm_u4p12[3]	26214	
_APADisableRateX_HwNm_u4p12[4]	26624	
APADisableRateX HwNm u4p12[5]	27034	
APADisableRateY pSec u7p9[0]	42496	
APADisableRateY pSec u7p9[1]	43008	
APADisableRateY pSec u7p9[2]	43520	
_APADisableRateY_pSec_u7p9[3]	44032	
APADisableRateY pSec u7p9[4]	44544	
_APADisableRateY_pSec_u7p9[5]	45056	
_APASmoothX_Uls_u2p14[0]	8684	
_APASmoothX_Uls_u2p14[1]	8847	
_APASmoothX_Uls_u2p14[2]	9011	
_APASmoothX_Uls_u2p14[3]	9175	
_APASmoothX_Uls_u2p14[4]	9339	
_APASmoothX_Uls_u2p14[5]	9503	
_APASmoothX_Uls_u2p14[6]	9667	
_APASmoothX_Uls_u2p14[7]	9830	
_APASmoothX_Uls_u2p14[8]	9994	
_APASmoothX_Uls_u2p14[9]	10158	
_APASmoothY_Uls_u2p14[0]	5734	
_APASmoothY_Uls_u2p14[1]	5898	
_APASmoothY_Uls_u2p14[2]	6062	
_APASmoothY_Uls_u2p14[3]	6226	
_APASmoothY_Uls_u2p14[4]	6390	
_APASmoothY_Uls_u2p14[5]	6554	
_APASmoothY_Uls_u2p14[6]	6717	

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TrqArblim_Per1

TrqArblim_Per1			1000 Cab
Name	Input Value		
t_APASmoothY_Uls_u2p14[7]	6881		
t_APASmoothY_Uls_u2p14[8]	7045		
t_APASmoothY_Uls_u2p14[9]	7209		
t_LKASlewX_Kph_u8p8[0]	35840		
t_LKASlewX_Kph_u8p8[1]	36352		
t_LKASlewX_Kph_u8p8[2]	36864		
t_LKASlewX_Kph_u8p8[3]	37376		
t_LKASlewX_Kph_u8p8[4]	37888		
t_LKASlewX_Kph_u8p8[5]	38400		
t_LKASlewY_NmpSec_u4p12[0]	28672		
t_LKASlewY_NmpSec_u4p12[1]	29082		
t_LKASlewY_NmpSec_u4p12[2]	29491		
t_LKASlewY_NmpSec_u4p12[3]	29901		
t_LKASlewY_NmpSec_u4p12[4]	30310		
t_LKASlewY_NmpSec_u4p12[5]	30720		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en	ium	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	tate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	0		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.01875019	-5.01875019 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.30000019	7.30000019 ± 0.00024414062	25
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0849998	2.08500004 ± 0.03125	✓
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.21201801	3.21201992 ± 0000390625	✓
	0.550007550		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TroArblim Per1 CP1 CheckpointReached	1	_

0.559997559

Test Step 3.87 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.774999976
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

0.559997976 ± 0000390625





Name	Input Value
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.386000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.5999999
k_APAEnableRate_pSec_f32	411.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.1400001
k_LKAUseSlewCal_lgc	0 25395
t_APADisableRateX_HwNm_u4p12[0] t_APADisableRateX_HwNm_u4p12[1]	25805
t_APADisableRateX_HwNm_u4p12[2]	26214
t_APADisableRateX_HwNm_u4p12[3]	26624
t_APADisableRateX_HwNm_u4p12[4]	27034
t_APADisableRateX_HwNm_u4p12[5]	27443
t_APADisableRateY_pSec_u7p9[0]	43008
t_APADisableRateY_pSec_u7p9[1]	43520
t_APADisableRateY_pSec_u7p9[2]	44032
t_APADisableRateY_pSec_u7p9[3]	44544
t_APADisableRateY_pSec_u7p9[4]	45056
t_APADisableRateY_pSec_u7p9[5]	45568
t_APASmoothX_Uls_u2p14[0]	8847
t_APASmoothX_Uls_u2p14[1]	9011
t_APASmoothX_Uls_u2p14[2]	9175
t_APASmoothX_Uls_u2p14[3]	9339
t_APASmoothX_Uls_u2p14[4]	9503
t_APASmoothX_Uls_u2p14[5]	9667
t_APASmoothX_Uls_u2p14[6]	9830
t_APASmoothX_Uls_u2p14[7]	9994
t_APASmoothX_UIs_u2p14[8]	10158
t_APASmoothX_UIs_u2p14[9]	10322
t_APASmoothY_Uls_u2p14[0]	5898 6062
t_APASmoothY_Uls_u2p14[1] t_APASmoothY_Uls_u2p14[2]	6226
t_APASmoothY_Uls_u2p14[3]	6390
t_APASmoothY_Uls_u2p14[4]	6554
t_APASmoothY_Uls_u2p14[5]	6717
t_APASmoothY_Uls_u2p14[6]	6881
t_APASmoothY_Uls_u2p14[7]	7045
t_APASmoothY_Uls_u2p14[8]	7209
t_APASmoothY_Uls_u2p14[9]	7373
t_LKASlewX_Kph_u8p8[0]	36352
t_LKASlewX_Kph_u8p8[1]	36864
t_LKASlewX_Kph_u8p8[2]	37376
t_LKASlewX_Kph_u8p8[3]	37888
t_LKASlewX_Kph_u8p8[4]	38400
t_LKASlewX_Kph_u8p8[5]	38912
t_LKASlewY_NmpSec_u4p12[0]	29082
t_LKASlewY_NmpSec_u4p12[1]	29491
t_LKASlewY_NmpSec_u4p12[2]	29901
t_LKASlewY_NmpSec_u4p12[3]	30310
t_LKASlewY_NmpSec_u4p12[4]	30720
t_LKASlewY_NmpSec_u4p12[5]	31130
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIst_imited_Cpt_loc	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCState_state_enum target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate State enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
target_rte_inst_Ap_rtqArbiint.rtqArbiint_rer_rteturibbr actor_ois_isz	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32
	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32 5.19999981

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Name	Input Value		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.94999981		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.01999998		
target_TrqArblim_Per1_LKAState_State_enum.value	0		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	444.643005		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.5		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.29999995		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.606999993	0.606999993 ± 0000390625	✓
TrqArblim_ESC_State_Cnt_M_enum	4	4	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.72650003	-4.72650003 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	9.71000004	9.71000004 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.59399986	2.5940001 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.59399986	2.5940001 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	2.51673651	2.51674008 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.88 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrgArblim APADisableRate UIs M f32	0.80000012
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	2.57999992
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t_APADisableRateY_pSec_u7p9[0]	43520
t_APADisableRateY_pSec_u7p9[1]	44032
t_APADisableRateY_pSec_u7p9[2]	44544
t_APADisableRateY_pSec_u7p9[3]	45056
t_APADisableRateY_pSec_u7p9[4]	45568
t_APADisableRateY_pSec_u7p9[5]	46080
t_APASmoothX_Uls_u2p14[0]	9011
t_APASmoothX_Uls_u2p14[1]	9175
t_APASmoothX_Uls_u2p14[2]	9339
t_APASmoothX_Uls_u2p14[3]	9503
t_APASmoothX_Uls_u2p14[4]	9667
t_APASmoothX_Uls_u2p14[5]	9830
t_APASmoothX_Uls_u2p14[6]	9994
t_APASmoothX_Uls_u2p14[7]	10158
t_APASmoothX_Uls_u2p14[8]	10322
t_APASmoothX_Uls_u2p14[9]	10486
t_APASmoothY_Uls_u2p14[0]	6062
t_APASmoothY_Uls_u2p14[1]	6226
t_APASmoothY_Uls_u2p14[2]	6390
t_APASmoothY_Uls_u2p14[3]	6554

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[4]	6717		
t_APASmoothY_Uls_u2p14[5]	6881		
t_APASmoothY_Uls_u2p14[6]	7045		
t_APASmoothY_Uls_u2p14[7]	7209		
t_APASmoothY_Uls_u2p14[8]	7373		
t_APASmoothY_Uls_u2p14[9]	7537		
t LKASlewX Kph u8p8[0]	36864		
t_LKASlewX_Kph_u8p8[1]	37376		
t_LKASlewX_Kph_u8p8[2]	37888		
t_LKASlewX_Kph_u8p8[3]	38400		
t_LKASlewX_Kph_u8p8[4]	38912		
t_LKASlewX_Kph_u8p8[5]	39424		
t_LKASlewY_NmpSec_u4p12[0]	29491		
t_LKASlewY_NmpSec_u4p12[1]	29901		
t_LKASlewY_NmpSec_u4p12[2]	30310		
t_LKASlewY_NmpSec_u4p12[3]	30720		
t_LKASlewY_NmpSec_u4p12[4]	31130		
t_LKASlewY_NmpSec_u4p12[4]	31539		
target Rte Inst Ap TrqArblim.TrqArblim Per1 AssistDDFactor Uls f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
		_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_DampingDDFactor_ target TrqArblim Per1 ESCCmd HwNm f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	•	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_ft		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	_,	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.43424988	-4.43424988 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.69999981	7.69999981 ± 0.000244140625	5
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	
	_		

Test Step Call Trace				•
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

3.54216909

0.539978027

3

0 ± 0000390625

3 ± 0.0000390625

3 ± 0.0000390625

3.54217005 ± 0000390625

0.539978027 ± 0000390625

Test Step 3.89 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim

target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value

target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value

 $target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value$

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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Name	Input Value
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.600000024
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.5999999
k_APAEnableRate_pSec_f32	470.25
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.239999995
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22118
t_APADisableRateX_HwNm_u4p12[1]	22528
t_APADisableRateX_HwNm_u4p12[2]	22938 23347
t_APADisableRateX_HwNm_u4p12[3] t_APADisableRateX_HwNm_u4p12[4]	23757
t_APADisableRateX_HwNm_u4p12[5]	24166
t APADisableRateY pSec u7p9[0]	33280
t APADisableRateY pSec u7p9[1]	33792
t_APADisableRateY_pSec_u7p9[1]	34304
t_APADisableRateY_pSec_u7p9[3]	34816
t_APADisableRateY_pSec_u7p9[4]	35328
t_APADisableRateY_pSec_u7p9[5]	35840
t_APASmoothX_Uls_u2p14[0]	9175
t_APASmoothX_Uls_u2p14[1]	9339
t_APASmoothX_Uls_u2p14[2]	9503
t_APASmoothX_Uls_u2p14[3]	9667
t_APASmoothX_Uls_u2p14[4]	9830
t_APASmoothX_Uls_u2p14[5]	9994
t_APASmoothX_Uls_u2p14[6]	10158
t_APASmoothX_Uls_u2p14[7]	10322
t_APASmoothX_Uls_u2p14[8]	10486
t_APASmoothX_Uls_u2p14[9]	10650
t_APASmoothY_Uls_u2p14[0]	6226
t_APASmoothY_Uls_u2p14[1]	6390
t_APASmoothY_Uls_u2p14[2]	6554
t_APASmoothY_Uls_u2p14[3]	6717
t_APASmoothY_Uls_u2p14[4]	6881
t_APASmoothY_Uls_u2p14[5]	7045
t_APASmoothY_Uls_u2p14[6]	7209 7373
t_APASmoothY_UIs_u2p14[7]	7537
t_APASmoothY_Uls_u2p14[8] t_APASmoothY_Uls_u2p14[9]	7700
t_LKASlewX_Kph_u8p8[0]	39424
t_LKASlewX_Kph_u8p8[1]	39936
t_LKASlewX_Kph_u8p8[2]	40448
t_LKASlewX_Kph_u8p8[3]	40960
t_LKASlewX_Kph_u8p8[4]	41472
t LKASlewX Kph u8p8[5]	41984
t_LKASlewY_NmpSec_u4p12[0]	31539
t_LKASlewY_NmpSec_u4p12[1]	31949
t_LKASlewY_NmpSec_u4p12[2]	32358
t_LKASlewY_NmpSec_u4p12[3]	32768
t_LKASlewY_NmpSec_u4p12[4]	33178
t_LKASlewY_NmpSec_u4p12[5]	33587
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosServEnable_Cnt_lgc target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_inst_Ap_frqArblim.frqArblim_Peri_PosSrvoCmd_witrNm_fs2 target_Rte_inst_Ap_frqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_PossrvoCmd_mtrNm_rs2 target_TrqArblim_Per1_ReturnDDFactor_Uls_f32
anger_me_mer_pp_rrypromm. rrypromm_retr_netamouractor_ots_132	Targot_Try/10mm_T of T_Notamber actor_015_132



Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOs	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.23999977			
target_TrqArblim_Per1_ESCState_State_enum.value	0			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.69999981			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	0.795000017	0.795000017 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	0	0	✓	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.97300005	-2.97300005 ± 0.03125	✓	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	✓	
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58360004	5.58360004 ± 0.03125	✓	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.626373291	0.626372993 ± 0000390625	✓	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.626373291	0.626372993 ± 0000390625	✓	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	✓	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.560440063	0.560440004 ± 0000390625	✓	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.626373291	0.626372993 ± 0000390625	✓	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.90 (Repeat Count = 1)	le au l
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.94999988
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.0999999
k_APAEnableRate_pSec_f32	480
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	0.140000001
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	22528
t_APADisableRateX_HwNm_u4p12[1]	22938
t_APADisableRateX_HwNm_u4p12[2]	23347
t_APADisableRateX_HwNm_u4p12[3]	23757
t_APADisableRateX_HwNm_u4p12[4]	24166
t_APADisableRateX_HwNm_u4p12[5]	24576
t_APADisableRateY_pSec_u7p9[0]	34304
t_APADisableRateY_pSec_u7p9[1]	34816
t_APADisableRateY_pSec_u7p9[2]	35328
t_APADisableRateY_pSec_u7p9[3]	35840
t_APADisableRateY_pSec_u7p9[4]	36352
t_APADisableRateY_pSec_u7p9[5]	36864
t_APASmoothX_Uls_u2p14[0]	9339
t_APASmoothX_Uls_u2p14[1]	9503
t_APASmoothX_Uls_u2p14[2]	9667
t_APASmoothX_Uls_u2p14[3]	9830
t_APASmoothX_Uls_u2p14[4]	9994
t_APASmoothX_Uls_u2p14[5]	10158
t_APASmoothX_Uls_u2p14[6]	10322
t_APASmoothX_Uls_u2p14[7]	10486
t_APASmoothX_Uls_u2p14[8]	10650
t_APASmoothX_Uls_u2p14[9]	10813
t_APASmoothY_Uls_u2p14[0]	6390

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TrqArblim_Per1

Name	Input Value		
t_APASmoothY_Uls_u2p14[1]	6554		
t_APASmoothY_Uls_u2p14[2]	6717		
t_APASmoothY_Uls_u2p14[3]	6881		
t_APASmoothY_Uls_u2p14[4]	7045		
t_APASmoothY_Uls_u2p14[5]	7209		
t_APASmoothY_Uls_u2p14[6]	7373		
t_APASmoothY_Uls_u2p14[7]	7537		
t_APASmoothY_Uls_u2p14[8]	7700		
t_APASmoothY_Uls_u2p14[9]	7864		
t_LKASlewX_Kph_u8p8[0]	39936		
t_LKASlewX_Kph_u8p8[1]	40448		
t_LKASlewX_Kph_u8p8[2]	40960		
t_LKASlewX_Kph_u8p8[3]	41472		
t_LKASlewX_Kph_u8p8[4]	41984		
t_LKASlewX_Kph_u8p8[5]	42496		
t_LKASlewY_NmpSec_u4p12[0]	31949		
t_LKASlewY_NmpSec_u4p12[1]	32358		
t_LKASlewY_NmpSec_u4p12[2]	32768		
t_LKASlewY_NmpSec_u4p12[3]	33178		
t_LKASlewY_NmpSec_u4p12[4]	33587		
t_LKASlewY_NmpSec_u4p12[5]	33997		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_U	-	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	_	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f3:		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State enu		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_UIs_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls		
target Rte Inst Ap TrgArblim.TrgArblim Per1 TrgOscCmd MtrNm f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target TrqArblim Per1 GMOSHOscillate State enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.5		
target TrqArblim Per1 LKACmd HwNm f32.value	-3.56999993		
target TrqArblim Per1 LKAState State enum.value	2		
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	489.792999		
target TrqArblim Per1 PosServEnable Cnt Igc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.60000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.69999981		
Name	Actual Value	Expected Value	Result
TrgArblim APADisableRate Uls M f32	0.815999985	0.815999985 ± 0000390625	-
TrgArblim ESC State Cnt M enum	1	1	~
TrgArblim HwTorqueSV HwNm M Str.SV UIs f32	-2.68074989	-2.68074989 ± 0.03125	-
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75	1.75 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.09399986	6.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	1	1	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-0.140000001	-0.140000001 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-0.140000001	-0.140000001 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.685412109	0.68541199 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

1 ± 0000390625

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value



Test Step 3.91 (Repeat Count = 1)	🗸
Name	Input Value
Rte_Inst_Ap_TrqArblim	target Rte Inst Ap TrgArblim
TrqArblim_APADisableRate_Uls_M_f32	0.975000024
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.100000001
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.474000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	2
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.5999999
k_APALeaAPmpScale_lag	489.75
k_APAUseADmpScale_lgc k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0.75
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	22938
t_APADisableRateX_HwNm_u4p12[1]	23347
t_APADisableRateX_HwNm_u4p12[2]	23757
t_APADisableRateX_HwNm_u4p12[3]	24166
t_APADisableRateX_HwNm_u4p12[4]	24576
t_APADisableRateX_HwNm_u4p12[5]	24986
t_APADisableRateY_pSec_u7p9[0]	29696 30208
t_APADisableRateY_pSec_u7p9[1] t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	9503
t_APASmoothX_Uls_u2p14[1]	9667
t_APASmoothX_Uls_u2p14[2]	9830
t_APASmoothX_Uls_u2p14[3]	9994
t_APASmoothX_UIs_u2p14[4]	10158
t_APASmoothX_UIs_u2p14[5]	10322 10486
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	10650
t_APASmoothX_Uls_u2p14[8]	10813
t_APASmoothX_Uls_u2p14[9]	10977
t_APASmoothY_Uls_u2p14[0]	6554
t_APASmoothY_Uls_u2p14[1]	6717
t_APASmoothY_Uls_u2p14[2]	6881
t_APASmoothY_Uls_u2p14[3]	7045
t_APASmoothY_Uls_u2p14[4]	7209
t_APASmoothY_Uls_u2p14[5]	7373
t_APASmoothY_Uls_u2p14[6] t_APASmoothY_Uls_u2p14[7]	7537 7700
t_APASmoothY_Uls_u2p14[8]	7864
t_APASmoothY_Uls_u2p14[9]	8028
t_LKASlewX_Kph_u8p8[0]	40448
t_LKASlewX_Kph_u8p8[1]	40960
t_LKASlewX_Kph_u8p8[2]	41472
t_LKASlewX_Kph_u8p8[3]	41984
t_LKASlewX_Kph_u8p8[4]	42496
t_LKASlewX_Kph_u8p8[5]	43008
t_LKASlewY_NmpSec_u4p12[0]	29901
t_KASlewY_NmpSec_u4p12[1]	30310 30720
t_LKASlewY_NmpSec_u4p12[2] t_LKASlewY_NmpSec_u4p12[3]	31130
t_LKASlewY_NmpSec_u4p12[4]	31539
t_LKASlewY_NmpSec_u4p12[5]	31949
target_Rte_Inst_Ap_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32
	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
	target TroArblim Per1 LKACmd HwNm f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32 target_TrgArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	





Name	Input Value			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServ	target_TrqArblim_Per1_PosServEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvc	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnD	DFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOsc0	Cmd_MtrNm_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992			
target_TrqArblim_Per1_ESCState_State_enum.value	2			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.1500001			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.22000003			
target_TrqArblim_Per1_LKAState_State_enum.value	1			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	496.243011			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.10000038			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.9000001			
Name	Actual Value	Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	2	2	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.38850021	-2.38849998 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-8.10000038	-8.10000038 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.5843997	6.58440018 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.510009766	0.510010004 ± 0000390625	~	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.510009766	0.510010004 ± 0000390625	~	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	1	1	~	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0.75	0.75 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	0.75	0.75 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.931078911	0.93107897 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.510009766	0.510010004 ± 0000390625	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.92 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.850000024
TrqArblim_ESC_State_Cnt_M_enum	4
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqArblim_PosSrvoCmd_MtrNm_M_f32	0.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.0999999
k_APAEnableRate_pSec_f32	441
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	1.27999997
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	23347
t_APADisableRateX_HwNm_u4p12[1]	23757
t_APADisableRateX_HwNm_u4p12[2]	24166
t_APADisableRateX_HwNm_u4p12[3]	24576
t_APADisableRateX_HwNm_u4p12[4]	24986
t_APADisableRateX_HwNm_u4p12[5]	25395
t_APADisableRateY_pSec_u7p9[0]	30208
t_APADisableRateY_pSec_u7p9[1]	30720
t_APADisableRateY_pSec_u7p9[2]	31232
t_APADisableRateY_pSec_u7p9[3]	31744
t_APADisableRateY_pSec_u7p9[4]	32256
t_APADisableRateY_pSec_u7p9[5]	32768
t_APASmoothX_Uls_u2p14[0]	9667
t_APASmoothX_Uls_u2p14[1]	9830
t_APASmoothX_Uls_u2p14[2]	9994
t_APASmoothX_Uls_u2p14[3]	10158
t_APASmoothX_Uls_u2p14[4]	10322
t_APASmoothX_Uls_u2p14[5]	10486
t_APASmoothX_UIs_u2p14[6]	10650
t_APASmoothX_Uls_u2p14[7]	10813

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TrqArblim_Per1

TIQAIDIIII_T GIT				
Name	Input Value			
t_APASmoothX_Uls_u2p14[8]	10977			
t_APASmoothX_Uls_u2p14[9]	11141			
:_APASmoothY_Uls_u2p14[0]	6717			
t_APASmoothY_Uls_u2p14[1]	6881			
t_APASmoothY_Uls_u2p14[2]	7045			
t_APASmoothY_Uls_u2p14[3]	7209			
t_APASmoothY_Uls_u2p14[4]	7373			
t_APASmoothY_Uls_u2p14[5]	7537			
t_APASmoothY_Uls_u2p14[6]	7700			
t_APASmoothY_Uls_u2p14[7]	7864			
t_APASmoothY_Uls_u2p14[8]	8028			
t_APASmoothY_Uls_u2p14[9]	8192			
t_LKASlewX_Kph_u8p8[0]	37888			
t_LKASlewX_Kph_u8p8[1]	38400			
t_LKASlewX_Kph_u8p8[2]	38912			
t_LKASlewX_Kph_u8p8[3]	39424			
t_LKASlewX_Kph_u8p8[4]	39936			
t_LKASlewX_Kph_u8p8[5]	40448			
t_LKASlewY_NmpSec_u4p12[0]	30310			
t_LKASlewY_NmpSec_u4p12[1]	30720			
t_LKASlewY_NmpSec_u4p12[2]	31130			
t_LKASlewY_NmpSec_u4p12[3]	31539			
t_LKASlewY_NmpSec_u4p12[4]	31949			
t_LKASlewY_NmpSec_u4p12[5]	32358			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	m_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	s_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	n_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021			
target_TrqArblim_Per1_ESCState_State_enum.value	3			
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2			
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.9000001			
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.96999979			
target_TrqArblim_Per1_LKAState_State_enum.value	3			
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	463.993011			
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0			
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.10000038			
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.9000001			
Name	Actual Value	Expected Value	Resul	
TrqArblim_APADisableRate_Uls_M_f32	0.731999993	0.731999993 ± 0000390625		
TrqArblim_ESC_State_Cnt_M_enum	3	3		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.84975004	-3.84975004 ± 0.03125		
TrgArblim PosSrvoCmd MtrNm M f32	0.75	0.75 ± 0.000244140625		
TrgArblim PreLKATrgReg HwNm M f32	4.08419991	4.08419991 ± 0.03125		
target TrgArblim Per1 AssistDDFactor Uls f32.value	0.634000003	0.634000003 ± 0000390625		
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.634000003	0.634000003 ± 0000390625		
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0		
target TrgArblim Per1 ESCTorqueDelivered HwNm f32.value	0	0 ± 0000390625		
target_TrgArblim_Per1_IaTraOv_HwNm_f32_value	3	3 + 0 0000390625		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

0.274500012

0.634000003

3

 $target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value$

 $target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

 $target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value$

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value$

3 ± 0.0000390625

3 ± 0.0000390625

0.274500012 ± 0000390625

0.634000003 ± 0000390625



Test Step 3.93 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.875
TrqArblim_ESC_State_Cnt_M_enum	0
TrgArblim HwTorqueSV HwNm M Str.SV Uls f32	-1.29999995
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.430000007
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.5999999
k_APAEnableRate_pSec_f32	450.75
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.69000006
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0] t_APADisableRateX_HwNm_u4p12[1]	19661 20070
t_APADisableRateX_HwNm_u4p12[2]	20480
t_APADisableRateX_HwNm_u4p12[3]	20890
t_APADisableRateX_HwNm_u4p12[4]	21299
t_APADisableRateX_HwNm_u4p12[5]	21709
t_APADisableRateY_pSec_u7p9[0]	30720
t_APADisableRateY_pSec_u7p9[1]	31232
t_APADisableRateY_pSec_u7p9[2]	31744
t_APADisableRateY_pSec_u7p9[3]	32256
t_APADisableRateY_pSec_u7p9[4]	32768
t_APADisableRateY_pSec_u7p9[5]	33280
t_APASmoothX_Uls_u2p14[0]	9830
t_APASmoothX_Uls_u2p14[1]	9994
t_APASmoothX_Uls_u2p14[2]	10158
t_APASmoothX_Uls_u2p14[3]	10322
t_APASmoothX_UIs_u2p14[4]	10486
t_APASmoothX_UIs_u2p14[5]	10650 10813
t_APASmoothX_Uls_u2p14[6] t_APASmoothX_Uls_u2p14[7]	10977
t_APASmoothX_Uls_u2p14[8]	11141
t_APASmoothX_Uls_u2p14[9]	11305
t_APASmoothY_Uls_u2p14[0]	6881
t_APASmoothY_Uls_u2p14[1]	7045
t_APASmoothY_Uls_u2p14[2]	7209
t_APASmoothY_Uls_u2p14[3]	7373
t_APASmoothY_Uls_u2p14[4]	7537
t_APASmoothY_Uls_u2p14[5]	7700
t_APASmoothY_Uls_u2p14[6]	7864
t_APASmoothY_Uls_u2p14[7]	8028
t_APASmoothY_Uls_u2p14[8]	8192
t_APASmoothY_Uls_u2p14[9]	8356
t_LKASlewX_Kph_u8p8[0]	38400
t_LKASlewX_Kph_u8p8[1] t_LKASlewX_Kph_u8p8[2]	38912 39424
t_LKASiewX_Kpii_uopo[2] t_LKASiewX_Kph_u8p8[3]	39936
t_LKASlewX_Kph_u8p8[4]	40448
t_LKASlewX_Kph_u8p8[5]	40960
t_LKASlewY_NmpSec_u4p12[0]	30720
t_LKASlewY_NmpSec_u4p12[1]	31130
t_LKASlewY_NmpSec_u4p12[2]	31539
t_LKASlewY_NmpSec_u4p12[3]	31949
t_LKASlewY_NmpSec_u4p12[4]	32358
t_LKASlewY_NmpSec_u4p12[5]	32768
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target Rte Inst Ap TrqArblim.TrqArblim Per1 GMOSHOscillate State enum	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32 target_TrqArblim_Per1_GMOSHOscillate_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_IrqArblim_Per1_GMOSHOScillate_State_enum target_TrqArblim_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32
<u> </u>	·





Name	Input Value				
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph	_f32 target_TrqArbl	m_Per1_MinSecureVehicleS	Speed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArbl	arget_TrqArblim_Per1_OpTrqOv_MtrNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArbl	arget_TrqArblim_Per1_PosServEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArbl	m_Per1_PosSrvoCmd_MtrN	lm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArbl	m_Per1_ReturnDDFactor_U	Jls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArbl	m_Per1_TrqOscCmd_MtrNr	m_f32		
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954				
target_TrqArblim_Per1_ESCState_State_enum.value	4				
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	1				
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-6.55000019				
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-4.61999989				
target_TrqArblim_Per1_LKAState_State_enum.value	4				
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	470.442993				
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1				
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.30000019				
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.0999999				
Name	Actual Value		Expected Value	Result	
TrqArblim_APADisableRate_Uls_M_f32	1		1 ± 0000390625	~	
TrqArblim_ESC_State_Cnt_M_enum	4		4	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.55749989		-3.55749989 ± 0.03125	~	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.30000019		8.30000019 ± 0.000244140625	~	
TrqArblim_PreLKATrqReq_HwNm_M_f32	4.59399986		4.59399986 ± 0.03125	~	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1		1 ± 0000390625	~	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1		1 ± 0000390625	~	
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0		0	~	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0		0 ± 0000390625	~	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3		3 ± 0.0000390625	~	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3		3 ± 0.0000390625	~	
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	8.33308125		8.33308029 ± 0000390625	~	
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1		1 ± 0000390625	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	•
Rte Call TroArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

Test Step 3.94 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.89999976
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.949999988
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.25
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.099999
k_APAEnableRate_pSec_f32	460.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	0
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	20070
t_APADisableRateX_HwNm_u4p12[1]	20480
t_APADisableRateX_HwNm_u4p12[2]	20890
t_APADisableRateX_HwNm_u4p12[3]	21299
t_APADisableRateX_HwNm_u4p12[4]	21709
t_APADisableRateX_HwNm_u4p12[5]	22118
t_APADisableRateY_pSec_u7p9[0]	29696
t_APADisableRateY_pSec_u7p9[1]	30208
t_APADisableRateY_pSec_u7p9[2]	30720
t_APADisableRateY_pSec_u7p9[3]	31232
t_APADisableRateY_pSec_u7p9[4]	31744
t_APADisableRateY_pSec_u7p9[5]	32256
t_APASmoothX_Uls_u2p14[0]	9994
t_APASmoothX_Uls_u2p14[1]	10158
t_APASmoothX_Uls_u2p14[2]	10322
t_APASmoothX_Uls_u2p14[3]	10486
t_APASmoothX_Uls_u2p14[4]	10650
t_APASmoothX_Uls_u2p14[5]	10813





10977			
11141			
11305			
11469			
7045			
7209			
	422		
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	1_102		
	Expected Value	Resu	
	•	Resu	
0.479980469			
0	1		
117	0 ± 0000390625		
	0 + 0 0000300635		
0	0 ± 0.0000390625		
	0 ± 0.0000390625 0 ± 0.0000390625 8.72017002 ± 0000390625		
	7045 7209 7373 7537 7700 7864 8028 8192 8356 8520 38912 39424 39936 40448 40960 41472 31130 31539 31949 32358 32768 33178 target_TrqArblim_Per1_AssistDDFactor_Ultarget_TrqArblim_Per1_DampingDDFactor_target_TrqArblim_Per1_ESCCmd_HwNm_ftarget_TrqArblim_Per1_ESCState_State_ertarget_TrqArblim_Per1_ESCState_State_ertarget_TrqArblim_Per1_HwTorque_HwNm_target_TrqArblim_Per1_HwTorque_HwNm_ftarget_TrqArblim_Per1_LKACmd_HwNm_ftarget_TrqArblim_Per1_LKACmd_HwNm_ftarget_TrqArblim_Per1_LKAState_State_ertarget_TrqArblim_Per1_PosSrvoCmd_MtrNnn10.1800003 2 1 6.19999981 4.269999982 2 476.893005 1 8.5 4.30000019 Actual Value 1 2 -3.26525021 8.5 5.08379984 0.479980469 0.479980469 0.479980469	7045 7209 7373 7537 7700 7864 8028 8192 8356 8520 38912 39424 39936 40448 40980 41472 31130 31539 31949 32258 32768 33178 32768 33178 32768 33178 4048 40890 41472	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	~

Test Step 3.95 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.949999988
TrqArblim_ESC_State_Cnt_M_enum	2
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.099999
k APAEnableRate pSec f32	480
k APAUseADmpScale Igc	0
APAUseAsstScale lgc	0
<_APAUseRetScale_lgc	0
c ESCMax HwNm f32	5
<pre>C_LKAUseSlewCal_lgc</pre>	0
ueocolonoui_go :_APADisableRateX_HwNm_u4p12[0]	22528
: APADisableRateX HwNm u4p12[1]	22938
:_APADisableRateX_HwNm_u4p12[2]	23347
: APADisableRateX_HWNm_u4p12[3]	23757
t_APADisableRateX_nwnii_u4p12[3] t_APADisableRateX_HwNm_u4p12[4]	24166
	24576
:_APADisableRateX_HwNm_u4p12[5]	245/6 34304
t_APADisableRateY_pSec_u7p9[0]	
t_APADisableRateY_pSec_u7p9[1]	34816
t_APADisableRateY_pSec_u7p9[2]	35328
t_APADisableRateY_pSec_u7p9[3]	35840
t_APADisableRateY_pSec_u7p9[4]	36352
t_APADisableRateY_pSec_u7p9[5]	36864
:_APASmoothX_Uls_u2p14[0]	9339
t_APASmoothX_Uls_u2p14[1]	9503
t_APASmoothX_Uls_u2p14[2]	9667
t_APASmoothX_Uls_u2p14[3]	9830
t_APASmoothX_Uls_u2p14[4]	9994
t_APASmoothX_Uls_u2p14[5]	10158
t_APASmoothX_Uls_u2p14[6]	10322
t_APASmoothX_Uls_u2p14[7]	10486
t_APASmoothX_Uls_u2p14[8]	10650
t_APASmoothX_UIs_u2p14[9]	10813
t_APASmoothY_Uls_u2p14[0]	6390
t_APASmoothY_Uls_u2p14[1]	6554
t_APASmoothY_Uls_u2p14[2]	6717
t_APASmoothY_Uls_u2p14[3]	6881
t_APASmoothY_Uls_u2p14[4]	7045
_APASmoothY_Uls_u2p14[5]	7209
t_APASmoothY_Uls_u2p14[6]	7373
t_APASmoothY_Uls_u2p14[7]	7537
_APASmoothY_Uls_u2p14[8]	7700
:_APASmoothY_Uls_u2p14[9]	7864
LKASlewX Kph u8p8[0]	39936
LKASlewX Kph u8p8[1]	40448
LKASlewX_Kph_u8p8[2]	40960
LKASlewX_Kph_u8p8[3]	41472
:_LKASlewX_Kph_u8p8[4]	41984
:_LKASlewX_Kph_u8p8[5]	42496
_LKASlewY_NmpSec_u4p12[0]	31949
_LKASlewY_NmpSec_u4p12[0]	32358
: LKASlewY NmpSec_u4p12[1]	32768
:_LKASlewY_NmpSec_u4p12[3]	33178
:_LKASlewY_NmpSec_u4p12[4]	33587
LKASlewY_NmpSec_u4p12[5]	33997
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f3	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	target_TrqArblim_Per1_GMOSHOscillate_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-3.75999999		
target_TrqArblim_Per1_ESCState_State_enum.value	1		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.5		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.56999993		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	489.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.60000038		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.6999981		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.815999985	0.815999985 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	1	1	~
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.68074989	-2.68074989 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.75	1.75 ± 0.000244140625	~
TrqArblim_PreLKATrqReq_HwNm_M_f32	6.09399986	6.09399986 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	~
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-3.75999999	-0.140000001 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	~
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.685412109	0.68541199 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.96 (Repeat Count = 1)	v v
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.449999988
TrqArblim_ESC_State_Cnt_M_enum	3
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-7.25
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.10000002
k_APAEnableRate_pSec_f32	285
k_APAUseADmpScale_lgc	0
k_APAUseAsstScale_lgc	0
k_APAUseRetScale_lgc	0
k_ESCMax_HwNm_f32	3.25
k_LKAUseSlewCal_lgc	0
t_APADisableRateX_HwNm_u4p12[0]	20890
t_APADisableRateX_HwNm_u4p12[1]	21299
t_APADisableRateX_HwNm_u4p12[2]	21709
t_APADisableRateX_HwNm_u4p12[3]	22118
t_APADisableRateX_HwNm_u4p12[4]	22528
t_APADisableRateX_HwNm_u4p12[5]	22938
t_APADisableRateY_pSec_u7p9[0]	33280
t_APADisableRateY_pSec_u7p9[1]	33792
t_APADisableRateY_pSec_u7p9[2]	34304
t_APADisableRateY_pSec_u7p9[3]	34816
t_APADisableRateY_pSec_u7p9[4]	35328

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[5]	35840		
t_APASmoothX_Uls_u2p14[0]	10813		
t_APASmoothX_Uls_u2p14[1]	10977		
t_APASmoothX_Uls_u2p14[2]	11141		
t_APASmoothX_Uls_u2p14[3]	11305		
t_APASmoothX_Uls_u2p14[4]	11469		
t_APASmoothX_Uls_u2p14[5]	11633		
t_APASmoothX_Uls_u2p14[6]	11796		
t_APASmoothX_Uls_u2p14[7]	11960		
t_APASmoothX_Uls_u2p14[8]	12124		
t_APASmoothX_Uls_u2p14[9]	12288		
t_APASmoothY_Uls_u2p14[0]	7864		
t_APASmoothY_Uls_u2p14[1]	8028		
t_APASmoothY_Uls_u2p14[2]	8192		
t_APASmoothY_Uls_u2p14[3]	8356		
t_APASmoothY_Uls_u2p14[4]	8520		
t_APASmoothY_Uls_u2p14[5]	8684		
t_APASmoothY_Uls_u2p14[6]	8847		
t_APASmoothY_Uls_u2p14[7]	9011		
t_APASmoothY_Uls_u2p14[8]	9175 9339		
t_APASmoothY_Uls_u2p14[9]			
t_LKASlewX_Kph_u8p8[0]	35328 35840		
t_KASlewX_Kph_u8p8[1] t_KASlewX_Kph_u8p8[2]	36352		
	36864		
t_LKASlewX_Kph_u8p8[3] t LKASlewX Kph u8p8[4]	37376		
t_LKASlewX_Kph_u8p8[5]	37888		
t_LKASlewY_NmpSec_u4p12[0]	28262		
t_LKASlewY_NmpSec_u4p12[1]	28672		
t_LKASlewY_NmpSec_u4p12[2]	29082		
t_LKASlewY_NmpSec_u4p12[3]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901		
t_LKASlewY_NmpSec_u4p12[5]	30310		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UI	s f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32			
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_PosServEnable_Cr	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	lm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	ls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNn	n_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-9.52000046		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	5.13000011		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	10.0200005		
target_TrqArblim_Per1_LKAState_State_enum.value	2		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	360.792999		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	4.9000001		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	0.69999988		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	0.319999993	0.310000002 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.24166012	-4.24166012 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	5.94000006	5.94000006 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	-1.09399998	-1.09399998 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	•
		1 ± 0000390625	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 1 0000390023	
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	

-1.09399998

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value$

-1.09399998 ± 0.0000390625





Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.9123469	0.883835971 ± 0000390625	✓
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte Call TrqArblim Per1 CP1 CheckpointReached	1	Rte Call TrqArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.97 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	0
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999
<pre><_APAEnableRate_pSec_f32</pre>	421.5
<pre>c_APAUseADmpScale_lgc</pre>	1
c_APAUseAsstScale_lgc	1
<pre>c_APAUseRetScale_lgc</pre>	1
c_ESCMax_HwNm_f32	2.57999992
c_LKAUseSlewCal_lgc	1
_APADisableRateX_HwNm_u4p12[0]	25805
_APADisableRateX_HwNm_u4p12[1]	26214
_APADisableRateX_HwNm_u4p12[2]	26624
_APADisableRateX_HwNm_u4p12[3]	27034
_APADisableRateX_HwNm_u4p12[4]	27443
_APADisableRateX_HwNm_u4p12[5]	27853
_APADisableRateY_pSec_u7p9[0]	43520
_APADisableRateY_pSec_u7p9[1]	44032
_APADisableRateY_pSec_u7p9[2]	44544
_APADisableRateY_pSec_u7p9[3]	45056
_APADisableRateY_pSec_u7p9[4]	45568
_APADisableRateY_pSec_u7p9[5]	46080
_APASmoothX_Uls_u2p14[0]	9011
_APASmoothX_Uls_u2p14[1]	9175
_APASmoothX_Uls_u2p14[2]	9339
_APASmoothX_Uls_u2p14[3]	9503
_APASmoothX_Uls_u2p14[4]	9667
_APASmoothX_Uls_u2p14[5]	9830
_APASmoothX_Uls_u2p14[6]	9994
_APASmoothX_Uls_u2p14[7]	10158
_APASmoothX_Uls_u2p14[8]	10322
_APASmoothX_Uls_u2p14[9]	10486
_APASmoothY_Uls_u2p14[0]	6062
_APASmoothY_Uls_u2p14[1]	6226
_APASmoothY_Uls_u2p14[2]	6390
APASmoothY Uls u2p14[3]	6554
_APASmoothY_Uls_u2p14[4]	6717
_APASmoothY_Uls_u2p14[5]	6881
_APASmoothY_Uls_u2p14[6]	7045
APASmoothY Uls u2p14[7]	7209
_APASmoothY_Uls_u2p14[8]	7373
APASmoothY Uls u2p14[9]	7537
LKASlewX_Kph_u8p8[0]	36864
_LKASlewX_Kph_u8p8[1]	37376
LKASlewX Kph u8p8[2]	37888
_LKASlewX_Kph_u8p8[3]	38400
_LKASlewX_Kph_u8p8[4]	38912
_LKASlewX_Kph_u8p8[5]	39424
_LKASlewY_NmpSec_u4p12[0]	29491
_LKASlewY_NmpSec_u4p12[1]	29901
	30310
_LKASlewY_NmpSec_u4p12[2]	
:_LKASlewY_NmpSec_u4p12[3]	30720
_LKASlewY_NmpSec_u4p12[4]	31130
t_LKASlewY_NmpSec_u4p12[5]	31539
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_UIs_f32 target_TrqArblim_Per1_DampingDDFactor_UIs_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_St	ate_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered	I_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNn	n_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Uls	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-4.71999979		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.43424988	-4.43424988 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.6999981	7.69999981 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc.value	0	0	•
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	•

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

3.54216909

0.539978027

3.54217005 ± 0000390625

0.539978027 ± 0000390625

Test Step 3.98 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.800000012
TrqArblim_ESC_State_Cnt_M_enum	1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.3499999
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999
k_APAEnableRate_pSec_f32	421.5
k_APAUseADmpScale_lgc	1
k_APAUseAsstScale_lgc	1
k_APAUseRetScale_lgc	1
k_ESCMax_HwNm_f32	4
k_LKAUseSlewCal_lgc	1
t_APADisableRateX_HwNm_u4p12[0]	25805
t_APADisableRateX_HwNm_u4p12[1]	26214
t_APADisableRateX_HwNm_u4p12[2]	26624
t_APADisableRateX_HwNm_u4p12[3]	27034
t_APADisableRateX_HwNm_u4p12[4]	27443
t_APADisableRateX_HwNm_u4p12[5]	27853
t_APADisableRateY_pSec_u7p9[0]	28160
t_APADisableRateY_pSec_u7p9[1]	28672

 $target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value\\ target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value$

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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Name	Input Value		
t_APADisableRateY_pSec_u7p9[2]	29184		
t_APADisableRateY_pSec_u7p9[3]	29696		
t_APADisableRateY_pSec_u7p9[4]	30208		
t_APADisableRateY_pSec_u7p9[5]	30720		
t_APASmoothX_Uls_u2p14[0]	9011		
t_APASmoothX_Uls_u2p14[1]	9175		
t_APASmoothX_Uls_u2p14[2]	9339		
t_APASmoothX_Uls_u2p14[3]	9503		
t_APASmoothX_Uls_u2p14[4]	9667		
t_APASmoothX_Uls_u2p14[5]	9830		
t_APASmoothX_Uls_u2p14[6]	9994		
t_APASmoothX_Uls_u2p14[7]	10158		
t_APASmoothX_Uls_u2p14[8]	10322		
t_APASmoothX_Uls_u2p14[9] t_APASmoothY_Uls_u2p14[0]	10486 6062		
t_APASmoothY_Uls_u2p14[1]	6226		
t_APASmoothY_Uls_u2p14[2]	6390		
t_APASmoothY_Uls_u2p14[3]	6554		
t_APASmoothY_Uls_u2p14[4]	6717		
t APASmoothY Uls u2p14[5]	6881		
t_APASmoothY_Uls_u2p14[6]	7045		
t_APASmoothY_Uls_u2p14[7]	7209		
t_APASmoothY_Uls_u2p14[8]	7373		
t_APASmoothY_Uls_u2p14[9]	7537		
t_LKASlewX_Kph_u8p8[0]	36864		
t_LKASlewX_Kph_u8p8[1]	37376		
t_LKASlewX_Kph_u8p8[2]	37888		
t_LKASlewX_Kph_u8p8[3]	38400		
t_LKASlewX_Kph_u8p8[4]	38912		
t_LKASlewX_Kph_u8p8[5]	39424		
t_LKASlewY_NmpSec_u4p12[0]	29491		
t_LKASlewY_NmpSec_u4p12[1]	29901		
t_LKASlewY_NmpSec_u4p12[2]	30310		
t_LKASlewY_NmpSec_u4p12[3]	30720		
t_LKASlewY_NmpSec_u4p12[4]	31130		
t_LKASlewY_NmpSec_u4p12[5]	31539		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32 target Rte Inst Ap TrqArblim.TrqArblim Per1 MinSecureVehicleSpeed Kph f32	target_TrqArblim_Per1_LKATorqueDelivered		
	target_TrqArblim_Per1_MinSecureVehicleSpace		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_lgc	target_TrqArblim_Per1_OpTrqOv_MtrNm_t3		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSetVEnable_Cnt_lgc target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSetvErlable_Crit target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_Ul		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	-8.56000042	_102	
target TrqArblim Per1 ESCState State enum.value	2		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999		
target TrgArblim Per1 LKACmd HwNm f32.value	-5.67000008		
target TrqArblim Per1 LKAState State enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.6999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	2	2	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.43424988	-4.43424988 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.6999981	7.69999981 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.08459997	3.08459997 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.539978027	0.539978027 ± 0000390625	•
target TrqArblim Per1 ESCIsLimited Cnt Igc.value	1	1	•

1

1

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Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	-4	-4 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	0	0 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.54216909	3.54217005 ± 0000390625	✓
target TrqArblim Per1 ReturnDDFactor Uls f32.value	0.539978027	0.539978027 ± 0000390625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Test Step 3.99 (Repeat Count = 1)	Immit Value	
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
FrqArblim_APADisableRate_Uls_M_f32	0.75	
rqArblim_ESC_State_Cnt_M_enum	2	
rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995	
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.375	
rqArblim_PosSrvoCmd_MtrNm_M_f32	9.42000008	
rqArblim_PreLKATrqReq_HwNm_M_f32	2.0999999	
_APAEnableRate_pSec_f32	402	
r_APAUseADmpScale_lgc	1	
c_APAUseAsstScale_lgc	1	
_APAUseRetScale_lgc	1	
_ESCMax_HwNm_f32	3	
_LKAUseSlewCal_lgc	1	
_APADisableRateX_HwNm_u4p12[0]	24986	
_APADisableRateX_HwNm_u4p12[1]	25395	
_APADisableRateX_HwNm_u4p12[2]	25805	
_APADisableRateX_HwNm_u4p12[3]	26214	
_APADisableRateX_HwNm_u4p12[4]	26624	
_APADisableRateX_HwNm_u4p12[5]	27034	
_APADisableRateY_pSec_u7p9[0]	26112	
_APADisableRateY_pSec_u7p9[1]	26624	
_APADisableRateY_pSec_u7p9[2]	27136	
_APADisableRateY_pSec_u7p9[3]	27648	
_APADisableRateY_pSec_u7p9[4]	28160	
_APADisableRateY_pSec_u7p9[5]	28672	
_APASmoothX_Uls_u2p14[0]	8684	
_APASmoothX_Uls_u2p14[1]	8847	
_APASmoothX_Uls_u2p14[2]	9011	
_APASmoothX_Uls_u2p14[3]	9175	
_APASmoothX_Uls_u2p14[4]	9339	
_APASmoothX_Uls_u2p14[5]	9503	
_APASmoothX_Uls_u2p14[6]	9667	
_APASmoothX_Uls_u2p14[7]	9830	
_APASmoothX_UIs_u2p14[8]	9994	
_APASmoothX_Uls_u2p14[9]	10158	
_APASmoothY_Uls_u2p14[0]	5734	
_APASmoothY_Uls_u2p14[1]	5898	
_APASmoothY_Uls_u2p14[1]	6062	
APASmoothY Uls u2p14[2]	6226	
_APASmoothY_Uls_u2p14[4]	6390	
_APASmoothY_Uls_u2p14[5]	6554	
_APASmoothY_Uls_u2p14[6]	6717	
_APASmoothY_Uls_u2p14[7]	6881	
_APASmoothY_Uls_u2p14[8]	7045	
_APASmoothY_Uls_u2p14[9]	7209	
_LKASlewX_Kph_u8p8[0]	35840	
_LKASlewX_Kph_u8p8[1]	36352	
_LKASlewX_Kph_u8p8[2]	36864	
_LKASlewX_Kph_u8p8[3]	37376	
_LKASlewX_Kph_u8p8[4]	37888	
_LKASlewX_Kph_u8p8[5]	38400	
_LKASlewY_NmpSec_u4p12[0]	28672	
_LKASlewY_NmpSec_u4p12[1]	29082	
_LKASlewY_NmpSec_u4p12[2]	29491	
_LKASlewY_NmpSec_u4p12[3]	29901	
_LKASlewY_NmpSec_u4p12[4]	30310	

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Name	Input Value		
t_LKASlewY_NmpSec_u4p12[5]	30720		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_	lgc	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_er	ium	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere	d_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3	2	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivere	d_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNi	m_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UI	s_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	6.57999992		
target_TrqArblim_Per1_ESCState_State_enum.value	3		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-8.30000019		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-6.36999989		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	438.192993		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.30000019		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.0999999		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	1	1 ± 0000390625	~
TrqArblim_ESC_State_Cnt_M_enum	3	3	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.01875019	-5.01875019 ± 0.03125	✓
TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.30000019	7.30000019 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	2.0849998	2.08500004 ± 0.03125	~
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.559997559	0.559997976 ± 0000390625	~
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	2.0849998	2.08500004 ± 0.0000390625	~
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.21201801	3.21201992 ± 0000390625	✓
-			

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

0.559997559

0.559997976 ± 0000390625

Test Step 3.100 (Repeat Count = 1)		~
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0.800000012	
TrqArblim_ESC_State_Cnt_M_enum	3	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.3499999	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.397000015	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	10	
TrqArblim_PreLKATrqReq_HwNm_M_f32	3.0999999	
k_APAEnableRate_pSec_f32	421.5	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	1	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	22528	
t_APADisableRateX_HwNm_u4p12[1]	22938	
t_APADisableRateX_HwNm_u4p12[2]	23347	
t_APADisableRateX_HwNm_u4p12[3]	23757	
t_APADisableRateX_HwNm_u4p12[4]	24166	

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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TrqArblim_Per1



1 · = ·			
Name	Input Value		
t_APADisableRateX_HwNm_u4p12[5]	24576		
t_APADisableRateY_pSec_u7p9[0]	29184		
t_APADisableRateY_pSec_u7p9[1]	29696		
t_APADisableRateY_pSec_u7p9[2]	30208		
t_APADisableRateY_pSec_u7p9[3]	30720		
t_APADisableRateY_pSec_u7p9[4]	31232 31744		
t_APADisableRateY_pSec_u7p9[5] t_APASmoothX_Uls_u2p14[0]	10158		
t_APASmoothX_Uls_u2p14[1]	10322		
t_APASmoothX_Uls_u2p14[2]	10486		
t_APASmoothX_Uls_u2p14[3]	10650		
t_APASmoothX_Uls_u2p14[4]	10813		
t_APASmoothX_Uls_u2p14[5]	10977		
t_APASmoothX_Uls_u2p14[6]	11141		
t_APASmoothX_UIs_u2p14[7]	11305		
t_APASmoothX_Uls_u2p14[8] t_APASmoothX_Uls_u2p14[9]	11469 11633		
t_APASmoothY_Uls_u2p14[0]	6717		
t_APASmoothY_Uls_u2p14[1]	6881		
t_APASmoothY_Uls_u2p14[2]	7045		
t_APASmoothY_Uls_u2p14[3]	7209		
t_APASmoothY_Uls_u2p14[4]	7373		
t_APASmoothY_Uls_u2p14[5]	7537		
t_APASmoothY_Uls_u2p14[6]	7700		
t_APASmoothY_UIs_u2p14[7]	7864		
t_APASmoothY_UIs_u2p14[8]	8028 8192		
t_APASmoothY_Uls_u2p14[9] t_LKASlewX_Kph_u8p8[0]	34816		
t_LKASlewX_Kph_u8p8[1]	35328		
t_LKASlewX_Kph_u8p8[2]	35840		
t_LKASlewX_Kph_u8p8[3]	36352		
t_LKASlewX_Kph_u8p8[4]	36864		
t_LKASlewX_Kph_u8p8[5]	37376		
t_LKASlewY_NmpSec_u4p12[0]	27853		
t_LKASlewY_NmpSec_u4p12[1]	28262		
t_LKASlewY_NmpSec_u4p12[2]	28672		
t_LKASlewY_NmpSec_u4p12[3] t_LKASlewY_NmpSec_u4p12[4]	29082 29491		
t_LKASlewY_NmpSec_u4p12[5]	29901		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESClsLimited_Cnt_lgc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_S	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_t target_TrqArblim_Per1_IqTrqOv_HwNm_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNr		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_ReturnDDFactor_Ularget_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_Rte_inst_Ap_rtqArbiiin. rtqArbiiin_Peri_rtqOscomd_MitNiii_132 target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	7.78000021	<u></u>	
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	2		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-7.5999999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-5.67000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.092987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	7.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	3.5	Function Males	-
Name Tradrhlim ADADisableDate IIIa M #22	Actual Value	Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32 TrqArblim ESC State Cnt M enum	1 4	1 ± 0000390625	
·	-4.43424988	4 -4.43424988 ± 0.03125	
TraArhlim HwTorqueSV HwNm M Str SV Llle f32			1
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqArblim_PosSrvoCmd_MtrNm_M_f32	7.6999981	7.69999981 ± 0.000244140625	

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Name	Actual Value	Expected Value	Result
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	~
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	~
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	✓
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	3.8499999	3.8499999 ± 0000390625	~
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	✓

Test Step 3.101 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim
TrqArblim_APADisableRate_Uls_M_f32	0.925000012
FrqArblim_ESC_State_Cnt_M_enum	4
FrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.600000024
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.451999992
rqArblim_PosSrvoCmd_MtrNm_M_f32	1.5
rqArblim_PreLKATrqReq_HwNm_M_f32	5.5999999
_APAEnableRate_pSec_f32	470.25
_APAUseADmpScale_lgc	1
_APAUseAsstScale_lgc	1
_APAUseRetScale_lgc	1
_ESCMax_HwNm_f32	2
	1
APADisableRateX HwNm u4p12[0]	22938
_APADisableRateX_HwNm_u4p12[1]	23347
_APADisableRateX_HwNm_u4p12[2]	23757
_APADisableRateX_HwNm_u4p12[3]	24166
_APADisableRateX_HwNm_u4p12[4]	24576
_APADisableRateX_HwNm_u4p12[5]	24986
_APADisableRateY_pSec_u7p9[0]	29696
APADisableRateY pSec u7p9[1]	30208
_APADisableRateY_pSec_u7p9[1]	30720
	31232
APADisableRateY_pSec_u7p9[3]	
_APADisableRateY_pSec_u7p9[4]	31744 32256
_APADisableRateY_pSec_u7p9[5]	
_APASmoothX_Uls_u2p14[0]	10813
_APASmoothX_Uls_u2p14[1]	10977
_APASmoothX_Uls_u2p14[2]	11141
_APASmoothX_Uls_u2p14[3]	11305
_APASmoothX_Uls_u2p14[4]	11469
_APASmoothX_Uls_u2p14[5]	11633
_APASmoothX_Uls_u2p14[6]	11796
_APASmoothX_Uls_u2p14[7]	11960
_APASmoothX_Uls_u2p14[8]	12124
_APASmoothX_Uls_u2p14[9]	12288
_APASmoothY_Uls_u2p14[0]	6881
_APASmoothY_Uls_u2p14[1]	7045
_APASmoothY_Uls_u2p14[2]	7209
_APASmoothY_Uls_u2p14[3]	7373
_APASmoothY_Uls_u2p14[4]	7537
_APASmoothY_Uls_u2p14[5]	7700
_APASmoothY_Uls_u2p14[6]	7864
_APASmoothY_Uls_u2p14[7]	8028
_APASmoothY_Uls_u2p14[8]	8192
_APASmoothY_Uls_u2p14[9]	8356
LKASlewX_Kph_u8p8[0]	35328
LKASlewX_Kph_u8p8[1]	35840
LKASlewX Kph u8p8[2]	36352
ration=wx_qpii_usps[2] _LKASlewX_Kph_u8p8[3]	36864
_LKASlewX_Kph_u8p8[4]	37376
_LKASlewX_Kph_u8p8[5]	37888
_LKASlewY_NmpSec_u4p12[0]	28262
_LKASlewY_NmpSec_u4p12[1]	28672

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Name	Input Value		
t_LKASlewY_NmpSec_u4p12[2]	29082		
t_LKASlewY_NmpSec_u4p12[3]	29491		
t_LKASlewY_NmpSec_u4p12[4]	29901		
t_LKASlewY_NmpSec_u4p12[5]	30310		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_e	num	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32$	target_TrqArblim_Per1_ESCTorqueDeliver	ed_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_	State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm	_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f3	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f	32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_e	num	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDeliver	ed_HwNm_f32	
$target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqArblim_Per1_MinSecureVehicles	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_t	f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Ci	nt_lgc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrN	lm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_U	Jls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNi	m_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	8.97999954		
target_TrqArblim_Per1_ESCState_State_enum.value	4		
target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0		
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-5.8499999		
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-3.92000008		
target_TrqArblim_Per1_LKAState_State_enum.value	1		
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	483.342987		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	0		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.69999981		
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	4.5		
Name	Actual Value	Expected Value	Result
TrqArblim_APADisableRate_Uls_M_f32	0.809000015	0.809000015 ± 0000390625	•
TrqArblim_ESC_State_Cnt_M_enum	4	4	•
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.97300005	-2.97300005 ± 0.03125	•
TrqArblim_PosSrvoCmd_MtrNm_M_f32	1.5	1.5 ± 0.000244140625	•
TrqArblim_PreLKATrqReq_HwNm_M_f32	5.58520031	5.58519983 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	•
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	•
target_TrqArblim_Per1_ESClsLimited_Cnt_lgc.value	0	0	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	3	3 ± 0.0000390625	
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	0.618896842	0.618897021 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	0.587402105	0.587401986 ± 0000390625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	•
Rte Call TrgArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	~

Test Step 3.102 (Repeat Count = 1)		~
Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
TrqArblim_APADisableRate_Uls_M_f32	0	
TrqArblim_ESC_State_Cnt_M_enum	0	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584996	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998	
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.80000019	
k_APAEnableRate_pSec_f32	0	
k_APAUseADmpScale_lgc	0	
k_APAUseAsstScale_lgc	0	
k_APAUseRetScale_lgc	0	
k_ESCMax_HwNm_f32	0	
k_LKAUseSlewCal_lgc	0	
t_APADisableRateX_HwNm_u4p12[0]	0	
t_APADisableRateX_HwNm_u4p12[1]	0	

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Name	Input Value	
t_APADisableRateX_HwNm_u4p12[2]	0	
t_APADisableRateX_HwNm_u4p12[3] t_APADisableRateX_HwNm_u4p12[4]	0	
t APADisableRateX HwNm u4p12[5]	0	
t_APADisableRateY_pSec_u7p9[0]	0	
t_APADisableRateY_pSec_u7p9[1]	0	
t_APADisableRateY_pSec_u7p9[2]	0	
t_APADisableRateY_pSec_u7p9[3]	0	
t_APADisableRateY_pSec_u7p9[4]	0	
t_APADisableRateY_pSec_u7p9[5]	0	
t_APASmoothX_UIs_u2p14[0] t_APASmoothX_UIs_u2p14[1]	0	
t APASmoothX Uls u2p14[2]	0	
t_APASmoothX_Uls_u2p14[3]	0	
t_APASmoothX_Uls_u2p14[4]	0	
t_APASmoothX_Uls_u2p14[5]	0	
t_APASmoothX_Uls_u2p14[6]	0	
t_APASmoothX_Uls_u2p14[7]	0	
t_APASmoothX_Uls_u2p14[8]	0	
t_APASmoothX_UIs_u2p14[9]	0	
t_APASmoothY_Uls_u2p14[0]	0	
t_APASmoothY_Uls_u2p14[1] t_APASmoothY_Uls_u2p14[2]	0	
t_APASmoothY_Uls_u2p14[3]	0	
t_APASmoothY_Uls_u2p14[4]	0	
t_APASmoothY_Uls_u2p14[5]	0	
t_APASmoothY_Uls_u2p14[6]	0	
t_APASmoothY_Uls_u2p14[7]	0	
t_APASmoothY_Uls_u2p14[8]	0	
t_APASmoothY_Uls_u2p14[9]	0	
t_LKASlewX_Kph_u8p8[0]	0	
t_LKASiewX_Kph_u8p8[1] t_LKASiewX_Kph_u8p8[2]	0	
t_LKASlewX_Kph_u8p8[3]	0	
t_LKASlewX_Kph_u8p8[4]	0	
t_LKASlewX_Kph_u8p8[5]	0	
t_LKASlewY_NmpSec_u4p12[0]	0	
t_LKASlewY_NmpSec_u4p12[1]	0	
t_LKASlewY_NmpSec_u4p12[2]	0	
t_LKASlewY_NmpSec_u4p12[3]	0	
t_LKASIewY_NmpSec_u4p12[4] t_LKASIewY_NmpSec_u4p12[5]	0	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_DampingDDFactor_Uls_f32	target_TrqArblim_Per1_DampingDDFactor_Uls_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCCmd_HwNm_f32	target_TrqArblim_Per1_ESCCmd_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_Igc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_GMOSHOscillate_State_enum	target_TrqArblim_Per1_GMOSHOscillate_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_HwTorque_HwNm_f32	target_TrqArblim_Per1_HwTorque_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32 target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_IqTrqOv_HwNm_f32 target_TrqArblim_Per1_LKACmd_HwNm_f32	
target_Rte_inst_Ap_TrqArblim.TrqArblim_Per1_LKAState_State_enum	target_TrqArblim_Per1_LKAState_State_enum	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosServEnable_Cnt_Igc	target_TrqArblim_Per1_PosServEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32	
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ReturnDDFactor_Uls_f32	target_TrqArblim_Per1_ReturnDDFactor_UIs_f32	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_TrqOscCmd_MtrNm_f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value target_TrqArblim_Per1_ESCState_State_enum.value	-10.2299995 0	
target_TrqArblim_Per1_ESCState_State_enum.value target_TrqArblim_Per1_GMOSHOscillate_State_enum.value	0	
target_TrqArblim_Per1_HwTorque_HwNm_f32.value	-10	
target_TrqArblim_Per1_LKACmd_HwNm_f32.value	-10.2399998	
target_TrqArblim_Per1_LKAState_State_enum.value	0	
target_TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32.value	0	
target_TrqArblim_Per1_PosServEnable_Cnt_Igc.value	0	
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	-8.80000019	
target_TrqArblim_Per1_TrqOscCmd_MtrNm_f32.value	-8.80000019	
Name	Actual Value Expected Value	Resul
TrqArblim_APADisableRate_Uls_M_f32 TrqArblim_ESC_State_Cnt_M_enum	0 0 ± 0000390625 0 0	

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TrqArblim_Per1

Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10	-10 ± 0.03125	~
TrqArblim_PosSrvoCmd_MtrNm_M_f32	-10.2399998	-10.2399998 ± 0.000244140625	✓
TrqArblim_PreLKATrqReq_HwNm_M_f32	-8.77299976	-8.77299976 ± 0.03125	•
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	1	1 ± 0000390625	✓
target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	1	1 ± 0000390625	•
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0	✓
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	0	0 ± 0000390625	•
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value	-3	-3 ± 0.0000390625	✓
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	-3	-3 ± 0.0000390625	•
target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0000390625	•
target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value	1	1 ± 0000390625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	~
Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP1_CheckpointReached	1	~

Name	Input Value	
Rte_Inst_Ap_TrqArblim	target_Rte_Inst_Ap_TrqArblim	
rqArblim_APADisableRate_Uls_M_f32	1	
rgArblim ESC State Cnt M enum	4	
rqArblim_Eco_clate_on_i_w_chain rqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	
rqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390027	
rqArblim_PosSrvoCmd_MtrNm_M_f32	10.2299995	
rqArblim_PreLKATrqReq_HwNm_M_f32	8.80000019	
_APAEnableRate_pSec_f32	500	
APAUseADmpScale lgc	1	
APAUseAsstScale_lgc	1	
_APAUseRetScale_lgc	1	
_AFAOSEREISCHIE_igc _ESCMax_HwNm_f32	5	
_LKAUseSlewCal_lgc	1	
	40960	
_APADisableRateX_HwNm_u4p12[0]		
_APADisableRateX_HwNm_u4p12[1]	40960 40960	
_APADisableRateX_HwNm_u4p12[2]		
APADisableRateX_HwNm_u4p12[3]	40960	
_APADisableRateX_HwNm_u4p12[4]	40960	
_APADisableRateX_HwNm_u4p12[5]	40960	
_APADisableRateY_pSec_u7p9[0]	65535	
APADisableRateY_pSec_u7p9[1]	65535	
APADisableRateY_pSec_u7p9[2]	65535	
_APADisableRateY_pSec_u7p9[3]	65535	
APADisableRateY_pSec_u7p9[4]	65535	
_APADisableRateY_pSec_u7p9[5]	65535	
APASmoothX_Uls_u2p14[0]	16384	
_APASmoothX_Uls_u2p14[1]	16384	
_APASmoothX_Uls_u2p14[2]	16384	
_APASmoothX_Uls_u2p14[3]	16384	
APASmoothX_Uls_u2p14[4]	16384	
APASmoothX_Uls_u2p14[5]	16384	
_APASmoothX_Uls_u2p14[6]	16384	
APASmoothX_Uls_u2p14[7]	16384	
_APASmoothX_Uls_u2p14[8]	16384	
_APASmoothX_Uls_u2p14[9]	16384	
_APASmoothY_Uls_u2p14[0]	16384	
APASmoothY_Uls_u2p14[1]	16384	
APASmoothY_Uls_u2p14[2]	16384	
APASmoothY_Uls_u2p14[3]	16384	
_APASmoothY_Uls_u2p14[4]	16384	
_APASmoothY_Uls_u2p14[5]	16384	
APASmoothY_Uls_u2p14[6]	16384	
APASmoothY_Uls_u2p14[7]	16384	
APASmoothY_Uls_u2p14[8]	16384	
APASmoothY_Uls_u2p14[9]	16384	
_LKASlewX_Kph_u8p8[0]	56320	
LKASlewX_Kph_u8p8[1]	56320	
LKASlewX_Kph_u8p8[2]	56320	
LKASlewX_Kph_u8p8[3]	56320	
LKASlewX_Kph_u8p8[4]	56320	

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TrqArblim_Per1

TrqArbiim_Peri		· ·	MACICAL
Name	Input Value		
t LKASlewX Kph u8p8[5]	56320		
t LKASlewY NmpSec u4p12[0]	55296		
t_LKASlewY_NmpSec_u4p12[1]	55296		
t_LKASlewY_NmpSec_u4p12[2]	55296		
t LKASlewY NmpSec u4p12[3]	55296		
t LKASlewY NmpSec u4p12[4]	55296		
t LKASlewY NmpSec u4p12[5]	55296		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_AssistDDFactor_Uls_f32	target_TrqArblim_Per1_AssistDDFactor_Uls	f32	
target Rte Inst Ap TrqArblim.TrqArblim Per1 DampingDDFactor Uls f32	target TrqArblim Per1 DampingDDFactor	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 ESCCmd HwNm f32	target TrqArblim Per1 ESCCmd HwNm f3	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCIsLimited_Cnt_Igc	target_TrqArblim_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_ESCState_State_enum	target_TrqArblim_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32	target_TrqArblim_Per1_ESCTorqueDelivere		
target Rte Inst Ap TrgArblim.TrgArblim Per1 GMOSHOscillate State enum	target TrqArblim Per1 GMOSHOscillate S		
target Rte Inst Ap TrqArblim.TrqArblim Per1 HwTorque HwNm f32	target TrqArblim Per1 HwTorque HwNm 1	_	
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_IqTrqOv_HwNm_f32	target TrqArblim Per1 IqTrqOv HwNm f32		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKACmd_HwNm_f32	target_TrqArblim_Per1_LKACmd_HwNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 LKAState State enum	target TrqArblim Per1 LKAState State en		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_LKATorqueDelivered_HwNm_f32	target_TrqArblim_Per1_LKATorqueDelivered		
target_Rte_Inst_Ap_TrqArblim.TrqArblim_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqArblim_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqArblim_TrqArblim_Per1_OpTrqOv_MtrNm_f32	target_TrqArblim_Per1_OpTrqOv_MtrNm_f3		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosServEnable Cnt Igc	target TrqArblim Per1 PosServEnable Cnt		
target Rte Inst Ap TrqArblim.TrqArblim Per1 PosSrvoCmd MtrNm f32	target TrqArblim Per1 PosSrvoCmd MtrNr		
target Rte Inst Ap TrqArblim.TrqArblim Per1 ReturnDDFactor UIs f32	target TrqArblim Per1 ReturnDDFactor Ul	_	
target Rte Inst Ap TrqArblim.TrqArblim Per1 TrqOscCmd MtrNm f32	target_TrqArblim_Per1_TrqOscCmd_MtrNm	_	
target_TrqArblim_Per1_ESCCmd_HwNm_f32.value	10.2299995	_102	
target TrqArblim Per1 ESCState State enum.value	4		
target TrqArblim Per1 GMOSHOscillate State enum.value	2		
target TrqArblim Per1 HwTorque HwNm f32.value	10		
target TrqArblim Per1 LKACmd HwNm f32.value	10.2299995		
target TrqArblim Per1 LKAState State enum.value	4		
target TrqArblim Per1 MinSecureVehicleSpeed Kph f32.value	511		
target_TrqArblim_Per1_PosServEnable_Cnt_lgc.value	1		
target_TrqArblim_Per1_PosSrvoCmd_MtrNm_f32.value	8.80000019		
target TrqArblim Per1 TrqOscCmd MtrNm f32.value	8.80000019		
Name	Actual Value	Expected Value	Result
TrqArblim APADisableRate Uls M f32	1	1 ± 0000390625	Result
TrqArblim ESC State Cnt M enum	4	4	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10	10 ± 0.03125	
TrqArblim_PosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019 ± 0.00024414062	
TrqArblim PreLKATrqReq HwNm M f32	8.77299976	8.77299976 ± 0.03125	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value	0	0.77299970 ± 0.03123 0 ± 0000390625	
target_TrqArblim_Per1_AssistDDFactor_Uls_f32.value target_TrqArblim_Per1_DampingDDFactor_Uls_f32.value	0	0 ± 0000390625	
	0	0 ± 0000390625	
target_TrqArblim_Per1_ESCIsLimited_Cnt_lgc.value	0	0 ± 0000390625	
target_TrqArblim_Per1_ESCTorqueDelivered_HwNm_f32.value	3	0 ± 0000390625 3 ± 0.0000390625	
target_TrqArblim_Per1_IqTrqOv_HwNm_f32.value			
target_TrqArblim_Per1_LKATorqueDelivered_HwNm_f32.value	3	3 ± 0.0000390625	<u> </u>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	Rte_Call_TrqArblim_Per1_CP0_CheckpointReached	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	3	IntplVarXY_u16_u16Xu16Y_Cnt	3	~
Rte Call TroArblim Per1 CP1 CheckpointReached	1	Rte Call TrgArblim Per1 CP1 CheckpointReached	1	_

8.80000019

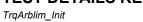
8.80000019 ± 0000390625

0 ± 0000390625

target_TrqArblim_Per1_OpTrqOv_MtrNm_f32.value

target_TrqArblim_Per1_ReturnDDFactor_Uls_f32.value

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 Project
 TrqArblim

 Module
 TrqArblim

 Test Object
 TrqArblim_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_TrqArblim
Configuration File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqArblim\src\Ap_TrqArblim.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_OFF -D_DATA_ACCESS= -Dconst= -l\$(PROJECTROOT)\TrqArblim\utp\contract \Ap_TrqArblim -l\$(PROJECTROOT)\TrqArblim\utp\contract -l\$(PROJECTROOT)\NxtrLib\include -l\$(PROJECTROOT)\StdDef\include -l\$ (Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-DBC_TRQARBLIM_FAULTINJECTIONPOINT=STD_OFF-D_DATA_ACCESS= -Dconst= -l\$(PROJECTROOT)\TrqArblim\utp\contract \Ap_TrqArblim -l\$(PROJECTROOT)\TrqArblim\utp\contract -l\$(PROJECTROOT)\NxtrLib\include -l\$(PROJECTROOT)\StdDef\include -l\$ (Compiler Install Path)\include

Comments/Descript	tion/Specification
Name	Text
Module 'TrqArblim'	Name of Tester:Priti Mangalekar
	Code File(s) Under Test: Ap_TrqArblim.c Code File(s) Version:5 Module Design Document:TrqArblim_MDD.docx Module Design Document Version:6 Data Dictionary Version:5 Unit Test Plan Version:4
	Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):1456 Total RAM Used (Bytes):33 Total CALS Used (Bytes):104
	Special Test Requirements: Test Date:9/16/2014 Comments: Note 1: Inline functions defined in ""GlobalMacro.h"" are not unit tested.
	Note 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference.
	Note 3:In function ""TrqArblim_Per1"" path ""(D_ONE_ULS_F32 <= ((D_2MS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" cannot be covered as ""((D_2MS_SEC_F32 * APAlowDisableRate_pSec_T_f32) + TrqArblim_APADisableRate_Uls_M_f32))"" always gets updated with a value less than 1."

Attributes		
Name	Value	
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>	
Float Precision	9	
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>	
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>	
Library	libc.a	

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TrqArblim_Init



Attributes	
Name	Value
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Time Enabled	false
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_TrqArblim\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TS1.13 ALL MAX



Test Case 1: Boundary Test Performance Metrics: (With "None" Instrumentation and "WithPS" Environment) Specification CPU Cycles: TS1.1 515.00 Cycles 515.00 514.00 475.00 461.00 447.00 432.00 475.00 475.00 457.00 457.00 457.00 TS1.2 TS1.3 TS1.4 TS1.5 TS1.6 TS1.7 TS1.8 TS1.9 TS1.10 TS1.11 TS1.12 Description Vector Description: TS1.1 k_APASmoothHwTrqLPFKn_Hz_f32 = MIN TS1.2 k_APASmoothHwTrqLPFKn_Hz_f32 = MAX TS1.3 k_APASmoothHwTrqLPFKn_Hz_f32 = POS TS1.4 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = MIN TS1.5 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = MAX TS1.6 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = ZERO TS1.7 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = NEG TS1.8 TrqArblim_HwTorqueSV_HwNm_M_Str.SV = POS TS1.9 TrqArblim_HwTorqueSV_HwNm_M_Str.K = MIN TS1.10 TrqArblim_HwTorqueSV_HwNm_M_Str.K = MAX TS1.11 TrqArblim_HwTorqueSV_HwNm_M_Str.K = POS TS1.12 ALL MIN TS1.13 ALL MIN

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.63		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.023		
k_APASmoothHwTrqLPFKn_Hz_f32	0.1		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.00125585 ± 0.03125	✓

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.087		
k_APASmoothHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.71539 ± 0.03125	✓

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.151		
k_APASmoothHwTrqLPFKn_Hz_f32	50.65		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470851779	0.470852 ± 0.03125	✓

Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.215		
k_APASmoothHwTrqLPFKn_Hz_f32	5.41		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0657246709	0.0657246 ± 0.03125	✓



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Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.279		
k_APASmoothHwTrqLPFKn_Hz_f32	11.42		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133686125	0.133686 ± 0.03125	✓

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.343		
k_APASmoothHwTrqLPFKn_Hz_f32	15.85		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.18059516	0.180595 ± 0.03125	✓

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407		
k_APASmoothHwTrqLPFKn_Hz_f32	20		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.222232342	0.222232 ± 0.03125	✓

Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.68		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.471		
k_APASmoothHwTrqLPFKn_Hz_f32	28.61		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.301991463	0.301991 ± 0.03125	✓

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.34		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00125585		
k_APASmoothHwTrqLPFKn_Hz_f32	36.86		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.370730996	0.370731 ± 0.03125	✓

Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5		
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.71539		
k_APASmoothHwTrqLPFKn_Hz_f32	42		
Name	Actual Value	Expected Value	Result
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	✓
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.4100914	0.410091 ± 0.03125	✓





Test Step 1.11 (Repeat Count = 1)				
Name	Input Value			
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.24			
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.01			
k_APASmoothHwTrqLPFKn_Hz_f32	48.34			
Name	Actual Value	Expected Value	Result	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.03125	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.455266416	0.455266 ± 0.03125	✓	

Test Step 1.12 (Repeat Count = 1)				
Name	Input Value			
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10			
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00125585			
k_APASmoothHwTrqLPFKn_Hz_f32	0.1			
Name	Actual Value	Expected Value	Result	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	0 ± 0.03125	~	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.00125585 ± 0.03125	✓	

Test Step 1.13 (Repeat Count = 1)				
Name	Input Value			
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10			
TrqArblim_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.71539			
k_APASmoothHwTrqLPFKn_Hz_f32	100			
Name	Actual Value	Expected Value	Result	
TrqArblim_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0	0 ± 0.03125	✓	
TrqArblim_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.71539 ± 0.03125	✓	