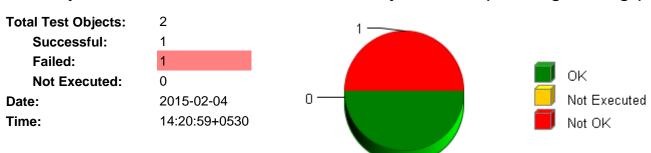




Overall Test Object Results (including Coverage)



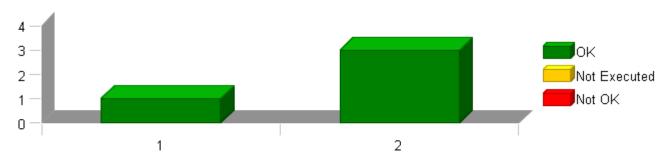
Selected Project Items

Test Object "CBD_UnitTest/TrqOvlSta/TrqOvlSta_Init" Test Object "CBD_UnitTest/TrqOvlSta/TrqOvlSta_Per1"

Used Test Environments

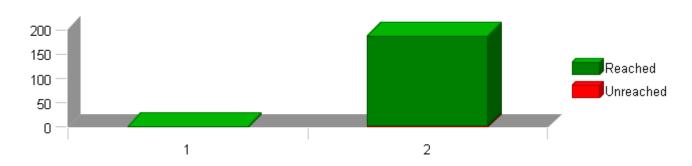
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

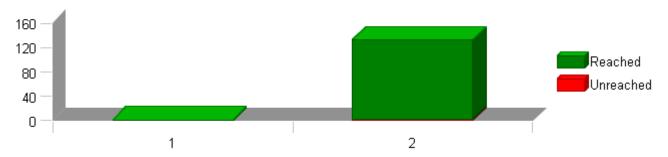
Statement (C0) Coverage: Total Statements for Each Test Object





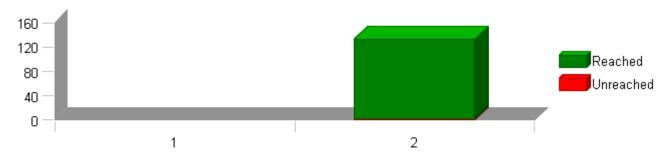
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

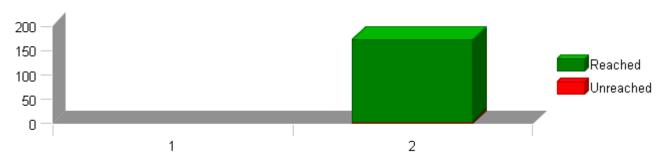
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object

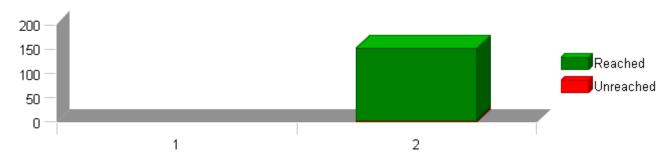


The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2015-02-04, 14:20:59+0530



Project CBD_UnitTest

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	МСС	Test Cases	Result
	CBD_UnitTest	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	*
	CBD_UnitTest	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	*
	TrqOvlSta	98.94 %	98.5 %	98.49 %	98.25 %	98.02 %	4 of 4 passed	*
1	TrqOvlSta_Init	100 %	100 %	-	-	-	1 of 1 passed	•
2	TrqOvlSta_Per1	98.93 %	98.49 %	98.49 %	98.25 %	98.02 %	3 of 3 passed	×

© Report created by TESSY V3.1.9, report template V2.0

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

 Project
 CBD_UnitTest

 Module
 TrqOvlSta

 Test Object
 TrqOvlSta_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	98.93 %
Decision Coverage	98.49 %
Branch (C1) Coverage	98.49 %
MCC Coverage	98.02 %
MC/DC Coverage	98.25 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\TrqOvlSta_CBD
Configuration File	D:\Synergy_Work_Area\TrqOvlSta_CBD\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqOvlSta\src\Ap_TrqOvlSta.c
Compiler Options	-DSKIP_MAGIC_NUMBER= -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqOvlsta\utp\contract -I\$(PROJECTROOT)\TrqOvlsta \utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments \ccsv4\tools\compiler\tms470 4.9.5\include

Comments/Description/Specification					
Name	Text				



Module 'TrqOvISta'

Name of Tester:Imran desai Code File(s) Under Test:Ap_TrqOvlSta.c Code File(s) Version:8

Module Design Document:TrqOvlSta_MDD.docx Module Design Document Version:7

Data Dictionary Version:6 Unit Test Plan Version:5

Ont test Plan Version:3
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):2128
Total RAM Used (Bytes):44

Total CALS Used (Bytes):65 Special Test Requirements:

Test Date:2/4/2015
Comments:"NOTE 1:- Inline functions defined in ""GlobalMacro.h"" are not unit tested.

NOTE 2:- ""CBD_Sandbox_dbg.map"" map file is embedded for reference.

NOTE 3:- In function ""TrqOvlSta_Per1"", for 100% path coverage, Variable ""TrqOvlSta_APAState_State_M_enum"" is given out of range value at Vector Number ""76"" in Path sheet, Hence at this test vector output variable ""TrqOvlSta_APAState_State_M_enum"" and ""APAState_State_enum" is going Out of Range.

NOTE 4:-In function ""TrqOvISta_Per1"", for 100% path coverage Variable ""TrqOvISta_HapticState_State_M_enum"" and ""TrqOvISta_ESCState_State_M_enum""s given out of range value at Vector Number ""78"" in Path sheet, Hence at this test vector, output variable
""TrqOvlSta_ESCState_State_M_enum"",""TrqOvlSta_HapticState_State_M_enum"",""ESCState_State_enum""and""GMOSHOscillateState_State_State_State_state_

Indovota_Escate_state_state_in_enum, indovosta_napreciate_m_enum, become _state_stat

NOTE 7:-In function ""TrqOvlSta_Per1"", (SWARTrgtAngRequest_HwDeg_T_f32<=(CCWPosition_HwDeg_T_f32 + k_SWARLimiter_HwDeg_f32)) = True and (SWARTrgtAngRequest_HwDeg_T_f32 >=(CWPosition_HwDeg_T_f32 - k_SWARLimiter_HwDeg_f32)) = True cannot be covered."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\Trq0vlSta_CBD\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



```
Test Case 1: Metrics Test
```

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 874.00 Cycles TS1.2 1053.00 Cycles

Description

Vector Description:

TS1.1"Shrotest Excution Path==>
(LKARequest_Cnt_T_f32 > D_ZERO_ULS_F32)=> TRUE
(HapticRequest_Cnt_T_gc == TRUE)=>FALSE
(ShiftLeverIsInReverse_Cnt_T_gc == TRUE)=>FALSE
(SecureVehicleSpeed_Kph_T_f32 >= k_StandstillThresh_Kph_f32)=>FALSE
(TrqOvISta_HapticState_State_M_enum != Haptic_Inactive)=>TRUE
(HwTrqFilt_HwNm_T_f32 >= k_APAMaxHwTrq_HwNm_f32)=>FALSE
((LKAFault_Cnt_T_lgc == TRUE) && (TrqOvISta_LKAPermFault_Cnt_M_lgc == FALSE))=>FALSE
(TrgOvISta_LKAMfgEnableTvH_Cnt_G_lgc == FALSE)=>FALSE

(TrqOvISta_LKAMfgEnableTyH_Cnt_G_lgc ==FALSE)=>FALSE (TrqOvISta_APAMfgEnableTyH_Cnt_G_lgc ==TRUE)=>FALSE Select (TrqOvISta_APAState_State_M_enum)

Case: Default

Select (TrqOvISta LKAState State M enum)

Case: Default

(TrqOvISta_HapticState_State_M_enum==Haptic_Active)=>TRUE (TrqOvISta_APAState_State_M_enum==APA_Active)=>FALSE

```
TS1.2"Longest Excution Path==>
(LKARequest_Cnt_T_f32 > D_ZERO_ULS_F32)=> FALSE
(HapticRequest_Cnt_T_g2 == TRUE)=>TRUE
(TrqOvlSta_Haptictime_Sec_M_f32 >= (k_MaxHapticReqTime_Sec_f32))=>FALSE
(ShiftLeverIsInReverse_Cnt_T_lgc == TRUE)=>TRUE
(TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 > (k_ShiftLevRevTime_Sec_f32))=>FALSE
(SecureVehicleSpeed_Kph_T_f32 >= k_StandstillTime_Sec_M_f32)=>TRUE
(TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 >= k_StandstillTime_Sec_f32)=>FALSE
(TrqOvlSta_HapticState_State_M_enum!= Haptic_Inactive)=>FALSE
(Abs_f32_m(HwTrqFilt_HwNm_T_f32) >= k_APAMaxHwTrq_HwNm_f32)=>TRUE
(TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 >= ((k_APAMaxHwTrqTime_Sec_f32)))=>FALSE
((LKAFault_Cnt_T_lgc == TRUE) && (TrqOvlSta_LKAPermFault_Cnt_M_lgc == FALSE) && (TrqOvlSta_LKAFault_Cnt_M_lgc == FALSE))=>TRUE
  FALSE))=>TRUE
 (TrqOvlSta_LKAPermFault_Cnt_M_u08 >= 3U)=>True && (*Rte_Pim_TrqOvlStaMfgEnable_Cnt_b08() & D_LKAMFGENABLE_CNT_U08) != D_LKAMFGENABLE_CNT_U08) ==>True && ((*Rte_Pim_TrqOvlStaMfgEnable_Cnt_b08() & D_APAMFGENABLE_CNT_U08) != D_APAMFGENABLE_CNT_U08) ==>False
 (TrqOvISta_LKAMfgEnableTyH_Cnt_G_lgc ==FALSE)=>FALSE
(TrqOvISta_APAMfgEnableTyH_Cnt_G_lgc ==TRUE)=>FALSE
Select (TrqOvISta_APAState_State_M_enum)
 Case(APA_AvailCntrl):
 Case(AFA_Availoriti),

(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=>FALSE
((SecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
((HapticRequest_Cnt_T_lgc==TRUE)&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)))=>FALSE
(k_APAIncludeHaptic_Cnt_lgc == FALSE)=>FALSE
Select (TrqOvISta_HapticState_State_M_enum)
 Case(Haptic_Inactive):

((HapticRequest_Cnt_T_Igc==FALSE) && (TrqOvlSta_ReadytoPulse_Cnt_M_Igc==FALSE))=>FALSE

(TrqOvlSta_ReadytoPulse_Cnt_M_Igc== TRUE)=>FALSE

((StandstillTime_Cnt_T_Igc==TRUE) && (APARequest_Cnt_T_Igc
 ==TRUE)&&(HapticRequest_Cnt_T_lgc==TRUE)&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))=>FALSE (HapticRequest_Cnt_T_lgc==TRUE)=>TRUE Select (TrqOvISta_LKAState_State_M_enum):
 DEFAULT
 (TrqOvISta_HapticState_State_M_enum==Haptic_Active)
 (TrqOvISta_APAState_State_M_enum==APA_Active)=>FALSE
```

Test Step 1.1 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	4	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	

TrqOvlSta_Per1

2015-02-04, 14:05:40+0530



Name	Input Value
TrqOvISta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	5270
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1.
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k APAMaxHwTrg HwNm f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k ESCMaxVehSpd Kph f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
	82.25
k_LKAMinVehSpd_Kph_f32	
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH ESCMfgEnable Cnt Igc	target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target TrqOvlSta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target TrgOvlSta Per1 HwTorque HwNm f32
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKAFault Cnt Igc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAState_State_cours
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-390.01001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360.980011
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0
target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc.value	0
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
angot_nqoviota_r or r_orridori_LoorriigEnable_offt_igu.value	1.

© Report created by TESSY V3.1.9, report template V2.1

4

2015-02-04, 14:05:40+0530



1 ± 1

3.25 ± 0.0009765625

TrqOvlSta_Per1

		• "	
Name	Input Value		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	-

1

3.25

Input Value
Innut Value
mpat value
target_Rte_Inst_Ap_TrqOvlSta
1
1
0
0
0
0
44450
0
-1.64999998
0.419
0
0
1
0
1.3999998
1
1
0.601666689
0
138.600006
12
0.462000012
1.15499997

 $target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value$

 $target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value$

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

· –			
Name	Input Value		
k_HapticFreq_Hz_f32	32.3019981		
k_HapticReacttime_Sec_f32	1.23199999		
k_LKAMaxVehSpd_Kph_f32	146.300003		
k_LKAMinVehSpd_Kph_f32	1		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	0		
k_ShiftLevRevTime_Sec_f32	0.308999985		
k_StandstillThresh_Kph_f32	0		
k_StandstillTime_Sec_f32	0.308999985		
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_RestrictionPerformance (Continuous Continuous C$	target_TrqOvlSta_Per1_APADrvrIntervention	Detected_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enumerators and the property of the pr$	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgE	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	:_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-388.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1	I=	
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	
TrqOvISta_ESCState_State_M_enum	2	2	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta ElapsedStandstillTime Sec M f32	0.00200000009	0.00200000009 ± 1	√

0.00200000009

 $TrqOvlSta_ElapsedStandstillTime_Sec_M_f32$

0.00200000009 ± 1

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

TrqOvlSta_Per1

2015-02-04, 14:05:40+0530



Test Case 2: Boundary Test

2015-02-04, 14:05:40+0530



Specification

TrqOvlSta_Per1

Performance Metrics (With "None"
Instrumentation and WithPS Environment)

CPU Cycles:
TS2.1 906.00 Cycles
TS2.2 891.00 Cycles
TS2.3 887.00 Cycles
TS2.3 887.00 Cycles
TS2.5 887.00 Cycles
TS2.5 887.00 Cycles
TS2.6 905.00 Cycles
TS2.7 884.00 Cycles
TS2.10 932.00 Cycles
TS2.10 932.00 Cycles
TS2.11 883.00 Cycles
TS2.12 912.00 Cycles
TS2.13 889.00 Cycles
TS2.14 890.00 Cycles
TS2.14 890.00 Cycles
TS2.15 893.00 Cycles
TS2.16 892.00 Cycles
TS2.17 903.00 Cycles
TS2.18 893.00 Cycles
TS2.19 900.00 Cycles
TS2.19 900.00 Cycles TS2.18 893.00 Cycles TS2.19 900.00 Cycles TS2.20 906.00 Cycles TS2.21 896.00 Cycles TS2.23 879.00 Cycles TS2.24 886.00 Cycles TS2.24 886.00 Cycles TS2.25 916.00 Cycles TS2.26 904.00 Cycles TS2.27 892.00 Cycles TS2.28 895.00 Cycles TS2.29 904.00 Cycles TS2.30 889.00 Cycles TS2.31 931.00 Cycles TS2.31 931.00 Cycles TS2.31 931.00 Cycles TS2.30 889.00 Cycles TS2.31 931.00 Cycles TS2.32 887.00 Cycles TS2.33 903.00 Cycles TS2.34 909.00 Cycles TS2.35 915.00 Cycles TS2.36 893.00 Cycles TS2.37 891.00 Cycles TS2.37 891.00 Cycles
TS2.38 913.00 Cycles
TS2.38 913.00 Cycles
TS2.40 889.00 Cycles
TS2.41 900.00 Cycles
TS2.42 902.00 Cycles
TS2.43 911.00 Cycles
TS2.44 911.00 Cycles
TS2.45 913.00 Cycles
TS2.46 907.00 Cycles
TS2.47 907.00 Cycles
TS2.48 903.00 Cycles
TS2.49 907.00 Cycles
TS2.49 907.00 Cycles
TS2.50 896.00 Cycles
TS2.51 899.00 Cycles
TS2.52 900.00 Cycles
TS2.53 915.00 Cycles
TS2.54 898.00 Cycles TS2.53 915.00 Cycles TS2.54 898.00 Cycles TS2.55 918.00 Cycles TS2.56 894.00 Cycles TS2.57 924.00 Cycles TS2.58 887.00 Cycles TS2.59 934.00 Cycles TS2.60 890.00 Cycles TS2.60 890.00 Cycles TS2.61 923.00 Cycles TS2.62 940.00 Cycles TS2.63 893.00 Cycles TS2.64 919.00 Cycles TS2.65 888.00 Cycles TS2.66 894.00 Cycles TS2.66 894.00 Cycles TS2.69 894.00 Cycles TS2.69 894.00 Cycles TS2.70 913.00 Cycles TS2.71 893.00 Cycles TS2.72 907.00 Cycles TS2.73 897.00 Cycles TS2.74 906.00 Cycles TS2.75 893.00 Cycles TS2.76 889.00 Cycles TS2.77 896.00 Cycles TS2.78 883.00 Cycles TS2.79 906.00 Cycles TS2.80 899.00 Cycles TS2.81 917.00 Cycles TS2.82 908.00 Cycles TS2.83 913.00 Cycles TS2.84 905.00 Cycles TS2.84 905.00 Cycles TS2.85 887.00 Cycles TS2.86 890.00 Cycles TS2.87 896.00 Cycles TS2.88 924.00 Cycles TS2.89 888.00 Cycles TS2.90 892.00 Cycles TS2.91 908.00 Cycles TS2.92 900.00 Cycles TS2.92 900.00 Cycles
TS2.93 888.00 Cycles
TS2.94 907.00 Cycles
TS2.95 907.00 Cycles
TS2.96 920.00 Cycles
TS2.96 920.00 Cycles
TS2.98 933.00 Cycles
TS2.98 930.00 Cycles
TS2.98 930.00 Cycles
TS2.100 912.00 Cycles
TS2.101 897.00 Cycles
TS2.101 897.00 Cycles
TS2.103 893.00 Cycles
TS2.104 953.00 Cycles
TS2.105 903.00 Cycles
TS2.105 903.00 Cycles
TS2.106 933.00 Cycles
TS2.106 933.00 Cycles
TS2.107 900.00 Cycles
TS2.108 890.00 Cycles TS2.108 890.00 Cycles TS2.109 900.00 Cycles TS2.110 883.00 Cycles

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



TS2.111 903.00 Cycles
TS2.112 883.00 Cycles
TS2.113 894.00 Cycles
TS2.114 895.00 Cycles
TS2.115 898.00 Cycles
TS2.115 898.00 Cycles
TS2.117 915.00 Cycles
TS2.118 883.00 Cycles
TS2.118 883.00 Cycles
TS2.119 899.00 Cycles
TS2.121 889.00 Cycles
TS2.122 883.00 Cycles
TS2.123 905.00 Cycles
TS2.124 883.00 Cycles
TS2.125 898.00 Cycles
TS2.125 898.00 Cycles
TS2.126 892.00 Cycles
TS2.127 902.00 Cycles
TS2.128 883.00 Cycles
TS2.131 889.00 Cycles
TS2.131 889.00 Cycles
TS2.132 896.00 Cycles
TS2.133 893.00 Cycles
TS2.134 883.00 Cycles
TS2.135 910.00 Cycles
TS2.136 883.00 Cycles
TS2.141 898.00 Cycles
TS2.142 896.00 Cycles
TS2.143 914.00 Cycles
TS2.144 883.00 Cycles
TS2.144 883.00 Cycles
TS2.145 899.00 Cycles
TS2.146 896.00 Cycles
TS2.147 899.00 Cycles
TS2.148 881.00 Cycles
TS2.149 903.00 Cycles
TS2.149 903.00 Cycles
TS2.149 899.00 Cycles
TS2.149 899.00 Cycles
TS2.150 893.00 Cycles
TS2.151 913.00 Cycles
TS2.153 999.00 Cycles
TS2.154 889.00 Cycles
TS2.155 899.00 Cycles
TS2.156 883.00 Cycles
TS2.157 908.00 Cycles
TS2.158 892.00 Cycles
TS2.159 904.00 Cycles
TS2.150 890.00 Cycles
TS2.151 890.00 Cycles
TS2.153 900.00 Cycles
TS2.154 883.00 Cycles
TS2.155 899.00 Cycles
TS2.156 883.00 Cycles
TS2.157 900.00 Cycles
TS2.158 892.00 Cycles
TS2.159 904.00 Cycles
TS2.159 904.00 Cycles





Description Vector Description:

TS2.1APANonRecoverableFaults_Cnt_lgc = MIN TS2.2APANonRecoverableFaults_Cnt_lgc = MAX TS2.2APANonRecoverableFaults_Cnt_lgc = MIN
TS2.3APARecoverableFaults_Cnt_lgc = MIN
TS2.4APARecoverableFaults_Cnt_lgc = MAX
TS2.5APARequest_Cnt_lgc = MIN
TS2.6APARequest_Cnt_lgc = MAX
TS2.7HapticRequest_Cnt_lgc = MIN
TS2.8HapticRequest_Cnt_lgc = MIN
TS2.9ShiftLeverIsInReverse_Cnt_lgc = MIN
TS2.10ShiftLeverIsInReverse_Cnt_lgc = MAX
TS2.11 KAFault_Cnt_lgc = MIN TS2.11LKAFault_Cnt_lgc = MIN TS2.11LKAFault_Cnt_lgc = MIN
TS2.12LKAFault_Cnt_lgc = MAX
TS2.13LKAInhibit_Cnt_lgc = MIN
TS2.14LKAInhibit_Cnt_lgc = MIN
TS2.15LKARequest_Cnt_lgc = MIN
TS2.16LKARequest_Cnt_lgc = MIN
TS2.16LKARequest_Cnt_lgc = MAX
TS2.17HwTorque_HwNm_f32 = MIN
TS2.18HwTorque_HwNm_f32 = MAX
TS2.19HwTorque_HwNm_f32 = ZERO
TS2.20HwTorque_HwNm_f32 = POS
TS2.21HwTorque_HwNm_f32 = NEG
TS2.22TrnOvISta_HanticState_State_N TS2.22TrqOvlSta_HapticState_State_M_enum = Haptic_Inactive TS2.23TrqOvlSta_HapticState_State_M_enum = Haptic_Active TS2.24TrqOvlSta_HapticState_State_M_enum = Haptic_Waiting TS2.25TrqOvlSta_LKAState_State_M_enum = LKA_Inactive
TS2.26TrqOvlSta_LKAState_State_M_enum = LKA_Active
TS2.27TrqOvlSta_LKAState_State_M_enum = LKA_TemporarilyLimited TS2.28TrqOvlSta_LKAState_State_M_enum = LKA_PermanentlyFailed TS2.29TrqOvISta_LKAState_State_Menum = LKA_ManufacturingDisable
TS2.39TrqOvISta_APAState_State_M_enum = APA_TempInhibit
TS2.31TrqOvISta_APAState_State_M_enum = APA_AvailCntrl
TS2.32TrqOvISta_APAState_State_M_enum = APA_Active
TS2.33TrqOvISta_APAState_State_M_enum = APA_PermanentlyFailed
TS2.33TrqOvISta_APAState_State_M_enum = APA_ManufacturingDisable
TS2.34TrqOvISta_APAState_State_M_enum = APA_ManufacturingDisable TS2.35TrqOvlSta_Haptictime_Sec_M_f32 = MIN TS2.36TrqOvlSta_Haptictime_Sec_M_f32 = MAX TS2.37TrqOvlSta_Haptictime_Sec_M_f32 = POS TS2.38TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 = MIN TS2.39TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32= MAX TS2.40TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TS2.41TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = MIN TS2.42TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = MAX TS2.43TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 = POS TS2.44TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = MIN
TS2.45TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = MAX
TS2.46TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 = POS IS2.46 I rQOVISta_LiapsedAPAMAXHW I rq I ime_Sec_M_132
TS2.47 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = MIN
TS2.48 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = MAX
TS2.49 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = ZERO
TS2.50 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = POS
TS2.51 I rqOVISta_HwTorqueSV_HwNm_M_Str.SV = NEG
TS2.52 I rqOVISta_HwTorqueSV_HwNm_M_Str.K = MIN
TS2.53 I rqOVISta_HwTorqueSV_HwNm_M_Str.K = MAX TS2.54TrqOvISta_HwTorqueSV_HwNm_M_Str.K = POS TS2.55TrqOvISta_LKAPermFault_Cnt_M_lgc = MIN TS2.56TrqOvISta_LKAPermFault_Cnt_M_lgc= MAX TS2.57TrqOvISta_LKAFault_Cnt_M_lgc = MIN TS2.57TrqOvISta_LKAFault_Cnt_M_lgc = MIN TS2.58TrqOvISta_LKAFault_Cnt_M_lgc = MAX TS2.59TrqOvISta_LKAPermFault_Cnt_M_u08 = MIN TS2.60TrqOvISta_LKAPermFault_Cnt_M_u08 = MAX TS2.61TrqOvISta_LKAPermFault_Cnt_M_u08 = POS TS2.62TrqOvISta_ReadytoPulse_Cnt_M_lgc = MIN TS2.63TrqOvISta_ReadytoPulse_Cnt_M_lgc= MAX TS2.64TrqOvISta_Hapticdur_Cnt_M_u16 = MIN TS2.65TrqOvISta_Hapticdur_Cnt_M_u16= MAX TS2.66TrqOvISta_Hapticdur_Cnt_M_u16 = POS TS2.67k_MaxHapticReqTime_Sec_f32 = MIN TS2.68k_MaxHapticReqTime_Sec_f32= MAX TS2.69k_MaxHapticReqTime_Sec_f32 = POS TS2.70k ShiftLevRevTime Sec f32 = MIN TS2.71k ShiftLevRevTime Sec f32 = MAX TS2.72k ShiftLevRevTime Sec f32 = POS TS2.73k_StandstillThresh_Kph_f32 = MIN TS2.74k_StandstillThresh_Kph_f32 = MAX TS2.75k_StandstillThresh_Kph_f32 = POS TS2.76k_StandstillTime_Sec_f32 = MIN TS2.77k_StandstillTime_Sec_f32= MAX TS2.78k_StandstillTime_Sec_f32 = POS TS2.79k_APAMaxHwTrq_HwNm_f32 = MIN TS2.80k_APAMaxHwTrq_HwNm_f32= MAX TS2.81k_APAMaxHwTrq_HwNm_f32 = POS TS2.82k_APAMaxHwTrqTime_Sec_f32 = MIN TS2.83k_APAMaxHwTrqTime_Sec_132 = MiN
TS2.83k_APAMaxHwTrqTime_Sec_132 = MAX
TS2.84k_APAMaxHwTrqTime_Sec_132 = POS
TS2.85k_APAMaxVehSpd_Kph_132 = MIN
TS2.86k_APAMaxVehSpd_Kph_132 = MAX
TS2.87k_APAMaxVehSpd_Kph_132 = POS
TS2.88k_HapticDuration_Sec_132 = MIN TS2.89k_HapticDuration_Sec_f32= MAX TS2.90k_HapticDuration_Sec_f32 = POS TS2.91k_HapticReacttime_Sec_f32 = MIN TS2.92k_HapticReacttime_Sec_f32= MAX TS2.93k_HapticReacttime_Sec_f32 = POS TS2.94k_LKAMinVehSpd_Kph_f32 = MIN



```
TS2.95k_LKAMinVehSpd_Kph_f32= MAX
 TS2.96k_LKAMinVehSpd_Kph_f32 = POS
TS2.97k_LKAMaxVehSpd_Kph_f32 = MIN
 TS2.98k_LKAMaxVehSpd_Kph_f32= MAX
 TS2.99k_LKAMaxVehSpd_Kph_f32 = POS
TS2.100k_HapticAmplitude_MtrNm_f32 = MIN
TS2.101k_HapticAmplitude_MtrNm_f32= MAX
 TS2.102k_HapticAmplitude_MtrNm_f32 = POS
TS2.103k_APAIncludeHaptic_Cnt_lgc = MIN
TS2.104k_APAIncludeHaptic_Cnt_lgc= MAX
TS2.105k_HapticFreq_Hz_f32=MIN
TS2.106k_HapticFreq_Hz_f32=MAX
TS2.107k_HapticFreq_Hz_f32=POS
TS2.108MaxSecureVehicleSpeed_Kph_f32=Min
TS2.109MaxSecureVehicleSpeed_Kph_f32=Max
TS2.110MaxSecureVehicleSpeed_Kph_f32=Pos
 TS2.111MinSecureVehicleSpeed_Kph_f32=Min
TS2.112MinSecureVehicleSpeed_Kph_f32=Max TS2.113MinSecureVehicleSpeed_Kph_f32=Pos
 TS2.114ESCRequest_Cnt_lgc=FALSE
IS2.114ESCRequest_Cnt_lgc=FALSE
TS2.115ESCRequest_Cnt_lgc=TRUE
TS2.116ESCFault_Cnt_lgc=FALSE
TS2.117ESCFault_Cnt_lgc=TRUE
TS2.118ESCISLimited_Cnt_lgc=FALSE
TS2.119ESCISLimited_Cnt_lgc=FRUE
TS2.120CWFound_Cnt_lgc=FALSE
TS2.121CWFound_Cnt_lgc=TRUE
TS2.123CCWFound_Cnt_lgc=FALSE
TS2.123CCWFound_Cnt_lgc=TRUE
TS2.124CPS_Trailbu_Cnt_lgc=TRUE
IS2.123CCWFound_Cnt_Igc=1RUE
TS2.124PoSTrajHwAngle_HwDeg_f32=Min
TS2.125PoSTrajHwAngle_HwDeg_f32=Max
TS2.125PoSTrajHwAngle_HwDeg_f32=Pos
TS2.127SWARTrgtAngRequest_HwDeg_f32=Min
TS2.128SWARTrgtAngRequest_HwDeg_f32=Max
TS2.129SWARTrgtAngRequest_HwDeg_f32=Pos
TS2.130SWARTrgtAngRequest_HwDeg_f32=Pos
 TS2.131SWARTrgtAngRequest_HwDeg_f32=Neg
TS2.132PosTrajEnable_Cnt_lgc=FALSE
TS2.133PosTrajEnable_Cnt_lgc=TRUE
 TS2.134CWPosition_HwDeg_f32=Min
TS2.135CWPosition_HwDeg_f32=Max
TS2.136CWPosition_HwDeg_f32=Pos
 TS2.137CCWPosition_HwDeg_f32=Min
TS2.138CCWPosition_HwDeg_f32=Max
TS2.139CCWPosition_HwDeg_f32=Neg
IS2.139CCWPosition_Hwbeg_f32=Neg
TS2.140GMOSH_APAMfgEnable_Cnt_lgc=FALSE
TS2.141GMOSH_APAMfgEnable_Cnt_lgc=TRUE
TS2.142GMOSH_LKAMfgEnable_Cnt_lgc=FALSE
TS2.143GMOSH_LKAMfgEnable_Cnt_lgc=FALSE
TS2.144GMOSH_ESCMfgEnable_Cnt_lgc=TRUE
TS2.145GMOSH_ESCMfgEnable_Cnt_lgc=TRUE
TS2.145TrqOvlSta_ESCState_State_M_enum=ESC_Inactive
 TS2.147TrqOvlSta_ESCState_State_M_enum=ESC_Active
TS2.148TrqOvlSta_ESCState_State_M_enum=ESC_TemporarilyLimited
TS2.149TrqOvlSta_ESCState_State_M_enum=ESC_PermanentlyFailed
 TS2.150TrqOvlSta_ESCState_State_M_enum=ESC_ManufacturingDisable TS2.151k_ESCMaxVehSpd_Kph_f32=Min TS2.152k_ESCMaxVehSpd_Kph_f32=Max
 TS2.153k_ESCMaxVehSpd_Kph_f32=Pos
TS2.154k_SWARLimiter_HwDeg_f32=Min
TS2.155k_SWARLimiter_HwDeg_f32=Max
 TS2.156k_SWARLimiter_HwDeg_f32=Pos
 TS2.157TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32=Min
TS2.158TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32=Max
TS2.159TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32=Pos
 TS2.160AII MIN
TS2.161ALL MAX
```

Test Step 2.1 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.850000024
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	15
TrqOvlSta_Haptictime_Sec_M_f32	75.6800003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.85999966
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00200000009
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.109999999

2015-02-04, 14:05:40+0530





Apption Appt		
i. APABANCHYSIQUE SIGN. 19. (2000000000000000000000000000000000000		·
	k_APAMaxHwTrq_HwNm_f32	0.100000001
Ligar-Continue Description Ligar-Continue Description Description Ligar-Continue Description Ligar-Continue Description Ligar-Continue Description Ligar-Continue Description Description Ligar-Continue Description Ligar-Continue Description Ligar-Continue Description Ligar-Continue Desc	k_APAMaxVehSpd_Kph_f32	0.200000003
Application No. 2012	k_ESCMaxVehSpd_Kph_f32	10.1000004
Lipspace Lips	k_HapticAmplitude_MtrNm_f32	0
Linguistic Plance 1.00000000000000000000000000000000000	k_HapticDuration_Sec_f32	0.50999999
L.Markinyonispot, Nr., 192 L.Markinyonispot, Str., 192 L.Markinyonispotes Time, Sec., 192 L.Markinyonispotes Time, Sec., 192 L.Shinkowith Time, Sec., 193 L.Shinkowith Time, S	k_HapticFreq_Hz_f32	3
M. Audarplicht-Park Sec. 12	k_HapticReacttime_Sec_f32	0.159999996
M. MANTENDER 128 20000005	k_LKAMaxVehSpd_Kph_f32	0
S.SWARDINER HORSE, 122	k_LKAMinVehSpd_Kph_f32	0.400000006
K.Simbell/Time-Sec, 12 K.Simbell/Time-Sec, 12 K.Simbell/Time-Sec, 12 K.Simbell/Time-Sec, 12 Larger, Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. APADor/intervention/Detected_Cot. Lipsurge_Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. CovPosind_Cot. Lipsurge_Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. CovPosind_Cot. Lipsurge_Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. CovPosind_Cot. Lipsurge_Rin, Inst. 4, p. Troc/sits. Troc/sits. Pert. ESCELL, Inst. 2, p. Troc/sits. Pert. APADor/sits. Pert. ESCELL, Inst. 2, p. Troc/sits.	k_MaxHapticReqTime_Sec_f32	0.0149999997
k. Sandardimen, Jedr. 122 langel, Rie, Jed. A. Trody-Sta. Frody-Sta. Perf. ARAborotex-vestionDefeded. Cnt. particular River River. A Trody-Sta. Trody-Sta. Perf. ARAborotex-vestionDefeded. Cnt. particular River. A Trody-Sta. Trody-Sta. Perf. ARAborotex-vestion Ratio. Cnt. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Cnt. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Cnt. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Pp. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Pp. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABorotex-vestion Ratio. Pp. pp. langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. SCORDA Long. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. SCORDA Long. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. SCORDA Long. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. SCORDA Long. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Scorda Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Scorda Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Scorda Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Scorda Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Scorda Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Scorda Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Scorda Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. ARABOROS. Scorda Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. LANGer-July. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. LANGer-July. Langer. Riv. July. A Trody-Sta. Trody-Sta. Perf. LANGer-July. Langer. Riv. July. A Trody-Sta. Perf. LANGer-July. Langer. Riv. July. A Trody-Sta. Perf. Langer. Langer. Langer. Langer. Riv. July. A Trody-Sta. Perf.	k_SWARLimiter_HwDeg_f32	2.20000005
k Satestatime, Sec. 12 larget Rin Int. An Trochista Trochista Pert APADrottenevertionDebedded Cot. 1 to tracet Rin Int. An Trochista Trochista Pert APADrottenevertionDebedded Cot. 1 to tracet Rin Int. An Trochista Trochista Pert APADrottenevertionDebedded Cot. 1 to tracet Rin Int. An Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1 Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1 Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1 Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1 Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1 Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1 Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1 Trochista Trochista Pert APADrottenevertionScales Sci. 1 to tracet. 1 Trochista Trochista Pert Communication Sci. 2 to tracet. 1 Trochista Trochista Pert Communication Sci. 2 to tracet. 1 Trochista Trochista Pert Communication Sci. 2 to tracet. 1 Trochista Trochista Pert ESCELIAmical Cot. 1 to tracet. 1 Trochista Trochista Pert ESCELIAMICA Cot. 1 to tracet. 1 Trochista Trochista Pert ESCELIAMICA Cot. 1 to tracet. 1 Trochista Trochista Pert ESCELIAMICA Cot. 1 to tracet. 1 Trochista Trochista Pert ESCELIAMICA Cot. 1 to tracet. 1 Trochista Trochista Pert ESCELIAMICA Cot. 1 to tracet. 1 Trochista Trochista Pert ESCELIAMICA Cot. 1 to tracet. 1 Trochista Trochista Pert ESCELIAMICA Cot. 1 to tracet. 1 Trochista Trochista Pert ESCELIAMICA Cot. 1 to tracet. 1 Trochista Trochista Pert Esceliamica Cot. 1 to tracet. 1 Trochista Trochista Trochista Pert Esceliamica Cot. 1 to tracet. 1 Trochista Trochista Pert Esceliamica Cot. 1 to tracet. 1 Trochista Trochista Pert Esceliamica Cot. 1 to tracet. 1 Trochista Trochista Pert Escelamica Cot. 1 to tracet. 1 Trochista Trochista Pert Escelamica Cot. 1 to tracet. 1 Trochista Trochista Pert Escelamica Cot. 1 to tracet. 1 Trochista Trochista Pert Escelamica Cot. 1 to tracet. 1 Trochista Trochista Pert Escelamica Cot. 1 to t	k_ShiftLevRevTime_Sec_f32	0.00100000005
Larget Rise Inst AD TROVISIA TOROVISE Port I APADOMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APADOMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APADOMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APAROMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APAROMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APAROMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I APAROMINION CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE Port I CONTROL 1500 Larget Rise Inst AD TROVISIA TOROVISE PORT LARGET COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT TOROVISE COLLIGE Larget Rise Inst AD TROVISIA TOROVISE PORT TOR	k_StandstillThresh_Kph_f32	0
tissed Rin Ind AD TROVISTA TORONS Pert J APANORecoversitération Cit ly target, Trojotists, Trovista Trovista pert J APARocoversitération Cit ly target, Trojotists (Trovista Trovista Pert J APARocoversitération) arranget, Trojotists (Trovista Trovista Trovista Pert J APARocoversitération) arranget, Trojotists (Trovista Trovista Pert J APARocoversitération) arranget, Trojotists (Trovista Trovista Trovista Pert J APARocoversitération) arrangement (Trovista Trovista Trovista Trovista Pert J APARocoversitération) arrangement (Trovista Trovista Trovista Pert J APARocoversitération) arrangement (Trovista Trovista Trovista Trovista Pert J APARocoversitération) arrangement (Trovista Trovista Trovist	k_StandstillTime_Sec_f32	0.00100000005
integer. Ref., Junel. Ap., Trock/Sta. Trock/Sta. Perl. J. PARRecoverable Faults, Cnt. Igo Integer. Ref., Junel. Ap. (Trock/Sta. Trock/Sta. Perl. Ap. Parkette, Cnt. Igo Integer. Ref., Junel. Ap. (Trock/Sta. Trock/Sta. Perl. Ap. Parkette, Cnt. Igo Integer. Ref., Junel. Ap. (Trock/Sta. Trock/Sta. Perl. Ap. Parkette, Cnt. Igo Integer. Ref., Junel. Ap. (Trock/Sta. Trock/Sta. Perl. Ap. (Trock/Sta. Trock/Sta. P	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventInterven$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
tinger, Rei, Inst. Ap. TrighVisia TrighVisia Port I APARequiser, Crit. Ign tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASess is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES is size unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Port I APASES unim tinger, Rein Inst. Ap. TrighVisia TrighVisia Por	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target. Rev. Bat. Ap. Track/State Tracky. Perf. COVIGNO Crit 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. COVIGNO Crit 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. COVIGNO Crit 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. COVIGNO Crit 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. COVIGNO Crit 19 target. Rev. Bat. Ap. Tracky. State Perf. COVIGNO Crit 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Tracky. State Perf. 19 target. Rev. Bat. Ap. Tracky. State Perf	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
terget Re. land. An Track/Sta Track/Sta Pert C.CVWFound Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert C.CVWFound Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert C.WWFound Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert C.WWFound Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.WWFound Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.WWFound Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.WWFound Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCP State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCP State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCP State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCP State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State State enum terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Rel. land. An Track/Sta Track/Sta Pert L.SCR State Cnt. jos terget Track/Sta Pert L.SCR S	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
langet Rie, Inst. Ap. Track/sista provisis pert. CWWood Cnt. Igo larget, Rie, Inst. Ap. Track/sista provisis pert. CWWood Cnt. Igo larget, Rie, Inst. Ap. Track/sista provisis pert. CWWood Cnt. Igo larget, Rie, Inst. Ap. Track/sista provisis pert. CWWood Cnt. Igo larget, Rie, Inst. Ap. Track/sista provisis pert. CWWood Cnt. Igo larget, Rie, Inst. Ap. Track/sista provisis pert. CWWood Cnt. Igo larget, Rie, Inst. Ap. Track/sista provisis pert. Editional Cnt. Igo larget, Rie, Inst. Ap. Track/sista provisis pert. Editional Cnt. Igo larget, Rie, Inst. Ap. Track/sista provisis pert. Editional State, enum larget, Rie, Inst. Ap. Track/sista Pert. GMOSH Collaboration State, enum larget, Rie, Inst. Ap. Track/sista pert. BESCHaller, Best Edition Collaboration State, Inst. Ap. Track/sista provisis pert. Editional State, enum larget, Rie, Inst. Ap. Track/sista Track/sista, Pert. BESCHaller, Bert. BESCHaller, Bert. Inst. Rie, Inst. Ap. Track/sista Track/sista, Pert. BESCHaller, Bert. Inst. Rie, Inst. Ap. Track/sista Track/sista, Pert. BESCHaller, Bert. Inst. Rie, Inst. Ap. Track/sista Track/sista, Pert. BESCHaller, Bert. Inst. Rie, Inst. Ap. Track/sista Track/sista, Pert. BESCHaller, Bert. Inst. Rie, Inst. Ap. Track/sista Track/sista, Pert. Inst. Ap. Track/si	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target Rein Back AD Track/Sta Track/Sta Pert CVPFound Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert SECFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECRepent Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECRepent Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECRepent Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECRepent Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo targ	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
target Rein Back AD Track/Sta Track/Sta Pert CVPFound Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert SECFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert BESCFault Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECRepent Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECRepent Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECRepent Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECRepent Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I SECREPENT Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo target Rein Back AD Track/Sta Track/Sta Pert I Secrept Cnt Igo targ		
large, Rei, Ind.A.p. Tro,Orista, Tro/Orista, Pert J. CWPostlann Hubber, B.2 Larger, Rei, Ind.A.p. Tro,Orista Tro,Orista, Pert J. ESCISLIMINED, CRI. (pc larger, Rei, Ind.A.p. Tro,Orista Tro,Orista, Pert J. ESCISLIMINED, CRI. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. ESCISLIMINED, CRI. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. SCISCHARD, CRI. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. CMOSH-Oscillar/State, enum larger, Rei, Ind.A.p. Tro,Orista, Pert J. CMOSH-Oscillar/State, enum larger, Rei, Ind.A.p. Tro,Orista, Pert J. CMOSH-Descillar/State, enum larger, Rei, Ind.A.p. Tro,Orista, Pert J. CMOSH-DESCIMPATION, pert J. CMOSH-Oscillar/State, State, enum larger, Rei, Ind.A.p. Tro,Orista, Pert J. CMOSH-DESCIMPATION, pert J. Aphrophysis, Pert J. GMOSH-Oscillar/State, State, enum larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Haptic-Request, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Pert J. Posits, Pert J. Posits, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Pert J. Posits, Pert J. Posits, Pert J. Marker, Cri. (pc larger, Rei, Ind.A.p. Tro,Orista, Pert J. Marker, Pert J. Posits, Pert J. Marker, Pert J. Po		
targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SSCFaut. Oct. pc targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SSCFaut. Oct. pc targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SSCState. State. exum targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SSCState. State. exum targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SSCState. State. exum targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SSCState. State. exum targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SSCState. State. exum targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SSCS. Port J SMOSH J APAMIginable. Cnt. Ugo targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J SMOSH J APAMIginable. Cnt. Ugo targer, Rize, Ind. Ap. TropOsita. TropOsita. Port J Notice, Pert J SSCMS Port J SSCMS		
langer, Rein, Ind.A.D., TrigOVSta, Port J. SSCIal, mined, Ont. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. SSCIal, mined, Ont. (pot targer, Rein, Ind.A.D., TrigOVSta, TrigOVSta, Port J. SSCIAL, State, enum targer, Rein, Ind.A.D., TrigOVSta, TrigOVSta, Port J. SSCIAL, State, enum targer, Rein, Ind.A.D., TrigOVSta, TrigOVSta, Port J. GMOSH APAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. GMOSH APAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. GMOSH APAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. GMOSH APAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. MANDE, Port J. GMOSH APAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgEmble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgemble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgemble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. KAPAMIgemble, Crit. (pot targer, Rein, Ind.A.D., TrigOVSta, Port J. Pot Sing-Invalved, Ind.A.D., TrigOVSta, Port J. Pot Sing-Invalved, Inv.D., 2 target, TrigOVSta, Port J. MOSH, 2 target, TrigOVSta, Port J. TrigOVSta, Port J. Pot Sing-Invalved, Inv.D., 2 target, TrigOVSta, Port J. Tr		
torget, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J SSCRequest, Onl. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J SSCRequest, State, enum target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J GMOSH APAMIginable, Crit. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J GMOSH APAMIginable, Crit. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J GMOSH APAMIginable, Crit. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J GMOSH APAMIginable, Crit. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J GMOSH APAMIginable, Crit. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J SAGNOSH APAMIginable, Crit. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J SAGNOSH APAMIginable, Crit. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J SAGNOSH PORT J SAGNOSH APAMIginable, Crit. go target, Rei, Inst.Ap. Tro/OSIs Tro/OSIs Port J KARDOSH PORT J SAGNOSH PORT J SA		
largeR, Pte, Inst.Ap, TropOviSta, TropOviSta, Perf. ESCState, State, enum targeR, Pte, Inst.Ap, TropOviSta, Perf. GMOSH-APAM(gEnable, Cnt.l.go targer, Pte, Inst.Ap, TropOviSta, TropOviSta, Perf. GMOSH, APAM(gEnable, Cnt.l.go targer, Pte, Inst.Ap, TropOviSta, TropOviSta, Perf. GMOSH, APAM(gEnable, Cnt.l.go targer, Pte, Inst.Ap, TropOviSta, TropOviSta, Perf. GMOSH, APAM(gEnable, Cnt.l.go targer, Pte, Inst.Ap, TropOviSta, TropOviSta, Perf. JANGOVISE, P		
larget, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert. (MOSH-APAM(pEnable, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. (MOSH-APAM(pEnable, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. (MOSH-ESCM(pEnable, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. (MOSH-LKAM(pEnable, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. (MOSH-LKAM(pEnable, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. (MOSH-LKAM(pEnable, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. LKARIN, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. LKARIN, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. LKARIN, Cnt.) go target, Ret. Inst., Ap. TrqOvista, TrqOvista, Pert. LKARIN, Cnt.) go target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert. LKARIN, Cnt.) go target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert. LKARistae, State, enum target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert. JKARistae, State, enum target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert. JKARistae, State, enum target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., MaxSecure/velnic/Speed, fyth, 132 target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., PostRor-Enable, Cnt.) go target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., PostRor-Enable, Cnt.) go target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., PostRor-Enable, Cnt.) go target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., PostRor-Enable, Cnt.) go target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., PostRor-Enable, Cnt.) go target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., PostRor-Enable, Cnt., Igo target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., PostRor-Enable, Cnt., Igo target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., TrqOvista, Pert., PostRor-Enable, Cnt., Igo target, Ret., Inst., Ap. TrqOvista, TrqOvista, Pert., TrqOvista, Pert., PostRor-Enable, Cnt., Igo value target, TrqOvista, Pert., Devicto, Pert., PostRor-Enable, Cnt., Igo value target, TrqOvista, Pert., Devicto, Pert., PostRor-Enable, Cnt., Igo		
larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. GMOSH. APAMigEnable. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. GMOSH. ESCMigEnable. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. GMOSH. ESCMigEnable. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. GMOSH. ESCMigEnable. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. Haptoficequest. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. Likaratt. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. Likaratt. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. Likaratt. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. Likaratt. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. Likaratt. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. Likaratt. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. I MansSecure VerhicleSpeed. Kpt. 132 larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. J. MansSecure VerhicleSpeed. Kpt. 132 larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. J. MansSecure VerhicleSpeed. Kpt. 132 larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. DissTore Instelled. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. DissTore Instelled. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. DissTore Instelled. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. DissTore Instelled. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. DissTore Instelled. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. DissTore Instelled. Cnt. Igo: larget. Rite. Inst. Ap. TrqOvista. TrqOvista. Pert. J. TrqOvist		
larget_Re_Inst_Ap_TrqOvSta_TrqOvSta_Pert_GMOSH_EXMEGRable_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_GMOSH_EXMEGRable_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_HaptoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_HaptoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_HaptoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_HaptoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_HAPtoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_LKAPtoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_LKAPtoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_LKAPtoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_LKAPtoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_LKAPtoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_LKAPtoRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_NavaRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_NavaRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_NavaRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_NavaRequest_Cnt_lgo larget_Re_Inst_Ap_TrqOvSta_Pert_NavaRequest_NavaReq_Int_Na		
larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HotpicRequest_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HotpicRequest_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HotpicRequest_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MasSecureVehicleSpeed_Kph_32 larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PSes*Centable_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PSes*Centable_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PSes*Centable_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PSes*Centable_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Ses*Centable_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PSes*TrqOviSta_Pert_PSes*Centable_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Ses*Centable_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Ses*Centable_Cnt_lgc larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_Minkm_32 larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_Minkm_32 larget_TrqOviSta_Pert_Ses*Centable_Cnt_lgc larget_TrqOviSta_Pert_TrqOscAmp_Minkm_32 larget_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOviSta_Pert_CoWpostInc_Ap_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOviSta_Pert_ApARequest_Cnt_lgc_value larget_TrqOv		
target, Rie, Inst. Ap. TrqOVSIs TrqOVSIs Pert HapticRequest, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert HavTorque HawNin. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAFault, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert LKAState, State, enum larget, Rie, Inst. Ap. TrqOVSIs Pert MasSecurve/brickSpeed, Kph. 32 larget, Rie, Inst. Ap. TrqOVSIs Pert Pessev/Enable, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert Pessev/Enable, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert Pessev/Enable, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert Pessev/Enable, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert Pessev/Enable, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert Pessev/Enable, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs TrqOVSIs Pert Pessev/Enable, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs Pert TrqOssEndeverse, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs TrqOVSIs Pert TrqOssEndeverse, Cnt. got larget, Rie, Inst. Ap. TrqOVSIs TrqOVSIs Pert TrqOssEndeverse, Cnt. got larget, Rie, Inst. Ap. TrqOvSIs TrqOvSis Pert TrqOssEndeverse, Cnt. got larget, TrqOVSIs Pert ApRARcoversible Faults, Cnt. got value got TrqOvSis Pert APRARcoversible Faults, Cnt. got value got TrqOvSis Pert APRARcoversible Faults, Cnt. got value got TrqOvSis Pert APRARcoversible Cnt. got value got TrqOvSis Pert APRARcoversible Cnt. got value got TrqOvSis Pert APRARcoversible Cnt. got value got		
larget_Rie_Inst_Ap_TrqOvista_TrqOvista_Pert_LKAFabult_Cnt_lgc		
target, Rie, Inst. Ap, TrqOVISta. TrqOVISta. Perl _LKAFault_Cnt. Igc target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _LKARequest_Cnt. Igc target_Tite_Inst. Ap_TrqOVISta. TrqOVISta. Perl _LKARequest_Cnt. Igc target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _LKARequest_Cnt. Igc target_TrqOVISta. Perl _LKARequest_Cnt. Igc target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _LKAState_State_enum target_TrqOVISta. Perl _LKARequest_Cnt. Igc target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _NSSecruéPholiceSpeed_Kph_f32 target_TrqOVISta. Perl _NSSecruéPholiceSpeed_Kph_f32 target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _Postsorte/hable_Cnt. Igc target_TrqOVISta. Perl _Postsorte/hable_Cnt. Igc target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _Postraje/hable_Cnt. Igc target_TrqOVISta. Perl _Postraje/hable_Cnt. Igc target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _Postraje/hable_Cnt. Igc target_TrqOVISta. Perl _Postraje/hable_Cnt. Igc target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _SWARTrgAngkequest_HwDe_g. f32 target_TrqOVISta. Perl _Postraje/hable_Cnt. Igc target_Rie_Inst. Ap_TrqOVISta. TrqOVISta. Perl _TrqOScEnbe_Cnt. Igc target_TrqOVISta. Perl _Postraje/hable_Cnt. Igc target_TrqOVISta. Perl _APARecoverableFaults_Cnt. Igc.value 0 target_TrqOVISta. Perl _CVVPosition. HwDeg_f32 value 0 target_TrqOVISta. Perl _ESCRequest_Cnt. Igc.value 0 target_TrqOVISta. Perl _ESCRequest_Cn		
larget, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_LKAIAIII. Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_LKAIAIII. Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_LKAISIate, enum target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_LKAISIate, enum target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_LKAISIate, enum target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_MinSecureVerhicleSpeed, Kpf., 132 target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_MinSecureVerhicleSpeed, Kpf., 132 target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_PosSrevEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_PosSrevEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_PosSrevEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_PosTrajeInable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_PosTrajeInable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_PosTrajeInable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_PosTrajeInable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_TopSoEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_TopSoEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_TrqOScEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_TrqOScEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_TrqOScEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Pert_TrqOScEnable, Cnt. I.go target, TrqOVISIa. Pert_CRQOVISIa. TrqOVISIa. Pert_TrqOScEnable, Cnt. I.go target, TrqOVISIa. Pert_CRQOVISIa. Pert_CRQUE target, TrqOVISIa. Pert_CRQOVISIa. Pert_CRQUE target, TrqOVISIa. Pert_CRQOVISIa. Pert_CRQUE target, TrqOVISIa. Pert_CR		· - ·
target, Rie_Inst, Ap, TrqOviSla, TrqOviSla, Pert_LKARequest, Cnt_lgo target, Rie_Inst, Ap, TrqOviSla, TrqOviSla, Pert_LKAState_State_enum target, Rie_Inst, Ap, TrqOviSla, TrqOviSla, Pert_MaxSecureVehicleSpeed_Kph, 132 target, Rie_Inst, Ap, TrqOviSla, TrqOviSla, Pert_MaxSecureVehicleSpeed_Kph, 132 target, Rie_Inst, Ap, TrqOviSla, TrqOviSla, Pert_PosSrvohwAngle_HwDeg, 132 target, Rie_Inst, Ap, TrqOviSla, TrqOviSla, Pert_StrigHvAngle+twDeg, 132 target, Rie_Inst, Ap, TrqOviSla, TrqOviSla, Pert_StrighteverlsinReverse, Cnt_lgo target, Rie_Inst, Ap, TrqOv		
target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_PosSrvorHwAngle_HWDeg_f32 target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_SoviAttrqOviSta_Per1_SoviAttrqOviSta_Per1_PosTrajEnable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_ige target, Rie_Inst.Ap_TrqOviSta_Per1_APARenuest_Cnt_ige target_TrqOviSta_Per1_APARenuest_Cnt_ige.value target_TrqOviSta_Per1_APARenuest_Cnt_ige.value target_TrqOviSta_Per1_CWFound_Cnt_ige.value target_TrqOviSta_Per1_CWFound_Cnt_ige.value target_TrqOviSta_Per1_CWFound_Cnt_ige.value target_TrqOviSta_Per1_GMOSH_ESCMIgenable_Cnt_ige.value target_TrqOviSta_Per1_GMOSH_APAMigenable_Cnt_ige.value target_TrqOviSta_Per1_GMOSH_APAMigenable_Cnt_ige.value target_TrqOviSta_Per1_GMOSH_APAMigenable_Cnt_ige.value target_TrqOviSta_Per1_LKARenuest_Cnt_ige.value target_TrqOviSta_Per1_LKARenuest_Cnt_ige.value target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOviSta_Per1_Max		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvchiwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvchiwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SviAPTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SviAPTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SviAPTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_TrqOscArm_HwThm_f32 target_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_TrqOviSta_Pert_Ap_Ap_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_NinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrovEnable_End_HvDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_End_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_End_Igo target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SWARTrgMnghequet_HvDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SWARTrgMnghequet_HvDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SWARTrgMnghequet_HvDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Pert_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Pert_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Pert_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCAmp_MtrNm_f32 target_TrqOvlSta_Pert_Ap_TrqOvlSta_Pert		
target_Rte_Inst_Ap_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_PosTrajhabg_Ent_Des_Trajhabg_E		
target_Rte_Inst_Ap_TrqOv/Sta_Pert_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_DosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Pert_SwARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOv/Sta_Pert_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_TrqOsCAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_TrqOsCAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_TrqOsCAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_TrqOsCAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_TrqOsCANp_MtrNm_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_Pert_TrqOsCFreq_Hz_f32 target_TrqOv/Sta_Pert_Ap_Argen_MtrNm_f32 target_TrqOv/Sta_		
target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajHwAngie_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajHwAngie_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrqAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrqAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrqAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOsCahnp_MtrNm_132 target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_132_value target_TrqOviSta_Per1_APANangrecoverableFaults_Cnt_Igc_value 0 target_TrqOviSta_Per1_Cnt_Inst_ApARequest_Cnt_Igc_value 10 target_TrqOviSta_Per1_Cnt_Inst_ApARequest_Cnt_Igc_value 11 target_TrqOviSta_Per1_CwtPosition_HwDeg_132_value 12 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value 13 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value 14 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value 15 target_TrqOviSta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc_value 16 target_TrqOviSta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc_value 17 target_TrqOviSta_Per1_Inst_ApARequest_Cnt_Igc_value 18 target_TrqOviSta_Per1_Inst_Apart_Cnt_Igc_value 19 target_TrqOviSta_Per1_Inst_Apart_Cnt_Igc_value 10 target_TrqOviSta_Per1_Inst_Apart_Cnt_Igc_value 10 target_TrqOviSta_Per1_Inst_Apart_Cnt_Igc_value 11 target_TrqOviSta_Per1_Inst_Apart_Cnt_Igc_value 12 target_TrqOviSta_Per1_Inst_Apart_Request_Cnt_Igc_value 13 target_TrqOviSta_Per1_Inst_Apart_Request_Cnt_Igc_value 14 target_TrqOviSta_Per1_Inst_Apart_Request_Cnt_Igc_value 15 target_TrqOviSta_Per1_Inst_Apart_Request_Cnt_Igc_value 16 target_TrqOviSta_Per1_Inst_Apart_R		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_posTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_sWRATrgiAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SwiRteverlsnReverse_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwiRteverlsnReverse_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta_Per1_SwiRteverse_Cnt_igc target_TrqOviSta_Per1_AP_AP_NonRecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_AP_AP_NonRecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_AP_AP_NonRecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_AP_AP_NonRecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_CwPound_Cnt_igc.value target_TrqOviSta_Per1_CwPound_Cnt_igc.value target_TrqOviSta_Per1_CwPound_Cnt_igc.value target_TrqOviSta_Per1_CwPound_Cnt_igc.value target_TrqOviSta_Per1_CwPound_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_APAMfignable_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_APAMfignable_Cnt_igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value target_TrqOviSta_Per1_LKARailt_Cnt_igc.value target_TrqOviSta_Per1_LKARailt_Cnt_igc.value target_TrqOviSta_Per1_LKARailt_Cnt_igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value target_TrqOviSta_Per1_LKARailt_Cnt_igc.value target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value		
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_SWARTrgtAngRequest_HwDeg_i31 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_strintLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscAmp_MtrNm_i32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscAmp_MtrNm_i32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscAmp_MtrNm_i32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscFreq_Hz_i32 target_TrqOvIsta_Per1_APANoRecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverableFaults_Cnt_lgc.value target_TrqOvIsta_Per1_CWARecoverable_Cnt_lgc.value target_TrqOvIsta_Per1_ESCFault_Cnt_lgc.value target_TrqOvIsta_Per1_ESCFault_Cnt_lgc.value target_TrqOvIsta_Per1_ESCFault_Cnt_lgc.value target_TrqOvIsta_Per1_ESCFault_Cnt_lgc.value target_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvIsta_Per1_GMOSH_EXCMfgEnable_Cnt_lgc.value target_TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value target_TrqOvIsta_Per1_Mars&curveYehicleSpeed_Kph_f32.value target_TrqOvIsta_Per1_Mins&curveYehicleSpeed_Kph_f32.value target_TrqOvIsta_Per1_PostrajEnable_Cnt_lgc.value target_TrqOvIsta_Per1_Mars&curveYehicleSpeed_Kph_f32.value target_TrqOvIsta_Per1_PostrajEnable_Cnt_lgc.value target_TrqOvIsta_Per1_PostrajEnable_Cnt_lgc.value target_TrqOvIsta_Per1_PostrajEnable_Cnt_lgc.val		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_CvWFound_Cnt_Igc.value 0 target_TrqOviSta_Per1_CvWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_CvWFound_Cnt_Igc.value 0 target_TrqOviSta_Per1_CvWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_ExCEPault_Cnt_Igc.value 0 target_TrqOviSta_Per1_ExCEPault_Cnt_Igc.value 0 target_TrqOviSta_Per1_ExCEPault_Cnt_Igc.value 0 target_TrqOviSta_Per1_ExCEPault_Cnt_Igc.value 0 target_TrqOviSta_Per1_ExCRequest_Cnt_Igc.value 0 target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOviSta_Per1_GMOSH_ExCMfgEnable_Cnt_Igc.value 1 target_TrqOviSta_Per1_GMOSH_ExCMfgEnable_Cnt_Igc.value 1 target_TrqOviSta_Per1_HwTorque_HwNm_f32.value 1 target_TrqOviSta_Per1_HwTorque_HwNm_f32.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOviSta_Per1_MARequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_MARequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_MARequest_Cnt_Igc.value 2 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 3 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 4 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 4 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 5 target_TrqOviSta_Per1_Marequest_Cnt_Igc.value 4 target_TrqOviSta_Per1_Marequest_		
target_Rte_Inst_Ap_TrqOvISta_PrqOvISta_Prq1_TrqOscAmp_MtrNm_i32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Prq1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Prq1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Prq1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Prq1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Prq1_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Prq1_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Prq1_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Prq1_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Prq1_CCWFound_Cnt_Igc.value target_TrqOvISta_Prq1_CCWFound_Cnt_Igc.value target_TrqOvISta_Prq1_CCWFound_Cnt_Igc.value target_TrqOvISta_Prq1_CCWFound_Cnt_Igc.value target_TrqOvISta_Prq1_CCWFound_Cnt_Igc.value target_TrqOvISta_Prq1_ESCFault_Cnt_Igc.value target_TrqOvISta_Prq1_ESCFault_Cnt_Igc.value target_TrqOvISta_Prq1_ESCFault_Cnt_Igc.value target_TrqOvISta_Prq1_ESCFault_Cnt_Igc.value target_TrqOvISta_Prq1_ESCFault_Cnt_Igc.value target_TrqOvISta_Prq1_ESCFault_Cnt_Igc.value target_TrqOvISta_Prq1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Prq1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Prq1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Prq1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Prq1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Prq1_HavIorque_HwNm_f32_value target_TrqOvISta_Prq1_HavIorque_HwNm_f32_value target_TrqOvISta_Prq1_LKARequest_Cnt_Igc.value target_TrqOvISta_Prq1_LKARequest_Cnt_Igc.value target_TrqOvISta_Prq1_LKARequest_Cnt_Igc.value target_TrqOvISta_Prq1_LKARequest_Cnt_Igc.value target_TrqOvISta_Prq1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Prq1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Prq1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Prq1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Prq1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Prq1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Prq1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Prq1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Prq1_PosTrajEnable_Cnt_Igc.value		
target_Rte_Inst_Ap_TrqOvlSta_Prq_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_VrlSta_PrqT_TrqOscFreq_Hz_f32 target_TrqOvlSta_Prq_APARcoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HubricRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HubricRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HubricRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HubricRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HxAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MinsecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinsecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 0.100000001		
target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APARoncecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APAReccoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APAReccoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARequest_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_ManSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_ManSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_ManSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1140.10999 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 360 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_APAMIgEnable_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_ESCMigEnable_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_LKAMIgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_Hyriorque_HwNm_f32.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 25.299992 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.299992 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.299992 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 26.29999985 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 27.2999992 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 27.29999985		
target_TrqOvISta_Pert_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Pert_APARequest_Cnt_lgc.value 0 target_TrqOvISta_Pert_CCWFound_Cnt_lgc.value 0 target_TrqOvISta_Pert_CCWFound_Cnt_lgc.value -1140.10999 target_TrqOvISta_Pert_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Pert_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Pert_CWFound_Cnt_lgc.value 360 target_TrqOvISta_Pert_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Pert_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Pert_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Pert_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Pert_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Pert_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Pert_LKAFault_Cnt_lgc.value 0 target_TrqOvISta_Pert_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Pert_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Pert_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Pert_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Pert_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Pert_MancecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvISta_Pert_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvISta_Pert_PosTrajfnwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 0 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value -1140.10999 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 0 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 360 target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value 0 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 0 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 0 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 0 target_TrqOvISta_Per1_GMOSH_KAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 0.20999993 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 25.299992 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.299992 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 0 target_TrqOvISta_Per1_DosTrajEnable_Cnt_Igc.value 0 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 0 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 0		
target_TrqOvISta_Pert_CCWFound_Cnt_lgc.value	0 = 1 = = = = = = = = = = = = = = = = =	
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 360 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.20999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARquest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0 0.100000001		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.29999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.2999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.2999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 0.209999993 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 25.2999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.099985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.099985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 25.2999992 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 35.0999985 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
	· · · · · · · · · · · · · · · · · · ·	
target_rrqUviSta_Per1_SWAR1rgtAngRequest_HwDeg_t32.value -32.5		
target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_Igc.value	target_rrqUviSta_Peri_SnittLeverIsinReverse_Cnt_lgc.value	<u>I</u>

© Report created by TESSY V3.1.9, report template V2.1

13

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
TrgOvISta APAState State M enum	4	4	✓
TrgOvlSta ESCState State M enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.460000008	0.460000008 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45	45 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	15	15 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	75.6800003	75.6800003 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.83985996	-9.83985996 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.20000005	1.20000005 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1.20000005	1.20000005 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrqOvlSta
TrgOvISta APAState State M enum	2
TrgOvISta ESCState State M enum	1
FrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	7.11999989
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	132.839996
FrgOvlSta ElapsedStandstillTime Sec M f32	8
FrgOvlSta HapticState State M enum	2
FrgOvISta Hapticdur Cnt M u16	240
TrgOvISta Haptictime Sec M f32	150
TrgOvISta HwTorqueSV HwNm M Str.SV UIs f32	-9.25
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.00419999985
TrgOvISta LKAFault Cnt M Igc	0
TrgOvISta LKAPermFault Cnt M Igc	1
TrgOvISta LKAPermFault Cnt M u08	2
TrgOvISta LKAState State M enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	2.29999995
TrgOvlSta ReadytoPulse Cnt M lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k APAMaxHwTrqTime Sec f32	0.119999997
APAMaxHwTrg HwNm f32	0.0799999982
<pre>c_APAMaxVehSpd_Kph_f32</pre>	1.79999995
k ESCMaxVehSpd Kph f32	12.5
C HapticAmplitude MtrNm f32	0.00600000005
HapticDuration Sec f32	0.0149999997
k_HapticFreq_Hz_f32	3.53999996
K_HapticReacttime_Sec_f32	0.0160000008
<pre>c_LKAMaxVehSpd_Kph_f32</pre>	1.89999998
x_LKAMinVehSpd_Kph_f32	1.75
k_MaxHapticReqTime_Sec_f32	0.029999993
c_SWARLimiter_HwDeg_f32	3.5999999
k_ShiftLevRevTime_Sec_f32	0.00499999989
StandstillThresh_Kph_f32	0.039999991
StandstillTime_Sec_f32	0.00499999989
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc

2015-02-04, 14:05:40+0530





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc$	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>_</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlOta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH LKAMfgEn		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HwTorque HwNm f32	target TrqOvISta Per1 HwTorque HwNm f		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1130.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	364.940002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.560000002		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.2000008		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004 0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value		Function Value	Descrit
Name Tracoulste ADAState State M. cours	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime Sec_M_f32 TrqOvlSta ElapsedShiftLevRevTime Sec_M f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_ElapsedStatidstill*Iffle_Sec_ivi_132 TrqOvISta_HapticState_State_M_enum	2	2	_
TrqOvISta Hapticdur Cnt M u16	240	240 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrgOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-9.25	-9.25 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TraCulSto BoodytoBulgo Cot M Igo	1	1 1 1	

0

3

target_TrqOvlSta_Per1_APAState_State_enum.value

target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value

TrqOvlSta_ReadytoPulse_Cnt_M_lgc

1 ± 1

0 ± 1

3

3

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target TrgOvlSta Per1 GMOSHOscillateState State enum.value	2	2	- 100 all
target TrqOvlSta Per1 LKAState State enum.value	3	3	•
target TrgOvlSta Per1 PosServEnable Cnt lgc.value	1	1±1	
target TrgOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.109999999	0.109999999 ± 0.0009765625	~
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.00600000005	0.00600000005 ± 0.0009765625	~
target TrqOvlSta Per1 TrqOscEnable Cnt Igc.value	0	0 ± 1	•
target TrgOvlSta Per1 TrgOscFreg Hz f32.value	3.53999996	3.53999996 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvISta_APAState_State_M_enum	1
FrqOvISta_ESCState_State_M_enum	2
FrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	85
FrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	220.679993
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	68.3000031
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	530
rqOvlSta_Haptictime_Sec_M_f32	860.26001
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-8.64000034
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	1
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3.400001
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.129999995
_APAMaxHwTrq_HwNm_f32	0.159999996
APAMaxVehSpd Kph f32	3.5999999
_ESCMaxVehSpd_Kph_f32	14.8999996
HapticAmplitude_MtrNm_f32	0.0120000001
_HapticDuration_Sec_f32	0.029999993
	4
HapticReacttime_Sec_f32	0.0320000015
 _LKAMaxVehSpd_Kph_f32	3.79999995
LKAMinVehSpd Kph f32	3.5
	0.0450000018
SWARLimiter_HwDeg_f32	5
ShiftLevRevTime_Sec_f32	0.00899999961
StandstillThresh Kph f32	0.079999982
_StandstillTime_Sec_f32	0.00899999961
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOviSta_Per1_APARequest_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APAState State enum	target TrqOvISta Per1 APAState State enum
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOviSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc

2015-02-04, 14:05:40+0530





Name	Input Value		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKAState State enum	target TrqOvISta Per1 LKAState State en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlota_Fer1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvlSta Per1 PosTrajEnable Cnt		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajHwAngle HwDeg f32	target TrqOvlSta Per1 PosTrajHwAngle H		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3		·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1119.91003		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	369.880005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.910000026		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	35.0999985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	44.9000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	530	530 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	860.26001	860.26001 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.64000034	-8.64000034 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	V
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvISta_LKAState_State_M_enum	4	4	*
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3.4000001	3.4000001 ± 0.0009765625	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1±1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	0 4	0 ± 1 4	
target_TrqOvISta_Per1_ESCState_State_enum.value	4	4	-
target_TrqOvISta_Per1_ESCState_State_enum.value	1	1	
target_TrqOvISta_Per1_LKAState_State_enum.value	4	4	-
target TrqOvISta Per1 PosServEnable Cnt Igc.value	0	0 ± 1	
target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3.4000001	3.4000001 ± 0.0009765625	-
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0120000001	0.0120000001 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4	4 ± 0.0009765625	-
	7	T = 0.00001 000EJ	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓





Sept March Sept	Test Step 2.4 (Repeat Count = 1)	▼
Reg_Ind_Ind_PropNiss		Input Value
Toporation Section S	Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TigONSE_Depended Microskin (Time Sec. M. 192	TrqOvlSta_APAState_State_M_enum	1
Tophosis EpispedShift orNer Time Serg, M, 502 Tophosis EpispedShift orNer Time Serg, M, 502 Tophosis EpispedShift orNer Time Serg, M, 502 Tophosis EpispedShift Shift or M anim Tophosis EpispedShift Shift or M, 502 Tophosis EpispedShift Shift or M, 503 Tophosis EpispedShift Shift or M, 503 Tophosis EpispedShift Shift or M, 503 Tophosis EpispedShift Shift	TrqOvlSta_ESCState_State_M_enum	3
TopOrtion Deposition column 2 25 25 25 25 25 25 25	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007
TOCHOSE, Haptocho, CM, Julie 984 TOCHOSE, Haptocho, Ser, M, SSP 985 TOCHOSE, Haptocho, Ser, M, SSP 985 TOCHOSE, Landone, Table M, Ber 90 TOCHOSE, MASSINE, State M, BER 90 TOCHOSE, MASSIN	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989
TROMSE_Haptering Set_MED	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	225.639999
TROMOSIA Horizones M. Horizonia M. Ser K. Us. 152 0.0080000029	TrqOvlSta_HapticState_State_M_enum	
TROORISE_NATIONALY, NAME, MS 150, LIG. 122 0.000000000000000000000000000000000		
TopOrisis, JAPamira, CM, Mys 15 TopOrisis, JAPamira, CM, Mys 15 TopOrisis, JAPamira, CM, Mys 15 TopOrisis, JAPamira, CM, Mys 16 TopOrisis, JAPamira, CM, Mys 17 TopOrisis, JAPamira, CM, Mys 17 TopOrisis, JaPamira, CM, Mys 17 TopOrisis, JaPamira, CM, Mys 18 TopOrisis, JaPamira, CM, Mys 18 TopOrisis, JaPamira, CM, Mys 19 TopOrisis, JaPamira, CM, M		
TROMOSIS LAPAmental Cort M pigs		
TipO/SISE_LARAPemiFall_Cott_M_106		
TimpOrding LARAemeriant Lord Muses TimpOrding LARAemeriant Lord Muses TimpOrding Prosition Having to Muse 1		
TrigONSIs Passivalvange Habols, M. 92 1 TrigONSIs Passivalvange Link Mige 1 TrigONSIs Passivalvange Crit (ge 1 TrigONSIs Passivalvange) Crit (ge) 1 Tri		
Trigly/Stap Parts/brishe Crit Mg o	·	
TripOVSE_ReadyNorDisc_Crit De		
K. APAMAMA* WITE, UNION, 112 C. APAMAMAMA* WITE, UNION, 112 C. APAMAMA* WITE, UNION, 112 C. APAMAMAMA* WITE, UNION, 112 C. APAMAMAMAMA* WITE, UNION, 112 C. APAMAMAMAMAMAMAMAMAMAMAMAMAMAMAMAMAMAMA	, – , – , – , – , – , – , – , – , – , –	
k. APAMachir Torline, Sec. 92 k. APAMachir Torline, Sec. 92 k. APAMachir Mary, 1999. k. Apamachi		
k. APAMAWNISD, KOh. 12 k. ESCMAWNISD, KOh. 12 k. HopicArryllutor, Minn. 132 k. HopicArryllutor,		0.140000001
k APAMAYerSpd Koh 12 k Hapicharoli Koh 12 k Hapicha		
k. Hapichuration. Sec. 172		5.4000001
K. Hapide Feet, Ltt. 232 K. Shaft-Hapide Feet, Ltt. 232 Lt	k_ESCMaxVehSpd_Kph_f32	17.2999992
k HabipFreachter, Sec., 152 k, LKAMANVeNSpd, Kph, 152 k, LKAMANVeNSpd, Kph, 152 k, LKAMANVeNSpd, Kph, 152 k, LKAMANVeNSpd, Kph, 152 k, SAMAStlimber, HwDeg, 152 k, SAMAStlimber, HwDeg, 152 k, SAMAStlimber, HwDeg, 152 k, SAMASTLIMBer, HwDeg, 152 k, SAMASTLIMBER, Sec., 152 0.119999997 clarger, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, APADOVINIENVENIODEEccl., Chi, Igo targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, APARASIde, State, canus targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, APARASIde, State, canus targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, APARASIde, State, canus targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, APARASIde, State, canus targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, APARASIde, State, canus targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, APARASIde, State, canus targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, CWFound, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, CWFound, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIsa, Pert, APARASIde, State, canus targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIs, Pert, CWFound, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIs, Pert, ESCRequest, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIs, Pert, ESCRequest, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIs, Pert, ESCRequest, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIs, Pert, ISCREQUEST, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIsa, TrqOVSIs, Pert, ISCREQUEST, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIs, TrqOVSIs, Pert, ISCREQUEST, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIs, TrqOVSIs, Pert, ISCREQUEST, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIs, TrqOVSIs, Pert, ISCREQUEST, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIs, TrqOVSIs, Pert, ISCREQUEST, Cnt. Igo targer, Rei, Inst., Ap., TrqOVSIs, TrqOVSIs, Pert, ISCREQUEST, Cnt. Igo targ	k_HapticAmplitude_MtrNm_f32	0.0179999992
K_LKAMarvehsSpd_Kph_152 K_LKAMarvehsSpd_Kph_152 S. 589999918 K_MarhapitcReqTime_Sec_152 0.00599999987 K_MarhapitcReqTime_Sec_152 0.0130000001 K_ShindtaiTrevention_Sec_152 0.0130000001 K_ShindtaiTrevention_Sec_152 0.0130000003 K_ShindtaiTrevention_Sec_152 0.0130000003 M_ShindtaiTrevention_Sec_152 0.0130000003 M_ShindtaiTrevention_Sec_152 0.0130000003 M_ShindtaiTrevention_Sec_152 0.0130000003 M_ShindtaiTrevention_Sec_152 0.0130000003 M_ShindtaiTrevention_Sec_152 0.0130000003 M_ShindtaiTrevention_Sec_152 M_MShindtaiTrevention_Sec_152 M_MShindtaiTrevention_Sec_152 M_MShindtaiTrevention_Sec_152 M_MShindtaiTrevention_Sec_152 M_MShindtaiTrevention_Sec_152 M_MShindtaiTrevention_Sec_152 M_MShindt	k_HapticDuration_Sec_f32	
K, LKAMAVehSpd, Kph, 132 5.8 K, LKAMAVehSpd, Kph, 132 6.000001 K, SWARLHumber, HwDeg, 132 6.000001 K, SMARLHumber, HwDeg, 132 0.100000003 K, SMARLHUMBER, Kph, 132 0.119999997 K, SMARLHUMBER, Sec, 132 0.119999997 K, SMARLHUMBER, SEC, 132 0.119999997 K, SMARLHUMBER, SEC, 132 0.119999997 Larger, Re, Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, APARREcoverableFaults, Crt. Lyc larger, Re, Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, APARREcoverableFaults, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, APARREcoverableFaults, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, COWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, COWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, CWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, CWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, CWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, CWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, CWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, CWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, CWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, CWFound, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, LAGREGUER, Crt. Lyc larger, Rev. Inst. Ap, TrqOVISIa, TrqOVISIa, Perl, LAGREGUER, Crt		
LEMANIVERSER JOS 12 L NAMAMPHORREQ TIME See JS2 L NAMAMPHORREQ SEE JS2 L N		
MarchapticheqTime_Sec_122	_	
k_ShriftLerNevTime_Sec_f32 k_ShandstillTime_Sec_f32 k_Bapt_TrovVslsa_Per1_APANorecoverableFaults_Cnt_lgc target_TrovVslsa_Per1_APANorecoverableFaults_Cnt_lgc target_TrovVslsa_Per1_CAPANdsta_Per1_CAPANd		
k, SandstillThreeh, Kpf., p32 k, StandstillThreeh, Kpf., p32 k, StandstillThreeh, Kpf., p32 clarged, Rie, Inst. Ap., TrqoVista, Perf., APADrivinterventionDetected_cnt. target, TrqoVista, Perf., APATROVIsta, Perf., APARcouserableFaults, Cnt. Igo. target, Rie. Inst. Ap., TrqoVista, TrqoVista, Perf., APARcouserableFaults, Cnt. Igo. target, Rie. Inst. Ap., TrqoVista, TrqoVista, Perf., CWFound_cnt. Igo. target, TrqoVista, Perf., APTROVIsta, Perf., CWFound_cnt. Igo. target, TrqoVista, Perf., APTROVIsta, Perf., CWFound_cnt. Igo. target, TrqoVista, Perf., LWFound_cnt. Igo. target, TrqoVista, P		
k, StandstillTrime, See, JS2 larger, Rte, Inst. Ap., TrqOviSta, Perl_APADrivrinterventionDetected_Cnt. targer, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_APANonRecoverableFaults, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_APARecoverableFaults, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_CoviPosition, HwDeg, JS2 targer, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_ESCFault, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_ESCRequest, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_ESCRequest, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_ESCRequest, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_GMOSH, Cottage, State, enumurager, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_GMOSH, ApAMigEnable, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_GMOSH, ApAMigEnable, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_GMOSH, ApAMigEnable, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_GMOSH, ApAMigEnable, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_GMOSH, ApAMigEnable, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_GMOSH, ESCMMigEnable, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_GMOSH, Lixender, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_LKARequest, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOviSta, Perl_LKARequest, Cnt.] gotarger, Rte, Inst. Ap., TrqOviSta, TrqOvi		
is arget. Rel. Inst. Ap. TrqOVIsta. Prof. APADovrinterventionDetected. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APADovrinterventionDetected. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APARcoverableFaults. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APARcoverableFaults. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APARcoverableFaults. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APARcoverableFaults. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APARcoverableFaults. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APARcoverableFaults. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APARcoverableFaults. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. APARcoverableFaults. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. CWFOund. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. Perl. CWFOund. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. TrqOVIsta. Perl. CWFOund. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. TrqOVIsta. Perl. ESCFault. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. TrqOVIsta. Perl. ESCFault. Cmt. Igc target. Rel. Inst. Ap. TrqOVIsta. TrqOVIsta. Perl. ESCFault. Cmt. Igc target. TrqOVIsta. Perl. ESCGetalmited. Cmt. Igc target. TrqOVIsta. Per		
Larget, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _APARovinnerventionDetected_Cnt. Igo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _APARecoverableFaulis_Cnt. Igo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _CCWFound_Cnt. Igo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _CMOSH_APAMIgEnable_Cnt. Igo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _CMOSH_APAMIgEnable_Cnt. Igo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _CMOSH_APAMIgEnable_Cnt. Igo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _LKAFault_Cnt. Igo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _MSSecureVehicleSpeed _Kph. Tgo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _MsSecureVehicleSpeed _Kph. Tgo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _MsSecureVehicleSpeed _Kph. Tgo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _PosSroriEnable_Cnt. Igo target, Rie, Inst. Ap. TrqOVSia.TrqOVSia.Perl _PosSroriEnable		
target, Rie, Inst. Ap., TrqOVSia, TrqOVSia, Perl _APARonecoverableFaults_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _APARequest_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _APARequest_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _APARequest_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _COWFound_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _ESCFault_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _ESCFault_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _ESCFault_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _ESCRequest_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _ESCSiate_State_enum target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _GSCSiate_State_enum target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _GSCSiate_State_enum target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _GSCSiate_State_Inst. ap. TrqOVSia, TrqOVSia, Perl _HVTorque_HvNm_132 target_TrqOVSia, Perl _GSCSiate_State_Inst. ap. TrqOVSia, TrqOVSia, Perl _HVTorque_HvNm_132 target_TrqOVSia, Perl _GSCSiate_State_Inst. ap. TrqOVSia, TrqOVSia, Perl _HVTorque_HvNm_132 target_TrqOVSia, Perl _PosTrajEnable_Cnt_ige target_Rie_Inst. Ap. TrqOVSia, TrqOVSia, Perl _PosTrajEnable_Cnt_ige target_Ri		
target_Rie_Inst_Ap_TrqOviSta_Peri_APARecoverableFaults_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Peri_APARecoverableFaults_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Peri_CSta_Peri_APARequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Peri_CSta_Peri_CWFound_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_CCWFound_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_CCWFound_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_CWFound_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_CWFound_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_CWFound_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCFault_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCFault_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCSTaut_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCState_und target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCState_und target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCState_und target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCState_und target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCState_und target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCState_und target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCState_und target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_ESCState_und target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_EMOSH_ESCM_Banable_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_EMOSH_ESCM_Banable_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_LKARequest_Cnt_Ugc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Peri_TossSevefanable_Cnt_Ugc target_Rie_Inst_		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_APARequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Perl_APAState_State_enun target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_COWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_COWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_COWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_COWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_COWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_COWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DSCRIBLE_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DSCRIBLE_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DSCRIBLE_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_LSCRIBLE_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MoSH_ESCMIGEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequet_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequet_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequet_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequet_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequet_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequet_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTrqiNange_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Tr		
target_Re_Inst_Ap_TrqOv/Sta_Perl_APAState_State_enum target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_CCWFootund_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_CCWFootund_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_CCWFootund_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_CWFootund_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_ESCFault_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_ESCFault_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_ESCFault_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_ESCFault_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_ESCS_Caputest_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_ESCS_Caputest_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_ESCS_Caputest_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_ESCS_Caputest_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_GMOSH_APAMfgenable_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_GMOSH_APAMfgenable_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_GMOSH_APAMfgenable_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MOSH_ESCMfgenable_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MOSH_DECC_CAI_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MOSH_DECC_CAI_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MOSH_DECC_CAI_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MOSH_DECC_CAI_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Terl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Terl_MCAPAUST_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Terl_SSev*Renable_Cnt_Ige target_Re_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Terl_SSev*Renable_Cnt		
target_Rte_Inst_Ap_TrqOviSta_Pert_CVWposttion_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_CWPound_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_Pert_CWPostton_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_CWPostton_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_CWPostton_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_Pert_ESCRState_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_Pert_ESCRState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRState_State_enum target_Rte_Inst_Ap_TrqOviSta_Pert_GMOSH_DESCRState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_DESCRState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_DESCRState_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_DESCRState_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_DESCRState_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_LKAMfigenable_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HwTorque_HwMm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_Pert_LKAFault_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_Pert_MaxSecureVerbicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Sorraiphable_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Sorraiphable_Cnt_Ige target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Sorraiphable_Cnt_Ige target_Rte_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOv/Sta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_CWFoustion_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_Pert_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_Pert_GMOSH_Sta_Pert_GMOSH_State_Enum target_Rte_Inst_Ap_TrqOv/Sta_Pert_GMOSH_ESCMfigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_GMOSH_ESCMfigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_Pert_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_Pert_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_LKAP_Bert_LEG_tn_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_LKAP_Bert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_LKAP_Bert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_LKAP_Bert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_LKAP_Bert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_LKAP_Bert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_LKAP_Bert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_MSAB_Pert_MSAB_Pert_LKAP_Bert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_MSAB_Pert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_MSAB_Pert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_DT_Igc target_Rte_Inst_Ap_TrqOv/Sta_Pert_MSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_Pert_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_DSAB_PERT_TRQOVSTa_PERT_TRQOVSTa_PERT_TRQOVSTa_PERT_TRQOVSTa_PERT_TRQOVSTa_PERT_TRQOVSTa_P	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFate_state_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFate_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSHOScillateState_state_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Hortorque_HwNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARIault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARiault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARiault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARiault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARiault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARiault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARiault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IMSceureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IMSceureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ShiftLeverIsInReverse_Cnt_Igc targe	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Pert_ESCREJLEmited_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Pert_ESCREQuest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCREQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCREQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCREQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCREQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MOSH_ESCMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HarbicRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HarbicRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HarbicRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_DescriveNehiceSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrevHaMagle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrevHaMagle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwiRtrigAngequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwiRtrigAngequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwiRtrigAngequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Pert_SwiRtri	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCISLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH-DScMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_BapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LAFAbutt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LAFAbutt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFautt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFabutt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFabutt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFabutt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFabutt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFabutt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFabutt_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MinsecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvothwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvothwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLevertsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLevertsinR	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_GMOSH_SCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_HarbitcRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_HarbitcRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_HarbitcRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Pert_SorTraj		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_GMOSH_DEADME_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_PosSrvchWangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_PosTrajHangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_PosTrajHangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_PosTrajHangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_PosTrajHangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_PosTrajHangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_StrajHangle_enubeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_TosCamp_MirNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_TrqOsCamp_MirNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_TrqOsCamp_MirNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Pert_TrqOsCamp_MirNm_f32 target_TrqOvlSta_Pert_APANonRecoverableFaults_Cnt_Igc_value 0 targ		
target_Rte_Inst_Ap_TrqOviSta_Prr1_GMOSHOscillateState_State_enum target_TrqOviSta_Prr1_GMOSH_APAMfgEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_Prr9_GMOSH_APAMfgEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_Prr9_GMOSH_ESCMfgEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_Prr9_GMOSH_ESCMfgEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_Prr9_GMOSH_ESCMfgEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_Prr9_HapticRequest_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_Prr9_HapticRequest_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_HapticRequest_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_LKAFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosSrveEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosSrveEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosSrveEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_PosTrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_StrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_StrajEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Prr9_StrajEnable_Cnt_Igo target_TrqOviSta_		
target_Rte_Inst_Ap_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvOrIMARngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvISta_Per1_APANgenceverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Per1_APARM		
target_Rte_Inst_Ap_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_LKAFaut_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Perl_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Perl_PosSrovEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_ShiffLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Perl_ShiffLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc target_TrqOvISta_Perl_TrqOscEnable_Cnt_lgc targe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_STrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFau		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SorrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SiritLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SiritLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOsCenable_Cnt_Igc target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_TrqOsCenable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCenable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCenable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCenable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCenable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCenable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 2 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 3 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 3 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 4 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.valu		
target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_PosSrohWAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Prq_SwartrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOvlSta_Prq_T		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Prq_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwaRTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Prq_TrqOvlSta_P		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Prq_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Prq_SwaRTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prq_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Prq_TrqOvlSta_Prq_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Prq_Tr		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target	$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEna		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1		

2015-02-04, 14:05:40+0530



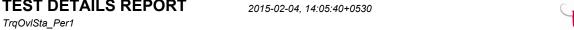


Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1109.81006		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	374.820007		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrgOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.25999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	40		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target TrgOvlSta Per1 ShiftLeverlsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Resu
TrqOvlSta APAState State M enum	0	0	
TrgOvISta ESCState State M enum	3	3	
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	308.519989	308.519989 ± 1	
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrgOvlSta HapticState State M enum	0	0	
TrgOvlSta Hapticdur Cnt M u16	684	684 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	
TrgOvlSta HwTorqueSV HwNm M Str.SV UIs f32	-8.02999973	-8.02999973 ± 0.000009	
TrqOvlSta LKAFault Cnt M lgc	1	1 ± 1	
TrgOvlSta LKAPermFault Cnt M lgc	1	1 ± 1	
TrgOvISta LKAPermFault Cnt M u08	3	3 ± 1	
TrqOvlSta_LKAState_State_M_enum	3	3	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.129999995	0.129999995 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
		3	
target TrqOvlSta Per1 ESCState State enum.value	3		
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
		0 3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	-	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0 3 1	3 1±1	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 3 1 0.12999995	3 1 ± 1 0.129999995 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	450.359985
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	396.359985
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	540
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	960
TrqOvlSta_Haptictime_Sec_M_f32	1830.43005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.42000008
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.150000006
k APAMaxHwTrq HwNm f32	0.319999993
k_APAMaxVehSpd_Kph_f32	7.19999981
k_ESCMaxVehSpd_Kph_f32	19.7000008
k_HapticAmplitude_MtrNm_f32	0.0240000002
k_HapticDuration_Sec_f32	0.0599999987
k_HapticFreq_Hz_f32	5.05999994
k_HapticReacttime_Sec_f32	0.064000003
k_LKAMaxVehSpd_Kph_f32	7.5999999
k_LKAMinVehSpd_Kph_f32	7
k_MaxHapticReqTime_Sec_f32	0.075000003
k_SWARLimiter_HwDeg_f32	7.80000019
k_ShiftLevRevTime_Sec_f32	0.0170000009
k_StandstillThresh_Kph_f32	0.159999996
k_StandstillTime_Sec_f32	0.0170000009
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target TrqOvlSta Per1 ESCIsLimited Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_IrqOvlSta_Peri_SwakTrgtangkequest_HwDeg_t32 target_TrqOvlSta_Peri_ShiftLeverlsInReverse_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Perl TrqOscAmp MtrNm f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0
target TrqOvISta Per1 APARecoverable aults_Git_gc.value	0
target_TrqOvISta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1099.70996
target TrqOvlSta Per1 CWFound Cnt Igc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	379.76001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.61000001
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	
	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1 1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1 1 0.5
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1 1 0.5 54.7000008
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1 1 0.5

© Report created by TESSY V3.1.9, report template V2.1

20

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-24.8999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	396.359985	396.359985 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	960	960 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.42000008	-7.42000008 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999	5.5999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	5.5999999	5.5999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0240000002	0.0240000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.05999994	5.05999994 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	1050
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.400000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.029999993
k_HapticDuration_Sec_f32	0.075000003
k_HapticFreq_Hz_f32	5.57399988
k_HapticReacttime_Sec_f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k_LKAMinVehSpd_Kph_f32	8.75
k_MaxHapticReqTime_Sec_f32	0.090000036
k_SWARLimiter_HwDeg_f32	9.19999981
k_ShiftLevRevTime_Sec_f32	0.0209999997
k_StandstillThresh_Kph_f32	0.200000003
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	~	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H	- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1089.60999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	384.700012		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target TrqOvlSta_Per1_HwTorque HwNm f32.value	1.96000004		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ExArcequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	
TrqOvISta_ESCState_State_M_enum	3	3	✓
TrgOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991	2315.65991 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.029999993	0.029999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.7 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrgOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1750
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	572.039978
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1800.45996
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	3575
TrqOvlSta_Haptictime_Sec_M_f32	2800.93994
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-6.19999981
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.0152000003
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrgOvISta LKAPermFault Cnt M Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.170000002
k_APAMaxHwTrq_HwNm_f32	0.479999989
k APAMaxVehSpd Kph f32	10.8000002
k_ESCMaxVehSpd_Kph_f32	19.7000008
k_HapticAmplitude_MtrNm_f32	0.0359999985
k_HapticDuration_Sec_f32	0.090000036
k_HapticFreq_Hz_f32	6.08799982
k_HapticReacttime_Sec_f32	0.0960000008
k_LKAMaxVehSpd_Kph_f32	11.3999996
k_LKAMinVehSpd_Kph_f32	10.5
k_MaxHapticReqTime_Sec_f32	0.104999997
k_SWARLimiter_HwDeg_f32	7.80000019
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	0.23999995
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
© Report created by TESSY V3.1.9, report template V2.1	23

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3:			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32	- ·		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cn target_TrqOvlSta_Per1_PosSrvoHwAngle_F		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvISta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle H		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f		-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3:	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1079.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	389.640015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target TrqOvlSta Per1 HwTorque HwNm f32.value	2.30999994		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	44.9000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	54.7000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-24.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	V
TrqOvlSta_Hapticdur_Cnt_M_u16	3575	3575 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.19999981	-6.19999981 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1 1 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	3	3±1	~
TrgOvISta LKAState State M enum	4	4	
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	5.5999999	5.5999999 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	_
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	5.5999999	5.5999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0359999985	0.0359999985 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.08799982	6.08799982 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.8 (Repeat Count = 1)	l
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2 0
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639,65991
TrqOvlSta_ElapsedArAwaxiwTrqTime_Sec_M_f32	659.880005
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	6100
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0 2
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOvISta ReadytoPulse Cnt M Igc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32 k_HapticReacttime_Sec_f32	6.60200024 0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	9.19999981
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target TrqOvlSta Per1 CCWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_mqovista.mqovista_reri_Gwlosh_ArAwiigenable_cnt_igc	target_TrqOvIsta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvonwAngle_Hwbeg_isz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
	torget TraOulSte Bert TraOceFree Hz f22
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
	0

2015-02-04, 14:05:40+0530

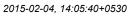


Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1069.41003		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	394.579987		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Resul
TrqOvISta_APAState_State_M_enum	0	0	
TrgOvISta ESCState State M enum	3	3	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	2639.65991	2639.65991 ± 1	,
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	659.880005	659.880005 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	,
TrgOvlSta HapticState State M enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	•
TrqOvISta Haptictime Sec M f32	3285.03003	3285.03003 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	•
TrqOvISta LKAFault Cnt M Igc	1	1 ± 1	
TrqOvISta LKAPermFault Cnt M Igc	1	1 ± 1	•
TrqOvISta LKAPermFault Cnt M u08	3	3 ± 1	
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	
TrqOvlSta ReadytoPulse Cnt M lgc	0	0 ± 1	•
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	1	1 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	
target TrqOvlSta Per1 GMOSHOscillateState State enum.value	0	0	,
	3	3	
target TrgOvlSta Per1 LKAState State enum.value		· ·	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1 0.150000006	1 ± 1 0.150000006 ± 0.0009765625	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value			•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0.150000006	0.150000006 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3529.32007
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	747.719971
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3539.93994
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	8625
TrqOvlSta_Haptictime_Sec_M_f32	3770.09009
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.98000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0196000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	10

TrqOvlSta_Per1





Imput Value	
k_APAMacukerHapite_Cet_Up k_APAMacukerHapite_Cet_Up k_APAMacukerHapite_Sec_G12 k_APAMacukerHapite_Sec_G12 k_APAMacukerHapite_Sec_G12 k_APAMacukerHapite_Sec_G12 k_APAMacukerHapite_Minim_G12 d_Cooperage_Sec_G12 k_APAMacukerHapite_Minim_G12 d_Cooperage_Sec_G12 k_AppticPreq_Hz_G12 k_AppticPreq_H	
K_APAMast-HvTng Time, See, f22 k_APAMast-HvTng Havhm, f32 d_APAMast-HvTng Havhm, f32	Cnt_M_lgc
K.APAMAVENSPJ, Kph. f32	.lgc
k_APAMaxVerSpd_Kpt_132 k_LKAMinVerSpd_Kpt_132	ec f32
k_ESCMaxVehSpd_Kph_132 k_EscMaxVehSpd_Kph_132 k_EscMaxVehSpd_Kph_132 k_HapicAmplitude_MtrNm_132 k_Debtacountion_Sec_132 k_HapicEnglibude_MtrNm_132 k_Debtacountion_Sec_132 k_HapicEnglibude_MtrNm_142 k_Debtacountion_Sec_132 k_HapicEnglibude_MtrNm_142 k_Debtacountion_Sec_132 k_HapicEnglibude_MtrNm_142 k_Debtacountion_Sec_132 k_LASMarkVehSpd_Kph_132 k_LKAMravVehSpd_Kph_132 k_LKAMravVehSpd_Kph_132 k_LKAMravVehSpd_Kph_132 k_LKAMravVehSpd_Kph_132 k_LKAMravVehSpd_Kph_132 k_MaxHapicEnglibude_MtrNm_182 k_SMarkLimiter_MADeg_132 k_ShandsillTimer_Sec_132 d_0.32999998 k_ShandsillTimer_Sec_132 d_0.32999998 k_ShandsillTimer_Sec_132 d_0.32999998 k_ShandsillTimer_Sec_132 d_0.32999998 k_ShandsillTimer_Sec_132 d_0.32999998 k_ShandsillTimer_Sec_132 d_0.32999998 d_0.3299	
k_Haptichamplitude_MinNm_152 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_LKAMavivehSpd_kph_132 15.999998 k_LKAMavivehSpd_kph_132 14 k_LKAMavivehSpd_kph_132 13.999998 k_LKAMinvehSpd_kph_132 13.999999 k_LKAMinvehSpd_kph_132 13.999999 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_StandstillTime_Sec_132 k_StandstillTime_Sec_132 k_StandstillTime_Sec_132 k_StandstillTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_StandstillTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_StandstillTime_Sec_132 k_StandstillTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_StandstillTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_13	
k_Haptichamplitude_MinNm_152 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_Hapticheactime_Sec_132 k_LKAMavivehSpd_kph_132 15.999998 k_LKAMavivehSpd_kph_132 14 k_LKAMavivehSpd_kph_132 13.999998 k_LKAMinvehSpd_kph_132 13.999999 k_LKAMinvehSpd_kph_132 13.999999 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_StandstillTime_Sec_132 k_StandstillTime_Sec_132 k_StandstillTime_Sec_132 k_StandstillTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_StandstillTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_StandstillTime_Sec_132 k_StandstillTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_StandstillTime_Sec_132 k_ShiftLevRevTime_Sec_132 k_ShiftLevRevTime_Sec_13	32
K_HapticPeq_Hz_F32	
k, HapticFreq.Hz, /12 k, HapticFreq.Hz, /12 k, HapticFreq.Hz, /13 k, LKAMav-HapticReqTime, Sec, /13 k, LKAMav-HapticReqTime, Sec, /13 k, LKAMav-HapticReqTime, Sec, /13 k, LKAMav-HapticReqTime, Sec, /13 k, MaxHapticReqTime, Sec, /13 k, MaxHapticReqTime, Sec, /13 k, ShittlevRevTime, Sec, /13 k, ShittlevRevTime, Sec, /13 k, ShittlevRevTime, Sec, /13 k, ShittlevRevTime, Sec, /13 k, StandstillTime, Sec, /13 kappet, TrqOviSta, Perl, APACtoviSta, Perl, CWPound, Cnt, lgc target, TrqOviSta, Perl, CWPound, Cnt, lgc target, TrqOviSta, Perl	_
k_LHAMIAVeNSpd_Kpf, 132 k_LHAMIAVeNSpd_Kpf, 132 k_LMAMIAVeNSpd_Kpf, 132 k_Max1apticReqTime_Sec_132 l_ 0.135000005 k_SWARLimiter_HwDeg_132 k_SMARLimiter_HwDeg_132 karget_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARovireteventionDetected_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARovireteventionDetected_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARecoverableFaults_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARecoverableFaults_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARecoverableFaults_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARecoverableFaults_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARecoverableFaults_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EXCEAult_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EXCRequest_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EXCRequest_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EXCRequest_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LHAPABler_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LHAPABler_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LHAPABler_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LHAPABler_Cnt_tige target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Pert_L	
k_LKAMANYchSpd_Kph_132 k_LKAMINYchSpd_Kph_132 k_LKAMINYchSpd_Kph_132 k_LMAMINTCHERPTIME_Sec_132 0.135000005 k_SWARLminter_HwDeg_132 k_SMARLminter_HwDeg_132 k_ShandstillTrine_Sec_132 0.319999998 k_StandstillTrine_Sec_132 target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_APARonrecoverableFaults_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_APARocoverableFaults_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_APARocoverableFaults_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_APARocoverableFaults_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_CVFound_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_CVFound_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_CVFound_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_CVFound_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCFault_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCFault_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCFault_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCStale_tstac_enun target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCStale_tstac_enun target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCStale_tstac_enun target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCStale_tstac_enun target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCRequest_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCRequest_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCRequest_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_ESCRequest_Cnt_I_ge target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_HwTorque_HwNm_132 target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_HwTorque_HwNm_132 target_Ric_Inst_AD_TrqOviSta_TrigOviSta_Per1_HwTorque_HwNm_132 target_Ric_Inst_AD_TrqOviSta_TrigOv	32
k_MakiapticReqTime_Sec_f32	
k_MaxHapticReqTime_Sec_f32 k_SMAxHamiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32 0.0329999998 k_ShiftLevRevTime_Sec_f32 0.03299999998 k_ShiftLevRevTime_Sec_f32 0.0329999998 k_ShiftLevRevTime_Sec_f32 target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APANorrinterventionDetected_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APANecoverableFaults_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_BiscRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapicRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapicRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapicRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapicRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapicRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapicRequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Markequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Markequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Markequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Markequest_Cnt_lgc target_Ric_Inst_Ap_TrqOviSta_TrqOviSta	
k_SMARLimiter_HwDeg_f32 k_StandstillTime_Sec_f32 k_Teq_Violita_Per1_APARceoverableFaults_Cnt_lgc target_Time_Nal_P_Time_Nal_Time_Sec_fac_uest_Cnt_lgc target_Time_Nal_P_Time_Nal_Time_Nal_Per1_ESCFault_Cnt_lgc target_Time_Nal_P_Time_Nal_Time_Nal_Per1_ESCFault_Cnt_lgc target_Time_Nal_P_Time_Nal_Time_Nal_Per1_LikARequest_Cnt_lgc target_Time_Nal_P_Time_Nal_Per1_LikARequest_Cnt_lgc target_Time_Nal_P_Time_Nal_Per1_LikARequest_Cnt_lgc target_Time_Nal_P_Time_Nal_Per1_LikARequest_Cnt_lgc target_Time_Nal_P_Time_Nal_Per1_LikARequest_Cnt_lgc target_Time_Nal_P_Time_Nal_Per1_LikA	
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_F132 k_StandstillThresh_Kph_F132 k_StandstillThresh_Kph_F132 k_StandstillThresh_Kph_F132 k_StandstillThresh_Sec_f32 0.329999998 target_Rte_Inst_Ap_TrqOv/Sta_Per1_APANDrvinterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOv/Sta_Per1_APANDrvinterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOv/Sta_Per1_APANDrvinterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOv/Sta_Per1_APARecoverableFaults_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_APARecquest_Ont_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_CVVPound_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_CVVPound_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_CVVPound_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_CVVPound_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCFault_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCFault_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCFault_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/MgEnable_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/MgEnable_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/MgEnable_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GNOSH_DESC/MgEnable_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_Hav/Torque_HwNm_r32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_Hav/Torque_HwNm_r32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_Hav/Torque_HwNm_r32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_Mordeyes_Cnt_tgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_Mav/SecurevehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_Mav/SecurevehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_T	
k_StandstillThresh_Kph_f32 karget_Rte_Inst_Ap_TrqOvIsta_Per1_APARORecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_APARocoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_APARocoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvIsta_Per1_CWFOund_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_SCSCault_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_SCSCault_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_SCSCault_State_enum target_Rte_Inst_Ap_TrqOvIsta_Per1_GMOSH_APAMIgfenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_GMOSH_ESCMIgfenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_BMOSH_ESCMIgfenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_RtqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_RtqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_RtqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_RtqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_RtqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_RtqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_RtqOvIsta_Per1_LKAPAINGEnable_Cnt_lgc target	
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SECFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SECRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_OscillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HubTrque_Hwhm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HubTrque_Hwhm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_L	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCReduest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequet_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequet_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_DAMMige_nable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARSquest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARSquest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARSquest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARSquest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARSquest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARSquest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARSquest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARSquest_C	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_COWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_COWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_COWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_COWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_COWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_DSCREQUEST_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_DSCREQUEST_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_DSCREQUEST_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_DSCREQUEST_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_DSCREQUEST_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HyDrorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HyDrorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqO	
target_Rte_Inst_Ap_TrqOviSta_Prq1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCR_Ut_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCR_Ut_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCR_Ut_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCR_Ut_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCR_Ut_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCR_Ut_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IKAFault_Cnt_lgc t	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCS_tainited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCS_tainited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCS_tate_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCS_tate_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCS_tate_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCS_tate_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_DESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LkAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHw	
target_Rte_Inst_Ap_TrqOvISta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCIst_Imited_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_TrqOvISta_Per1_LKAState_State_enum target_TrqOvISta_Per1_LKAState_State_enum target_TrqOvISta_Per1_LKAState_State_enum target_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rt	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_OScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServE	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCSLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCSLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc target_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_TrqO	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_DosSrvoHwA	/ISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_state_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARFault_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARFault_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARFault_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServ	/ISta.TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrveHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrveHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrveHwAngle_HwDeg_f32	/ISta.TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	/ISta.TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_DosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	/ISta.TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_DosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	/ISta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	vlSta.TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_DosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	vlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Lnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	/ISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgr
target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	/ISta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgr
target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	/ISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igo
target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_DosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
talget_Rte_linst_Ap_indovista_relin_roshtajEnable_Cht_igc	
towart Dto Jost An Tracovicto Tracovicto Dord Don Tracillavando Llavando Llavando Tracovicto Dord Don Don Tracovicto Dord Don	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -1059.31006	
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 0	WFound_Cnt_lgc.value
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 399.519989	WPosition_HwDeg_f32.value
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0	SCFault_Cnt_lgc.value
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value 0	SCIsLimited_Cnt_lgc.value
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0	SCRequest_Cnt_lgc.value
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0	MOSH_APAMfgEnable_Cnt_lgc.value
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0	
target TrqOvlSta Per1 HwTorque HwNm f32.value 3.00999999	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0	
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value 0	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_LKARequest_Ont_gc.value 0 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 64.5	
· - · · · - · - · - · · - · · · · ·	
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0 target_TraOvlSta_Per1_PosTrajEhuAngle_HuDog_f22.value 0.180000007	
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.180000007	ps majnwangie_nwbeg_tsz.value

© Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.2999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	8625	8625 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.98000002	-4.98000002 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	10	10 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	10	10 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0480000004	0.0480000004 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.11600018	7.11600018 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	14.8000002
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	eg_t32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	f22	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_I	ac .	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg	•	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_en		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvISta Per1 PosTrajEnable Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	· ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1049.20996		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	404.459991		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	-15.3999996 1		
		Expected Value	Popult
Name TrachylSta ARAStata State M enum	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	4	4	-
TrqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	4418.97998	4418.97998 ± 1	
TrqOvISta ElapsedShiftLevRevTime Sec M f32	835.559998	835.559998 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvISta_ESCState_State_M_enum	0
FrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5308.64014
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	923.400024
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	5279.41992
TrqOvISta_HapticState_State_M_enum	2
rqOvISta_Hapticdur_Cnt_M_u16	13675
rqOvISta_Haptictime_Sec_M_f32	4740.43018
rqOvISta HwTorqueSV HwNm M Str.SV Uls f32	-3.75999999
rqOvISta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0240000002
rqOvISta_HwT0rque3v_HwMin_iv_3ti.K_0is_i32	1
· ·	
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	0
rqOvISta_LKAState_State_M_enum	12.199998
rqOvISta_PosSrvoHwAngle_HwDeg_M_f32	12.1999998
rqOvISta_ReadytoPulse_Cnt_M_lgc	
_APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	0.20999993
_APAMaxHwTrq_HwNm_f32	0.800000012
x_APAMaxVehSpd_Kph_f32	18
ESCMaxVehSpd_Kph_f32	34.099985
_HapticAmplitude_MtrNm_f32	0.059999987
(_HapticDuration_Sec_f32	0.150000006
x_HapticFreq_Hz_f32	8.1440005
_HapticReacttime_Sec_f32	0.159999996
x_LKAMaxVehSpd_Kph_f32	19
C_LKAMinVehSpd_Kph_f32	17.5
_MaxHapticReqTime_Sec_f32	0.165000007
_SWARLimiter_HwDeg_f32	16.2000008
x_ShiftLevRevTime_Sec_f32	0.0410000011
_StandstillThresh_Kph_f32	0.400000006
_StandstillTime_Sec_f32	0.0410000011
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HwTorque HwNm f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

© Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvISta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvlSta Per1 PosTrajEnable Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDec			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1039.10999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	409.399994		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.71000004		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	74.3000031		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	84.0999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-13.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0	1	
	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	4	4	<i>y</i>
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4 4 0	4 4 0 ± 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4 4 0 0	4 4 0±1 0±1	<i>y</i>
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4 4 0 0 0	4 4 0±1 0±1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	4 4 0 0 0 0 2	4 4 0±1 0±1 0 2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	4 4 0 0 0 0 2 13675	4 4 0±1 0±1 0 2 13675±1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	4 4 0 0 0 0 2	4 4 0±1 0±1 0 2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	4 4 0 0 0 0 2 13675	4 4 0±1 0±1 0 2 13675±1 0±1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4 4 0 0 0 0 2 13675 0 -3.75999999	4 4 0 ± 1 0 ± 1 0 2 13675 ± 1 0 ± 1 -3.75999999 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	4 4 0 0 0 0 2 13675 0 -3.75999999	4 4 0 ± 1 0 ± 1 0 2 13675 ± 1 0 ± 1 -3.75999999 ± 0.000009 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	4 4 0 0 0 0 2 13675 0 -3.75999999 0	4 4 0 ± 1 0 ± 1 0 2 13675 ± 1 0 ± 1 -3.75999999 ± 0.000009 0 ± 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	4 4 0 0 0 0 2 13675 0 -3.75999999 0	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 0 4 12.1999998	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1 4 12.1999998±0.0009765625 1±1	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 0 4 12.1999998 1	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1 4 12.1999998±0.0009765625 1±1 0±1	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 0 4 12.1999998 1 0 4	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1 4 12.1999998±0.0009765625 1±1 0±1 4	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 0 4 12.1999998 1 0 4 4 4 4	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1 4 12.1999998±0.0009765625 1±1 0±1 4 4	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAPault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 1 0 4 12.1999998 1 0 4 4 4 2	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1 4 12.1999998±0.0009765625 1±1 0±1 4 4	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HavTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 1 0 4 12.1999998 1 0 4 4 2	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1 4 12.1999998±0.0009765625 1±1 0±1 4 4 2	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAPault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 0 4 12.1999998 1 0 4 4 2 4 0	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1 4 12.1999998±0.0009765625 1±1 0±1 4 4 2 4 0±1	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 0 4 12.1999998 1 0 4 4 2 4 0 12.1999998	4 4 0 ± 1 0 ± 1 0 2 13675 ± 1 0 ± 1 -3.75999999 ± 0.000009 0 ± 1 1 ± 1 0 ± 1 4 12.1999998 ± 0.0009765625 1 ± 1 0 ± 1 4 2 4 12.1999998 ± 0.0009765625	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_DesSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	4 4 0 0 0 0 2 13675 0 -3.75999999 0 1 0 4 12.1999998 1 0 4 4 2 4 0	4 4 0±1 0±1 0 2 13675±1 0±1 -3.75999999±0.000009 0±1 1±1 0±1 4 12.1999998±0.0009765625 1±1 0±1 4 4 2 4 0±1	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	V



est Step 2.12 (Repeat Count = 1)	Input Value
	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
	1
	6198.2998
	1011.23999
	6149.16016
	1
	16200
	5225.33984
	-3.1500001
	0.0262000002
	1
	0
	2
	0
	13.3000002
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	0
. = = :	0.219999999
	0.87999995
_APAMaxVehSpd_Kph_f32	19.7999992
_ESCMaxVehSpd_Kph_f32	36.5
_HapticAmplitude_MtrNm_f32	0.0659999996
_HapticDuration_Sec_f32	0.165000007
_HapticFreq_Hz_f32	8.65799999
_HapticReacttime_Sec_f32	0.175999999
_LKAMaxVehSpd_Kph_f32	20.8999996
_LKAMinVehSpd_Kph_f32	19.25
_MaxHapticReqTime_Sec_f32	0.180000007
_SWARLimiter_HwDeg_f32	17.6000004
_ShiftLevRevTime_Sec_f32	0.0450000018
	0.43999998
	0.0450000018
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvlSta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
	target TrqOvISta Per1 ESCRequest Cnt Igc
	target_IrqOvlSta_Per1_ESCRequest_Cnt_igc target TrqOvlSta_Per1_ESCState_State_enum
	target_rrqOvlSta_Per1_GMOSHOScillateState_State_enum
	target_IrqOvlSta_Per1_GMOSH_APAMigEnable_Cnt_igc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc
	target_TrqOvlSta_Per1_GMOSH_ESCMigEnable_Cnt_lgc
	target_rrqOvIsta_Per1_GwloSh_EKAwiigEnable_Chit_igc
0 = = = : : = = = :	target_TrqOvISta_Per1_HwTorque_HwNm_f32
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAI atit_Ont_lgc
	target TrqOvISta Per1 LKARequest Cnt Igc
	target TrqOvISta Per1 LKAState State enum
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	talget ilgoviota reli ilgoscalip ivitivili ioz
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_igc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_igc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_igc.value	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_igc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_igc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_igc.value arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_igc.value	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0

2015-02-04, 14:05:40+0530



TrqOviSta_Peri		Ida	TIMO
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	414.339996		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.05999994		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	89		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-11.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999	1011.23999 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	16200	16200 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001	-3.1500001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value		0.109999999 ± 0.0009765625	✓
3	0.109999999	0.109999999 ± 0.0009703023	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.109999999 0.0659999996	0.0659999996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7087.95996
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1099.07996
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	7018.8999
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	18725
TrqOvlSta_Haptictime_Sec_M_f32	5710.16992
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53999996
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0284000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996

2015-02-04, 14:05:40+0530



TIQOVISIA_PELI	THE CITY OF THE CONTRACT OF TH
Name	Input Value
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
C_APAMaxHwTrqTime_Sec_f32	0.230000004
_APAMaxHwTrq_HwNm_f32	0.959999979
C_APAMaxVehSpd_Kph_f32	21.6000004
c_ESCMaxVehSpd_Kph_f32	38.9000015
	0.0719999969
	0.180000007
HapticFreq Hz f32	9.17199993
HapticReacttime Sec f32	0.192000002
- '	22.7999992
	21
	0.194999993
SWARLimiter_HwDeg_f32	19
S_ShiftLevRevTime_Sec_f32	0.0489999987
_StandstillThresh_Kph_f32	0.479999989
_StandstillTime_Sec_f32	0.0489999987
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOviota.TrqOviota_Fet1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreg Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
	0
	0
arget_TrqOvISta_Per1_APARequest_Cnt_Igc.value	1
	0
rrget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1018.90997
0 = 1 = = = = = = = = = = = = = = = = =	0
	419.279999
312 41 112 1 2 11 11 21 23	0
0 = 1 = = = = = = = = = = = = = = = = =	0
rget_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
rget_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
rget_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
	0
arget_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.40999985
	0
	0
	1
arget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	84.0999985
arget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value arget_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	93.9000015
arget_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value arget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value arget_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value arget_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.69999981		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	18725	18725 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	5710.16992	5710.16992 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.53999996	-2.53999996 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996	14.3999996 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	14.3999996	14.3999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0719999969	0.0719999969 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	9.17199993	9.17199993 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7977.62012
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1186.92004
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	7888.64014
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	21250
TrqOvlSta_Haptictime_Sec_M_f32	6195
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.92999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	15.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.239999995
k_APAMaxHwTrq_HwNm_f32	1.03999996
k_APAMaxVehSpd_Kph_f32	23.3999996
k_ESCMaxVehSpd_Kph_f32	41.2999992
k_HapticAmplitude_MtrNm_f32	0.0780000016
k_HapticDuration_Sec_f32	0.194999993
k_HapticFreq_Hz_f32	9.68599987
k_HapticReacttime_Sec_f32	0.208000004
k_LKAMaxVehSpd_Kph_f32	24.7000008
k_LKAMinVehSpd_Kph_f32	22.75
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	20.3999996
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	0.519999981
k_StandstillTime_Sec_f32	0.0529999994
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnd$	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	eg_t32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	f22	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_I	ac .	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lo	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_en		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrgOvlSta Per1 MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc	target_TrqOvISta_Per1_PosServEnable_Cnt	- ' -	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1008.81		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	424.220001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0 4.76000023		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	89		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	98.8000031		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.80000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	V
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1 0	
TrqOvISta_ElapsedStatidstill*Iffle_Sec_ivi_132 TrqOvISta_HapticState_State_M_enum	2	2	-
TrgOvlSta Hapticdur Cnt M u16	21250	21250 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.92999995	-1.92999995 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	_

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0780000016	0.0780000016 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	9.68599987	9.68599987 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

est Step 2.15 (Repeat Count = 1)	Inner Makes
lame	Input Value
tte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	4
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8867.28027
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1274.76001
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	8758.37988
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	23775
rqOvlSta_Haptictime_Sec_M_f32	6679.83008
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0328000002
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.25
_APAMaxHwTrq_HwNm_f32	1.12
_APAMaxVehSpd_Kph_f32	25.2000008
_ESCMaxVehSpd_Kph_f32	43.7000008
_HapticAmplitude_MtrNm_f32	0.083999989
_HapticDuration_Sec_f32	0.20999993
_HapticFreq_Hz_f32	10.1999998
_HapticReacttime_Sec_f32	0.224000007
_LKAMaxVehSpd_Kph_f32	26.6000004
_LKAMinVehSpd_Kph_f32	24.5
_MaxHapticReqTime_Sec_f32	0.224999994
_SWARLimiter_HwDeg_f32	21.7999992
_ShiftLevRevTime_Sec_f32	0.057
_StandstillThresh_Kph_f32	0.560000002
_StandstillTime_Sec_f32	0.057
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected	d_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_0	Cnt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_	lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State	_enun target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cn	t_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cn	nt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt	t_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-998.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	429.160004		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.11000013		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0 0.140000001		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	Nesuit
TrqOvISta_ESCState_State_M_enum	4	4	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	1274.76001	1274.76001 ± 1	V
TrgOvlSta ElapsedStandstillTime Sec M f32	8758.37988	8758.37988	
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	23775	23775 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	6679.83008	6679.83008 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005	-1.32000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004	16.6000004 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	V
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	*
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 16.6000004	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0839999989	16.6000004 ± 0.0009765625 0.0839999989 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	10.1999998	10.1999998 ± 0.0009765625	
2. 7. 7. 4			

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.16 (Repeat Count = 1)	Innut Walin
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	9756.94043
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1362.59998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	9628.12012
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	26300
TrqOvlSta_Haptictime_Sec_M_f32	7164.66016
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-0.709999979
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0350000001
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	17.7000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.25999999
k_APAMaxHwTrq_HwNm_f32	1.20000005
k_APAMaxVehSpd_Kph_f32	27
k_ESCMaxVehSpd_Kph_f32	46.0999985
k_HapticDuration_Sec_f32	0.090000036 0.22499994
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	0.224999994
k_HapticReacttime_Sec_f32	0.23999995
k_LKAMaxVehSpd_Kph_f32	28.5
k_LKAMinVehSpd_Kph_f32	26.25
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	23.2000008
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	0.60000024
k_StandstillTime_Sec_f32	0.0610000007
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target TrgOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_MinSecurevenicleSpeed_Rpn_t32 target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecurevenicleSpeed_kpn_132 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
	0
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-988.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	434.100006		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	98.8000031		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	108.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	- -
TrqOvlSta_HapticState_State_M_enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	26300	26300 ± 1	- •
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.709999979	-0.709999979 ± 0.0000009	- •
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.090000036	0.0900000036 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	10.7139997	10.7139997 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13315.5801
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13107.0801
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	36400
TrqOvlSta_Haptictime_Sec_M_f32	9103.98047
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

Injust Value		
APAModelviller Sec. 2	Name	Input Value
APAMAMATION (1944) Name (192	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
	k_APAIncludeHaptic_Cnt_lgc	1.
A.	k_APAMaxHwTrqTime_Sec_f32	0.30000012
Microbion Micr	k APAMaxHwTrq HwNm f32	1.51999998
1.14	k APAMaxVehSpd Kph f32	34.2000008
1.14	k ESCMaxVehSpd Kph f32	55.7000008
Application Sec. 152 12700055 Application Sec. 152 127		
127700005		
A. MANAWYSE, M. P. M. P. P. M. P. P. M. P. P. M. P.		
M. AMANIVANISSE, JAPI, 122 3.0 00000052		
K. MAMPHONES Refs 12		
Montheriner See, 12 Montheriner Montholog, 12 Montheriner, 1966, 12 Montheriner, 1966, 12 Montheriner, 1966, 12 Montheriner, 1966, 13 Montheriner, 1966		
k. SMR-Lanter, thr. Coop. 12 k. StandaulThreen, Key. 12 kongel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected, Cht. Integel, Rie, Jen. A. TroOVSta, TriOVSta, Perl. APARovinterventionDetected		
k. Shirtle. Rev. Time, Sec., 132 k. Shandstifffines, Sec., 132 k. Shandstifffines, Sec., 132 k. Shandstifffines, Sec., 132 k. Shandstiffines, Sec., 132 k. Shandstiffines, Sec., 132 k. Shandstiffines, Sec., 132 larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APADovinterventionDetected, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , APARterovenable Faults, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , ESCSiate, State, enum larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , ESCSiate, State, enum larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , ESCSiate, State, enum larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , ESCSiate, State, enum larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , GMOSH, APARterioted, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , GMOSH, APARterioted, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , GMOSH, APARterioted, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , GMOSH, APARterioted, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , GMOSH, APARterioted, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , Mosh, Aparterioted, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , Mosh, Aparterioted, Crt. Igo larget, Rei, Inst., Ap., TroVolsia, TroVolsia, Perl , Mosh, Aparterioted		
k. SandaulThreeh Koft, 192 k. StondaulThree Koft, 192 k. StondaulThree Koft, 192 k. StondaulThree Koft, 192 kande, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APADrivinterventionDetected, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia TrigOVSia, Perl. APADrivinterventionDetected, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia TrigOVSia, Perl. APARecoverableFaults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia TrigOVSia, Perl. APARecoverableFaults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverableFaults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverableFaults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverableFaults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverableFaults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverableFaults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverableFaults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverable Faults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverable Faults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARecoverable Faults, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. ESCEPault, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. ESCEPault, Cni. Ligo Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. BECSIA Estate, genum Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. BECSIA Estate, genum Langel, Rise Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARECoverable, Cni. Ligo Langel, Rise, Jeat, A.D. TrigOVSia, TrigOVSia, Perl. APARECoverable, Cni. Ligo Langel, Rise, Jeat, A.D. TrigOVSia, TrigOVSia, Perl. LANGelsea, Cni. Ligo Langel, Rise, Jeat, A.D. TrigOVSia, TrigOVSia, Perl. LANGelsea, Cni. Ligo Langel, Rise, Jeat, A.D. TrigOVSia, TrigOVSia, Perl. LANGelsea, Cni. Ligo Langel, Rise, Jeat, A.D. TrigOVSia, TrigOVSia, Perl. LANGelsea, Cni. Ligo Langel, Rise, Jeat, A.D. TrigOVSia, TrigOVSia, Perl. LANGelsea, Cni. Ligo Langel, Rise,		
k Standstrilline, Sec. (32 target Rie, Inst. Ap. Triqovista. Pert. JAPADor/interventionDetected. Cnt. 1go target Rie. Inst. Ap. Triqovista. Prod Stat. Pert. JAPANscoreverableFaults. Cnt. 1go target Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Rie. Inst. Ap. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Triqovista. Pert. JAPANscoreverableFaults. Cnt. 1go target. Tr		
tanget. Ret. Mat. An. Tropo/Sta. Tropo/Sta. Pert. J. APADovrinterventionDebeded., Orl. Josept. River. Mat. An. Tropo/Sta. Tropo/Sta. Pert. J. APADovrinterventionDebeded. Orl. Josept. River. Mat. An. Tropo/Sta. Tropo/Sta. Pert. J. APARecoverableFaults. Crit. Jose Larget. River. Mat. An. Tropo/Sta. Tropo/Sta. Pert. J. APARecoverableFaults. Crit. Jose Larget. River. Mat. An. Tropo/Sta. Tropo/Sta. Pert. J. APARecoverableFaults. Crit. Jose Larget. River. Mat. An. Tropo/Sta. Pert. J. APARecoverableFaults. Crit. Jose Larget. River. Mat. An. Tropo/Sta. Pert. J. COWFound. Crit. Jose Larget. River. Mat. An. Tropo/Sta. Pert. J. COWFound. Crit. Jose Larget. River. Mat. An. Tropo/Sta. Pert. J. COWFound. Crit. Jose Larget. River. Mat. An. Tropo/Sta. Pert. J. COWFound. Crit. Jose Larget. River. Lar. Tropo/Sta. Pert. J. COWFound. Crit. Jose Larget. River. Lar. Tropo/Sta. Pert. L. SESCFault. Crit. Jose Larget. River. Lar. An. Tropo/Sta. Pert. J. COWFound. Crit. Jose Larget. River. Lar. An. Tropo/Sta. Pert. L. SESCFault. Crit. Jose Larget. River. Lar. An. Tropo/Sta. Pert. L. SESCFault. Crit. Jose Larget. River. Lar. An. Tropo/Sta. Pert. L. SESCRequest. Crit. Jose Larget. River. Lar. An. Tropo/Sta. Pert. L. SESCRequest. Crit. Jose Larget. Riv. Lar. An. Tropo/Sta. Pert. L. SESCRequest. Crit. Jose Larget. Riv. Lar. An. Tropo/Sta. Pert. L. SESCReguest. Crit. Jose Larget. Riv. Lar. An. Tropo/Sta. Pert. L. SESCReguest. Crit. Jose Larget. Riv. Lar. An. Tropo/Sta. Pert. L. SESCReguest. Crit. Jose Larget. Riv. Lar. An. Tropo/Sta. Pert. L. SKASta. Pert. L		
target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. APARORECoverableFaults. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. APARORECOVERABEFAULTS. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. APARORECOVERABEFAULTS. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. APARORECOVERABEFAULTS. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. CVPCeation. Horber. 12 target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. CVPCeation. Horber. 12 target. Rei. Inst. Ap. TrigONSta. Pert. CVPCeation. Horber. 12 target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. CVPCeation. Horber. 12 target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. SCENERAL Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. SCENERAL Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. SCENERAL Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCENERAL Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCENERAL Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCENERAL Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. SCHASIBLE. Cnt. Igo target. Rei. Inst. Ap. TrigONSta. TrigONSta. Pert. I. Post TrigONSta. Pert. I. TrigONSta. P		
target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. APARecoverable faults, Cnt. Lgo larget. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. APARecoverable faults, Cnt. Lgo larget. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. APAStaie. State. enum target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. COWFound, Cnt. Lgo larget. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. CWFound, Cnt. Lgo larget. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. CWFound, Cnt. Lgo larget. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. CWFound, Cnt. Lgo larget. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. CWFound, Cnt. Lgo larget. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Perd. Low		
target. Ries. Jac. Tro, OviSta. Perd. APARequest. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Perd. APAStates. State. enum target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. COWFound. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. COWFound. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. COWFound. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. COWFound. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. COWFound. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. ESCIS. Limited. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. ESCIS. Limited. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. ESCIS. Limited. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. ESCIS. Limited. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. ESCIS. Perd. ESCIS. Limited. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. ESCIS. Perd. MOSH. ESCIMIGEnable. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. ESCIS. Limited. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. I. KARFequest. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. I. KARFequest. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. I. KARFequest. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. I. KARFequest. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. I. KARFencest. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. I. KARFencest. Cht. Igo: target. Ries. Inst. Ap. Tro, OviSta. Tro, OviSta. Perd. I. KARFencest. Cht. Igo: target. Ries. Inst. Ap. Tro, Ovi		
target Rie Inst. Ap. TrqOvSta Pert APAState, Sate anum target Rie Inst. Ap. TrqOvSta Pert COWFound Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert COWFound Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert COWFound Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert COWFound Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert COWFound Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert COWFound Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cnt. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LESCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault Cot. Igc target Rie Inst. Ap. TrqOvSta TrqOvSta Pert LASCFault		
target, Rie, Inst. Ap. TrqOvista TrqOvista, Perl. CCWPosition (HvDeg. 152 target, Rie, Inst. Ap. TrqOvista TrqOvista, Perl. CCWPosition (HvDeg. 152 target, Rie, Inst. Ap. TrqOvista TrqOvista, Perl. CCWPosition (HvDeg. 152 target, Rie, Inst. Ap. TrqOvista TrqOvista, Perl. CCWPosition (HvDeg. 152 target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. ESCIst. Estate, enum target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. GMOSH-DescillateState, Cerl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. GMOSH-ESCIMGE, Cerl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. HapticRequest, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. HapticRequest, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. LKAFault, Crl. Ige target, Rie, Inst. Ap. TrqOvista, TrqOvista, Perl. Inst. Perl. Inst. Ap. TrqOvista, TrqOvista, Perl. Inst. Perl. Inst. Perl. TrqOvista, Perl. Inst. Perl. Inst. Perl. TrqOvista, Pe		
target Rie Jinst Ap. TrqOvista TrqOvista Perl JCCWPosition HwDeg J32 target Rie Jinst Ap. TrqOvista TrqOvista Perl JCWPosition HwDeg J32 target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCFault Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCFault Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCFault Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCFault Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCFault Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCRequest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCRequest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCRequest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCRequest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCREquest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCREquest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCREquest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCREquest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JSCREquest Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap. TrqOvista TrqOvista Perl JKAFarult Cnt I ge target Rie Jinst Ap.		
target, Ret, Inst, Ap. TrqOvista TrqOvista Pert CWFound Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert CWFound Ent, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert ESCFoutt, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert ESCFoutt, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert ESCFoutt, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert ESCFoutt, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert ESCFoutt, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert GMOSH OSCIllates tate, enum target, Ret, Inst, Ap. TrqOvista TrqOvista Pert GMOSH OSCIllates tate, enum target, Ret, Inst, Ap. TrqOvista TrqOvista Pert GMOSH APAMIgenable, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert GMOSH ESCMIgenable, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert GMOSH LEAMIgenable, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert GMOSH LEAMIgenable, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert JApolicRequest, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert JApolicRequest, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert JApolicRequest, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista Pert JApolicRequest, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista, Pert J. KAFautt, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista, Pert J. KAFautt, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista, Pert J. KAFautt, Cnt, Igo target, Ret, Inst, Ap. TrqOvista TrqOvista, Pert J. Massecurve NeticleSpeed, Knt, Ido target, Ret, Inst, Ap. TrqOvista TrqOvista, Pert J. Massecurve NeticleSpeed, Knt, Ido target, Ret, Inst, Ap. TrqOvista TrqOvista, Pert J. Massecurve NeticleSpeed, Knt, Ido target, Ret, Inst, Ap. TrqOvista TrqOvista, Pert J. Massecurve NeticleSpeed, Knt, Ido target, Ret, Inst, Ap. TrqOvista TrqOvista, Pert J. Massecurve NeticleSpeed, Knt, Ido target, Ret, Inst, Ap. TrqOvista, TrqOvista, Pert J. Massecurve NeticleSpeed, Knt, Ido target, Ret, Inst, Ap. TrqOvista, TrqOvista, Pert J. Shartte, Cott, Igo target, Ret, Inst, Ap. TrqOvis		
target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. ESCFault, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia Perf. ESCFault, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia Perf. ESCFault, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia Perf. ESCFault, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia Perf. ESCFault, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia Perf. ESCFault, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia Perf. ESCFault, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia Perf. ESCFault, State, enum target, Rite, Inst. Ap. TrqOv/Sia Perf. GMOSH Devil Eschale, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia Perf. GMOSH Devil Eschale, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. GMOSH LEXMINGEnable, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. LAWARIE, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. Possorvenbele, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. Sesorvenbele, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. Sesorvenbele, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. Sesorvenbele, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. Sesorvenbele, Cit. Igo: target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. Sesorvenbele,		
target. Rie. Inst. Ap. TrqOVISta. TrqOVISta. Perl. ESCFault. Cnt. Igo: target. Rie. Inst. Ap. TrqOVISta. Pred. ESCR. Sci. Imited. Cnt. Igo: target. Rie. Inst. Ap. TrqOVISta. Pred. ESCR. Sci. Imited. Cnt. Igo: target. Rie. Inst. Ap. TrqOVISta. Pred. ESCR. Sci. Imited. Cnt. Igo: target. Rie. Inst. Ap. TrqOVISta. Pred. ESCR. Sci. Inst. Imited. Rie. Inst. Ap. TrqOVISta. Pred. PESCState. State. enum target. Rie. Inst. Ap. TrqOVISta. Pred. CNOSH. APAMIGEnable. Cnt. Igo: target. Rie. Inst. Ap. TrqOVISta. Pred. CNOSH. APAMIGEnable. Cnt. Igo: target. Rie. Inst. Ap. TrqOVISta. Pred. Sci. Inst. Ins		
target. Rie. Inst. Ap. TrqOviSta. TrqOvSta. Perl _ESCRetuels Cnt.l.go target. Rie. Inst. Ap. TrqOviSta. TrqOvSta. Perl _ESCRetuels Cnt.l.go target. Rie. Inst. Ap. TrqOviSta. TrqOvSta. Perl _ESCState_State_enum target. Rie. Inst. Ap. TrqOviSta. TrqOvSta. Perl _GSCState_State_enum target. Rie. Inst. Ap. TrqOvSta. TrqOvSta. Perl _GSCState_State_enum target. Rie. Inst. Ap. TrqOvSta. TrqOvSta. Perl _GMOSH-DeScMigEnable_Cnt.l.go target. Rie. Inst. Ap. TrqOvSta. TrqOvSta. Perl _GMOSH-ESCMigEnable_Cnt.l.go target. Rie. Inst. Ap. TrqOvSta. TrqOvSta. Perl _LKARAINIT. Rie. TrqOvSta. Perl _GMOSH-ESCMigEnable_Cnt.l.go target. Rie. Inst. Ap. TrqOvSta. TrqOvSta. Perl _LKARAINIT. Rie. TrqOvSta. Perl _ResTrqOvSta. Perl _LKARAINIT. Rie. TrqOvSta. Perl _LKARAINIT. Rie	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSHO_ScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LixAFaul_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LixAFaul_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LixAFaul_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LixAFaul_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LixAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSevreInable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSevreInable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSevreInable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSevreInable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwiArtrgiAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwiArtrgiAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwiArtrgiAngle_twDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwiArtrgiAngle_twDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwiArtrgiAngle_twDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_CoviSta_Pert_TrqOscEnable_Cnt_lgc target_TrqOviSta_Pert_APARcquest_Cnt_lgc_value 1 target_TrqOviSta_Pert_EsCRequest_Cnt_lgc_value 1 target_TrqOviSta_Per	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_ESCSIate_State_enum target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_GMOSH_APAMS_Enable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_GMOSH_APAMS_Enable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_GMOSH_APAMS_Enable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_GMOSH_APAMS_Enable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_HapticRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_HapticRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_HapticRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_HapticRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_HapticRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_HapticRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_HapticRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_LKARaquest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_LKARaquest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_LKARaquest_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_MaxSecureVehicleSpeed_Kph_12i target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_MaxSecureVehicleSpeed_Kph_12i target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_Possevienable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_Possevienable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_Possevienable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_Possevienable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_Possevienable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_Possevienable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_Possevienable_Cnt_Igo target_Rie_Inst_Ap_TrqOvSia_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable_Cnt_Igo target_TrqOvSia_Per1_Possevienable	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _GMOSHOScillateState_State_enum target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _GMOSH _PSCMignable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _GMOSH _ESCMignable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _GMOSH _ESCMignable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _HwTorque_HwNn_f32 target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _HwTorque_HwNn_f32 target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _HxFault_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _LKAFault_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _LKAFault_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _LKAFault_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _LKARequest_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _LKAState_State_enum target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _MinSecureVehicleSpeed_Kph_f32 target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _MinSecureVehicleSpeed_Kph_f32 target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _DsSsoviEnable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosSroviEnable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _TosTrajEnable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _TosTrajEnable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _TosSeconderable_Ap. TrqOviSta. Perl _PosTrajEnable_Cnt_lgo target_Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _TosSeconderable_Ap. TrqOviSta. Perl _PosTrajEnable_Cnt_lgo target_Rie. Inst. Ap. TrqOviS	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_APAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HaviTorque_HwNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFaitt_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFaitt_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFaitt_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARSate_State_num target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajhwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajhwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajhwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajhwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajhwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCand_Minkm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCand_Minkm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCand_Minkm_f32 target_TrqOviSta_Perl_ApARecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Perl_ApARecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Perl_ApARecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Perl_CWFound_Cnt_lgc_value 1 target_TrqOviSta_Perl_CWFound_Cnt_lgc_value 1 target_TrqOviSta_Perl_GMOSH_APAMfgEnable_Cnt_lgc_value 1 target_TrqOviSta_Perl_GMOSH_APAMfgEnable_Cnt_lgc_value 1 target_TrqOviSta_Perl_GMOSH_APAMfgEnable_Cn	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HaptiRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HaptiRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARSault_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARSault_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARSault_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSroviEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajiEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SorTrqOviSta_Pert_PosTrajiHvAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SorTrqOviSta_Pert_PosTrajiHvAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOvScnp_Mrtvm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOvScnp_Mrtvm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOvScnp_Mrtvm_f32 target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOvScnp_Mrtvm_f32 target_Rie_Inst_Ap_TrqOviSta_Pert_ApANonecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_ApANonecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_ApANonecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_CWFound_Cnt_Igc_value target_TrqOviSta_Pert_Solialinited_Cnt_Igc_value targe	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enun$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOv/Sta_Perl_GMOSH_LKAMfgenable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_Perl_MaxSecureVehicleSpeed_Kpt_Sta_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MaxSecureVehicleSpeed_Kpt_Sta_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perl_MaxSecureVehicleSpeed_Kpt_Sta_Perl_MaxSecureVehicleSpeed	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rie_inst_Ap_TrqOviSta_Perl_HapticRequest_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_Perl_HavTorque_HwNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_TrqOviSta_Perl_MavSecurevehicleSpeed_Kph_f32 target_Rie_inst_Ap_TrqOviSta_TrqOviSta_Perl_MavSecurevehicleSpeed_Kph_f32 target_Rie_inst_Ap_TrqOviSta_Perl_Passeventehable_Cnt_lgc target_Rie_inst_Ap_TrqOviSta_Perl_SvARATrgdAngRequest_HvDeg_f32 target_Rie_inst_Ap_TrqOviSta_Perl_SvARATrgdAngRequest_HvDeg_f32 target_Rie_inst_Ap_TrqOviSta_Perl_SvARATrgdAngRequest_HvDeg_f32 target_Rie_inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitNm_f32 target_Rie_inst_Ap_TrqOviSta_Perl_Expansed target_TrqOviSta_Perl_ApARecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Perl_CWFound_Cnt_lgc_value 1 target_TrqOviSta_Perl_Expansed 1 target_TrqOviSta_Perl_Expansed 1 targ	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kpt_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kpt_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MarsecureVehicleSpeed_Kpt_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrevDehAvafie_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SVMARTrgtAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SVMARTrgtAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SVMARTrgtAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCannp_MtrNm_132 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOsCannp_MtrNm_132 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOsCannp_MtrNm_132 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOsCannp_MtrNm_132 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOsCannp_MtrNm_132 target_TrqOviSta_Per1_TrqOsCannp_MtrNm_132 target_TrqOviSta_Per1_TrqOsCannp_MtrNm_133 target_TrqOviSta_Per1_DrqOsCa	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKASequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MASecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServehable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServehable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServehable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServehable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServehable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SovARPTrqIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ShiftLeverIsinReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCAmp_MinM_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCAmp_MinM_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCAmp_MinM_f32 target_Rie_Inst_Ap_TrqOviSta_Perl_APARequest_Cnt_lgc_value target_TrqOviSta_Perl_TrqOsCAmp_MinM_f32 target_TrqOviSta_Perl_APARequest_Cnt_lgc_value target_TrqOviSta_Perl_APARequest_Cnt_lgc_value target_TrqOviSta_Perl_CWFoontion_HwDeg_f32_value target_TrqOviSta_Perl_CWFoontion_HwDeg_f32_value target_TrqOviSta_Perl_ScCalle_Interl_Cnt_lgc_value target_TrqOviSta_Perl_ESCE_Limited_Cnt_lgc_value target_TrqOviSta_Perl_ESCE_Limited_Cnt_lgc_value target_TrqOviSta_Perl_ScCalle_Interl_Cnt_lgc_value target_TrqOviSta_Perl_ScCalle_Interl_Cnt_lgc_value target_TrqOviSta_Perl_ScCalle_Interl_Cnt_lgc_value target_TrqOviSta_Perl_ScCalle_Interl_Cnt_lgc_value target_TrqOviSta_Perl_ScCalle_Interl_Cnt_lgc_value target_TrqOviSta_Perl_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAIRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAIRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosSrvorHwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajInBable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajInBable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajInBable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajInBable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_ShifitLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_TrqOvIsta_Perl_CowPosition_HwDeg_f32.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value tar	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAIRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_LKAIRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosSrvorHwangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajInBable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajInBable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajInBable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_PosTrajInBable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_ShifitLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_TrqOvIsta_Perl_TrqOsCenable_Cnt_Igc target_TrqOvIsta_Perl_CowPosition_HwDeg_f32.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value target_TrqOvIsta_Perl_EsCenable_Cnt_Igc.value tar	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt lgc	target TrqOvlSta Per1 LKAFault Cnt lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwaRtrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwaRtrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwaRtrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwaRtrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Pert_ApARecoverableFaults_Cnt_Igc_value 0 target_TrqOviSta_Pert_ApARecoverableFaults_Cnt_Igc_value 1 target_TrqOviSta_Pert_ApARecoverableFaults_Cnt_Igc_value 1 target_TrqOviSta_Pert_CwFound_Cnt_Igc_value 1 target_TrqOviSta_Pert_CwFound_Cnt_Igc_value 1 target_TrqOviSta_Pert_CwFound_Cnt_Igc_value 1 target_TrqOviSta_Pert_EsCFaul_Cnt_Igc_value 1 target_		
target_Rte_Inst_Ap_TrqOviSta_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SorTajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Stargetquest_HwDeg_f33 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Stargetquest_HwDeg_f33 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Stargetquest_HwDeg_f33 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCanp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCande_Ott_Igc_value target_TrqOviSta_Perl_APANonRecoverableFaults_Cnt_Igc_value 0 target_TrqOviSta_Perl_APARecoverableFaults_Cnt_Igc_value 1 target_TrqOviSta_Perl_CWPound_Cnt_Igc_value 1 target_TrqOviSta_Perl_CWPound_Cnt_Igc_value 1 target_TrqOviSta_Perl_CWPound_Cnt_Igc_value 1 target_TrqOviSta_Perl_CWPound_Cnt_Igc_value 1 target_TrqOviSta_Perl_ESCStaut_Cnt_Igc_value 1 target_TrqOviSta_Perl_ESCStaut_Cnt_Igc_value 1 target_TrqOviSta_Perl_ESCStautIc_Cnt_Igc_value 1 target_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrvorhwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrvorhwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCEnable_Cnt_lgc target_TrqOvlSta_Pert_Sta_DEnt_InqOsCEnable_Cnt_lgc target_TrqOvlSta_Pert_TrqOsCEnable_Cnt_lgc target_TrqOvlSta_Pert_ApARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Pert_ApARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Pert_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_SCSCault_Cnt_lgc.value 1 target_TrqOvlSta_Pert_ESCRault_Cnt_lgc.value 1 target_TrqOvlSta_Pert_ESCRault_Cnt_lgc.value 1 target_TrqOvlSta_Pert_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Pert_ESCReques		
target_Rte_Inst_Ap_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCFrae_Rt_f32 target_TrqOvISta_Per1_TrqOsCFrae_Rt_f32 target_TrqOvISta_Per1_ApAnonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFoostion_HwDeg_f32.value 499.040009 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRe		
target_Rte_Inst_Ap_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APARnecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value		
target_Rte_inst_Ap_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_inst_Ap_TrqOviSta_Per1_PosTrajEnable_Cnt_igc target_Rte_inst_Ap_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_inst_Ap_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_inst_Ap_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_inst_Ap_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_inst_Ap_TrqOviSta_Per1_SHiftLeverIsInReverse_Cnt_igc target_Rte_inst_Ap_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_igc target_Rte_inst_Ap_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_igc target_Rte_inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_inst_Ap_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_Rte_inst_Ap_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_TrqOviSta_Per1_TrqOscEnable_Cnt_igc target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_APARecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_APARecoverableFaults_Cnt_igc.value target_TrqOviSta_Per1_CCWFound_Cnt_igc.value target_TrqOviSta_Per1_CCWFound_Cnt_igc.value target_TrqOviSta_Per1_CWFound_Cnt_igc.value target_TrqOviSta_Per1_CWFound_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCFault_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value		
target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_EsCRequest_Cnt_Igc.value target_TrqOviSta_Per1_EsCRequest_Cnt_Igc.value target_TrqOviSta_Per1_EsCRequest_Cnt_Igc.value target_TrqOviSta_Per1_EsCRequest_Cnt_Igc.value target_TrqOviSta_Per1_EsCRequest_Cnt_Igc.value target_TrqOviSta_Per1_EsCRequest_Cnt_Igc.value target_TrqOviSta_Per1_EsCRequest_Cnt_Igc.value		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_Ap_AnonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1		V = 1
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1	· · · · · · · · · · · · · · · · · · ·	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFosition_HwDeg_f32.value 439.040009 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIs_Limited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value -978.51001 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 439.04009 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIs_Limited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value -978.51001 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 439.04009 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -978.51001 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 439.040009 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 439.040009 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 439.040009 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	439.040009
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
torret Tar Outline Pand ON ONL FROM Frankla Out I provide	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_1rqovista_Per1_Giviosh_EscivingEnable_Cnt_igc.value 1	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -10		-10
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0	· · · · · · · · · · · · · · · · · · ·	
target TrgOvlSta Per1 LKAInhibit Cnt Igc.value 0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 0	~ - ,	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 118.400002		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 128.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.189999998		
g vorial,	gsqs.nota_r orr oor.ajrim nigio_r moog_toz.value	1

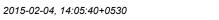
2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.5999999		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996	1713.95996 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	36400	36400 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002	1.73000002 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.114	0.114 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	12.7700005	12.7700005 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.18 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta APAState State M enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	14205.2402
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1801.80005
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13976.8203
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	38925
TrqOvlSta_Haptictime_Sec_M_f32	9588.80957
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	23.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.310000002
k_APAMaxHwTrq_HwNm_f32	1.60000002
k_APAMaxVehSpd_Kph_f32	36
k_ESCMaxVehSpd_Kph_f32	58.0999985
k_HapticAmplitude_MtrNm_f32	0.119999997
k_HapticDuration_Sec_f32	0.300000012
k_HapticFreq_Hz_f32	13.2840004
k_HapticReacttime_Sec_f32	0.319999993
k_LKAMaxVehSpd_Kph_f32	38
k_LKAMinVehSpd_Kph_f32	35
k_MaxHapticReqTime_Sec_f32	0.314999998
k_SWARLimiter_HwDeg_f32	30.2000008
k_ShiftLevRevTime_Sec_f32	0.0810000002
k_StandstillThresh_Kph_f32	0.800000012
k_StandstillTime_Sec_f32	0.0810000002
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enutarget TrqOvlSta_Per1_CCWFound Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target TrqOvISta Per1 CCWPosition HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	9_102	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lg	дс	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvISta_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKARequest Cnt Igc	target TrgOvISta Per1 LKARequest Cnt Ig	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-968.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	443.980011		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target TrqOvlSta Per1 LKAInhibit Cnt lqc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	123.300003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	133.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.100000001 5.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	1801.80005	1801.80005 ± 1	V
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0	0	V
TrqOvlSta_Hapticdur_Cnt_M_u16	38925	38925 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	9588.80957	9588.80957 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.33999991	2.33999991 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	V
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1	1 ± 1	V
TrqOvlSta_ExAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	23.2000008	23.2000008 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	23.2000008	23.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.119999997	0.119999997 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	13.2840004	13.2840004 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

lamo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvISta_APAState_State_M_enum	0
FrqOvISta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15094.9004
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1889.64001
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	14846.5596
FrqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	41450
rqOvlSta_Haptictime_Sec_M_f32	10073.6396
FrqOvISta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.95000005
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0482000001
FrqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	4
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992
FrqOvISta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.319999993
APAMaxHwTrq HwNm f32	1.67999995
CAPAMaxVehSpd_Kph_f32	37.7999992
ESCMaxVehSpd Kph f32	60.5
:_HapticAmplitude_MtrNm_f32	0.126000002
(_HapticDuration_Sec_f32	0.31499998
K_HapticFreq_Hz_f32	13.7980003
x_HapticReacttime_Sec_f32	0.335999995
x_LKAMaxVehSpd_Kph_f32	39.900015
C_KAMinVehSpd_Kph_f32	36.75
x_MaxHapticReqTime_Sec_f32	0.330000013
SWARLimiter_HwDeg_f32	31.6000004
_ShiftLevRevTime_Sec_f32	0.0850000009
_StandstillThresh_Kph_f32	0.839999974
_StandstillTime_Sec_f32	0.0850000009
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cr	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrgOvlSta Per1 ESCRequest Cnt lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESGState_State_enum arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igr	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvIsta_Per1_PosServEnable_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvlSta Per1 PosTrajEnable C		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		_,	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDe			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Ig			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_Mtrl	Nm_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_	f32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-958.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	448.920013		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	138		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	7.4000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Namo			I
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	•
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	0 3	3	•
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0 3 0	0 3 0±1	•
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0 3 0 0	0 3 0±1 0±1	•
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0 3 0 0	0 3 0±1 0±1	•
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0 3 0 0 0 1	0 3 0±1 0±1 0	•
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	0 3 0 0 0 0 1 41450	0 3 0±1 0±1 0 1 41450±1	•
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	0 3 0 0 0 1	0 3 0±1 0±1 0 1 41450±1 10073.6396±1	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	0 3 0 0 0 1 41450 10073.6396	0 3 0±1 0±1 0 1 41450±1	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0 3 0 0 0 1 41450 10073.6396 2.95000005	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc	0 3 0 0 0 1 41450 10073.6396 2.95000005	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	0 3 0 0 0 1 41450 10073.6396 2.95000005 1	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 0±1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.109999999	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 0±1 4 0.109999999±0.0009765625 0±1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.109999999 0	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 0±1 4 0.109999999±0.0009765625 0±1 0±1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.109999999 0 0	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 0±1 4 0.1099999999±0.0009765625 0±1 0±1 0	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.109999999 0 0 0 3	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 4 0.109999999±0.0009765625 0±1 0±1 0 3	
TrqOvIsta_APAState_State_M_enum TrqOvIsta_ESCState_State_M_enum TrqOvIsta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvIsta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvIsta_ElapsedStandstillTime_Sec_M_f32 TrqOvIsta_HapticState_State_M_enum TrqOvIsta_HapticState_State_M_enum TrqOvIsta_Hapticdur_Cnt_M_u16 TrqOvIsta_Haptictime_Sec_M_f32 TrqOvIsta_HartorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvIsta_LKAFault_Cnt_M_lgc TrqOvIsta_LKAPermFault_Cnt_M_lgc TrqOvIsta_LKAPermFault_Cnt_M_u08 TrqOvIsta_LKAPermFault_Cnt_M_u08 TrqOvIsta_LKAState_State_M_enum TrqOvIsta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvIsta_ReadytoPulse_Cnt_M_lgc target_TrqOvIsta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvIsta_Per1_APAState_State_enum.value target_TrqOvIsta_Per1_GMOSHOscillateState_State_enum.value	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.109999999 0 0 0 3 1	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.9500005±0.000009 1±1 0±1 4 0.109999999±0.0009765625 0±1 0±1 0 3 1	
TrqOvIsta_APAState_State_M_enum TrqOvIsta_EISCState_State_M_enum TrqOvIsta_EIapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvIsta_EIapsedShiftLevRevTime_Sec_M_f32 TrqOvIsta_EIapsedStandstillTime_Sec_M_f32 TrqOvIsta_HapticState_State_M_enum TrqOvIsta_HapticState_State_M_enum TrqOvIsta_Haptictime_Sec_M_f32 TrqOvIsta_Haptictime_Sec_M_f32 TrqOvIsta_Haptictime_Sec_M_f32 TrqOvIsta_HartorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvIsta_LKAFault_Cnt_M_lgc TrqOvIsta_LKAPermFault_Cnt_M_lgc TrqOvIsta_LKAPermFault_Cnt_M_u08 TrqOvIsta_LKAPermFault_Cnt_M_u08 TrqOvIsta_LKAState_State_M_enum TrqOvIsta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvIsta_ReadytoPulse_Cnt_M_lgc target_TrqOvIsta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvIsta_Per1_APAState_State_enum.value target_TrqOvIsta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvIsta_Per1_LKAState_State_enum.value	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.109999999 0 0 0 3 1 4	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 4 0.109999999±0.0009765625 0±1 0±1 0 3 1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticIme_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.1099999999 0 0 0 3 1 4 1	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 4 0.109999999±0.0009765625 0±1 0±1 0 3 1 4 1±1	
TrqOvISta_APAState_State_M_enum TrqOvISta_EISCState_State_M_enum TrqOvISta_EIapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_EIapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_EIapsedStandstillTime_Sec_M_f32 TrqOvISta_EIapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticOtl_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_LKAState_State_M_enum TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.109999999 0 0 3 1 4 1 0.109999999	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 0±1 4 0.109999999±0.0009765625 0±1 0 3 1 4 1±1 0.109999999±0.0009765625	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticIme_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	0 3 0 0 0 1 41450 10073.6396 2.95000005 1 0 0 4 0.1099999999 0 0 0 3 1 4 1	0 3 0±1 0±1 0 1 41450±1 10073.6396±1 2.95000005±0.000009 1±1 0±1 4 0.109999999±0.0009765625 0±1 0±1 0 3 1 4 1±1	Result

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V





Name Input Value Rte_Inst_Ap_TrqOvISta target_Rte_Inst_Ap_TrqOvISta TrqOvISta_APAState_State_Menum 4 TrqOvISta_ESCState_State_Menum 2 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 15984.5596 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 1977.47998 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 15716.2998 TrqOvISta_HapticState_State_M_enum 0 TrqOvISta_Hapticdur_Cnt_M_u16 43975 TrqOvISta_Haptictime_Sec_M_f32 10558.4697 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 3.55999994 TrqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32 0.0504000001 TrqOvISta_LKAFault_Cnt_M_igc 1 TrqOvISta_LKAPemFault_Cnt_M_igc 1	
frqOvISta_APAState_State_M_enum 4 frqOvISta_ESCState_State_M_enum 2 frqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 15984.5596 frqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 1977.47998 frqOvISta_ElapsedStandstillTime_Sec_M_f32 15716.2998 frqOvISta_HapticState_State_M_enum 0 frqOvISta_Hapticdur_Cnt_M_u16 43975 frqOvISta_Haptictime_Sec_M_f32 10558.4697 frqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 3.55999994 frqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32 0.0504000001 frqOvISta_LKAFault_Cnt_M_lgc 1 frqOvISta_LKAPermFault_Cnt_M_lgc 1	
TrqOvISta_ESCState_State_M_enum 2 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 15984.5596 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 1977.47998 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 15716.2998 TrqOvISta_HapticState_State_M_enum 0 TrqOvISta_Hapticdur_Cnt_M_u16 43975 TrqOvISta_Haptictime_Sec_M_f32 10558.4697 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 3.55999994 TrqOvISta_HwTorqueSV_HwNm_M_str.K_UIs_f32 0.0504000001 TrqOvISta_LKAFault_Cnt_M_lgc 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1	
frqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 15984.5596 frqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 1977.47998 frqOvISta_ElapsedStandstillTime_Sec_M_f32 15716.2998 frqOvISta_HapticState_State_M_enum 0 frqOvISta_Hapticdur_Cnt_M_u16 43975 frqOvISta_Haptictime_Sec_M_f32 10558.4697 frqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 3.55999994 frqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32 0.0504000001 frqOvISta_LKAFault_Cnt_M_lgc 1 frqOvISta_LKAPermFault_Cnt_M_lgc 1	
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 1977.47998 rqOvISta_ElapsedStandstillTime_Sec_M_f32 15716.2998 rqOvISta_HapticState_State_M_enum 0 rqOvISta_Hapticdur_Cnt_M_u16 43975 rqOvISta_Haptictime_Sec_M_f32 10558.4697 rqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 3.55999994 rqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32 0.0504000001 rqOvISta_LKAFault_Cnt_M_lgc 1 rqOvISta_LKAPermFault_Cnt_M_lgc 1	
rqOvISta_ElapsedStandstillTime_Sec_M_f32 15716.2998 rqOvISta_HapticState_State_M_enum 0 rqOvISta_Hapticdur_Cnt_M_u16 43975 rqOvISta_Haptictime_Sec_M_f32 10558.4697 rqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 3.55999994 rqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32 0.0504000001 rqOvISta_LKAFault_Cnt_M_lgc 1 rqOvISta_LKAPermFault_Cnt_M_lgc 1	
TqOvISta_HapticState_State_M_enum 0 43975 43975 10558.4697 10558.4697 10558_Hapticdur_State_HwNm_M_Str.SV_UIs_f32 3.55999994 10504000001 105040000001 105040000001 105040000000000000000000000000000000000	
frqOvISta_Hapticdur_Cnt_M_u16 43975 frqOvISta_Haptictime_Sec_M_f32 10558.4697 frqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 3.55999994 frqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32 0.0504000001 frqOvISta_LKAFault_Cnt_M_lgc 1 frqOvISta_LKAPermFault_Cnt_M_lgc 1	
rqOvISta_Haptictime_Sec_M_f32	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 3.55999994 0.0504000001 0.0504000001 TrqOvISta_LKAFault_Cnt_M_lgc 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1	
rqQvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32 0.0504000001 rqQvISta_LKAFault_Cnt_M_lgc 1 rqQvISta_LKAPermFault_Cnt_M_lgc 1	
rqOvlSta_LKAFault_Cnt_M_lgc 1 rqOvlSta_LKAPermFault_Cnt_M_lgc 1	
rqOvlSta_LKAPermFault_Cnt_M_lgc 1	
rqOvlSta_LKAPermFault_Cnt_M_u08 4	
rgOvISta_LKAState_State_M_enum 1	
rqOvISta_PosSrvoHwAngle_HwDeg_M_f32 25.3999996	
qOvISta_ReadytoPulse_Cnt_M_lgc 1	
APAIncludeHaptic_Cnt_lgc 0	
_APAMaxHwTrqTime_Sec_f32 0.330000013	
APAMaxHwTrq_HwNm_f32 1.75999999	
_ESCMaxVehSpd_Kph_f32 62.9000015	
_HapticAmplitude_MtrNm_f32 0.131999999	
_HapticDuration_Sec_f32 0.330000013	
_HapticFreq_Hz_f32 14.3120003	
_HapticReacttime_Sec_f32 0.351999998	
_LKAMaxVehSpd_Kph_f32 41.7999992	
_LKAMinVehSpd_Kph_f32 38.5	
_MaxHapticReqTime_Sec_f32 0.344999999	
_SWARLimiter_HwDeg_f32 33	
_ShiftLevRevTime_Sec_f32 0.0890000015	
_StandstillThresh_Kph_f32 0.879999995	
_StandstillTime_Sec_f32 0.0890000015	
arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_ig target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_igc	
arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_APAState_State_enum	
target_TrqOvISta_PrqOvISta_Per1_CCWFound_Cnt_Igc target_TrqOvISta_Per1_CCWFound_Cnt_Igc	
target_TrqOvISta_TrqOvISta_Per1_CCWPosition_HwDeg_f32 arget_TrqOvISta_Per1_CCWPosition_HwDeg_f32	
arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32	
target_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_TrqOvISta_Per1_ESCFault_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESClsLimited_Cnt_Igc	
rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc	
rrget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_ESCState_State_enum	
rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_state_enum target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum	
rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
rrget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc	
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 HwTorque HwNm f32 target TrgOvlSta Per1 HwTorque HwNm f32	
· · - · - · - · - · - · - · - · -	
rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta.TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_LKAState_State_enum	
target_TrqOvISta_Per1_LKAFault_Cnt_lgc target_TrqOvISta_Per1_LKAFault_Cnt_lgc target_TrqOvISta_Per1_LKAFault_Cnt_lgc target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKAState_State_enum target_TrqOvISta_Per1_LKAState_State_enum target_TrqOvISta_Per1_LKAState_State_enum target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc	
target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_TrqO	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc tar	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rget	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_Trq	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-948.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	453.859985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.4000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	9.30000019		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15984.5596	15984.5596 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1977.47998	1977.47998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	15716.2998	15716.2998	✓
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	43975	43975 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.60233593	3.60233593 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.3999996	25.3999996 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target TrqOvlSta Per1 GMOSHOscillateState State enum.value	0	0	~
3.5 1		4	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4		
	0	0 ± 1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value		0 ± 1 25.3999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0		*
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 25.3999996	25.3999996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16874.2207
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	16586.0391
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	46500
TrqOvlSta_Haptictime_Sec_M_f32	11043.2998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	26.5

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.340000004
k_APAMaxHwTrq_HwNm_f32	1.84000003
k APAMaxVehSpd Kph f32	41.4000015
k_ESCMaxVehSpd_Kph_f32	65.3000031
k HapticAmplitude MtrNm f32	0.137999997
k_HapticDuration_Sec_f32	0.344999999
k HapticFreq Hz f32	14.8260002
k HapticReacttime Sec f32	0.368000001
k_LKAMaxVehSpd_Kph_f32	43.7000008
k LKAMinVehSpd Kph f32	40.25
k_MaxHapticReqTime_Sec_f32	0.360000014
k SWARLimiter HwDeg f32	34.4000015
k_ShiftLevRevTime_Sec_f32	0.0930000022
k StandstillThresh Kph f32	0.920000017
k_StandstillTime_Sec_f32	0.0930000022
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-938.109985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	458.799988
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.4000001
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	138
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007	2065.32007 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	46500	46500 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008	4.17000008 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.137999997	0.137999997 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	14.8260002	14.8260002 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	17763.8809
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2153.15991
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	17455.7793
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	49025
TrqOvlSta_Haptictime_Sec_M_f32	11528.1299
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.78000021
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.349999994
k_APAMaxHwTrq_HwNm_f32	1.91999996
k_APAMaxVehSpd_Kph_f32	43.2000008
k_ESCMaxVehSpd_Kph_f32	67.6999969
k_HapticAmplitude_MtrNm_f32	0.143999994
k_HapticDuration_Sec_f32	0.360000014
k_HapticFreq_Hz_f32	15.3400002
k_HapticReacttime_Sec_f32	0.384000003
k_LKAMaxVehSpd_Kph_f32	45.5999985
k_LKAMinVehSpd_Kph_f32	42
k_MaxHapticReqTime_Sec_f32	0.375
k_SWARLimiter_HwDeg_f32	35.7999992
k_ShiftLevRevTime_Sec_f32	0.0970000029
k_StandstillThresh_Kph_f32	0.959999979
k_StandstillTime_Sec_f32	0.0970000029
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H	- ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvlSta Per1 PosTrajEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	463.73999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	142.899994		
target TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	152.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target TrgOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	- Troount
TrqOvISta_ESCState_State_M_enum	4	4	·
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	17763.8809	17763.8809 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	-
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	0	0	·
TrqOvISta_Hapticdur_Cnt_M_u16	49025	49025 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	11528.1299	11528.1299 ± 1	·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4.38105631	4.38105583 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	4	4 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	_
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004	27.6000004 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
			-

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	27.6000004	27.6000004 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.143999994	0.143999994 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.3400002	15.3400002 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	•
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

	Test Step 2.23 (Repeat Count = 1)	v v
Interpolation Interpolatio		Input Value
TOO/SSE SESSINE State M. enum O TOO/SSE ElapsedAPAMANHA VTG1 (m. Sec. M. 52 TOO/SSE ElapsedAPAMANHA VTG1 (m. Sec. M. 52 TOO/SSE ElapsedBML enver Time, Sec. M. 52 TOO/SSE LapsedBML State M. enum TOO/SSE LAPSEDBML Enver Time, Sec. M. 52 TOO/SSE LAPSEDBML Enver Time, Sec. 52 A. PAPAMANHA TIME, TIME, Sec. 52 A. PAPA		· ·
TOO/SIS_ESCHEAMANASHINGTINE, See, M. F02 TOO/SIS_ESPEADAMASHINGTINE, See, M. F02 TOO/SIS_ESPEADAMASHINGTINE, See, M. F02 TOO/SIS_ESPEADAMASHINGTINE, See, M. F02 TOO/SIS_ESPEADAMASHINGTINE, See, M. F02 TOO/SIS_Hapitoday, Crit, M. Life TOO/SIS_HAPITOQASS_HAMMA, M. Sir. SV, Uis, 52 TOO/SIS_HAPITOQASS_HAMMA, M. Sir. SV, Uis, 52 TOO/SIS_LAKFault, Crit, M. Joe TOO		
TropOSIs EapsedStandsITTime, Sec, M, 132 18853-391 170pOSIs EapsedStandsITTime, Sec, M, 132 18325-5195 170pOSIs EapsedStandsITTime, Sec, M, 132 18325-5195 170pOSIs HapticRate, State, M, enum		
TrigoviSta Elapsed ShiftLoreName Sec. M. 132 18325-195 1	·	
TropVisia_Lapscbala_state_N_mum		
1		
TrigOMSis Hapticotur_Cnt_M_u16 TrigOMSis Mapticotur_See_M_S2 TrigO		
TropVisia phytriongueSV_Hwhm_M_Str.SV_Uis_f32 TropVisia_HwTrongueSV_Hwhm_M_Str.SV_Uis_f32 TropVisia_LwTrongueSV_Hwhm_M_Str.Luis_f32 TropVisia_LwTrongueSV_Hwhm_MM_Str.Luis_f32 TropVisia_LwTrongueSV_HwmM_Str.Luis_f32 TropVisia_LwTropVisia_LwTrongueSvalueSV_Hwhm_Mm_Str.Luis_f32 TropVisia_LwTropVisia		
TrigoVisia_LixAFault_Cnt_Migs		
TrqOvSta_LKAFaut_Cnt_M_gc		
TropONSb_LKAPemFault_Cnt_M_lgc		
TrigOviSta_LKAPermFault_Cnt_M_u08		
TrqONSIa_LKAStels_State_M_enum		
TrqOVISIa_LKAState_State_M_enum		
Triqovista_Possvothwangle_HwDeg_M_f32		
TrqOvISta_ReadytoPulse_Cnt_M_lgc		
k APA/AncludeHaptic Cnt Ligo k APA/AnchurtyTrime_Sec_f32 k APA/AnchurtyTrime_Sec_f32 k APA/AnchurtyTrime_Sec_f32 k APA/AnchurtyThy HwNm_f32 k APA/AnchurtyThy HwNm, F32 k APA/AnchurtyThy HwNm, F32 k APA/AnchurtyThy HwNm_f32 k APA/AnchurtyThy HwNm, F32 k APA/AnchurtyThy HwN		
K_APAMaxHwTrqTime_Sec_f32 K_APAMaxHwTrq_HwNm_f32 K_APAMaxWehSpd_Kph_f32 K_ESCMaxVehSpd_Kph_f32 K_ESCMaxVehSpd_Kph_f32 K_Bapichamplitude_MirNm_f32 K_Hapitchamplitude_MirNm_f32 K_HAMplitude_MirNm_f32 K_HA		
K, APAMaxHwTrq, HwNm f32 2 K, APAMaxVehSpd, Kph, f32 45 K, ESCMaxVehSpd, Kph, f32 70,0999985 k, HapticChuration, Sec, f32 0,150000006 k, HapticDuration, Sec, f32 0,375 k, HapticReacttime, Sec, f32 0,400000006 k, LKAMAVehSpd, Kph, f32 47.5 k, LKAMIvSpd, Kph, f32 43.75 k, LKAMIvENSpd, Kph, f32 43.75 k, SWANELImiter, HwDeg, f32 0,389999986 k, SWINELIMER, HwDeg, f32 0,101000004 k, StindestillThresh, Kph, f32 0,101000004 k, StandstillThresh, Kph, f32 0,10100004 k, StandstillThr		
K_APAMaxVehSpd_Kph_f32		
K_BSCMaxVehSpd_Kph_132 K_HapticAmplitude_MtrNm_132 0.150000006 K_HapticAmplitude_MtrNm_132 0.150000006 K_HapticFeq_Hz_132 0.375 K_HapticFeq_Ltz_132 15.8540001 0.400000006 K_LKAMaxVehSpd_Kph_132 47.5 K_LKAMaxVehSpd_Kph_132 47.5 K_LKAMaxVehSpd_Kph_132 47.5 K_LKAMinVehSpd_Kph_132 47.5 K_SAMSVENSPD_Kph_132 0.389999986 K_SWARRLimiter_HwDeg_132 K_SWARRLimiter_HwDeg_132 K_SWARRLimiter_HwDeg_132 K_SMARLimiter_HwDeg_132 K_SHandstillTrine_Sec_132 0.101000004 K_StandstillTrine_Sec_132 0.101000004 K_StandstillTrine_Sec_132 0.101000004 K_StandstillTrine_Sec_132 0.101000004 K_StandstillTres_Sec_132 0.101000004 K_StandstillTres_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APARovista_Per_1_APA		
K_HapticRamplitude_MtrNm_f32	_ : -: -	
K_HapticDuration_Sec_f32 K_HapticFreq_Hz_f32 K_HapticFreq_Hz_f32 K_BapticFreq_Hz_f32 K_HapticFreq_Hz_f32 K_HapticFreq_Hz_f32 K_LKAMinVehSpd_Kph_f32 K_LKAMinVehSpd_Kph_f32 K_LKAMinVehSpd_Kph_f32 K_LKAMinVehSpd_Kph_f32 K_LKAMinVehSpd_Kph_f32 K_MaxHapticReqTime_Sec_f32 M_SSMARLimiter_HwDeg_f32 K_SWARLimiter_HwDeg_f32 K_SMARLimiter_HwDeg_f32 K_StandstillTime_Sec_f32 D.101000004 K_StandstillTime_Sec_f32 D.101000004 K_StandstillTime_Sec_f32 D.101000004 M_StandstillTime_Sec_f32 M_StandstillTime_Sec_f32 D.101000004 M_StandstillTime_Sec_f32 M_StandstillTime_Sec_f32 D.101000004 M_StandstillTime_Sec_f32 M_StandstillTime_Sec_f32 M_StandstillTime_Sec_f32 M_StandstillTime_Sec_f32 M_StandstillTime_Sec_f32 D.101000004 M_StandstillTime_Sec_f32 M_StandstillTime_f3c_f32 M_StandstillTime_Sec_f32 M_StandstillT		
k_HapticFreq_Hz_f32 k_HapticFreq_Hz_f32 k_HapticFreq_Hz_f32 k_HapticReacttime_Sec_f32 k_LKAMaxVehSpd_Kph_f32 k_LKAMaxVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32 d_3.75 d_38999986 k_SWARLimiter_HwDeg_f32 d_3.7200008 k_StindstillThresh_Kph_f32 k_StindstillThresh_Kph_f32 k_StindstillThresh_Kph_f32 d_101000004 k_StandstillTime_Sec_f32 d_101000004 target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_APADrivrinterventionDetected_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqoVista_TrqoVista_Perl_ESCRequest_Cnt_lgc target_TrqoVista_Perl_ESCRequest_Cnt_lgc target_TrqoVista_Perl_ESCRequest_Cnt_lgc target_TrqoVista_Perl_ESCRequest_Cnt_lgc target_TrqoVista_Perl_ESCRequest_Cnt_lgc target_TrqoVista_Perl_ESCRequest_Cnt_lgc target_TrqoVista_Perl_ESCRequest_Cnt_lgc target_TrqoVista_Perl_ESCReduest_Cnt_lgc targ		
K_HapticReactitime_Sec_f32 k_LKAMiaxVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32 d. 389999966 k_SWARLimiter_HwDeg_f32 k_SMRLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32 d. 101000004 k_StandstillThresb_Kph_f32 k_StandstillThresb_Kph_f32 k_StandstillTime_Sec_f32 d. 101000004 target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_APANorRecoverableFaults_cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_APANorRecoverableFaults_cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_APARequest_Cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_APAState_State_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCStaut_Cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCScaut_Cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCScaut_Cnt_igc target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_DSCIIlateIState_State_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_DSCIIlateIState_Enum_ target_TrqOvISta_Per1_ESCState_State_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc target_TrqOvISta_Per1_ESCMState_State_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc target_TrqOvISta_Per1_ESCMState_State_enum target_Re_inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc target_		
k_LKAMaxVenSpd_Kph_f32 k_LKAMinVenSpd_Kph_f32 k_KamAxHapticReqTime_Sec_f32 k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32 d_SWARLimiter_HwDeg_f32 d_SWARLimiter_HwDeg_f33 d_SWARLimiter_HwDeg_f34 d_SWARLimiter_HyDeg_f34 d_SWARLimiter_HyDeg_f34 d_SWARLimiter_HyDeg_f34 d_SWARLimiter_HyDeg_f34		
k_KKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32 target_Rel_Inst_Ap_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_Pre1_CWPosition_HwDeg_f32 target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPosition_HwDeg_f32 target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRault_Cnt_lgc target_Rel_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rel_Inst_Ap_TrqO		
k_MaxHapticReqTime_Sec_f32 k_SMARLimiter_HwDeg_f32 k_StandstillTrime_Sec_f32 k_StandstillTrime_Sec_f32 larget_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARcoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARcoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARcoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARcoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARcoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARcoverableFaults_Cnt_lgc target_TrqOviSta_Per1_APARcoverableFaults_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_APARcoverableFaults_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_CWFound_Cnt_lgc target_TrqOviSta_Per1_ESCFault_Cnt_lgc target_TrqOviSta_Per1_ESCFault_Cnt_lgc target_TrqOviSta_Per1_ESCFault_Cnt_lgc target_TrqOviSta_Per1_ESCFault_Cnt_lgc target_TrqOviSta_Per1_ESCFault_Cnt_lgc target_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_TrqOviSta_Per1_E	- : - : - : - : - : - : - : - : - : - :	
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32 k_StandstillTimes_Kph_f32 L_K_StandstillTimes_Sec_f32 L_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APAState_State_enum Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APAState_State_enum Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWPosition_HwDeg_f32 Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCSLimited_Ent_lgc Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_DScillateState_enum Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_DScillateState_enum Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_DScillateState_enum Larget_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_PAMfigEnable_Cnt_lgc Larget_TrqOvISta_Per1_GMOSH_ESCMfigEnable_Cnt_lgc Larget_TrqOvISta_Per1_GMOSH_ESCMfigEnable_Cnt_lgc Larget_TrqOvISta_Per1_GMOSH_ESCMfigEnable_Cnt_lgc Larget_TrqOvISta_Per1_GMOSH_ESCMfigEnable_Cnt_lgc Larget_TrqOvISta_Per1_GMOSH_ESCMfigEnable_Cnt_lgc Larget_		
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32 1 0.101000004 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMO		
k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOSHOSillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOSHOSillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOSHOSillateState_enum_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOSHOSEC_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_target_TrqOvlSta_	- · · · · · · · · · · · · · · · · · · ·	
k_StandstillTime_Sec_f32 0.101000004 target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_state_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_Sta_TrqOvlSta_Per1_GMOSH_Sta_TrqOvlSta_Per1_GMOSH_Sta_TrqOvlSta_Per1_GMOSH_Sta_TrqOvlSta_Per1_GMOSH_Sta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMO		
target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_state_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOSCIBLateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOSCIBLateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOSCIBLateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOSCIBLateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	· · · · · ·	
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnab		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc		
raiget_ivie_inist_Ap_indoviola_reii_mapticheduest_ont_igc laiget_indoviola_reii_mapticheduest_ont_igc		
target Dte Inct An TraQuiSta TraQuiSta Peril HwTorque HwNm f22		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_HwTorque_HwNm_f32	target_Inte_inst_Ap_Tryoviota.Tryoviota_PerT_nwTorque_nwiviii_i32	talget_11qOviota_Fe11_ftw101que_ftw10111_102

2015-02-04, 14:05:40+0530





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-917.909973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 468.679993		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_rer1_ESCFault_Crit_tgc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_E3CRequest_Crit_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-2.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	147.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	51550	51550 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987	5.38999987 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	V
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1±1	- 4
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	1	1	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	-
target_rqoviSta_Peri_PosServEnable_Cnt_igc.value target_TrqOviSta_Peri_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	-
target_TrqOviSta_Per1_PosStvonwArigie_nwbeg_isz.value target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000006	0.150000006 ± 0.0009765625 0.150000006 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.8540001	15.8540001 ± 0.0009765625	
target rigoticula i ori i rigotori rog i iz 102. Valub	10.00 1000 1	. 0.00 TOOO 1 ± 0.00007 000E0	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.24 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrgOvISta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	19543.1992
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2328.84009
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	19195.2598
TrqOvlSta_HapticState_State_M_enum	2
TrqOvISta_Haptictime_See M #22	54075 12497.79
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvISta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1 0.370000005
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	2.07999992
k_APAMaxVehSpd_Kph_f32	46.7999992
k_ESCMaxVehSpd_Kph_f32	72.5
k_HapticAmplitude_MtrNm_f32	0.156000003
k_HapticDuration_Sec_f32	0.38999986
k_HapticFreq_Hz_f32	16.368
k_HapticReacttime_Sec_f32	0.416000009
k_LKAMaxVehSpd_Kph_f32	49.4000015
k_LKAMinVehSpd_Kph_f32	45.5
k_MaxHapticReqTime_Sec_f32	0.405000001
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	38.5999985 0.104999997
k StandstillThresh Kph f32	1.0399996
k StandstillTime Sec f32	0.104999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_inst_Ap_irqovista.irqovista_Peri_irqoscAmp_mtrnm_i32 target_Rte_inst_Ap_TrqoviSta.TrqoviSta_Peri_TrqOscEnable_Cnt_lgc	target_IrqOviSta_Peri_IrqOvsCamp_intrivm_f32 target_TrqOviSta_Peri_TrqOvsCEnable_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
<u> 3.2 4 2 </u>	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-907.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	473.619995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	152.699997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.8999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2328.84009	2328.84009 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	54075	54075 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6	6 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992	29.7999992 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target TrqOvlSta Per1 LKAState State enum.value	4	4	✓
	0	0 ± 1	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	U		
	29.7999992	29.7999992 ± 0.0009765625	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	-	29.7999992 ± 0.0009765625 0.156000003 ± 0.0009765625	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	29.7999992		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.25 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	20432.8594
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2416.67993
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	20065
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	56600
TrqOvlSta_Haptictime_Sec_M_f32	12982.6201
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.61000013
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0614
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	30.8999996

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.37999995
k_APAMaxHwTrq_HwNm_f32	2.16000009
k_APAMaxVehSpd_Kph_f32	48.5999985
k_ESCMaxVehSpd_Kph_f32	74.9000015
k HapticAmplitude MtrNm f32	0.162
k_HapticDuration_Sec_f32	0.405000001
k_HapticFreq_Hz_f32	16.882
k_HapticReacttime_Sec_f32	0.432000011
k_LKAMaxVehSpd_Kph_f32	51.2999992
k LKAMinVehSpd Kph f32	47.25
k_MaxHapticReqTime_Sec_f32	0.419999987
k SWARLimiter HwDeg f32	40
-	
k_ShiftLevRevTime_Sec_f32	0.108999997
k_StandstillThresh_Kph_f32	1.08000004 0.108999997
k_StandstillTime_Sec_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_state_enumerators and the property of the pr$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ShiftLeverIsInReverse Cnt Igc	
	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-897.710022
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	478.559998
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.8999998
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlota_rer1_Ervarvequest_Ont_ge.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.399994
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
g.sqo.nota_r or i_r oo raji iiw iigio_r iibog_loz.valido	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	✓
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	20432.8594	20432.8594 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	20065	20065	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	56600	56600 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	12982.6201	12982.6201 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.08748627	6.08748579 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.162	0.162 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	16.882	16.882 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.26 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21322.5195
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2504.52002
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	20934.7402
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	59125
TrqOvlSta_Haptictime_Sec_M_f32	13467.4502
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.21999979
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0636000037
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	32
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.389999986
k_APAMaxHwTrq_HwNm_f32	2.24000001
k_APAMaxVehSpd_Kph_f32	50.4000015
k_ESCMaxVehSpd_Kph_f32	77.3000031
k_HapticAmplitude_MtrNm_f32	0.167999998
k_HapticDuration_Sec_f32	0.419999987
k_HapticFreq_Hz_f32	17.3959999
k_HapticReacttime_Sec_f32	0.448000014
k_LKAMaxVehSpd_Kph_f32	53.2000008
k_LKAMinVehSpd_Kph_f32	49
k_MaxHapticReqTime_Sec_f32	0.435000002
k_SWARLimiter_HwDeg_f32	41.4000015
k_ShiftLevRevTime_Sec_f32	0.112999998
k_StandstillThresh_Kph_f32	1.12
k_StandstillTime_Sec_f32	0.112999998
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_ target_TrqOvlSta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	52	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	rc	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvlSta Per1 PosTrajEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	483.5		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value			
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.70000005 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta_ren_ErxRequest_Cnt_igc.value target TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	172.300003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	- TOOUIT
TrqOvISta_ESCState_State_M_enum	4	4	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	2	2	V
TrqOvISta_Hapticdur_Cnt_M_u16	59125	59125 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	V
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.21999979	7.21999979 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	V
TrqOvISta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	32	32 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	32	32 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.167999998	0.167999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	17.3959999	17.3959999 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

ame	Input Value
te_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
qOvlSta_APAState_State_M_enum	2
QOVISta_ESCState_State_M_enum	4
OvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	22212.1797
OvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2592.36011
QOVISta_ElapsedStandstillTime_Sec_M_f32	21804.4805
QOVISta_HapticState_State_M_enum	1
OvlSta_Hapticdur_Cnt_M_u16	61650
OvlSta_Haptictime_Sec_M_f32	13952.2803
OvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.82999992
OvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0658000037
OvISta_LKAFault_Cnt_M_lgc	1
OvlSta_LKAPermFault_Cnt_M_lgc	1
OvlSta_LKAPermFault_Cnt_M_u08	4
OvlSta_LKAState_State_M_enum	2
OvlSta_PosSrvoHwAngle_HwDeg_M_f32	33.099985
OvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	0.40000006
APAMaxHwTrq_HwNm_f32	2.31999993
APAMaxVehSpd_Kph_f32	52.2000008
ESCMaxVehSpd_Kph_f32	79.6999969
HapticAmplitude_MtrNm_f32	0.173999995
HapticDuration_Sec_f32	0.435000002
HapticFreq_Hz_f32	17.9099998
HapticReacttime_Sec_f32	0.463999987
_KAMaxVehSpd_Kph_f32	55.0999985
_KAMinVehSpd_Kph_f32	50.75
MaxHapticReqTime_Sec_f32	0.449999988
SWARLimiter_HwDeg_f32	42.7999992
ShiftLevRevTime_Sec_f32	0.116999999
StandstillThresh_Kph_f32	1.15999997
StandstillTime_Sec_f32	0.116999999
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_C	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_en	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lg	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lg	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Ig	c target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H target TrqOvlSta Per1 PosTrajEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvISta_Fe11_F0STrajHwAngle Hv	- -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.		·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-877.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	488.440002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1		
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	167.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	177.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.6000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1	le	l
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum	3 4	3	-
TrqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrqOvISta ElapsedShiftLevRevTime Sec M f32	2592.36011	2592.36011 ± 1	-
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	1	1	~
TrqOvlSta Hapticdur Cnt M u16	61650	61650 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	13952.2803	13952.2803 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.82999992	7.82999992 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.18999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓ ✓
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	3	3 1±1	-
target_IrqOviSta_Peri_PosServEnable_Cnt_lgc.value target TrqOviSta_Peri_PosSrvoHwAngle HwDeg f32.value	0.189999998	0.189999998 ± 0.0009765625	-
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.173999995	0.173999995 ± 0.0009765625	-
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	17.9099998	17.9099998 ± 0.0009765625	-
○	1 1111		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Fest Step 2.28 (Repeat Count = 1)	Input Value
	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
	4
	0
, _ , _ , ,	23101.8398
	2680.19995
. = . = = =	22674.2207
1	0
rqOvlSta_Hapticdur_Cnt_M_u16	64175
rqOvlSta_Haptictime_Sec_M_f32	14437.1104
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.43999958
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0680000037
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	1
rqOvlSta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
	0.40999996
·	2.4000001
	54
	82.0999985
_ , _ , _	0.180000007
	0.449999988
	18.4239998
- ·	0.47999989
	57
- '-'-	
	52.5
	0.465000004
	44.200008
	0.120999999
	1.20000005
	0.120999999
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_enum
	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target TrqOvISta_Per1_GWoSt1_ERAWIgEtrable_CIT_gc
0 = = = 1	target_rrqOvlSta_Per1_HapticRequest_Ont_lgc
	· - ·
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
	tanget_indexicat_i eni_enn arriga angredaest_imbeg_iez
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32

2015-02-04, 14:05:40+0530





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-867.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	493.380005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	172.300003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	182.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	23101.8398	23101.8398 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	64175	64175 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.77767944	7.77767992 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvISta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008	34.2000008 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	34.2000008	34.2000008 ± 0.0009765625	~
target_11q0viota_1 et 1_1 030tvot tw/Angle_11wbeg_132.value			
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.180000007	0.180000007 ± 0.0009765625	-
	0.180000007	0.180000007 ± 0.0009765625 0 ± 1	Ž

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.29 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	2	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	23991.5	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2768.04004	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	23543.9609	
TrqOvlSta_HapticState_State_M_enum	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	2450	
TrqOvlSta_Haptictime_Sec_M_f32	14921.9404	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0702000037	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	
TrqOvlSta_LKAState_State_M_enum	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	35.2999992	

2015-02-04, 14:05:40+0530





· -	
Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.419999987
k_APAMaxHwTrq_HwNm_f32	2.48000002
k_APAMaxVehSpd_Kph_f32	55.7999992
k_ESCMaxVehSpd_Kph_f32	84.5
k_HapticAmplitude_MtrNm_f32	0.186000004
k_HapticDuration_Sec_f32	0.465000004
k_HapticFreq_Hz_f32	18.9379997
k_HapticReacttime_Sec_f32	0.495999992
k_LKAMaxVehSpd_Kph_f32	58.9000015
k_LKAMinVehSpd_Kph_f32	54.25
k_MaxHapticReqTime_Sec_f32	0.479999989
k_SWARLimiter_HwDeg_f32	45.5999985 0.125
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32	1.24000001
k_StandstillTime_Sec_f32	0.125
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrqOvISta Per1 APAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrgOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-857.309998
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	498.320007
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_IrqOviSta_Peri_GMOSH_ESCMitgEnable_Cnt_igc.value target TrqOviSta_Peri_GMOSH_LKAMfgEnable_Cnt_igc.value	1
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value	0
target TrqOvISta_Fer1_Habitckequest_Orit_igc.value	-1.10000002
target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value	1
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	177.199997
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	187
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999

© Report created by TESSY V3.1.9, report template V2.1

62

2015-02-04, 14:05:40+0530





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2768.04004	2768.04004 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	2450	2450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019	9.05000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.186000004	0.186000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	18.9379997	18.9379997 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.30 (Repeat Count = 1) Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrgOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvISta_ESCState_State_M_enum	2
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	24881.1602
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	2855.87988
TrgOvISta ElapsedStandstillTime Sec M f32	24413,6992
TrqOvISta HapticState State M enum	2
TrqOvISta Hapticdur Cnt M u16	8460
TrqOvlSta Haptictime Sec M f32	15406.7695
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	9.65999985
TrqOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.0724000037
TrgOvISta LKAFault Cnt M lgc	1
TrqOvlSta LKAPermFault Cnt M lgc	1
TrqOvISta LKAPermFault Cnt M u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvISta PosSrvoHwAngle HwDeg M f32	36.4000015
TrqOvISta ReadytoPulse Cnt M lgc	1
k APAIncludeHaptic Cnt Igc	1
k APAMaxHwTrqTime Sec f32	0.430000007
k APAMaxHwTrq HwNm f32	2.55999994
k APAMaxVehSpd Kph f32	57.5999985
k ESCMaxVehSpd Kph f32	86.900015
k HapticAmplitude MtrNm f32	0.192000002
k HapticDuration Sec f32	0.47999989
k HapticFreq Hz f32	19.4519997
k HapticReacttime Sec f32	0.512000024
k LKAMaxVehSpd Kph f32	60.7999992
k LKAMinVehSpd Kph f32	56
k MaxHapticReqTime Sec f32	0.495000005
k_SWARLimiter_HwDeg_f32	47
k ShiftLevRevTime Sec f32	0.128999993
k StandstillThresh Kph f32	1.27999997
k_StandstillTime_Sec_f32	0.128999993
	nDetected Cnt target TrgOvlSta Per1 APADrvrInterventionDetected Cnt lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	IC	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target TrqOvlSta Per1 PosTrajHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3		-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrgOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-847.210022		
target TrqOvlSta Per1 CWFound Cnt Igc.value	0		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	503.26001		
	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.899999976		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	191.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	28.2999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0	1	
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	-
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	24413.6992	24413.6992	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	8460	8460 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	15406.7695	15406.7695 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.65999985	9.65999985 ± 0.000009	•
		0 ± 1	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc		1 ± 1	
	0		~
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 1 2	1 ± 1 2 ± 1	•

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	36.4000015	36.4000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.192000002	0.192000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	19.4519997	19.4519997 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.31 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrgOvISta
TrqOvISta_APAState_State_M_enum	1
TrqOvISta_St Actate_Clate_M_critish	3
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	25770.8203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2943.71997
TrqOvlSta ElapsedStandstillTime Sec M f32	25283.4395
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta Hapticdur Cnt M u16	14470
TrqOvlSta_Haptictime_Sec_M_f32	15891.5996
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	9.97999954
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0746000037
TrgOvISta_HW10Ique3v_HW11II_W_3ti.N_0is_i32	1
,	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvISta_LKAState_State_M_enum	2
TrqOvlSta_ExAState_state_in_endin TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	37.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k APAIncludeHaptic Cnt Igc	1
k APAMaxHwTrqTime Sec f32	0.43999998
k APAMaxHwTrq HwNm f32	2.6400001
	59.400001
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	89.3000031
k HapticAmplitude MtrNm f32	0.197999999
	0.495000005
k_HapticForg Hz #22	19.9659996
k_HapticPreq_Hz_f32	0.527999997
k_HapticReacttime_Sec_f32 k_LKAMaxVehSpd_Kph_f32	62.7000008
_ : -: -	57.75
k_LKAMinVehSpd_Kph_f32	0.50999999
k_MaxHapticReqTime_Sec_f32	
k_SWARLimiter_HwDeg_f32	48.4000015 0.133000001
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32	1.32000005
k_StandstillTime_Sec_f32	0.133000001
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSHOscillateState_State_enur	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
© Report created by TESSY V3.1.9, report template V2.1	65

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3		_ <u>-</u> _ <u>-</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-837.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	508.200012		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.69999988		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	187		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	196.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0.129999995		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.2000008		
target TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	Result
TrgOvISta ESCState State M enum	3	3	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	25770.8203	25770.8203 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	V
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	14470	14470 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	15891.5996	15891.5996 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.18327141	9.18327236 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1±1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.197999999	0.197999999 ± 0.0009765625 0 ± 1	-
target_TrqOviSta_Per1_TrqOscEnable_Crit_igc.value target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	19.9659996	19.9659996 ± 0.0009765625	-
Larget_1140 viola_1 of 1_1140361 req_112_102.value	10.000000	10.0000000 ± 0.00007 00020	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Input Value target_Rte_Inst_Ap_TrqOvlSta 2 4 26660.4805 3031.56006 26153.1797 2 20480 16376.4297 -9 0.0768000036 1 1 0 0 1 38.599985 1 0 0.44999988 2.7200003 61.200008 91.6999969 0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 48.7999992 0.136999995
2 4 26660.4805 3031.56006 26153.1797 2 20480 16376.4297 -9 0.0768000036 1 1 0 0 1.38.5999985 1 0 0 0.449999988 2.7200003 61.2000008 91.6999969 0.20399996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
4 26660.4805 3031.56006 26153.1797 2 20480 16376.4297 -9 0.0768000036 1 1 0 0 1 38.5999985 1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.20399999 0.20399999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
26660.4805 3031.56006 26153.1797 2 20480 16376.4297 -9 0.0768000036 1 1 0 0 1 38.5999985 1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.20399999 0.20399999 0.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
3031.56006 26153.1797 2 20480 16376.4297 -9 0.0768000036 1 1 0 0 1 38.5999985 1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.20399996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
26153.1797 2 20480 16376.4297 -9 0.0768000036 1 1 0 0 1 38.5999985 1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.20399996 0.50999999 20.479995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
2 20480 16376.4297 -9 0.0768000036 1 1 1 0 1 38.5999985 1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.20399996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
20480 16376.4297 -9 0.0768000036 1 1 1 0 1 38.5999985 1 0 0.449999988 2.7200003 61.2000008 91.6999969 0.20399996 0.50999999 20.4799995 0.5440003 64.599985 59.5 0.52499976 49.7999992
16376.4297 -9 0.0768000036 1 1 1 0 1 38.5999985 1 0 0.449999988 2.7200003 61.200008 91.6999969 0.20399996 0.50999999 20.4799995 0.5440003 64.599985 59.5 0.52499976 49.799992
-9 0.0768000036 1 1 1 0 1 38.5999985 1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.203999996 0.50399999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
0.0768000036 1 1 0 1 38.5999985 1 0 0.449999988 2.7200003 61.200008 91.699969 0.203999996 0.50999999 20.4799995 0.5440003 64.5999985 59.5 0.524999976 49.7999992
1 1 0 1 38.5999985 1 0 0.44999988 2.7200003 61.200008 91.6999999 0.203999996 0.50999999 20.479995 0.5440003 64.599985 59.5 0.524999976 49.7999992
1 0 1 38.5999985 1 0 0.449999988 2.72000003 61.200008 91.6999969 0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
1 38.5999985 1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
38.5999985 1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
1 0 0.449999988 2.72000003 61.2000008 91.6999969 0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
0 0.44999988 2.7200003 61.200008 91.6999969 0.20399999 0.50999999 20.4799995 0.5440003 64.599985 59.5 0.52499976 49.799992
0.44999988 2.7200003 61.200008 91.6999969 0.20399999 0.5099999 20.4799995 0.5440003 64.599985 59.5 0.52499976 49.799992
2.72000003 61.2000008 91.6999969 0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
61.2000008 91.6999969 0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
61.2000008 91.6999969 0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
0.203999996 0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
0.50999999 20.4799995 0.54400003 64.5999985 59.5 0.524999976 49.7999992
20.479995 0.5440003 64.5999985 59.5 0.524999976 49.7999992
0.54400003 64.599985 59.5 0.52499976 49.7999992
64.599985 59.5 0.52499976 49.7999992
59.5 0.524999976 49.7999992
0.524999976 49.7999992
49.7999992
0.136000005
0.100000000
1.36000001
0.136999995
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_TrqOvlSta_Per1_APAState_State_enum
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_TrqOvlSta_Per1_ESCState_State_enum
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_TrqOvlSta_Per1_LKAState_State_enum
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
0
0
1
0

TrqOvlSta LKAPermFault Cnt M lgc

TrqOvlSta_LKAPermFault_Cnt_M_u08

TrqOvlSta LKAState State M enum

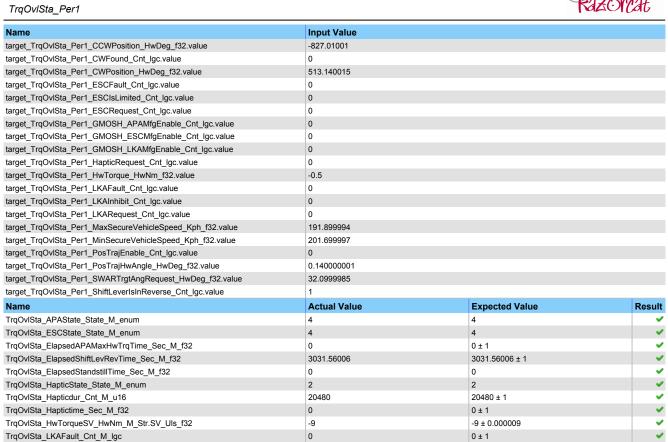
2015-02-04, 14:05:40+0530



1 ± 1

0 ± 1

4



TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.5999985	38.5999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	38.5999985	38.5999985 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.203999996	0.203999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	20.4799995	20.4799995 ± 0.0009765625	✓
			-
Test Step Call Trace			y

0

4

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.33 (Repeat Count = 1) ✓	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	27550.1406
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3119.3999
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	27022.9199
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	26490
TrqOvlSta_Haptictime_Sec_M_f32	16861.2598
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0790000036
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	39.7000008

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

<u> </u>	
Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.460000008
k_APAMaxHwTrq_HwNm_f32	2.7999995
k_APAMaxVehSpd_Kph_f32	63
k_ESCMaxVehSpd_Kph_f32	94.0999985
k_HapticAmplitude_MtrNm_f32	0.20999993
k_HapticDuration_Sec_f32	0.524999976
k_HapticFreq_Hz_f32	20.9939995
k_HapticReacttime_Sec_f32	0.560000002
k_LKAMaxVehSpd_Kph_f32	66.5
k_LKAMinVehSpd_Kph_f32	61.25
k_MaxHapticReqTime_Sec_f32	0.540000021
k_SWARLimiter_HwDeg_f32	51.2000008 0.141000003
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32	1.3999998
k_StandstillTime_Sec_f32	0.141000003
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrgOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-816.909973
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	518.080017
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_IrqOviSta_Peri_GMOSH_ESCMitgEnable_Cnt_igc.value target TrqOviSta_Peri_GMOSH_LKAMfgEnable_Cnt_igc.value	1
target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value	1
target TrqOvISta_Fer1_Habitckequest_Orit_igc.value	-0.300000012
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value	0
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1
target_TrqOviSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	196.800003
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	206.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3119.3999	3119.3999 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	26490	26490 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	16861.2598	16861.2598 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.5	0.5 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.209999993	0.209999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	20.9939995	20.9939995 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.34 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	28439.8008
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3207.23999
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	27892.6602
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	32500
TrqOvlSta_Haptictime_Sec_M_f32	17346.0898
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0812000036
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	40.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.469999999
k_APAMaxHwTrq_HwNm_f32	2.88000011
k_APAMaxVehSpd_Kph_f32	64.8000031
k_ESCMaxVehSpd_Kph_f32	96.5
k_HapticAmplitude_MtrNm_f32	0.216000006
k_HapticDuration_Sec_f32	0.540000021
k_HapticFreq_Hz_f32	21.5079994
k_HapticReacttime_Sec_f32	0.575999975
k_LKAMaxVehSpd_Kph_f32	68.4000015
k_LKAMinVehSpd_Kph_f32	63
k_MaxHapticReqTime_Sec_f32	0.555000007
k_SWARLimiter_HwDeg_f32	52.5999985
k_ShiftLevRevTime_Sec_f32	0.144999996
k_StandstillThresh_Kph_f32	1.44000006
k_StandstillTime_Sec_f32	0.144999996
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



· -			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ılts_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enu	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_TrqOvlSta_Per1_ESCState_State_enu		
	target_TrqOvISta_Per1_GMOSHOscillateStar		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target TrqOvISta Per1 GMOSH APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f3	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
	target_TrqOvlSta_Per1_MaxSecureVehicleSp		
	target_TrqOvISta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosSrvoHwAngle_Hv target TrqOvlSta Per1 PosTrajEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_gc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEriable_Crit_	•	
	target_TrqOvISta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrqOvlSta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-806.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	523.02002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.100000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	201.699997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	211.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-33.0999985 0		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value		Funcated Value	Daguit
Name	Actual Value	Expected Value 4	Result
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ESCState_state_w_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	J
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvISta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	32500	32500 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	17346.0898	17346.0898 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015	6.34000015 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc		1 ± 1	~
40	1	1-1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	0 ± 1 2 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	0 2 4	0±1 2±1 4	~ ~
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	0 ± 1 2 ± 1	~

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	40.7999992	40.7999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.216000006	0.216000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	21.5079994	21.5079994 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.35 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvISta ESCState State M enum	2
TrqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	29329.4609
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3295.08008
TrqOvlSta ElapsedStandstillTime Sec M f32	28762.4004
TrqOvlSta_HapticState_State_M_enum	1
TrqOvISta Hapticdur Cnt M u16	38510
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvISta HwTorqueSV HwNm M Str.SV UIs f32	-5.86000013
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0834000036
TrgOvISta_KKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41.9000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt lgc	1
k_APAmiciudenapiic_crit_igc k_APAMaxHwTrqTime_Sec_f32	0.479999989
k APAMaxHwTrq HwNm f32	2.96000004
k APAMaxVehSpd Kph f32	66.599985
k ESCMaxVehSpd Kph f32	98.9000015
k HapticAmplitude MtrNm f32	0.222000003
k_HapticDuration_Sec_f32	0.555000007
k_HapticFreq_Hz_f32	22.0219994
k_HapticReacttime_Sec_f32	0.592000008
k_KAMaxVehSpd Kph f32	70.3000031
k_LKAMinVehSpd_Kph_f32	64.75
k_MaxHapticReqTime_Sec_f32	0.569999993
k SWARLimiter HwDeg f32	54
k_ShiftLevRevTime_Sec_f32	0.149000004
k_StandstillThresh_Kph_f32	1.48000002
k_StandstillTime_Sec_f32	0.149000002
k_claindstiff fille_dec_rdz target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lo	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_linst_Ap_mqovista.mqovista_reri_ArARequest_ont_igc target_Rte_linst_Ap_mqovista.TrqOvlSta_Per1_APAState_State_enum	target TrgOvlSta Per1 APAState State enum
· · · ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target TrgOvlSta Per1 CWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrgOvlSta Per1 CWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32 target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt lgc	
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrgOvISta Per1 ESCRequest Cnt Igc
target_Rie_Irist_Ap_TrqOviSta.TrqOviSta_PerT_ESCRequest_Crit_igc target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_PerT_ESCState_State_enum	
target_Rte_inst_Ap_1rq0vista.1rq0vista_Per1_ESC5tate_5tate_enum target Rte Inst Ap Trq0vlSta.Trq0vlSta Per1 GM0SH0scillateState State enur	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HapticRequest Cnt lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
· · · · · · · · ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
© Report created by TESSY V3.1.9, report template V2.1	





Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-796.710022			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	1			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	527.960022			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1			
target TrqOvlSta_rer1_ESCIsLimited Cnt Igc.value	1			
target_TrqOvlSta_FeT_ESCRequest_Cnt_lgc.value	1			
target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc.value	1			
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1			
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1			
target TrqOvlSta Per1 HwTorque HwNm f32.value	0.100000001			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	216.399994			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-31.5			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	0	0	~	
TrqOvlSta_ESCState_State_M_enum	3	3	~	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3295.08008	3295.08008 ± 1	~	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	28762.4004	28762.4004		
TrqOvlSta_HapticState_State_M_enum	1	1	V	
TrqOvlSta_Hapticdur_Cnt_M_u16	38510	38510 ± 1		
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.86000013	-5.86000013 ± 0.000009	✓ ✓	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1±1 1±1		
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	3	3±1	-	
TrgOvISta_LKAState State M enum	4	4		
TrqOvlSta_ExAstate_state_injeriorii TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	-	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1		
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	-	
target TrqOvISta Per1 ESCState State enum.value	3	3	-	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-	
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	V	
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	-	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.222000003	0.222000003 ± 0.0009765625	•	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	22.0219994	22.0219994 ± 0.0009765625	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.36 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	30219.1191
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3382.91992
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	29632.1406
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	44520
TrqOvlSta_Haptictime_Sec_M_f32	4.28999987e+009
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.649999976 0.0526000001
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.4900001
k_APAMaxHwTrq_HwNm_f32	3.0399996
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	68.4000015 101.300003
k_HapticAmplitude_MtrNm_f32	0.228
k HapticDuration Sec f32	0.569999993
k_HapticFreq_Hz_f32	22.5359993
k_HapticReacttime_Sec_f32	0.60799998
k_LKAMaxVehSpd_Kph_f32	72.1999969
k_LKAMinVehSpd_Kph_f32	66.5
k_MaxHapticReqTime_Sec_f32	0.584999979
k_SWARLimiter_HwDeg_f32	55.4000015
k_ShiftLevRevTime_Sec_f32	0.152999997
k_StandstillThresh_Kph_f32	1.51999998
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.152999997 target TroOvISta Per1 APADryInterventionDetected Cnt Inc.
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAStete_State_cours
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per I MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	0
	I'

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-786.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	532.900024		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.30000012		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	211.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	221.300003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-29.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvISta ESCState State M enum	4	4	
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	3382.91992	3382.91992 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta HapticState State M enum	2	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	44520	44520 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	4.28999987e+009	4.28999987e+009 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.649999976	0.649999976 ± 0.0000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvISta LKAPermFault Cnt M u08	4	4 ± 1	
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	43	43 ± 0.0009765625	
TrqOvlSta ReadytoPulse Cnt M lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target TrqOvlSta Per1 GMOSHOscillateState State enum.value	2	2	
	4	4	
target TrgOvlSta Per1 LKAState State enum.value	4		
· · · ·	0	0 ± 1	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value		0 ± 1 43 ± 0.0009765625	
	0		•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 43	43 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.37 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31108.7793
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3470.76001
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	30501.8809
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	50530
TrqOvlSta_Haptictime_Sec_M_f32	35000.5391
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	44.0999985





	l
Name TraCulSta ReaduteRules Cot M les	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.5
k_APAMaxHwTrq_HwNm_f32	3.11999989
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	103.699997
k_HapticAmplitude_MtrNm_f32	0.233999997
k_HapticDuration_Sec_f32	0.584999979
k_HapticFreq_Hz_f32	23.0499992 0.624000013
k_HapticReacttime_Sec_f32 k_LKAMaxVehSpd_Kph_f32	74.099985
k LKAMinVehSpd Kph f32	68.25
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	56.7999992
k_ShiftLevRevTime_Sec_f32	0.157000005
k_StandstillThresh_Kph_f32	1.55999994
k_StandstillTime_Sec_f32	0.157000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target TrqOvlSta Per1 APARecoverableFaults Cnt lgc.value	0
target_TrqOvISta_Per1_APARecoverablePaulis_Crit_igc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-776.51001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	537.840027
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_frqOviSta_Per1_GMOSH_APAMitgEnable_Cnt_igc.value target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value	1
target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.5
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	216.399994 226.199997
target_IrqOviSta_Peri_minSecurevenicleSpeed_kpn_rsz.value target_TrqOviSta_Peri_PosTrajEnable_Cnt_lgc.value	226.199997
target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	50530	50530 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	1.21000004 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.233999997	0.233999997 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	23.0499992	23.0499992 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.38 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31998.4395
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	31371.6191
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	3240
TrqOvlSta_Haptictime_Sec_M_f32	18460.6309
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.8900001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0340000018
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.50999999
k_APAMaxHwTrq_HwNm_f32	3.3599999
k_APAMaxVehSpd_Kph_f32	75.5999985
k_ESCMaxVehSpd_Kph_f32	106.099998
k_HapticAmplitude_MtrNm_f32	0.252000004
k_HapticDuration_Sec_f32	0.629999995
k_HapticFreq_Hz_f32	23.5639992
k_HapticReacttime_Sec_f32	0.671999991
k_LKAMaxVehSpd_Kph_f32	79.8000031
k_LKAMinVehSpd_Kph_f32	73.5
k_MaxHapticReqTime_Sec_f32	0.644999981
k_SWARLimiter_HwDeg_f32	58.2000008
k_ShiftLevRevTime_Sec_f32	0.169
k_StandstillThresh_Kph_f32	1.67999995
k_StandstillTime_Sec_f32	0.169
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cr	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau	v	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target TrqOvlSta Per1 CCWPosition HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	9_102	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvISta_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKARequest Cnt Igc	target TrgOvISta Per1 LKARequest Cnt Ig	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_H target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajErlable_Cllt_igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajEnable_Cht_ target_TrqOvISta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-766.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	542.780029		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lqc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.10000002		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	221.300003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.100000001 -26.7000008		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.002000000009 ± 1	V
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0	0	Y
TrqOvISta_Hapticdur_Cnt_M_u16	3240	3240 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	18460.6309	18460.6309 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.82914019	2.82913995 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	1	1 ± 1 4	~
TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	4 45.2000008	4 45.2000008 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
			-





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	45.2000008	45.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.252000004	0.252000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	23.5639992	23.5639992 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

lame	Input Value
te_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
qOvlSta_APAState_State_M_enum	1
qOvlSta_ESCState_State_M_enum	1
qOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	32888.1016
qOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009
QOvlSta_ElapsedStandstillTime_Sec_M_f32	32241.3594
qOvlSta_HapticState_State_M_enum	2
qOvlSta_Hapticdur_Cnt_M_u16	46
QOvlSta_Haptictime_Sec_M_f32	21465
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.45000005
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0450000018
gOvlSta LKAFault Cnt M lgc	0
qOvlSta LKAPermFault Cnt M lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	2
qOvlSta_LKAState_State_M_enum	0
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	46.2999992
qOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic Cnt Igc	0
APAMaxHwTrqTime_Sec_f32	0.519999981
APAMaxHwTrq_HwNm_f32	3.4400006
APAMaxVehSpd Kph f32	77.4000015
ESCMaxVehSpd Kph f32	108.5
HapticAmplitude_MtrNm_f32	0.257999986
HapticDuration_Sec_f32	0.644999981
HapticFreq Hz f32	24.0779991
HapticReacttime_Sec_f32	0.688000023
LKAMaxVehSpd_Kph_f32	81.6999969
LKAMinVehSpd_Kph_f32	75.25
MaxHapticRegTime Sec f32	0.660000026
SWARLimiter_HwDeg_f32	59.5999985
ShiftLevRevTime_Sec_f32	0.172999993
StandstillThresh_Kph_f32	1.72000003
StandstillTime Sec f32	0.172999993
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_C	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lc	
get Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target TrqOvlSta Per1 APAState State enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target TrqOvlSta Per1 CCWFound Cnt Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_e	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32





			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
	target_TrqOvISta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRevers target TrqOvlSta Per1 TrqOscAmp MtrNm	:	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_rer1_TrqOscFreq Hz f32	gc	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-756.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	547.719971		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.2999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	226.199997 236		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.1000004		
target TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	- Court
TrgOvISta ESCState State M enum	3	3	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	_
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	4.2949673e+009	4.2949673e+009 ± 1	~
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	46	46 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.45000005	3.45000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 3	0 3	V
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	-
target_rrqOvlSta_Per1_PosServErlable_Cnt_gc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.10999999	0.109999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_FosSivonwArigie_Hwbeg_isz.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.257999986	0.257999986 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	24.0779991	24.0779991 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓



Name	Test Step 2.40 (Repeat Count = 1)	▼
New Journal Port Process Proce	Name	Input Value
Trockloade Scale Sub M. Arumn 2 1 1 1 1 1 1 1 1 1		·
TimpOoSean Epispeedin Market Press 56.00 1.00	TrqOvlSta_APAState_State_M_enum	· · · ·
Topological planes desiration See Marco	TrqOvlSta_ESCState_State_M_enum	2
TopioSta: PaperdismetalTime_Step. M. 1920 1920	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	33777.7617
Trigotides Japaces 10 10 10 10 10 10 10 1	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	40350.6602
TrepOrties Hapticines See, 192 2477 TrepOrties Hapticines See, 192 2477 TrepOrties Hapticines See, 192 2477 TrepOrties Hapticines See, 192 105000033 TrepOrties LAFF Annual N. Ser, 20, 193 2477 TrepOrties LAFF Annual N. Ser, 20, 193 247, 193 2	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	
TROORES INFORMES VINNER & SPS. VILL B2 4000023 TROORES INFORMES VINNER & SPS. VILL B2 4000023 TROORES INFORMES VINNER & SPS. VILL B2 505000017 TROORES INFORMES VINNER & SPS. VILL B2 51 TROORES VIL		
TERONICAS INTERNACY Mark M. J. M. P. C. U. B. D. 2		
TIRONS IN JAPParel Paul, Crit M. 190 1 TIRONS IN JAPParel Paul CRIT M. 190 1 TIRONS IN JAPPAREL PA		
TROMBES JAMPSENDL COL M. JUGS		
TROMBIL LAPPermissuit, Crit. Jul. 208 1	TrgOvlSta LKAFault Cnt M lgc	
TROMSINE JANSBERGE SINE M, Junuan TORONSINE POSITIONAMINE CHILD M, Job APARAMANTATION, SEC. D3 APARAMA	TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
1700/368 Packyothe Crit Inju 1	TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TROWNER Ready-Private Crit III go A PANAMAVITOTIME Sec 52 A PANAMAVITOTIME SEC 54 A PANAMAVITORIME SEC 54 A PANAMAVIT	TrqOvlSta_LKAState_State_M_enum	0
A.PAMacHostPort, Prince, Sc. 12 0.52099971	TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	
A.PAMasherTrolline Sec. (32 3.5999998 A.PAMasherTrolline Sec. (33 3.599998 A.PAMasherTrolline Sec. (33 3.599998 A.PAMasherTrolline Sec. (33 3.599998 A.PAMasherTrolline Sec. (34 3.599	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	
A. APAMAWNIST, Mph. 12 3,5190008		
A. PANAW-NISKE Kpt. 12		
1.		
k. Hapick/mittor, Sec., 132 k. Lipokint/mittor, Sec., 132 k. Lipokint/mittor, Sec., 132 k. Lipokint/mittor, Sec., 132 k. Markafult/mittor, Sec., 132 k. Sandattime, Sec., 132 k. Sandatt		
Lipsopher Neth 22 K Hapickhee Methods K Hapickhee	k_HapticAmplitude_MtrNm_f32	
Lipsophere Hr. 192 Lipsophere Hr	k_HapticDuration_Sec_f32	
KLAMAINVESS of Kort. 152 KLAMAINVESS OF KOR	k_HapticFreq_Hz_f32	24.5919991
K, MoxHapticReqTime, Sec, 132 K, MoxHapticReqTime, Sec, 132 K, ShiftserNerVime, Sec, 132 Larget, Rie, Inst. Ap, TriqOviSta TrqOviSta, Pert J, APANorinterventionDetected Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, APANorinterventionDetected Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, APANorinterventionDetected Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, APANorinterventionDetected Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, CWPosition, HwDeg, 132 Larget, Rie, Inst., Ap, TrqOviSta, Pert C, WPosition, HwDeg, 132 Larget, Rie, Inst., Ap, TrqOviSta, TrqOviSta, Pert J, CWPosition, HwDeg, 132 Larget, Rie, Inst., Ap, TrqOviSta, TrqOviSta, Pert J, CWPosition, HwDeg, 132 Larget, Rie, Inst., Ap, TrqOviSta, Pert J, CWPosition, HwDeg, 132 Larget, Rie, Inst., Ap, TrqOviSta, Pert J, CWPosition, HwDeg, 132 Larget, Rie, Inst., Ap, TrqOviSta, Pert J, CWPosition, HwDeg, 132 Larget, Rie, Inst., Ap, TrqOviSta, Pert J, CWOSHA, Pert J, SCRequest, Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, CWOSHA, Pert J, SCRequest, Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, CWOSHA, Pert J, SCRequest, Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, CWOSHA, Pert J, SCRequest, Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, CWOSHA, Pert J, SCRequest, Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, CWOSHA, Pert J, SCRequest, Cnt., Igo target, Rie, Inst., Ap, TrqOviSta, Pert J, KANSHA, Pert J, KANS	k_HapticReacttime_Sec_f32	0.703999996
K, Marklamiter (Hwong, 132 K, Standstiffmer, Mong, 132 K, Standstiffmer, Bec, 132 C, Standstiffmer, Ber, 132 C, Standstiffmer, Bec, 132 C, Standstiffmer, Be	k_LKAMaxVehSpd_Kph_f32	83.5999985
K, SMALEUREYTIME, Sec. JS2 A, StandstellTimes Ny, In. 32 A, StandstellTimes Sec. JS2 A, TrigoviSta TrigoviSta Perl APARotecoreableFaults. Cnt. Igo target, Rel. Inst. Ap. TrigoviSta Perl APARotecoreableFaults. Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl APARotecoreableFaults. Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl CWPosition, Hwbeg. JS2 target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl CWPosition, Hwbeg. JS2 target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl CWPosition, Hwbeg. JS2 target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl CWPosition, Hwbeg. JS2 target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl ESCRequest, Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl ESCRequest, Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl ESCRequest, Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl CMPosition, Hwbeg. JS2 target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl AMAROtecoreable Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl Social Sec. Inst. Parl Inst. Ap. TrigoviSta TrigoviSta Perl AMAROtecoreable Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl AMAROtecoreable Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl AMAROtecoreable Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl AMAROtecoreable Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl AMAROtecoreable Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl Lixabibit Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl Lixabibit Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl Lixabibit Cnt. Igo target, Rel. Inst. Ap. TrigoviSta TrigoviSta Perl Lixabibit Cnt. I	k_LKAMinVehSpd_Kph_f32	
K, Shind-RevTime Sec, 132 K, StandstillTimesh, Kph. 132 J. 175999999 J. 177900001 K, StandstillTimesh, Kph. 132 J. 175999999 J. 175999999 J. 175999999 J. 175900001 J. 175900001 J. 1759000000 J. 1759000000 J. 1759000000 J. 1759000000 J. 1759000000 J. 1759000000000000000000000000000000000000		
KS SandstillTrines Fec_152 arget_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_APADrvrinterventionDetected_Cnt. target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_APADrvrinterventionDetected_Cnt. target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_APADrvrinterventionDetected_Cnt. target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_APADrvrinterventionDetected_Cnt. target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_APADrvvinterventionDetected_Cnt. target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_APADrvvinterventionDetected_Cnt. target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_APADrvvinterventionDetected_Cnt. target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_CCWFound_Cnt.lgc target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_CCWFound_Cnt.lgc target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_CCWFound_Cnt.lgc target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_CWFoustion_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_ESCRequest_Cnt.lgc target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_ESCRequest_Cnt.lgc target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_ESCRequest_Cnt.lgc target_Rie_Inst_Ap_TrqOviSta_TrqOwSta_Per1_GNOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHO		
k, Sandstill'ime, Sec_132 target, Rie, Inst. Ap. TrqOviSta.TrqOviSta. Perl. APADrvinterventionDetected Cnt. target, Rie, Inst. Ap. TrqOviSta.TrqOviSta. Perl. APARonoRecoverableFaults. Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. APARonoRecoverableFaults. Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. APARonoRecoverableFaults. Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. APARonoRecoverableFaults. Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. APARolate, State, enum target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. CCWPound, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. CCWPound, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. CCWPound, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. CWPound, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. CWPound, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. Scotaut, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. ESCIStautted. Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. ESCIStauted, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. ESCIState, State, enum target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. GNOSH, APANIgEnable, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. GNOSH, APANIgEnable, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. GNOSH, APANIgEnable, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. GNOSH, ESCMGEnable, Cnt. Igic target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl.		
target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. APADovintenvereinonDetected. Cm. larget, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. APARecoverableFaults. Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. APARecoverableFaults. Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. APARecoverableFaults. Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. APARecoverableFaults. Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. APARecoverableFaults. Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. CCWPosition, HwDeg, 32 target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. CCWPosition, HwDeg, 32 target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. CCWPosition, HwDeg, 32 target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. ESCRault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. ESCRault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. ESCRequest, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. GNOSH-OscillateState, State, enum target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. GNOSH-OscillateState, State, enum target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. GNOSH-OscillateState, State, enum target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. GNOSH-DSCRIPECQUEST, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. GNOSH-DSCRIPECQUEST, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. GNOSH-DSCRIPECQUEST, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. LKAFault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. LKAFault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. LKAFault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. LKAFault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. LKAFault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. LKAFault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. JKAFault, Cnt. l. go target, Rel. Inst. Ap. TrqOvisia. TrqOvisia. Perl. JKAFault, Cnt. l. go target, Rel. Inst. Ap.		
target, Rie, Inst. Ap. TroOviSta TrqOviSta Perf. APARcoverableFaults, Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. APARcoverstelFaults Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. APARcoverstelFaults Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. APARcoverstelFaults Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. APARcoverstelFaults Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. CVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. CVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. CVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVProup. Lie Perf. EVPround. Cnt. Igs target, Rie Inst. Ap. TrqOviSta TrqOviSta Perf. EVProup. Lie Pe		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_APARequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFootin_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFootin_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFootin_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFootin_Hobeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFootin_Hobeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFootin_Hobeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCFootin_Upc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCFootin_Upc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCFootin_Upc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCFootin_Upc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCSCfault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCSCState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCSState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_APAMigEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MOSH_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MOSH_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Lext_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DestruiCe_Inst_gc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DestruiCe_Inst_gc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DestruiCe_Inst_gc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DestruiCe_Inst_gc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Pest_TrqDist_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_Perl_Pest_TrqOviSta_		
target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APAState_State_enum target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCS_taut_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCS_taut_State_enum target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCS_taut_State_enum target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCS_taut_State_enum target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCS_taut_State_enum target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCS_taut_State_enum target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCM_Genable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCM_Genable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HabitoRequest_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARAput_End_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARAput_End_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARAput_End_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARAput_End_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARAput_End_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARaput_End_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevfEnable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevfEnable_Cnt_Igo target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevf	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCStatinted_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCStatinted_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCStatinted_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCMgenable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_DScaliateState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_DScaliateState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_RASSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_RASSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrajehabe_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrajehabe_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrajehabe_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrajehabe_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrajehabe_Cnt_Igc target_Rie_Inst_Ap_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWPooldOn_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPooldOn_Ligo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_EMOSH_Dev1_ESCMIgenable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_EMOSH_Dev1_ESCMIgenable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_EMOSH_Dev1_ESCMIgenable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAPnobiSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HAState_State_enum target_TrqOviSta_Per1_HaptiCReques_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HAState_State_enum target_TrqOviSta_Per1_HAState_State_enum target_TrqOviSta_Per1_HAState_State_Enum target_TrqOviSta_Per1_HAState_State_Enum target_TrqOviSta_Per1_HAState_State_Enum target_TrqOviSta_Per1_HAState_State_Enum target_TrqOviSta_Per1_PasservEnable_Cnt_Igo target_TrqOviSta_Per1_PasservEnable_Cnt_Igo target_TrqOviSta_Per1_PasservEnable_Cnt_Igo target_TrqOviSta_Per1_PasservEnable_Cnt_Igo target_TrqOviSta_Per1_PasservEnable_Cn	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRatequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRatequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRatequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRatequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRate_num target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHOSHO	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCStault_Edut_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCStault_Estate_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MoSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapfoRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MassecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinsecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraphwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraphwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraphwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraphwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArp_Mthm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArp_Mthm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscArp_Mthm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraphwAngle_HvDeg_f32 targe		
target Rite_Inst_Ap_TrqOviSta_Perf_ESCFault_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_Perf_ESCISaltmited_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_ESCISaltmited_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_ESCISalte_enum target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_ESCISalte_enum target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_SCISalte_enum target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_SCISAlte_enum target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_ESCMigEnable_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_ESCMigEnable_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_GMOSH_ESCMigEnable_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_MOSH_ESCMigEnable_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_Mosh_EscMigEnable_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_LivToviDist_Perf_Mosh_EscMigEnable_Cnt_lgc target_Rite_Inst_Ap_TrqOviSta_TrqOviSta_Perf_LivTovIDist_Perf_LivTovI		
target Rte Inst Ap TrqOv/Sta Per1 ESC/SLimited Cnt Igc target Rte Inst Ap TrqOv/Sta Per1 ESC/State State enum target Rte Inst Ap TrqOv/Sta TrqOv/Sta Per1 ESC/State State enum target Rte Inst Ap TrqOv/Sta TrqOv/Sta Per1 ESC/State State enum target Rte Inst Ap TrqOv/Sta TrqOv/Sta Per1 ESC/State State enum target Rte Inst Ap TrqOv/Sta TrqOv/Sta Per1 ESC/State State enum target Rte Inst Ap TrqOv/Sta TrqOv/Sta Per1 ESC/State State enum target Rte Inst Ap TrqOv/Sta TrqOv/Sta Per1 ESC/State State enum target Rte Inst Ap TrqOv/Sta TrqOv/Sta Per1 ESC/State State enum target TrqOv/Sta Pe		
target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARABIt_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARABIt_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARabit_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARabit_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARabit_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARabit_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKARabit_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_MapsecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_SorTrajEnable_Cnt_lgc target_Rte_I		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_DavaSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_DosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SosAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOvSta_Pert_ShiffLevertsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOvlSta_Pert_APANeoneescoverableFauits_Cnt_Igc.value 1 target_TrqOvlSta_Pert_APARecoverableFauits_Cnt_Igc.valu		
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_APARequest_Cnt_lgc.target_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOviSta_Per1_APARequest_Cnt_lgc.value	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosSrvEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOsCenable_Cnt_lgc target_TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOsCenable_Cnt_lgc target_TrqOvISta_Perl_TrqOsCenable_Cnt_lgc target_TrqOvISta_Perl_APARonRecoverableFaults_Cnt_lgc.value 1 target_TrqOvISta_Perl_APARequest_Cnt_lgc.value 1 target_TrqOvISta_Perl_APARequest_Cnt_lgc.value	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAShibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorMRTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorMRTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorMRTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_DosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServSortHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvISta_Per1_DosServEnable_Cnt_Igc target_TrqOvISta_Per1_DosServEnable_Cnt_Igc target_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_TrqOvISta_Per1_PosSrvehAngle_HwDeg_f32 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANenRecoverableFaults_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_SortIRQvISta.Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_SortIRQvISta.Per1_TrqOvISta.Pe	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvohwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_igc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_TrqOvlSta_Per1_TrqOscEreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscEreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscErable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_TrqOvlota_rer1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0	target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-746.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	552.659973		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	240.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	33111.1016	33111.1016	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	1352	1352 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	24470	24470 ± 1	*
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.01000023	4.01000023 ± 0.000009	Y
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	*
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	Y
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	Y
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	47.4000015	47.4000015 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	V
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	Y
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	Y
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	V
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	<i>y</i>
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	47.4000015	47.4000015 ± 0.0009765625	Ž
	0.263999999	0.263999999 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value		4.14	
target_TrqOviSta_Per1_TrqOscAmp_MtrNm_132.value target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value target TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	1 24.5919991	1 ± 1 24.5919991 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.41 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	5270
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	48.5

2015-02-04, 14:05:40+0530





· -	
Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.540000021
k APAMaxHwTrq HwNm f32	3.7599999
k APAMaxVehSpd Kph f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	113.300003
k HapticAmplitude MtrNm f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	25.1060009
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k LKAMinVehSpd Kph f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k SWARLimiter HwDeg f32	1.60000002
k_ShiftLevRevTime_Sec_f32	0.188999996
k StandstillThresh Kph f32	1.88
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target TrgOvlSta Per1 APADrvrInterventionDetected Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrqOvlSta Per1 CWFound Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvISta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-736.109985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	557.599976
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	236
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	245.800003
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-21.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293	27475.5293 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.1060009	25.1060009 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.42 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	35557.0781
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	8620
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	6576
TrqOvlSta_Haptictime_Sec_M_f32	30480
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.25
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.100000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.550000012
k_APAMaxHwTrq_HwNm_f32	3.83999991
k_APAMaxVehSpd_Kph_f32	86.4000015
k_ESCMaxVehSpd_Kph_f32	115.699997
k_HapticAmplitude_MtrNm_f32	0.287999988
k_HapticDuration_Sec_f32	0.720000029
k_HapticFreq_Hz_f32	25.6200008
k_HapticReacttime_Sec_f32	0.768000007
k_LKAMaxVehSpd_Kph_f32	91.1999969
k_LKAMinVehSpd_Kph_f32	84
k_MaxHapticReqTime_Sec_f32	0.735000014
k_SWARLimiter_HwDeg_f32	1.89999998
k_ShiftLevRevTime_Sec_f32	0.193000004
k_StandstillThresh_Kph_f32	1.91999996
k_StandstillTime_Sec_f32	0.193000004
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Section 1) and the property of the $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	0 - 1 1	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-726.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	562.539978		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.2999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	240.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	250.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-20.2999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	6576	6576 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	30480	30480 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6.25	6.25 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
		2.4	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	
	4	4	•
TrqOvlSta_LKAPermFault_Cnt_M_u08			





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	49.5999985	49.5999985 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.287999988	0.287999988 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.6200008	25.6200008 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

est Step 2.43 (Repeat Count = 1)	Innut Value
lame	Input Value
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	2
rqOvlSta_ESCState_State_M_enum	0
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36446.7383
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11790
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	76432.1094
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	7882
rqOvlSta_Haptictime_Sec_M_f32	33485
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.80999994
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.111000001
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	1
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.7000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime_Sec_f32	0.560000002
APAMaxHwTrq HwNm f32	3.92000008
APAMaxVehSpd Kph f32	88.1999969
ESCMaxVehSpd Kph f32	118.099998
	0.294
HapticAmplitude_MtrNm_f32	
_HapticDuration_Sec_f32	0.735000014
_HapticFreq_Hz_f32	26.1340008
_HapticReacttime_Sec_f32	0.783999979
_LKAMaxVehSpd_Kph_f32	93.099985
_LKAMinVehSpd_Kph_f32	85.75
_MaxHapticReqTime_Sec_f32	0.75
_SWARLimiter_HwDeg_f32	2.20000005
_ShiftLevRevTime_Sec_f32	0.196999997
_StandstillThresh_Kph_f32	1.96000004
_StandstillTime_Sec_f32	0.196999997
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enui	
get_Rte_inst_Ap_frqovista.frqovista_Perf_givioshosciliatestate_state_entil rget_Rte_inst_Ap_frqovista.frqovista_Perf_givioshoshosciliatestate_state_entil	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
	target_TrqOvISta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscFreq Hz f32	target TrqOvISta Per1 TrqOscFreq Hz f32	gc	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-715.909973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	567.47998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target TrqOvlSta_Per1_LKAInhibit Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARiniibit_Crit_igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	245.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	255.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-18.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36446.7383	36446.7383 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11790	11790 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	7882	7882 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	6.3315897	6.33159018 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	V
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	V
TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3 0.150000006	3 0.150000006 ± 0.0009765625	~
TrqOviSta_PosStvonwArigie_nwbeg_m_isz TrqOviSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	1	1±1	-
target_TrqOvISta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.294	0.294 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	26.1340008	26.1340008 ± 0.0009765625	~
			-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.44 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	14960
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	12024
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11800
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	36490.3516 8.48999977
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.143999994
TrgOvlSta LKAFault Cnt M Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	51.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.56999993
k_APAMaxHwTrq_HwNm_f32	0.10000001
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	93.5999985 120.5
k_HapticAmplitude_MtrNm_f32	0.312000006
k HapticDuration Sec f32	0.779999971
k_HapticFreq_Hz_f32	26.6480007
k_HapticReacttime_Sec_f32	0.832000017
k_LKAMaxVehSpd_Kph_f32	98.8000031
k_LKAMinVehSpd_Kph_f32	91
k_MaxHapticReqTime_Sec_f32	0.795000017
k_SWARLimiter_HwDeg_f32	2.5
k_ShiftLevRevTime_Sec_f32	0.20900006
k_StandstillThresh_Kph_f32	2.07999992
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.209000006 target TroOvISta Per1 APADryInterventionDetected Cnt Inc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAStete_State_cours
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	0
	I.

2015-02-04, 14:05:40+0530



TIQOVISIA_T ELT			
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-705.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	572.419983		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	250.699997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	260.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	14960	14960 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11800	11800 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	7.71384001	7.71384001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	51.7999992	51.7999992 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	51.7999992	51.7999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.312000006	0.312000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	26.6480007	26.6480007 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.45 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18130
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18570
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	13106
TrqOvlSta_Haptictime_Sec_M_f32	39495
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.05000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.155000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	52.9000015

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k APAIncludeHaptic Cnt Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.579999983
k_APAMaxHwTrq_HwNm_f32	0.5
k_APAMaxVehSpd_Kph_f32	95.4000015
k_ESCMaxVehSpd_Kph_f32	122.900002
k_HapticAmplitude_MtrNm_f32	0.31799989
k_HapticDuration_Sec_f32	0.795000017 27.1620007
k_HapticFreq_Hz_f32 k HapticReacttime Sec f32	0.84799999
k_LKAMaxVehSpd_Kph_f32	100.699997
k_LKAMinVehSpd_Kph_f32	92.75
k_MaxHapticReqTime_Sec_f32	0.810000002
k_SWARLimiter_HwDeg_f32	2.79999995
k_ShiftLevRevTime_Sec_f32	0.213
k_StandstillThresh_Kph_f32	2.11999989
k_StandstillTime_Sec_f32	0.213
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt Igc	target TrqOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target TrqOvlSta Per1 CCWPosition HwDeg f32.value	1 -695.710022
target_TrqOvISta_Per1_CCWPosition_HWDeg_t32.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value	-095.710022 1
target TrqOvlSta Per1 CWPosition HwDeg f32.value	577.359985
target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0 3.29999995
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value	3.2999999
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	265.399994
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18130	18130 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18570	18570	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	13106	13106 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.15875053	8.15874958 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.317999989	0.317999989 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	27.1620007	27.1620007 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.46 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	684235.25
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	21300
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	25116
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	14412
TrqOvlSta_Haptictime_Sec_M_f32	42500
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.60999966
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.165999994
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	54
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.589999974
k_APAMaxHwTrq_HwNm_f32	7
k_APAMaxVehSpd_Kph_f32	97.1999969
k_ESCMaxVehSpd_Kph_f32	125.300003
k_HapticAmplitude_MtrNm_f32	0.324000001
k_HapticDuration_Sec_f32	0.810000002
k_HapticFreq_Hz_f32	27.6760006
k_HapticReacttime_Sec_f32	0.864000022
k_LKAMaxVehSpd_Kph_f32	102.599998
k_LKAMinVehSpd_Kph_f32	94.5
k_MaxHapticReqTime_Sec_f32	0.824999988
k_SWARLimiter_HwDeg_f32	3.0999999
k_ShiftLevRevTime_Sec_f32	0.216999993
k_StandstillThresh_Kph_f32	2.16000009
k_StandstillTime_Sec_f32	0.216999993
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlOta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg	1 132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_I	ac	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	JC .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlOta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 SWARTrgtAngRequest HwDeg f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrqOvISta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-685.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	582.299988		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Crit_Igc.value	0		
target TrgOvlSta Per1 HapticRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	260.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	270.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-13.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	684235.25	684235.25 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	14412	14412 ± 1	•
TrgOvISta Haptictime Sec M f32	42500	42500 ± 1	
Tryo viola_Tapticume_000_ivi_102		72000 I I	
TraOvISta HwTorqueSV HwNm M Str SV Llls f32		8 59574032 + 0 000000	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	8.59573936	8.59574032 ± 0.000009 1 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc	8.59573936 1	1 ± 1	•
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	8.59573936		•
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	8.59573936 1	1 ± 1 1 ± 1	~
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	8.59573936 1 1	1±1 1±1 1±1	

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	54	54 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.324000001	0.324000001 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	27.6760006	27.6760006 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.47 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrgOvISta ESCState State M enum	4
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	152460
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	24470
TrqOvlSta ElapsedStandstillTime Sec M f32	31662
TrqOvlSta_HapticState_State_M_enum	0
TrqOvISta Hapticdur Cnt M u16	18330
TrqOvlSta_Haptictime_Sec_M_f32	45505
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-10
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.199000001
TrgOvISta LKAFault Cnt M Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	55.0999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k APAIncludeHaptic Cnt Igc	0
k APAMaxHwTrqTime Sec f32	0.600000024
k APAMaxHwTrg HwNm f32	4.55999994
k APAMaxVehSpd Kph f32	102.599998
k ESCMaxVehSpd Kph f32	127.699997
k HapticAmplitude MtrNm f32	0.342000008
k_HapticDuration_Sec_f32	0.855000019
k_HapticFreq_Hz_f32	28.1900005
k_HapticReacttime_Sec_f32	0.912
k LKAMaxVehSpd Kph f32	108.300003
k_LKAMinVehSpd_Kph_f32	99.75
k_MaxHapticReqTime_Sec_f32	0.870000005
k SWARLimiter HwDeg f32	3.4000001
k_ShiftLevRevTime_Sec_f32	0.229000002
k_StandstillThresh_Kph_f32	2.27999997
k_StandstillTime_Sec_f32	0.229000002
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_GWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCFault Cnt Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvISta Per1 ESCRequest Cnt Igc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCState State enum	target_TrqOvISta_Per1_ESCState_state_enum
target_Inte_Inst_Ap_TrqOvIsta.TrqOvIsta_Fe11_S0state_state_enum target_Rte_Inst_Ap_TrqOvIsta.TrqOvISta_Pe11_GMOSHOscillateState_State_enum	
target_Rte_inst_Ap_11q0vista.11q0vista_Fe11_Giil0Si10Sciilatestate_state_entit target Rte Inst Ap Trq0viSta.Trq0viSta Per1 GM0SH APAMfgEnable Cnt Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fet	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
argot_rto_mot_rp_rrqoviota.rrqoviota_r el 1_riw rolque_riwiviii_i32	Talgot_11qOnota_1 of 1_1W101quo_11W1111_102





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-675.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	587.23999		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	1		
target TrqOvISta_rer1_ESCISEIIIIIted_Off_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	4.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	265.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	275.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-12.3000002		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	152460	152460 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	18330	18330 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	45505	45505 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-7.1940999	-7.1940999 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	
TrqOvISta_LKAState_State_M_enum	0	0	✓
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	0 ± 1 1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	-
	0	0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	V
target_rqovista_Peri_LRAState_state_enum.value target_TrqOviSta_Peri_LRAState_state_enum.value	1	1 ± 1	-
target_IrqOviSta_Peri_PosServEnable_Cnt_igc.value target_TrqOviSta_Peri_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.342000008	0.342000008 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	28.1900005	28.1900005 ± 0.0009765625	-
2. 7. 7. 4			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.48 (Repeat Count = 1)	▼
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	186423
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	27640
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	38208
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	19636
TrqOvlSta_Haptictime_Sec_M_f32	48510.3398
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	10
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.209999993
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_W_gc	3
TrqOvISta_LKAState_State_M_enum	1
TrgOvlSta PosSrvoHwAngle HwDeg M f32	56.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.610000014
k_APAMaxHwTrq_HwNm_f32	4.63999987
k_APAMaxVehSpd_Kph_f32	104.400002
k_ESCMaxVehSpd_Kph_f32	130.100006
k_HapticAmplitude_MtrNm_f32	0.34799999
k_HapticDuration_Sec_f32	0.870000005
k_HapticFreq_Hz_f32	28.7040005
k_HapticReacttime_Sec_f32	0.927999973
k_LKAMaxVehSpd_Kph_f32	110.199997
k_LKAMinVehSpd_Kph_f32	101.5
k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32	0.88499999 3.70000005
k ShiftLevRevTime Sec f32	0.232999995
k_StandstillThresh_Kph_f32	2.31999993
k_StandstillTime_Sec_f32	0.232999995
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_rer1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfqEnable Cnt lqc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosSrvonwArigie_nwbeg_isz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvonwArigie_nwbeg_152 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap Irquvista. Irquvista Peri Swariratanareauest Hwdea 13:	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse Cnt lgc
	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 1
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 1

2015-02-04, 14:05:40+0530

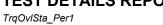


Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-665.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	592.179993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	270.299988		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	280.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-10.6999998		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	186423	186423 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	19636	19636 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.80300045	8.80300045 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvISta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	56.2000008	56.2000008 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
	4	4	~
target_TrqOvlSta_Per1_LKAState_State_enum.value		0 ± 1	-
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 = .	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 56.2000008	56.2000008 ± 0.0009765625	✓
			~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	56.2000008	56.2000008 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.49 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	220386	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	30810	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	44754	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	20942	
TrqOvlSta_Haptictime_Sec_M_f32	51515	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.221000001	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.2999992	

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.620000005
k_APAMaxHwTrq_HwNm_f32	4.71999979
k_APAMaxVehSpd_Kph_f32	106.199997
k_ESCMaxVehSpd_Kph_f32	132.5
k_HapticDuration_Sec_f32	0.354000002 0.88499999
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	29.2180004
k HapticReacttime Sec f32	0.944000006
k_LKAMaxVehSpd_Kph_f32	112.099998
k LKAMinVehSpd Kph f32	103.25
k MaxHapticRegTime Sec f32	0.89999976
k_SWARLimiter_HwDeg_f32	4
k_ShiftLevRevTime_Sec_f32	0.237000003
k_StandstillThresh_Kph_f32	2.3599999
k_StandstillTime_Sec_f32	0.237000003
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgauge and the property of the prope$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-655.309998
target_IrqOvISta_Per1_CCWPosition_Hwbeg_r3z.value target TrqOvISta_Per1_CWFound Cnt_lgc.value	-055.309998 1
target_TrqOvISta_Per1_CWPound_Crit_igc.value target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	597.119995
target_TrqOvIsta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvIsta_rerr_ESCrauit_Crit_igc.value	1
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.5
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	275.200012
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	285
	4
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1

© Report created by TESSY V3.1.9, report template V2.1

97

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.10000038		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	30810	30810 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	20942	20942 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	51515	51515 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.994499981	0.994499981 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	2	2	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.354000002	0.354000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	29.2180004	29.2180004 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.50 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	254349
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	33980
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	51300
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	22248
TrqOvlSta_Haptictime_Sec_M_f32	54520
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.34000015
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.231999993
TrqOvlSta_LKAFault_Cnt_M_lgc	1.
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	58.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.62999995
k_APAMaxHwTrq_HwNm_f32	4.80000019
k_APAMaxVehSpd_Kph_f32	108
k_ESCMaxVehSpd_Kph_f32	134.899994
k_HapticAmplitude_MtrNm_f32	0.360000014
k_HapticDuration_Sec_f32	0.899999976
k_HapticFreq_Hz_f32	29.7320004
k_HapticReacttime_Sec_f32	0.959999979
k_LKAMaxVehSpd_Kph_f32	114
k_LKAMinVehSpd_Kph_f32	105
k_MaxHapticReqTime_Sec_f32	0.915000021
k_SWARLimiter_HwDeg_f32	4.30000019
k_ShiftLevRevTime_Sec_f32	0.240999997
k_StandstillThresh_Kph_f32	2.4000001
k_StandstillTime_Sec_f32	0.240999997
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 _ 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	0	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-645.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	602.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	4.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	289.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	254349	254349 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	51300	51300	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	22248	22248 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.95951986	5.95951986 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	58.4000015	58.4000015 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
	Actual Value	· · · · · · · · · · · · · · · · · · ·	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	58.4000015	58.4000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.360000014	0.360000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	29.7320004	29.7320004 ± 0.0009765625	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.51 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	288312
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	37150
TrqOvlSta ElapsedStandstillTime Sec M f32	57846
TrqOvlSta_HapticState_State_M_enum	0
TrqOvISta Hapticdur Cnt M u16	23554
TrqOvlSta_Haptictime_Sec_M_f32	57525.3203
TrqOvISta HwTorqueSV HwNm M Str.SV UIs f32	-5.86000013
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.243000001
TrqOvISta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	59.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k APAMaxHwTrqTime Sec f32	0.639999986
k_APAMaxHwTrq HwNm f32	4.88000011
k APAMaxVehSpd Kph f32	109.800003
k ESCMaxVehSpd Kph f32	137.300003
k HapticAmplitude MtrNm f32	0.365999997
k_HapticDuration_Sec_f32	0.915000021
k_HapticFreq_Hz_f32	30.2460003
k_HapticReacttime_Sec_f32	0.976000011
k_KAMaxVehSpd Kph f32	115.900002
k_LKAMinVehSpd_Kph_f32	106.75
k_MaxHapticReqTime_Sec_f32	0.930000007
k SWARLimiter HwDeg f32	4.5999999
k_ShiftLevRevTime_Sec_f32	0.245000005
k_StandstillThresh_Kph_f32	2.44000006
k_StandstillTime_Sec_f32	0.245000005
k_ctailustiiiTille_dec_loz target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_t	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target TrgOvlSta Per1 CWPosition_nwbeg_isz
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrgOvlSta Per1 CWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_PerT_ESCPault_Cht_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_ESCIsLimited_Cht_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
· · · · ·	target TrqOvISta Per1 ESCRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	· - · · ·
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_ESCState_State_enum target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enui	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
© Report created by TESSY V3.1.9, report template V2.1	100

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3		_ <u>-</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-635.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	607		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_rer1_ESCFatit_Crit_tgc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_rer1_ESCISEIIIIIted_Off_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lqc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	4.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	285		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	294.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	37150	37150 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	23554	23554 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.24532008	-3.24532008 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	*
TrqOvlSta_LKAState_State_M_enum	3	3	Y
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	- 4
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3 3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value		0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 3	3	-
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	-
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.365999997	0.365999997 ± 0.0009765625	·
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	30.2460003	30.2460003 ± 0.0009765625	-
			1

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	V



Test Step 2.52 (Repeat Count = 1)	la avai
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1 4
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	322275
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	40320
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	64392
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	24860
TrqOvlSta_Haptictime_Sec_M_f32	60530
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.9000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 4
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	60.5999985
TrqOvISta ReadytoPulse Cnt M Igc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.649999976
k_APAMaxHwTrq_HwNm_f32	4.96000004
k_APAMaxVehSpd_Kph_f32	111.599998
k_ESCMaxVehSpd_Kph_f32	139.699997
k_HapticAmplitude_MtrNm_f32	0.372000009
k_HapticDuration_Sec_f32	0.930000007
k_HapticFreq_Hz_f32 k_HapticReacttime_Sec_f32	30.7600002 0.991999984
k_LKAMaxVehSpd_Kph_f32	117.800003
k_LKAMinVehSpd_Kph_f32	108.5
k_MaxHapticReqTime_Sec_f32	0.944999993
k_SWARLimiter_HwDeg_f32	4.9000001
k_ShiftLevRevTime_Sec_f32	0.248999998
k_StandstillThresh_Kph_f32	2.48000002
k_StandstillTime_Sec_f32	0.248999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target TrqOvlSta Per1 CCWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvonwAngle_Hwbeg_isz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
· - ·	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value	

2015-02-04, 14:05:40+0530



TrqOviSta_Peri		(WA	Citab
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-625.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	611.940002		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	289.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	299.700012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4.30000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	322275	322275 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	24860	24860 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	60530	60530 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.88493013	-6.88492966 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	60.5999985	60.5999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc		4.4	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	
torget TraCydSta Dorf ADAState State enum value	1 1	1 ± 1 1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value			· ·
target_TrqOviSta_Fer1_AFAState_State_enum.value target_TrqOviSta_Per1_ESCState_State_enum.value	1	1±1	~
	1 4	1 ± 1 4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	1 4 4	1±1 4 4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	4 4 0	1±1 4 4 0	· ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	4 4 0 4	1±1 4 4 0 4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1 4 4 0 4 0	1±1 4 4 0 4 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 4 4 0 4 0 4 0 60.5999985	1 ± 1 4 4 0 4 0 ± 1 60.5999985 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.53 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	356238
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	43490
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	70938
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	26166
TrqOvlSta_Haptictime_Sec_M_f32	63535
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.55000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	61.7000008

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.660000026
k_APAMaxHwTrq_HwNm_f32	5.03999996
k_APAMaxVehSpd_Kph_f32	113.400002
k_ESCMaxVehSpd_Kph_f32	142.100006
k_HapticAmplitude_MtrNm_f32	0.377999991
k_HapticDuration_Sec_f32	0.94499993
k_HapticFreq_Hz_f32	31.2740002
k_HapticReacttime_Sec_f32	1.00800002
k_LKAMaxVehSpd_Kph_f32	119.699997
k_LKAMinVehSpd_Kph_f32	110.25
k_MaxHapticReqTime_Sec_f32	0.959999979
k_SWARLimiter_HwDeg_f32	5.19999981
k_ShiftLevRevTime_Sec_f32	0.252999991
k_StandstillThresh_Kph_f32	2.51999998
k_StandstillTime_Sec_f32	0.252999991
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target TrqOvlSta Per1 TrqOscEnable Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvlSta Per1 TrqOscFreq Hz f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-614.909973
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	616.880005
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target TrgOvlSta Per1 HwTorque HwNm f32.value	5.30000019
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	294.799988
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	304.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

2015-02-04, 14:05:40+0530





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	26166	26166 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	63535	63535 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.92737675	1.92737687 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.377999991	0.377999991 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	31.2740002	31.2740002 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.54 (Repeat Count = 1)	▼ ·
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	390201
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	46660
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	77484
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	27472
TrqOvlSta_Haptictime_Sec_M_f32	66540.6328
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0500000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	62.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.670000017
k_APAMaxHwTrq_HwNm_f32	5.11999989
k_APAMaxVehSpd_Kph_f32	115.199997
k_ESCMaxVehSpd_Kph_f32	144.5
k_HapticAmplitude_MtrNm_f32	0.384000003
k_HapticDuration_Sec_f32	0.95999979
k_HapticFreq_Hz_f32	31.7880001
k_HapticReacttime_Sec_f32	1.02400005
k_LKAMaxVehSpd_Kph_f32	121.599998
k_LKAMinVehSpd_Kph_f32	112
k_MaxHapticReqTime_Sec_f32	0.975000024
k_SWARLimiter_HwDeg_f32	5.5
k_ShiftLevRevTime_Sec_f32	0.256999999
k_StandstillThresh_Kph_f32	2.55999994
k_StandstillTime_Sec_f32	0.256999999
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnd$	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	192	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_le	ac	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	V - '		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lqc	target_TrqOvlSta_Per1_MinSecureVehicleSp target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-604.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	621.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta_Per1_ESCIsLimited Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	0		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	299.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	309.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	-1.10000002 1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	Kesuit
TrqOvISta_ESCState_State_M_enum	4	4	·
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	390201	390201 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	46660	46660 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	27472	27472 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.61499977	-5.61499977 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08			
	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32			

0

0 ± 1

TrqOvlSta_ReadytoPulse_Cnt_M_lgc

TrqOvlSta_Per1

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	62.7999992	62.7999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.384000003	0.384000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	31.7880001	31.7880001 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Input Value
target_Rte_Inst_Ap_TrqOvlSta
4
2
424164
48750.6406
84030
0
28778
69545
-5.8499999
0.287
0
0
0
0
63.9000015
0
0
0.680000007
5.1999981
117
146.899994
0.38999986
0.975000024
32.3019981
1.03999996
123.5
113.75
0.99000001
5.80000019
0.261000007
2.5999999
0.261000007
tt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target TrqOvISta Per1 APARecoverable autis_CIII_gc
target_TrqOvlSta_Per1_APAState_State_enum target TrqOvlSta Per1 CCWFound Cnt lgc
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target TrqOvlSta Per1 CWFound Cnt lgc
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
target_TrqOvlSta_Per1_ESCState_State_enum
un target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc

© Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



		• •	
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3	2 target_TrqOvlSta_Per1_MaxSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1	2	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-594.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	626.76001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	314.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1	I=	- "
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	3	3	· ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	-	0 ± 1 48750.6406 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	48750.6406 84030	84030	
TrqOvlSta HapticState State M enum	0	0	_
TrqOvlSta_Hapticdut_Cnt_M_u16	28778	28778 ± 1	
TrgOvISta Haptictime Sec M f32	69545	69545 ± 1	
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-2.53515005	-2.53515005 ± 0.000009	_
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrgOvlSta LKAPermFault Cnt M lgc	0	0 ± 1	-
TrgOvISta LKAPermFault Cnt M u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	2	2	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.389999986	0.389999986 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.56 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	458127
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	49550.3203
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	90576
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	30084
TrqOvlSta_Haptictime_Sec_M_f32	72550
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	0.298000008
TrqOvISta_LKAPermFault_Cnt_M_lgc	1
TrgOvlSta LKAPermFault Cnt M u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	65
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.68999998
k_APAMaxHwTrq_HwNm_f32	5.28000021
k_APAMaxVehSpd_Kph_f32	118.800003
k_ESCMaxVehSpd_Kph_f32	149.300003
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0.39599998 0.99000001
k_HapticFreq_Hz_f32	32.8160019
k HapticReacttime Sec f32	1.05599999
k_LKAMaxVehSpd_Kph_f32	125.400002
k_LKAMinVehSpd_Kph_f32	115.5
k_MaxHapticReqTime_Sec_f32	0.100000001
k_SWARLimiter_HwDeg_f32	6.0999999
k_ShiftLevRevTime_Sec_f32	0.264999986
k_StandstillThresh_Kph_f32	2.6400001
k_StandstillTime_Sec_f32	0.264999986
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_irqOvlSta_Peri_minSecurevenicleSpeed_kpn_t32 target_trqOvlSta_Peri_minSecurevenicleSpeed_kpn_t32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	0
	I.

2015-02-04, 14:05:40+0530



			- 10010
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-584.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	631.700012		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	309.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	319.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	2.0999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	30084	30084 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	72550	72550 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5	-5.5 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	65	65 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	65	65 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.395999998	0.395999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.8160019	32.8160019 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.57 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	492090	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	50350	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	97122	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	31390	
TrqOvlSta_Haptictime_Sec_M_f32	75555.5781	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.308999985	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	
TrqOvlSta_LKAState_State_M_enum	1	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	66.099985	

2015-02-04, 14:05:40+0530



	(
Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k APAIncludeHaptic Cnt Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.69999988
k_APAMaxHwTrq_HwNm_f32	5.36000013
k_APAMaxVehSpd_Kph_f32	120.599998
k_ESCMaxVehSpd_Kph_f32	151.699997
k_HapticAmplitude_MtrNm_f32	0.40200001
k_HapticDuration_Sec_f32	1.005
k_HapticFreq_Hz_f32	33.3300018
k_HapticReacttime_Sec_f32	1.07200003
k_LKAMaxVehSpd_Kph_f32	127.300003
k_LKAMinVehSpd_Kph_f32	117.25
k_MaxHapticReqTime_Sec_f32	0.20000003
k_SWARLimiter_HwDeg_f32	6.4000001
k_ShiftLevRevTime_Sec_f32	0.268999994
k_StandstillThresh_Kph_f32	2.68000007
k_StandstillTime_Sec_f32	0.268999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_FeT1_AFAState_state_entilli target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target TrgOvISta Per1 CCWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_Hwbeg_I32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target TrqOvlSta_Per1_CWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_lgc	target_TrqOvIsta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-574.51001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	636.640015
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0
	1
target TrgOvlSta Per1 LKARequest Cnt lac.value	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	314.399994
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	314.39994 324.200012
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	324.200012
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	50350	50350 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	31390	31390 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.67375016	-1.67375004 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.40200001	0.40200001 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	33.3300018	33.3300018 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.58 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	526053
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51149.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	103668
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	32696
TrqOvlSta_Haptictime_Sec_M_f32	78560
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.319999993
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	67.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.709999979
k_APAMaxHwTrq_HwNm_f32	5.44000006
k_APAMaxVehSpd_Kph_f32	122.400002
k_ESCMaxVehSpd_Kph_f32	154.100006
k_HapticAmplitude_MtrNm_f32	0.407999992
k_HapticDuration_Sec_f32	1.01999998
k_HapticFreq_Hz_f32	33.8440018
k_HapticReacttime_Sec_f32	1.08800006
k_LKAMaxVehSpd_Kph_f32	129.199997
k_LKAMinVehSpd_Kph_f32	119
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	6.69999981
k_ShiftLevRevTime_Sec_f32	0.273000002
k_StandstillThresh_Kph_f32	2.72000003
k_StandstillTime_Sec_f32	0.273000002
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_In$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	Faults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CWFosition_Hwbeg_is2	target_TrqOvlSta_Per1_CCWPosition_HwDe target TrqOvlSta Per1 CWFound Cnt lgc	g_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lq	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn target_TrqOvlSta_Per1_HapticRequest_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
	target_TrqOvlSta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
	target_TrqOvISta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-564.409973 0		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	641.580017		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKARIIIIIIII Crit_igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	319.299988		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	329.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.30000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	*
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0 51149.6797	0 ± 1 51149.6797 ± 1	· ·
TrqOvISta_ElapsedSnintLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	51149.6797 ± 1 0	
TrqOvISta_ElapseuStatiustill lilie_Sec_iw_i32 TrqOvISta_HapticState_State_M_enum	2	2	•
TrgOvISta Hapticotate_State_M_Chain TrgOvISta Hapticdur Cnt M u16	32696	32696 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlOta_PosSrvoHwAngle_HwDeg_M_f32	67.1999969	67.1999969 ± 0.0009765625	· ·
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	67.1999969	67.1999969 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.407999992	0.407999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	33.8440018	33.8440018 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.59 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrgOvlSta
TrqOvISta_APAState_State_M_enum	1
TrgOvISta ESCState State M enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	560016
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51949.3594
TrqOvlSta ElapsedStandstillTime Sec M f32	110214
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta Hapticdur Cnt M u16	34002
TrqOvlSta_Haptictime_Sec_M_f32	81565.6406
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-4.44999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.331
TrgOvISta LKAFault Cnt M lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	68.3000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k APAIncludeHaptic Cnt Igc	1
k APAMaxHwTrqTime Sec f32	0.720000029
k APAMaxHwTrg HwNm f32	5.51999998
k APAMaxVehSpd Kph f32	124.199997
k ESCMaxVehSpd Kph f32	156.5
k HapticAmplitude MtrNm f32	0.414000005
k_HapticDuration_Sec_f32	1.03499997
k_HapticFreq_Hz_f32	34.3580017
k_HapticReacttime_Sec_f32	1.10399997
k LKAMaxVehSpd Kph f32	131.100006
k_LKAMinVehSpd_Kph_f32	120.75
k_MaxHapticReqTime_Sec_f32	0.40000006
k SWARLimiter HwDeg f32	7
k_ShiftLevRevTime_Sec_f32	0.27700001
 k_StandstillThresh_Kph_f32	2.75999999
k_StandstillTime_Sec_f32	0.27700001
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrqOvlSta Per1 APAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target TrqOvlSta Per1 CCWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target TrqOvlSta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target TrqOvISta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
0 = = = 1	

2015-02-04, 14:05:40+0530





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_e	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_	f32 target_TrqOvlSta_Per1_MaxSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_	target_TrqOvlSta_Per1_MinSecureVehicles	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_C	nt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosSrvoHwAngle_	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn	t_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosTrajHwAngle_I	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg	_f3 target_TrqOvlSta_Per1_SWARTrgtAngReq	uest_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgcaps and the property of the p$	target_TrqOvlSta_Per1_ShiftLeverIsInReve	erse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNr	m_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-554.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	646.52002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target TrqOvlSta_Per1_HwTorque HwNm f32.value	6.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta_Per1_ErxNequest_Crit_igc.value target TrqOvlSta_Per1_MaxSecureVehicleSpeed Kph f32.value	324.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	334		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.900001		
target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	0	0	
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	~
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	~
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	34002	34002 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	81565.6406	81565.6406 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981	-4.44999981 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.414000005	0.414000005 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	0.414000005 0 34.3580017	0.414000005 ± 0.0009765625 0 ± 1 34.3580017 ± 0.0009765625	V

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.60 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrqOvISta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	593979
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	52749.0391
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	116760
TrqOvlOta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	35308 84570
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.0999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342000008
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	69.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0 0.730000019
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	5.5999999
k APAMaxVehSpd Kph f32	126
k_ESCMaxVehSpd_Kph_f32	158.899994
k_HapticAmplitude_MtrNm_f32	0.419999987
k_HapticDuration_Sec_f32	1.04999995
k_HapticFreq_Hz_f32	34.8720016
k_HapticReacttime_Sec_f32	1.12
k_LKAMaxVehSpd_Kph_f32	133
k_LKAMinVehSpd_Kph_f32	122.5
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32 k ShiftLevRevTime Sec f32	7.3000019 0.280999988
k StandstillThresh Kph f32	2.7999995
k StandstillTime Sec f32	0.280999988
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enumerator and the property of the pro$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target TrqOvISta Per1 LKAInhibit Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_inst_Ap_frqOviSta.frqOviSta_Peri_SniftLeverisinReverse_Cnt_gc target_Rte_inst_Ap_frqOviSta.frqOviSta_Peri_frqOscAmp_MtrNm_f32	target_IrqOviSta_Per1_SnintLeverisinReverse_Cnt_igc target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530



Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-544.210022		
target TrqOvlSta Per1 CWFound Cnt lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	651.460022		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	0		
target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_igc.value	0		
target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	6.69999981		
target_TrqOviSta_Per1_LKAFault_Cnt_igc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target TrqOvlSta Per1 LKARequest Cnt lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	338.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	8.5		
target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0.5		
		Formated Wales	D14
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	- V
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	- J
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	116760	116760	
TrqOvlSta_HapticState_State_M_enum	35308	1	Ž
TrqOvlSta_Hapticdur_Cnt_M_u16		35308 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999	-4.0999999 ± 0.000009	Ž
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	- V
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	- V
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlOta_PosSrvoHwAngle_HwDeg_M_f32	69.4000015	69.4000015 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	1 ± 1 0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value			
target_TrqOvISta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1 4	
target_TrqOvlSta_Per1_LKAState_State_enum.value	4		- J
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	Ž
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	69.4000015	69.4000015 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.419999987	0.419999987 ± 0.0009765625	· ·
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	34.8720016	34.8720016 ± 0.0009765625	<u> </u>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.61 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	627942	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	53548.7188	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	123306	
TrqOvlSta_HapticState_State_M_enum	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	36614	
TrqOvlSta_Haptictime_Sec_M_f32	87575	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.352999985	
TrqOvlSta_LKAFault_Cnt_M_lgc	0.	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.5	

2015-02-04, 14:05:40+0530



	(14 17 10 10
Name	Input Value
	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.74000001
k_APAMaxHwTrq_HwNm_f32	5.67999983
k_APAMaxVehSpd_Kph_f32	127.800003
k_ESCMaxVehSpd_Kph_f32	161.300003
k_HapticAmplitude_MtrNm_f32	0.425999999
	1.06500006
- ' '	35.3860016
	1.13600004
	134.899994
k_LKAMinVehSpd_Kph_f32	124.25
	0.600000024
	7.599999 0.28499996
	2.83999991
	0.284999996
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target TrgOvlSta Per1 APARecoverableFaults Cnt lgc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvlSta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvISta_Per1_HwTorque_HwNm_f32
	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
0 = 1 = = = = = = = = = = = = = = = = =	0
· - · · ·	0
3-2 4	1
arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-534.109985
312 11 112 1 21 11 12 23	1
	656.400024
312 11 112 1 2 11 11 21 23 111	1
	1
0 = 1 = 2 = 20	1
	1
	1
argot_riqoviota_r or r_orridor r_cronvingcriable_ont_lgc.value	0
arget TraCy/Sta Per1 HanticRequest Cnt Ide value	
	6 900001
arget_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.9000001
arget_TrqOvlSta_Per1_HwTorque_HwNm_f32.value arget_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1 1 1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1 1 1 334

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	53548.7188	53548.7188 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	36614	36614 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75	-3.75 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.425999999	0.425999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	35.3860016	35.3860016 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.62 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	797757
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	57547.1211
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	156036
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	43144
TrqOvlSta_Haptictime_Sec_M_f32	102600
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1.
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	74.9000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.779999971
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003
k_ESCMaxVehSpd_Kph_f32	170.899994
k_HapticAmplitude_MtrNm_f32	0.456
k_HapticDuration_Sec_f32	1.13999999
k_HapticFreq_Hz_f32	37.4420013
k_HapticReacttime_Sec_f32	1.21599996
k_LKAMaxVehSpd_Kph_f32	144.399994
k_LKAMinVehSpd_Kph_f32	133
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	8.80000019
k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.03999996
k_StandstillTime_Sec_f32	0.305000007
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



· -			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	•	leFaults Cnt Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_I	gc	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrqOvlSta Per1 APAState State er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lg	c	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWPosition HwDeg f32	target TrqOvlSta Per1 CCWPosition HwD		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	-0	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDe	ı f32	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	5	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target TrqOvISta Per1 ESCIsLimited Cnt	lac	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_Igc	target TrqOvISta Per1 ESCRequest Cnt I	•	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target TrqOvISta Per1 ESCState State er	•	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSHOscillateState State enu			
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	V - '		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc	target TrqOvISta_Fe1_FiwTorque_FiwNin_	102	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	•	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	- -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	1_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-524.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	661.340027		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	353.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	363.39994		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
		Expected Value	Descri
Name Tracyclete ADAState State M. cours	Actual Value	Expected Value	Result
TrqOvlOta_APAState_State_M_enum	0	0	
TrqOvISta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	57547.1211	57547.1211 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	43144	43144 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.03919983	2.03920007 ± 0.000009	•
TrqOvISta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_Igc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	-

TrqOvlSta_Per1

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.456	0.456 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	37.4420013	37.4420013 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Alama	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	831720
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	58346.8008
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	162582
TrqOvlSta_HapticState_State_M_enum	0
FrqOvISta_Hapticdur_Cnt_M_u16	44450
FrqOvISta_Haptictime_Sec_M_f32	105605
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
FrqOvlSta_LKAFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	76
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic_Cnt_lgc	1
<pre>c_viralionaddinaptio_chit_igo</pre> <pre>c_APAMaxHwTrqTime_Sec_f32</pre>	0.790000021
APAMaxHwTrg HwNm f32	6.15999985
CAPAMaxVehSpd_Kph_f32	138.600006
CSCMaxVehSpd Kph f32	
- · · - · -	173.300003 0.462000012
x_HapticAmplitude_MtrNm_f32	
:_HapticDuration_Sec_f32	1.15499997
K_HapticFreq_Hz_f32	37.9560013
x_HapticReacttime_Sec_f32	1.23199999
x_LKAMaxVehSpd_Kph_f32	146.300003
C_LKAMinVehSpd_Kph_f32	134.75
C_MaxHapticReqTime_Sec_f32	0.600000024
s_SWARLimiter_HwDeg_f32	9.10000038
C_ShiftLevRevTime_Sec_f32	0.308999985
c_StandstillThresh_Kph_f32	3.07999992
_StandstillTime_Sec_f32	0.308999985
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Ci	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrgOvlSta Per1 ESCRequest Cnt lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
· · · · ·	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_en	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lg	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igr	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



		• • •	
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target TrqOvlSta Per1 APANonRecoverableFaults Cnt lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-513.909973		
target TrqOvlSta Per1 CWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	666.280029		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	358.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	368.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	4
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	Y
TrqOvlSta_HapticState_State_M_enum	0	0	Y
TrqOvISta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	· ·
TrqOvlSta_Haptictime_Sec_M_f32		0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009 0 ± 1	-
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2±1	-
TrqOvISta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	76	76 ± 0.0009765625	-
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	_
target TrqOvlSta Per1 APAState State enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value		4	•
0 _ 1	4		
target TrgOvlSta Per1 GMOSHOscillateState State enum.value	0	0	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target TrqOvlSta Per1 LKAState State enum.value		0	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt lgc.value	0		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvlSta_Per1_LKAState_State_enum.value	0 4	4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 4 0	4 0 ± 1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 4 0 76	4 0 ± 1 76 ± 0.0009765625	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~	
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V	





Test Step 2.64 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	865683
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	59146.4805
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	169128
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	108610.617
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-2 0.407999992
TrqOvISta_LKAFault_Cnt_M_lgc	0.40733332
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	77.0999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.800000012
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003 175.600007
k_ESCMaxVehSpd_Kph_f32	175.699997 0.456
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	1.13999999
k_HapticFreq_Hz_f32	38.4700012
k_HapticReacttime_Sec_f32	1.21599996
k_LKAMaxVehSpd_Kph_f32	144.399994
k_LKAMinVehSpd_Kph_f32	133
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	9.39999962
k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.0399996
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.305000007 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_IrqOviSta_Per1_minSecurevenicieSpeed_kpn_t32 target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
	1

2015-02-04, 14:05:40+0530



rrqOviSta_Peri		(WA)	CILAB
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-503.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	671.219971		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	363.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	373.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	19.7000008		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	59146.4805	59146.4805 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value		1 ± 1 0.189999998 ± 0.0009765625	~
	1		
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 0.18999998	0.189999998 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.65 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	899646
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	59946.1602
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	175674
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	65535
TrqOvlSta_Haptictime_Sec_M_f32	111615
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	78.1999969

2015-02-04, 14:05:40+0530



Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.810000002
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	178.100006
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997 38.9840012
k_HapticFreq_Hz_f32 k HapticReacttime Sec f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	9.69999981
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target TrgOvlSta Per1 APARecoverableFaults Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrgOvlSta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target TrqOvlSta Per1 TrqOscEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-493.709991
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target TrqOvlSta Per1 CWPosition HwDeg f32.value	0 676.159973
target_IrqOviSta_Peri_CwPosition_HwDeg_132.value target TrqOviSta_Peri_ESCFault_Cnt_lgc.value	0
target TrqOvISta_Per1_ESGFatit_Off_igc.value	0
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LtArkequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value	378.100006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	21.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	59946.1602	59946.1602 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	175674	175674	~
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	65535	65535 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	78.1999969	78.1999969 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	78.1999969	78.1999969 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	38.9840012	38.9840012 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.66 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	933609
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	60745.8398
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	182220
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	35640
TrqOvlSta_Haptictime_Sec_M_f32	114620
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	79.3000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.819999993
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	11.3000002
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	39.4980011
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	10
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnnote the property of the property $	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.		-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvISta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-483.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1		
	681.099976		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value			
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	373.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	383		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.8999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	60745.8398	60745.8398 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	35640	35640 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.64000000	-1.64999998 ± 0.000009	•
	-1.64999998		
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	→
		0 ± 1 0 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc	0		•
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 0 2	0 ± 1 2 ± 1	

2015-02-04, 14:05:40+0530

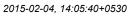




Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	39.4980011	39.4980011 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.67 (Repeat Count = 1)	
	Input Value
Rte Inst Ap TrgOvISta	target Rte Inst Ap TrqOvlSta
rqOvISta_APAState_State_M_enum	1
rqOvISta_ESCState_State_M_enum	2
rqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	967572
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	61545.5195
rqOvISta ElapsedStandstillTime Sec M f32	188766
rqOvISta_HapticState_State_M_enum	2
rgOvISta Hapticdur Cnt M u16	49674
rqOvISta_Haptictime_Sec_M_f32	117625.852
rgOviSta HwTorqueSV HwNm M Str.SV Uls f32	-0.25
rgOvISta HwTorqueSV HwNm M Str.K Uls f32	0.463
rqOviSta LKAFault Cnt M lqc	0
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	1
rqOvISta_LKAState_State_M_enum	2
rqOvlSta_ErxAstate_state_w_entitiii rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	80.4000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
APAIncludeHaptic Cnt Igc	1
APAMaxHwTrqTime Sec f32	0.829999983
APAMaxHwTrq HwNm f32	6.48000002
APAMaxVehSpd Kph f32	145.800003
ESCMaxVehSpd_Kph_f32	16.399996
_LapticAmplitude_MtrNm_f32	0.486000001
HapticDuration Sec f32	1.21500003
_HapticFreq_Hz_f32	40.012001
_HapticReacttime_Sec_f32	1.296
LKAMaxVehSpd Kph f32	153.899994
_LKAMinVehSpd_Kph_f32	141.75
MaxHapticReqTime_Sec_f32	0
SWARLimiter HwDeg f32	10.3000002
_ShiftLevRevTime_Sec_f32	0.324999988
_StandstillThresh_Kph_f32	3.24000001
_standstillTime_Sec_f32	0.324999988
_3tandstiff inte_3et_132 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target_frqOviSta_Per1_CCWPosition_Hwbeg_is2 target_frqOviSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target TrqOvlSta Per1 ESCFault Cnt lgc
arget_Rte_inst_Ap_rrqOviSta.rrqOviSta_Perr_ESCPault_Cnt_igc arget_Rte_inst_Ap_rrqOviSta.TrqOviSta_Perr_ESCIsLimited_Cnt_igc	target_TrqOvISta_Per1_ESCFault_Crit_igc target_TrqOvISta_Per1_ESCIsLimited_Crit_igc
· · · · · ·	target_TrqOviSta_Per1_ESCISLIMILEd_Crit_igc target_TrqOviSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	
arget_Rte_inst_Ap_1rqOviSta.1rqOviSta_Per1_ESCState_State_enum arget Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSHOscillateState State enu	target_TrqOvISta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo arget_Pta_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfqEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3	2 target_TrqOvlSta_Per1_MaxSecureVehicle	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3.	2 target_TrqOvlSta_Per1_MinSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_l	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_t	f3. target_TrqOvlSta_Per1_SWARTrgtAngReq	uest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve	rse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn	n_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-473.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	686.039978		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	378.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	387.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4	4 0±1	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4 0 0	4 0±1 0±1	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4 0 0 0	4 0±1 0±1 0	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	4 0 0 0 0 2	4 0±1 0±1 0	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	4 0 0 0 0 2 49674	4 0±1 0±1 0 2 49674±1	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	4 0 0 0 2 49674 117625.852	4 0±1 0±1 0 2 49674±1 117625.852±1	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	4 0 0 0 2 49674 117625.852 -0.25	4 0±1 0±1 0 2 49674±1 117625.852±1 -0.25±0.0000009	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc	4 0 0 0 2 49674 117625.852 -0.25	4 0±1 0±1 0 2 49674±1 117625.852±1 -0.25±0.0000009 1±1	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	4 0 0 0 2 49674 117625.852 -0.25 1	4 0±1 0±1 0 2 49674±1 117625.852±1 -0.25±0.0000009 1±1 0±1	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	4 0 0 0 2 49674 117625.852 -0.25 1 0	4 0±1 0±1 0 2 49674±1 117625.852±1 -0.25±0.0000009 1±1 0±1 2±1	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	4 0 0 0 2 49674 117625.852 -0.25 1 0	4 0±1 0±1 0 2 49674±1 117625.852±1 -0.25±0.0000009 1±1 0±1 2±1	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015	$\begin{array}{c} 4 \\ 0 \pm 1 \\ 0 \pm 1 \\ 0 \\ 2 \\ 49674 \pm 1 \\ 117625.852 \pm 1 \\ -0.25 \pm 0.0000009 \\ 1 \pm 1 \\ 0 \pm 1 \\ 2 \pm 1 \\ 4 \\ 80.4000015 \pm 0.0009765625 \end{array}$	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015	$\begin{array}{c} 4 \\ 0 \pm 1 \\ 0 \pm 1 \\ 0 \\ 2 \\ 49674 \pm 1 \\ 117625.852 \pm 1 \\ -0.25 \pm 0.0000009 \\ 1 \pm 1 \\ 0 \pm 1 \\ 2 \pm 1 \\ 4 \\ 80.4000015 \pm 0.0009765625 \\ 0 \pm 1 \end{array}$	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015 0	$\begin{array}{c} 4 \\ 0 \pm 1 \\ 0 \pm 1 \\ 0 \\ 2 \\ 49674 \pm 1 \\ 117625.852 \pm 1 \\ -0.25 \pm 0.0000009 \\ 1 \pm 1 \\ 0 \pm 1 \\ 2 \pm 1 \\ 4 \\ 80.4000015 \pm 0.0009765625 \\ 0 \pm 1 \\ 0 \pm 1 \\ \end{array}$	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015 0 0	$\begin{array}{c} 4 \\ 0 \pm 1 \\ 0 \pm 1 \\ 0 \\ 2 \\ 49674 \pm 1 \\ 117625.852 \pm 1 \\ -0.25 \pm 0.0000009 \\ 1 \pm 1 \\ 0 \pm 1 \\ 2 \pm 1 \\ 4 \\ 80.4000015 \pm 0.0009765625 \\ 0 \pm 1 \\ 0 \pm 1 \\ 4 \\ \end{array}$	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015 0 0 4	$ 4 \\ 0 \pm 1 \\ 0 \pm 1 \\ 0 \\ 2 \\ 49674 \pm 1 \\ 117625.852 \pm 1 \\ -0.25 \pm 0.0000009 \\ 1 \pm 1 \\ 0 \pm 1 \\ 2 \pm 1 \\ 4 \\ 80.4000015 \pm 0.0009765625 \\ 0 \pm 1 \\ 0 \pm 1 \\ 4 \\ 4 $	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015 0 0 4 4	$ 4 \\ 0 \pm 1 \\ 0 \pm 1 \\ 0 \\ 2 \\ 49674 \pm 1 \\ 117625.852 \pm 1 \\ -0.25 \pm 0.0000009 \\ 1 \pm 1 \\ 0 \pm 1 \\ 2 \pm 1 \\ 4 \\ 80.4000015 \pm 0.0009765625 \\ 0 \pm 1 \\ 0 \pm 1 \\ 4 \\ 4 \\ 2 $	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015 0 0 4 4 4 2	$ 4 \\ 0 \pm 1 \\ 0 \pm 1 \\ 0 \\ 2 \\ 49674 \pm 1 \\ 117625.852 \pm 1 \\ -0.25 \pm 0.0000009 \\ 1 \pm 1 \\ 0 \pm 1 \\ 2 \pm 1 \\ 4 \\ 80.4000015 \pm 0.0009765625 \\ 0 \pm 1 \\ 0 \pm 1 \\ 4 \\ 4 \\ 2 \\ 4 \\ 4 \\ 2 \\ 4 \\ 4 \\ 4 \\ 2 \\ 4 $	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticIme_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015 0 0 4 4 4 2 4	4 0 ± 1 0 ± 1 0 2 49674 ± 1 117625.852 ± 1 -0.25 ± 0.0000009 1 ± 1 0 ± 1 2 ± 1 4 80.4000015 ± 0.0009765625 0 ± 1 0 ± 1 4 4 4 2 4 0 ± 1	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value	4 0 0 0 0 0 0 2 49674 117625.852 -0.25 1 0 0 2 4 4 80.4000015 0 0 4 4 4 2 2 4 4 0 80.4000015	$ 4 \\ 0 \pm 1 \\ 0 \pm 1 \\ 0 \\ 2 \\ 49674 \pm 1 \\ 117625.852 \pm 1 \\ -0.25 \pm 0.0000009 \\ 1 \pm 1 \\ 0 \pm 1 \\ 2 \pm 1 \\ 4 \\ 80.4000015 \pm 0.0009765625 \\ 0 \pm 1 \\ 4 \\ 4 \\ 2 \\ 4 \\ 0 \pm 1 \\ 80.4000015 \pm 0.0009765625 $	
TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticIme_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	4 0 0 0 2 49674 117625.852 -0.25 1 0 2 4 80.4000015 0 0 4 4 4 2 4	4 0 ± 1 0 ± 1 0 2 49674 ± 1 117625.852 ± 1 -0.25 ± 0.0000009 1 ± 1 0 ± 1 2 ± 1 4 80.4000015 ± 0.0009765625 0 ± 1 0 ± 1 4 4 4 2 4 0 ± 1	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.68 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1001535
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	62345.1992
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	195312
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	50980
TrqOvlSta_Haptictime_Sec_M_f32	120630
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.100000001 0.474000007
TrgOvlSta LKAFault Cnt M lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	0
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	81.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.839999974
k_APAMaxHwTrq_HwNm_f32	6.55999994
k_APAMaxVehSpd_Kph_f32	147.600006
k_ESCMaxVehSpd_Kph_f32	21.5 0.492000014
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	1.23000002
k_HapticFreq_Hz_f32	40.526001
k_HapticReacttime_Sec_f32	1.31200004
k_LKAMaxVehSpd_Kph_f32	155.800003
k_LKAMinVehSpd_Kph_f32	143.5
k_MaxHapticReqTime_Sec_f32	1
k_SWARLimiter_HwDeg_f32	10.6000004
k_ShiftLevRevTime_Sec_f32	0.328999996
k_StandstillThresh_Kph_f32	3.27999997
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.328999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc target TrqOvlSta Per1 ESCRequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	V - 1
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_IrqOviSta.IrqOviSta_Peri_MaxSecurevenicleSpeed_Kpn_132 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Peri_MinSecureVehicleSpeed_Kph_132	target_rqovista_Peri_maxsecureveniciespeed_kpn_rs2 target_rqovista_Peri_maxsecureveniciespeed_kpn_rs2
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-463.410004		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	690.97998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	383		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	392.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	62345.1992	62345.1992 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	50980	50980 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	120630	120630 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001	0.100000001 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvISta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	~
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.492000014	0.492000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.69 (Repeat Count = 1)	variable and the second of
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1035498
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63144.8789
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	201858
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	52286
TrqOvlSta_Haptictime_Sec_M_f32	123635
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	82.5999985

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.850000024
k_APAMaxHwTrq_HwNm_f32	6.63999987
k_APAMaxVehSpd_Kph_f32	149.399994
k_ESCMaxVehSpd_Kph_f32	26.6000004
k_HapticAmplitude_MtrNm_f32	0.497999996
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	1.245 41.0400009
k HapticReacttime Sec f32	1.32799995
k_LKAMaxVehSpd_Kph_f32	157.699997
k_LKAMinVehSpd_Kph_f32	145.25
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	10.8999996
k_ShiftLevRevTime_Sec_f32	0.333000004
k_StandstillThresh_Kph_f32	3.31999993
k_StandstillTime_Sec_f32	0.333000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-453.309998
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	695.919983 0
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-7.5999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LnArkequest_Cnt_gc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	387.899994
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	397.700012
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

© Report created by TESSY V3.1.9, report template V2.1



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	27.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63144.8789	63144.8789 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	52286	52286 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	123635	123635 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988	0.449999988 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	82.5999985	82.5999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	82.5999985	82.5999985 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.497999996	0.497999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	41.0400009	41.0400009 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.70 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1069461
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63944.5586
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	208404
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	53592
TrqOvlSta_Haptictime_Sec_M_f32	126640.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.800000012
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	83.6999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.860000014
k_APAMaxHwTrq_HwNm_f32	6.71999979
k_APAMaxVehSpd_Kph_f32	151.199997
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.504000008
k_HapticDuration_Sec_f32	1.25999999
k_HapticFreq_Hz_f32	41.5540009
k_HapticReacttime_Sec_f32	1.34399998
k_LKAMaxVehSpd_Kph_f32	159.600006
k_LKAMinVehSpd_Kph_f32	147
k_MaxHapticReqTime_Sec_f32	0.60000024
k_SWARLimiter_HwDeg_f32	11.1999998
k_ShiftLevRevTime_Sec_f32	0
k_StandstillThresh_Kph_f32	3.3599999
k_StandstillTime_Sec_f32	0.100000001
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-443.209991		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	700.859985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-7.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	402.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	29.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63944.5586	63944.5586 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	208404	208404	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	53592	53592 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	126640.641	126640.641 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.800000012	0.800000012 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.504000008	0.504000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	41.5540009	41.5540009 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
	•
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1103424
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	64744.2383
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	214950
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	54898
TrqOvlSta_Haptictime_Sec_M_f32	129645
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.14999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	84.8000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.870000005
k_APAMaxHwTrq_HwNm_f32	6.80000019
k_APAMaxVehSpd_Kph_f32	153
k_ESCMaxVehSpd_Kph_f32	36.7999992
k_HapticAmplitude_MtrNm_f32	0.5099999
k_HapticDuration_Sec_f32	1.27499998
k_HapticFreq_Hz_f32	42.0680008
k_HapticReacttime_Sec_f32	1.36000001
k LKAMaxVehSpd Kph f32	161.5
k_LKAMinVehSpd_Kph_f32	148.75
k MaxHapticRegTime Sec f32	0.69999988
k_SWARLimiter_HwDeg_f32	11.5
k_ShiftLevRevTime_Sec_f32	0.5
k_StandstillThresh_Kph_f32	3.4000001
k StandstillTime Sec f32	0.47999989
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	
:arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_C	
target_Rte_Inst_Ap_TrqOviota.TrqOviota_Fer1_APARecoverableFaults_Cnt_it	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
target Rte Inst Ap TrqOvIsta.TrqOvISta Per1 CCWFound Cnt Igc	target TrqOvlSta Per1 CCWFound Cnt Igc
rarget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_frqOviSta_ren_ccvvround_cnt_igc target_frqOviSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCVVFosition_nwDeg_is2 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
	target_rrqOvlSta_Per1_CWPoultiq_Cnt_igc target TrqOvlSta_Per1_CWPosition_HwDeg_f32
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_Igc
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc target TrqOvISta Per1 ESCRequest Cnt Iqc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	0 - 1 1 0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_h		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target TrqOvlSta Per1 APANonRecoverableFaults Cnt lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANotiRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target TrqOvISta Per1 CCWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-433.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	705.799988		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.5999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	397.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	407.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	64744.2383	64744.2383 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0 2	0	~
TrqOvISta_HapticState_State_M_enum	54898	2	
TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	0	54898 ± 1 0 ± 1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.14999998	1.14999998 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1±1	_
TrqOvlSta LKAPermFault Cnt M Igc	1	1±1	
TrgOvISta LKAPermFault Cnt M u08	1	1±1	~
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	84.8000031	84.8000031 ± 0.0009765625	•
TrgOvlSta ReadytoPulse Cnt M Igc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	84.8000031	84.8000031 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.50999999	0.50999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	42.0680008	42.0680008 ± 0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



est Step 2.72 (Repeat Count = 1)	Input Value
te_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1137387
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	65543.9219
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	221496
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	56204
rqOvlSta_Haptictime_Sec_M_f32	132650
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.518000007
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	85.9000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	0.87999995
APAMaxHwTrq_HwNm_f32	6.88000011
APAMaxVehSpd_Kph_f32	154.800003
ESCMaxVehSpd_Kph_f32	41.9000015 0.515999973
HapticAmplitude_MtrNm_f32 HapticDuration_Sec_f32	1.28999996
HapticFreq_Hz_f32	42.5820007
HapticReacttime_Sec_f32	1.37600005
_LKAMaxVehSpd_Kph_f32	163.399994
LKAMinVehSpd_Kph_f32	150.5
MaxHapticReqTime_Sec_f32	0.800000012
SWARLimiter_HwDeg_f32	11.8000002
ShiftLevRevTime_Sec_f32	0.300000012
StandstillThresh Kph f32	3.4400006
StandstillTime_Sec_f32	0.349999994
 irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
rget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
rrget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value rrget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
rger transvista peri Apapeleet Cet lacualus	

2015-02-04, 14:05:40+0530



Trigovista_r err			
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-423.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	710.73999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	402.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	412.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	32.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	rtooun
TrgOvISta ESCState State M enum	3	3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvISta ElapsedShiftLevRevTime Sec M f32	65543.9219	65543.9219 ± 1	
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrqOvlSta HapticState State M enum	0	0	_
TrqOvISta_Hapticdur_Cnt_M_u16	56204	56204 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.5	1.5 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta LKAPermFault Cnt M Igc	0	0 ± 1	
	2	2 ± 1	
TrqOvISta_LKAStoto_State_M_agum	2	2	
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.170000002	0.170000002 ± 0.0009765625	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0.170000002	0.170000002 ± 0.0009763625	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1 0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
	3	3	
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	
target_TrqOvlSta_Per1_LKAState_State_enum.value			
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.515999973	0.515999973 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	· ·
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	42.5820007	42.5820007 ± 0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.73 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1171350
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	66343.6016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	228042
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	57510
TrqOvlSta_Haptictime_Sec_M_f32	135655.938
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.85000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.528999984
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	87

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.889999986
k_APAMaxHwTrq_HwNm_f32	6.96000004
k_APAMaxVehSpd_Kph_f32	156.600006
k_ESCMaxVehSpd_Kph_f32	47
k_HapticAmplitude_MtrNm_f32	0.522000015
k_HapticDuration_Sec_f32	1.30499995 98.5240021
k_HapticFreq_Hz_f32 k HapticReacttime Sec f32	1.39199996
k_LKAMaxVehSpd_Kph_f32	165.300003
k_LKAMinVehSpd_Kph_f32	152.25
k_MaxHapticReqTime_Sec_f32	0.89999976
k_SWARLimiter_HwDeg_f32	12.1000004
k_ShiftLevRevTime_Sec_f32	0.360000014
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.360000014
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target TrgOvlSta Per1 APARecoverableFaults Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrgOvlSta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target TrgOvlSta Per1 TrgOscEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-412.910004
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target TrqOvlSta Per1 CWPosition HwDeg f32.value	0 715.679993
target_IrqOviSta_Peri_CwPosition_HwDeg_132.value target TrqOviSta_Peri_ESCFault_Cnt_lgc.value	0
target TrqOvISta_Per1_ESGFatit_Off_igc.value	0
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-5.5999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LtArkequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	407.5
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value	417.299988
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-33.0999985		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	57510	57510 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	135655.938	135655.938 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.09104967	-2.09104991 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	87	87 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	87	87 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.522000015	0.522000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	98.5240021	98.5240021 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.74 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOviSta
TrqOvISta APAState State M enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1205313
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	67143.2813
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	234588
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	58816
TrqOvlSta_Haptictime_Sec_M_f32	138660
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.9000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.540000021
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	88.0999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.899999976
k_APAMaxHwTrq_HwNm_f32	7.03999996
k_APAMaxVehSpd_Kph_f32	158.399994
k_ESCMaxVehSpd_Kph_f32	52.0999985
k_HapticAmplitude_MtrNm_f32	0.527999997
k_HapticDuration_Sec_f32	1.32000005
k_HapticFreq_Hz_f32	85
k_HapticReacttime_Sec_f32	1.40799999
k_LKAMaxVehSpd_Kph_f32	167.199997
k_LKAMinVehSpd_Kph_f32	154
k_MaxHapticReqTime_Sec_f32	0.0149999997
k_SWARLimiter_HwDeg_f32	12.3999996
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	5
k_StandstillTime_Sec_f32	0.00100000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	V - '		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosServEnable_Cnt		
	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target TrqOvlSta Per1 APANonRecoverableFaults Cnt lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	:	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-402.809998		
target_TrqOvISta_Per1_CWFound_Cnt_Igc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	720.619995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrgOvISta Per1 HwTorque HwNm f32.value	4.69999981		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	412.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	422.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f32.value	-31.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvISta APAState State M enum	0	0	
TrqOvISta_ESCState_State_M_enum	4	4	~
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	-
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	_
TrqOvlSta_HapticState_State_M_enum	0	0	·
TrqOvlSta_Hapticdur_Cnt_M_u16	58816	58816 ± 1	_
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.9000001	-6.9000001 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc		0 ± 1	✓
	0		
	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_lgc		0 ± 1 3 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0		~
TrqOvISta_LKAPermFault_Cnt_M_lgc	0 3	3 ± 1	~

target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value

TrqOvlSta_Per1

2015-02-04, 14:05:40+0530



85 ± 0.0009765625

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.18999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.527999997	0.527999997 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

85

Test Step 2.75 (Repeat Count = 1)	ر.
Name	Input Value
	•
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1239276
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	67942.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	241134
TrqOvISta_HapticState_State_M_enum	60122
TrqOvlSta_Hapticdur_Cnt_M_u16	
TrqOvlSta_Haptictime_Sec_M_f32	141665
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.55000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.550999999
TrqOvlSta_LKAFault_Cnt_M_lgc	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvISta_LKAState_State_M_enum	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	89.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.910000026
k_APAMaxHwTrq_HwNm_f32	7.11999989
k_APAMaxVehSpd_Kph_f32	160.199997
k_ESCMaxVehSpd_Kph_f32	57.2000008
k_HapticAmplitude_MtrNm_f32	0.533999979
k_HapticDuration_Sec_f32	1.33500004
k_HapticFreq_Hz_f32	72.697998
k_HapticReacttime_Sec_f32	1.42400002
k_LKAMaxVehSpd_Kph_f32	169.100006
k_LKAMinVehSpd_Kph_f32	155.75
k_MaxHapticReqTime_Sec_f32	0.029999993
k_SWARLimiter_HwDeg_f32	12.699998
k_ShiftLevRevTime_Sec_f32	0.0049999989
k_StandstillThresh_Kph_f32	3.5
k_StandstillTime_Sec_f32	0.0049999989
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

© Report created by TESSY V3.1.9, report template V2.1





	1		
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_e		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_h		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeq			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReve		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-392.709991		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	725.559998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	427.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-29.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	67942.9609	67942.9609 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	241134	241134	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	60122	60122 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.55000019	-6.55000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	89.1999969	89.1999969 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	89.1999969	89.1999969 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.533999979	0.533999979 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
	72.697998	72.697998 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	72.697998	72.697998 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.76 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1273239
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	68742.6406
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	247680
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	61428
TrqOvlSta_Haptictime_Sec_M_f32	144670
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-6.1999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.561999977
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAFault_Cnt_M_lga	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvISta_LKAState_State_M_enum	4
TrgOvlSta PosSrvoHwAngle HwDeg M f32	10.1999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.920000017
k_APAMaxHwTrq_HwNm_f32	7.19999981
k_APAMaxVehSpd_Kph_f32	162
k_ESCMaxVehSpd_Kph_f32	62.2999992
k_HapticAmplitude_MtrNm_f32	0.540000021
k_HapticDuration_Sec_f32	1.35000002
k_HapticFreq_Hz_f32	44.6380005
k_HapticReacttime_Sec_f32	1.44000006
k_LKAMaxVehSpd_Kph_f32	171
k_LKAMinVehSpd_Kph_f32	157.5 0.0450000018
k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32	13
k ShiftLevRevTime Sec f32	0.00899999961
k_StandstillThresh_Kph_f32	3.5999999
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgarget_RecoverableFau$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_irqovista.irqovista_Peri_minsecureveniciespeed_kpn_132	target_IrqOviSta_Per1_minSecure venicleSpeed_kpn_ts2 target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	1
	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	730.5		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	422.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	432		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	61428	61428 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	144670	144670 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.19999981	-6.19999981 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	•
	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	~
		0.540000021 ± 0.0009765625 0 ± 1	✓ ✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.77 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1307202
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	69542.3203
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	254226
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	62734
TrqOvlSta_Haptictime_Sec_M_f32	147675.844
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.8499999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.573000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.6000004

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.930000007
k_APAMaxHwTrq_HwNm_f32	7.28000021
k_APAMaxVehSpd_Kph_f32	163.800003
k_ESCMaxVehSpd_Kph_f32	67.4000015
k_HapticAmplitude_MtrNm_f32	0.546000004
k_HapticDuration_Sec_f32	1.36500001
k_HapticFreq_Hz_f32	45.1520004
k_HapticReacttime_Sec_f32	1.45599997
k_LKAMaxVehSpd_Kph_f32	172.899994
k_LKAMinVehSpd_Kph_f32	159.25
k_MaxHapticReqTime_Sec_f32	0.0599999987 13.3000002
k_SWARLimiter_HwDeg_f32 k ShiftLevRevTime Sec f32	0.0130000003
k StandstillThresh Kph f32	3.6400001
k_StandstillTime_Sec_f32	0.5
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-372.51001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	735.440002 0
target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.30000019
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	427.100006
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	436.899994
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0 0 110000007
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7000008		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	62734	62734 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	147675.844	147675.844 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.8499999	-5.8499999 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.6000004	11.6000004 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	11.6000004	11.6000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.546000004	0.546000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	45.1520004	45.1520004 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.78 (Repeat Count = 1)	▼ ·
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1341165
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	70342
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	260772
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	64040
TrqOvlSta_Haptictime_Sec_M_f32	150680
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.583999991
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	13
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.93999998
k_APAMaxHwTrq_HwNm_f32	7.36000013
k_APAMaxVehSpd_Kph_f32	165.600006
k_ESCMaxVehSpd_Kph_f32	72.5
k_HapticAmplitude_MtrNm_f32	0.551999986
k_HapticDuration_Sec_f32	1.38
k_HapticFreq_Hz_f32	45.6660004
k_HapticReacttime_Sec_f32	1.472
k_LKAMaxVehSpd_Kph_f32	174.800003
k_LKAMinVehSpd_Kph_f32	161
k_MaxHapticReqTime_Sec_f32	0.075000003
k_SWARLimiter_HwDeg_f32	13.6000004
k_ShiftLevRevTime_Sec_f32	0.0170000009
k_StandstillThresh_Kph_f32	3.68000007
k_StandstillTime_Sec_f32	0.300000012
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	·g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg	f2.2	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_102	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt I	nc .	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	able_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlOta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	C	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 LKAState State enum	target_TrqOvISta_Per1_LKAState_State_enu		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_ target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-362.410004		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	740.380005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	432 441.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	•
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	70342	70342 ± 1	V
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HanticState_State_M_enum	0	0	· ·
TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	64040	64040 ± 1	
TrqOvlSta Haptictime Sec M f32	0	0 ± 1	-
TrgOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5	-5.5 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrgOvISta LKAPermFault Cnt M u08	4	4 ± 1	~
			_
TrqOvlSta_LKAState_State_M_enum	3	3	
	3 0.129999995	3 0.129999995 ± 0.0009765625 0 ± 1	~

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.551999986	0.551999986 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	45.6660004	45.6660004 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

lame	Input Value
	+ ·
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvISta_APAState_State_M_enum	4
rqOvISta_ESCState_State_M_enum	4
rqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1375128
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	71141.6797
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	267318
rqOvISta_HapticState_State_M_enum	
rqOvISta_Hapticdur_Cnt_M_u16	65346
rqOvISta_Haptictime_Sec_M_f32	153685.688
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.595000029
rqOvISta_LKAFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_u08	4
rqOvISta_LKAState_State_M_enum	0
rqOvISta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.94999988
_APAMaxHwTrq_HwNm_f32	0
_APAMaxVehSpd_Kph_f32	167.399994
_ESCMaxVehSpd_Kph_f32	77.599985
_HapticAmplitude_MtrNm_f32	0.558000028
_HapticDuration_Sec_f32	1.39499998
_HapticFreq_Hz_f32	46.1800003
_HapticReacttime_Sec_f32	1.48800004
_LKAMaxVehSpd_Kph_f32	176.699997
_LKAMinVehSpd_Kph_f32	162.75
_MaxHapticReqTime_Sec_f32	0.090000036
_SWARLimiter_HwDeg_f32	13.8999996
_ShiftLevRevTime_Sec_f32	0.0209999997
_StandstillThresh_Kph_f32	3.72000003
_StandstillTime_Sec_f32	0.020999997
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	t_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgo	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgo	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530





<u> </u>			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrgOvlSta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1140.10999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	745.320007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.6999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	436.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	446.700012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1	I	1
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	V
TrqOvISta_ESCState_State_M_enum	4	4 1375128 ± 1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	71141.6797	71141.6797 ± 1	· ·
TrqOvISta_ElapsedStandstillTime Sec M f32	0	0	
TrqOvISta HapticState State M enum	0	0	
TrgOvlSta Hapticdur Cnt M u16	65346	65346 ± 1	_
TrqOvlSta_Haptictime_Sec_M_f32	153685.688	153685.688 ± 1	~
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	1.30575037	1.30575001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996	14.3999996 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	14.3999996 0.558000028	14.3999996 ± 0.0009765625 0.558000028 ± 0.0009765625	V
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.558000028	0.558000028 ± 0.0009765625 0 ± 1	V
target_TrqOvlSta_Per1_TrqOscEriable_Cnt_igc.value	46.1800003	46.1800003 ± 0.0009765625	
gorrqoviota_i ori_riqosorioq_riz_ioz.valac		.5555555 ± 5.0003705023	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.80 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1409091
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	71941.3594
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	273864
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	1000
TrqOvlSta_Haptictime_Sec_M_f32	156690
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	0.606000006
TrqOvISta_LKAPermFault_Cnt_M_lgc	1
TrgOvlSta LKAPermFault Cnt M u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	15.8000002
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.959999979
k_APAMaxHwTrq_HwNm_f32	10
k_APAMaxVehSpd_Kph_f32	169.199997
k_ESCMaxVehSpd_Kph_f32	82.6999969 0.66400001
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0.56400001 1.40999997
k_HapticFreq_Hz_f32	46.6940002
k HapticReacttime Sec f32	1.50399995
k_LKAMaxVehSpd_Kph_f32	178.600006
k_LKAMinVehSpd_Kph_f32	164.5
k_MaxHapticReqTime_Sec_f32	0.104999997
k_SWARLimiter_HwDeg_f32	14.1999998
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	3.75999999
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_APAnonRecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APAnoriRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_enum
target_Rte_inst_Ap_irqovista.irqovista_Peri_GivioShosciliateState_state_enun target Rte Inst Ap TrqoviSta.TrqoviSta Peri GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Ont_igc target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Crit_gc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
targot_rrqOviota_rerr_oovvrouniu_ont_lgc.value	1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1130.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	750.26001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	451.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-21.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	273864	273864	•
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	1000	1000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	156690	156690 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	•
		0.56400001 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.56400001	0.50400001 ± 0.0009705025	
	0.56400001	1 ± 1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.81 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1443054
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	72741.0391
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	280410
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	3000
TrqOvlSta_Haptictime_Sec_M_f32	159695.766
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.44999981
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.616999984
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	17.2000008

2015-02-04, 14:05:40+0530





rigovicia_i ci i	
Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.970000029
(_APAMaxHwTrq_HwNm_f32	7.6500001
	171
C_ESCMaxVehSpd_Kph_f32 C_HantisAmplitude Mtrhlm_f32	87.8000031 0.56999993
(_HapticAmplitude_MtrNm_f32 (_HapticDuration_Sec_f32	1.42499995
<pre><_HapticDuration_Sec_f32 <_HapticFreq_Hz_f32</pre>	47.2080002
C_HapticReacttime_Sec_f32	1.51999998
CHAPMAXVehSpd Kph f32	180.5
C_LKAMinVehSpd_Kph_f32	166.25
MaxHapticReqTime Sec f32	0.119999997
C_SWARLimiter_HwDeg_f32	14.5
C_ShiftLevRevTime_Sec_f32	0.0289999992
C_StandstillThresh_Kph_f32	3.7999995
s_StandstillTime_Sec_f32	0.0289999992
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_It	
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 APARequest Cnt Igc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APAState State enum	target_TrqOvISta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWPosition_Hwbeg_isz	target_TrqOvIsta_Per1_CGWFound_Cnt_Igc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPound_Crit_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOvIsta_Per1_ESCFault_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta_ren_ESCrauit_Citt_gc arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_Igc
rarget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_ren_Erxhr atit_Crit_igc rarget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAInhibit_Crit_igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
arget_Ne_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_ren_wiinSecurevenideSpeed_Rpii_i52 arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvISta_Per1_PosServEnable_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target TrqOvlSta Per1 PosTrajEnable Cnt Igc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajEnable_OnEigc	target TrqOvISta Per1 PosTrajHwAngle HwDeg f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_FeT1_F0STfajftWAngle_TWDeg_132 arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3	V - V V - V - V - V - V - V - V - V
arget_Ne_inst_Ap_inqoviola.inqoviola_ren_ovvaltrigionignequest_nwbeg_n	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_StillLeveristilReverse_Ont_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvIsta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1119.91003
arget_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	755 200042
arget_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	755.200012
arget_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0
arget_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	
arget_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.0999999
arget_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	446.700012
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	456.5
	0
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996

© Report created by TESSY V3.1.9, report template V2.1



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-20.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	72741.0391	72741.0391 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	3000	3000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.05934954	2.05935001 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	17.2000008	17.2000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	17.2000008	17.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.569999993	0.569999993 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	47.2080002	47.2080002 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.82 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1477017
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	73540.7188
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	286956
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	5000
TrqOvlSta_Haptictime_Sec_M_f32	162700
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.628000021
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	18.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0
k_APAMaxHwTrq_HwNm_f32	0.69999988
k_APAMaxVehSpd_Kph_f32	172.800003
k_ESCMaxVehSpd_Kph_f32	92.9000015
k_HapticAmplitude_MtrNm_f32	0.575999975
k_HapticDuration_Sec_f32	1.44000006
k_HapticFreq_Hz_f32	47.7220001
k_HapticReacttime_Sec_f32	1.53600001
k_LKAMaxVehSpd_Kph_f32	182.399994
k_LKAMinVehSpd_Kph_f32	168
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	14.8000002
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	3.83999991
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg	_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	99	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_I target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSHOscillateState State enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target TrqOvlSta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1109.81006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	760.140015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	451.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	461.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-18.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1	I=	1
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1477017	1477017 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	73540.7188	73540.7188 ± 1	
TrqOvISta_EiapseoStanostill Ime_Sec_m_132 TrqOvISta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	5000	5000 ± 1	
TrgOvISta Haptictime Sec M f32	0	0 ± 1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.43120003	2.43120003 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	
,			
TrgOvISta LKAPermFault Cnt M lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 2	1 ± 1 2 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08		1 ± 1 2 ± 1 3	•
	2	2 ± 1	

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.575999975	0.575999975 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	47.7220001	47.7220001 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Namo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1510980
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	74340.3984
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	293502
TrqOvlSta_HapticState_State_M_enum	0
FrqOvlSta_Hapticdur_Cnt_M_u16	7000
FrqOvlSta_Haptictime_Sec_M_f32	165705
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.638999999
FrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
FrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
FrgOvlSta PosSrvoHwAngle HwDeg M f32	20
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
<pre><_APAIncludeHaptic_Cnt_lgc</pre>	1
<pre><_APAMaxHwTrqTime_Sec_f32</pre>	1
CAPAMaxHwTrq HwNm f32	1.10000002
<pre><_APAMaxVehSpd_Kph_f32</pre>	174.600006
x_ESCMaxVehSpd_Kph_f32	98
x_HapticAmplitude_MtrNm_f32	0.582000017
<hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/>	1.45500004
<_HapticFreq_Hz_f32	48.2360001
K_HapticReacttime_Sec_f32	1.55200005
k_LKAMaxVehSpd_Kph_f32	184.300003
k_LKAMinVehSpd_Kph_f32	169.75
k_MaxHapticReqTime_Sec_f32	0.150000006
<_SWARLimiter_HwDeg_f32	15.1000004
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	3.88000011
c_StandstillTime_Sec_f32	0.0370000005
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_C	Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target TrgOvlSta Per1 APAState State enum
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Fer1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_Hwbeg_isz	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_er	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_l	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_l	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lç	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1099.70996 0		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	765.080017		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	0		
target TrqOvISta_rer1_ESCISEIIIIIted_Off_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	0		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	6.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	456.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	466.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1510980	1510980 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	7000	7000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	165705	165705 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.79974985	2.79975009 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	20	20 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value		1 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	4	4	
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	0	0 ± 1	-
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	20	20 ± 0.0009765625	-
target_TrqOviSta_Per1_PosStvonwArigie_nwbeg_13z.value target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32.value	0.582000017	0.582000017 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	48.2360001	48.2360001 ± 0.0009765625	-
24	1		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.84 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1544943
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	75140.0781
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	300048
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	9000
TrqOvlSta_Haptictime_Sec_M_f32	168710.594
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-3.400001 0.64999976
TrqOvISta_LKAFault_Cnt_M_lgc	0.043535570
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	0
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	21.3999996
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.550000012
k_APAMaxHwTrq_HwNm_f32	470,000004
k_APAMaxVehSpd_Kph_f32	176.399994
k_ESCMaxVehSpd_Kph_f32 k_HapticAmplitude_MtrNm_f32	103.099998 0.588
k HapticDuration Sec f32	1.47000003
k_HapticFreq_Hz_f32	48.75
k_HapticReacttime_Sec_f32	1.56799996
k_LKAMaxVehSpd_Kph_f32	186.199997
k_LKAMinVehSpd_Kph_f32	171.5
k_MaxHapticReqTime_Sec_f32	0.165000007
k_SWARLimiter_HwDeg_f32	15.3999996
k_ShiftLevRevTime_Sec_f32	0.0410000011
k_StandstillThresh_Kph_f32	3.92000008
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0410000011
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_IrqOviSta_Per1_minSecurevenicieSpeed_kpn_t32 target_TrqOviSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvcHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_APARequest_Ont_Igc.value target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value	1
a.gsqoviota_i oi i_oovii ouiia_oiit_igo.vaiac	<u> </u>

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1089.60999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	770.02002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	461.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	471.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta ESCState State M enum	4	4	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	1544943	1544943 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	,
TrqOvlSta HapticState State M enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	9000	9000 ± 1	•
TrqOvlSta Haptictime Sec M f32	0	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.16499996	3.16499996 ± 0.000009	•
TrqOvlSta LKAFault Cnt M Igc	1	1 ± 1	
TrqOvISta LKAPermFault Cnt M Igc	0	0 ± 1	,
TrqOvISta LKAPermFault Cnt M u08	1	1 ± 1	
TrqOvlSta_LKAState_State_M_enum	4	4	,
TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.189999998	0.189999998 ± 0.0009765625	
TrqOvlSta ReadytoPulse Cnt M lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target TrqOvlSta Per1 GMOSHOscillateState State enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
· - · - · - · · · · · · · · · · · · · ·		0.588 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.588	0.366 ± 0.0009703023	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.588	0.568 ± 0.6669765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.85 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1578906
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	75939.7578
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	306594
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	11000
TrqOvlSta_Haptictime_Sec_M_f32	171715
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.661000013
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.7999992

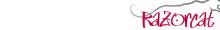
2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Technical Section Color Color	Name	Input Value
A.PANAMATER Top Top		·
APAMAselor To Time See, 122 APAMAselor To Time See,		
	. –	0.980000019
ESDIANCH 15 15 15 15 15 15 15 1	k_APAMaxHwTrq_HwNm_f32	7.92000008
Ligotophorate Devise 22	k_APAMaxVehSpd_Kph_f32	0
Lipsperion Lip	k_ESCMaxVehSpd_Kph_f32	108.199997
Light Ligh	k_HapticAmplitude_MtrNm_f32	0.593999982
Light Coloration Sec. 20 1.50000000		
MANAVENINE (MP. 20 15000007 1, MANAVENINE (MP. 20 15000007 1, MANAVENINE (MP. 20 15000007 1, MANAVENINE (MP. 20 150000007 1, MANAVENINE (MP. 20 1500000001 1, MANAVENINE (MP. 20 1, MANAVE		
L. J. Administration Sec. 12		
Monthsported Fines Sec. 122 1,50000007		
S. SWARE MINE Part Bor. 25 20 20 20 20 20 20 20		
k SmitherVerTine Sec 192 k SandridtTrines (Princh 192 k SandridtTrines (Pr		
A Spendolffringer, Spr. 102 Jargel, Rin, Let A. P. (Fochsta Trock) Sta. Pert. APADrinterventionDetect. Ont. Issper, Trock) Sta. Pert. APADrin		
Langel, Rie, Ind. Ap. (TOX)SSIA (TOX)SIA (TOX)SI		
target, Re, Inst. Ap. TriQVStall ToPOSE IN TOPOSE Der I APARocoventaberault. Cnt. Igo target, Re, Inst. Ap. TriQVStall ToPOSE Der I APARocoventaberault. Cnt. Igo target, Re, Inst. Ap. TriQVStall ToPOSE Der I APARocovent Cnt. Igo target, Re, Inst. Ap. TriQVStall ToPOSE Der I APARocovent Cnt. Igo target, Re, Inst. Ap. TriQVStall ToPOSE Der I COPPOSED DER I Stelland Community (Community) target, Re, Inst. Ap. TriQVStall ToPOSED Pert I COPPOSED DER I Stelland Community target, Re, Inst. Ap. TriQVStall ToPOSE Der I COPPOSED DER I STELLAND Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I COPPOSED DER I STELLAND Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I COPPOSED DER I STELLAND Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I COPPOSED DER I STELLAND Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I CORNS LESCHIPS TARGET Community target, TriQVStall ToPOSED TOPOSED DER I CORNS LESCHIPS TARGET Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I LANDS LESCHIPS TARGET Community target, TriQVStall ToPOSED TARGET Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I LANDS LESCHIPS TARGET Community target, TriQVStall ToPOSED TARGET Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I LANDS Leschier manner target, TriQVStall ToPOSED TARGET Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I LANDS Leschier manner target, TriQVStall ToPOSED TARGET Community target, Re, Inst. Ap. TriQVStall ToPOSED Pert I LANDS Leschier manne	k_StandstillTime_Sec_f32	0.0450000018
singer, Brief, Ap., TrigOVStin TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., TryOVStin, TryOVStin, Pert J.APARcoventelleraulie, Cnt. Igo singer, Brief, Mar., Try	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Restriction (APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Restriction (APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Restriction (APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Restriction (APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Restriction (APADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventInterventionDetected_Cnt_Inst_ApADrvrInterventInterv$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
saget, Re., Inst. Ap. Troch/Sta. Troch/Sta. Pert. APARequiet, Cnt. Jo. saget, Re. Inst. Ap. Troch/Sta. Troch/Sta. Pert. CNPArequiet, Cnt. Jo. saget, Re. Inst. Ap. Troch/Sta. Troch/Sta. Pert. CNPArequiet, Cnt. Jo. saget, Re. Inst. Ap. Troch/Sta. Troch/Sta. Pert. CNPArequiet, Cnt. Jo. saget, Re. Inst. Ap. Troch/Sta. Troch/Sta. Pert. CNPArequiet, Cnt. Jo. saget, Re. Inst. Ap. Troch/Sta. Troch/Sta. Pert. CNPArequiet, Cnt. Jo. saget, Re. Inst. Ap. Troch/Sta. Troch/Sta. Pert. CNPArequiet, Cnt. Jo. saget, Troch/Sta. Pert. Sca. Cnt. Sp. saget, Troch/Sta. Pert. CNPArequiet, Cnt. Jo. saget, Troch/Sta. Pert. CNPArequiet, Cnt. Jo. saget, Troch/Sta. Pert. Sca. Cnt. Sp. saget, Troch/Sta. Pert. Sca. Cnt.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
singel Reij net Ap Trock/sis Trotholis Pert LAPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LOPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij Net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij Net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij Net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij Net Ap Trock/sis Trotholis Pert LoPASise Siste enum stepel Reij Net Ap Trock/sis Trotholis Pert LoPASise Siste enu	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rib, Inst. Ap. Trad/OSIs Trad/OSIS Pert. COVFound Cnt (pt target. Trad/OSIs Trad/OSIS Pert. COVFound Cnt (pt target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS Pert. COVFound Cnt (pt target. Trad/OSIS Pert. COVFound Cnt (pt target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS Pert. SECRETARY (Pt. 1) target. Trad/OSIS Pert. COVFound Cnt (pt target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS Pert. SECRETARY (Pt. 1) target. Trad/OSIS Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS Pert. SECRETARY (Pt. 1) target. Trad/OSIS Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS. Pert. SECRETARY (Pt. 1) target. Trad/OSIS. Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS. Pert. SECRETARY (Pt. 1) target. Trad/OSIS. Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS. Pert. GNOSH SECRETARY (Pt. 1) target. Trad/OSIS. Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS. Pert. GNOSH SECRETARY (Pt. 1) target. Trad/OSIS. Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS. Pert. Market (Pt. 1) target. Trad/OSIS. Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS. Pert. Market (Pt. 1) target. Trad/OSIS. Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS Trad/OSIS Pert. Market (Pt. 1) target. Trad/OSIS Pert. SECRETARY (Pt. 1) target. Rib, Inst. Ap. Trad/OSIS Trad/OSIS Pert. M		
singet Rit, Jinst AD, Tradvisis Tradvisis Pert CWForation I-Mode, 122 singet Tradvisis I-Mode, 124 si		
target Rei, Inst. Ap. Trach/Stata Trach/Stata Pert (Different Cert. Job target, Trach/Stat. Pert (Different Cert. Job target, Rei, Inst. Ap. Trach/Stata Trach/Stata Pert. SECREL (Prt. Job target, Trach/Stat. Pert. Different Cert. Job target, Rei, Inst. Ap. Trach/Stata Trach/Stata Pert. SECREL (Prt. Job target, Trach/Stat. Pert. SECREL (Prt. Job target, Rei, Inst. Ap. Trach/Stata Trach/Stata Pert. SECREL (Prt. Job target, Trach/Stat. Pert. SECREL (Prt. Job target, Rei, Inst. Ap. Trach/Stata Trach/Stata Pert. SECREL (Prt. Job target, Trach/Stat. Pert. SECREL (Prt. Job target, Rein, Inst. Ap. Trach/Stata Trach/Stata Pert. SECREL (State). target, Trach/Stat. Pert. SECREL (Prt. Job target, Rein, Inst. Ap. Trach/Stata Trach/Stata Pert. Job/Stata Pert. SECREL (Prt. Job target, Trach/Stat. Pert. Job target, Rein, Inst. Ap. Trach/Stata Trach/State. Pert. Job/State. Pert. Job target, Trach/Stat. Pert. Job target, Rein, Inst. Ap. Trach/Stata Trach/State. Pert. Job target, Trach/Stat. Pert. Job target, Rein, Inst. Ap. Trach/Stata Trach/State. Pert. Job target, Trach/State. Pert. Job target, Rein, Inst. Ap. Trach/Stata Trach/State. Pert. Job target, Trach/State. Pert. Job target, Rein, Inst. Ap. Trach/Stata Trach/State. Pert. Job target, Trach/State. Pert. Job target, Rein, Inst. Ap. Trach/Stata Trach/State. Pert. Job target, Trach/State. Pert. Job <td></td> <td></td>		
langer, Rie, Inst. AD, Trajovišska Trajovišska, Pert. SCPC and, Crit. (p.) langer, Rie, Inst. AD, Trajovišska Trajovišska, Pert. SECSIder, Crit. (p.) langer, Rie, Inst. AD, Trajovišska Trajovišska, Pert. SECSIder, Crit. (p.) langer, Rie, Inst. AD, Trajovišska Trajovišska, Pert. SECSIder, State, cenum langer, Rie, Inst. AD, Trajovišska Trajovišska, Pert. SECSIder, State, cenum langer, Rie, Inst. AD, Trajovišska Trajovišska, Pert. CAMOSH - APAMMglemable, Crit. (p.) langer, Rie, Inst. AD, Trajovišska Trajovišska, Pert. CAMOSH - APAMMglemable, Crit. (p.) langer, Rie, Inst. AD, Trajovišska Trajovišska, Pert. CAMOSH - APAMMglemable, Crit. (p.) langer, Trajovišska, Pert. LAMASH - Crit. (p.) langer, Trajovišska, Pert		
targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SECRatur Cut. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SECRaturet Cort. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SECRequest, Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SECRequest, Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SECRequest, Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SECRequest, Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGEnable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGEnable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGEnable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGEnable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGEnable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGEnable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGEnable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGEnable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita, Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita, Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita, Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita, Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita, Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita TropOsita, Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita, Pert J. SMOSH-APAMIGENable Crt. [go targer, Rei, Inst. Ap. TropOsita, Pert J. SMOSH-APAMIGENable Crt. [go value targer, TropOsita, Pert J. SMOSH, Pert J. PropOsita, Pert J. SMOSH, Pert J. PropOsita, Pert J. SMOSH, Pert J. PropOsita, Pert J. SMOSH, Pert		
target Rie Inst. Ap. TropVisita Prof ISSCilatimated. Ont. go target. Rie Inst. Ap. TropVisita Prof ISSCilatimated. Ont. go target. Rie Inst. Ap. TropVisita Prof ISSC Prof ISSCILATION target. Rie Inst. Ap. TropVisita Prof ISSCI Prof ISSCILATION target. Rie Inst. Ap. TropVisita Prof ISSCI Prof ISSCILATION target. Rie Inst. Ap. TropVisita Prof ISSCI Prof ISSCILATION target. Rie Inst. Ap. TropVisita Prof ISSCILATION target. Rie Inst. Ap. TropVisita Prof ISSCI Prof ISSCILATION target. Rie Inst. Ap. TropVisita Prof ISSCI Prof ISSCILATION target. Rie Inst. Ap. TropVisita TropV		
target Rei, Inst. Ap. TrqOviSta TrqOviSta, Pert J. ESCRequest. Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta TrqOviSta. Pert J. ESCRequest. Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta TrqOviSta. Pert J. GMOSH JAPAMIGEnable Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. GMOSH JAPAMIGEnable Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. GMOSH JAPAMIGEnable Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. GMOSH JAPAMIGEnable Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. GMOSH JAVAMIGEnable Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. LAVARequest. Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. LAVARequest. Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. LAVARequest. Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. LAVARequest. Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. LAVARequest. Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. LAVARequest. Cnt. Ipc target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. ManSecureVehicleSpeed J. Kpt. 23 target. Rei. Inst. Ap. TrqOviSta. TrqOviSta. Pert J. SWARTIgAhriffequated. Thrug. State. Pert J. SWARTIgAhriffequated. T		
langer, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. ISSCSsite, Siste, enum larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. (MOSH, APAM/genable, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, Pert. (MOSH, APAM/genable, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. (MOSH, LEXM/genable, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. (MOSH, LEXM/genable, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. (MOSH), LEXM/genable, Cnt. (go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. LEXAFault, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. LEXAFault, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. LEXAFault, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. LEXAFault, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. LEXAFault, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. LEXAFault, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. LEXAFault, Cnt.) go: larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. MasSecure/vehicleSpeed (Kph., 52) larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. MasSecure/vehicleSpeed (Kph., 52) larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. MasSecure/vehicleSpeed (Kph., 52) larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. Pasisrovhwange, HwDeg, 522 larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. Pasisrovhwange, HwDeg, 522 larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSisa, Pert. Pasisrovhwange, HwDeg, 522 larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSis, Pert. Pasisrovhwange, HwDeg, 522 larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSis, Pert. Spring, Pasisrovhwange, HwDeg, 522 larger, Rie, Inst., Ap., TrqOvSisa, TrqOvSis, Pert. TrqOvSis, Pert. Spring, Pert., Spring,		
larged, Rel., Inst., Ap. Trq.Ovista. Prof. (SMCSHO-Scillate-State, State, enum arget, Rel., Inst., Ap. Trq.Ovista. Prof. (SMCSH. PARM/Epinable, Crit.) pc larget, Rel., Inst., Ap. Trq.Ovista. Trq.Ovista. Prof. (SMCSH. LKAM/Epinable, Crit.) pc larget, Rel., Inst., Ap. Trq.Ovista. Trq.Ovista. Prof. (SMCSH. LKAM/Epinable, Crit.) pc larget, Rel., Inst., Ap. Trq.Ovista. Trq.Ovista. Prof. (SMCSH. LKAM/Epinable, Crit.) pc larget, Trq.Ovista. Prof. (SMCSH. Prof. LKAM) larget, Trq.Ovista. Prof. LKAM, Trq.Ovista. Prof. (SMS. Prof. LKAM) larget, Trq.Ovista. Prof. LKAM, Trq.Ovista. Trq.Ovista. Prof. (SMS. Prof. LKAM) larget, Trq.Ovista. Prof. LKAM, Trq.Ovista. Trq.Ovista. Prof. (SMS. Prof. LTq.Ovista. Prof. LKAM, Trq.Ovista. Trq.Ovista. Prof. LKAM, Prof. Prof. Trq.Ovista. Prof. LKAM, Prof. Trq.Ovista. Trq.Ovista. Prof. Prof. Trq.Ovista. Prof. LKAM, Prof. Prof. Prof. Lkam, Prof.		
target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert GMOSH. ESOMBEnable. Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert GMOSH. ESOMBEnable. Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. Mortoque HwNm. Ts2 target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. Mortoque HwNm. Ts2 target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. Mortoque HwNm. Ts2 target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. Mortoque HwNm. Ts2 target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. SWART (TsqOv/Sta Pert J. KARINIBEL (Cnt. Igo target. Rie. Inst. Ap. TrqOv/Sta TrqOv/Sta Pert J. SWART (TsqOv/Sta Pert J. TsqOv/Sta Pert J. SWART (TsqOv/Sta Pert J. TsqOv/Sta Pert		
larget_Ret_Inst_Ap_TrqOvista_TrqOvista_Pert_HapticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_HapticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_LApticRequest_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_MassecureVehicleSpeed_Kph_G2 target_Ret_Inst_Ap_TrqOvista_Pert_Installed_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_Installed_Cnt_lgc target_Ret_Inst_Ap_TrqOvista_Pert_Pers_Pers_Pers_Pers_Pers_Pers_Pers_Pers		
target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl HapticRequest, Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. Perl HwTorque, HwWn. Inst. target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl LKAFault. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl LKAFault. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl LKAFault. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl LKAFault. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl LKASalte. State. enum target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl LKASalte. State. enum target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl LKASalte. State. enum target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl MaxSecureVehicleSpeed Kph. IS2 target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl MaxSecureVehicleSpeed Kph. IS2 target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl Perl PostSeviEnable. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl PostSeviEnable. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl PostTejiEnable. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl PostTejiEnable. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl PostTejiEnable. Cnt. Igo: target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl SwARTrgIAngRequest. HwDeg. IS2 target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl SwiRTrgIAngRequest. HwDeg. IS2 target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl TigoviSta. Perl TrqOviSta. Perl SwARTrgIAngRequest. HwDeg. IS2 target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl TrqOviSta. Per	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
larget Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ LKAFault_Cnt_lgo target_ TrqOviSta Perf _ LKAFault_Cnt_lgo target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ LKAFault_Cnt_lgo target_ TrqOviSta Perf _ LKAFault_Cnt_lgo target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ LKARequest_Cnt_lgo target_ TrqOviSta Perf _ LKAFault_Cnt_lgo target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ LKARequest_Cnt_lgo target_ TrqOviSta Perf _ LKARequest_Cnt_lgo target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ MinSecure VehicleSpeed_Kph_132 target_ TrqOviSta Perf _ LKARequest_Cnt_lgo target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ Possorvehable_Cnt_lgo target_ TrqOviSta Perf _ MinSecureVehicleSpeed_Kph_132 target_ TrqOviSta Perf _ MinSecureVehicleSpeed_Kph_132 target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ Possorvehable_Cnt_lgo target_ TrqOviSta Perf _ Mossorvehable_Cnt_lgo target_ TrqOviSta Perf _ Mossorvehable_Cnt_lgo target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ Post TrgiHxAngle_HwDeg_132 target_ TrqOviSta Perf _ Post TrgiHxAngle_HwDeg_132 target_ TrqOviSta Perf _ Straint_Mple_HwDeg_132 target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ TrqOseAmp_Mithin_132 target_ TrqOviSta Perf _ Straint_Mple_Request_LwDeg_132 target_ TrqOviSta Perf _ StwArting_Angle_HwDeg_132 target_ Rie, Inst. Ap. TrqOviSta TrqOviSta Perf _ TrqOseAmp_Mithin_132 target_ TrqOviSta Perf _ StwArting_Angle_HwDeg_132 target_ TrqOviSta Perf _ TwpOviSta Perf _ TwqOviSta P	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target, Rie, Inst., Ap., TrqOviSta, TrqOviSta, Perf_LKAFault, Cnt. Upc target, TrqOviSta, Perf_LKARequest, Cnt. Upc target, Rie, Inst., Ap., TrqOviSta, Perf_LKARequest, Cnt. Upc target, TrqOviSta, Perf_LKARequest, Cnt. Upc target, Rie, Inst., Ap., TrqOviSta, Perf_LKAState, State, enum target, TrqOviSta, Perf_LKAState, State, enum target, Rie, Inst., Ap., TrqOviSta, Perf, Massecure/PerioteSpeed, Kph. (32) target, Rie, Inst., Ap., TrqOviSta, Perf, Massecure/PerioteSpeed, Kph. (32) target, Rie, Inst., Ap., TrqOviSta, Perf, DesenvEnable, Cnt. Upc target, TrqOviSta, Perf, Massecure/PerioteSpeed, Kph. (32) target, Rie, Inst., Ap., TrqOviSta, TrqOviSta, Perf, PosenvEnable, Cnt. Upc target, TrqOviSta, Perf, DesenvEnable, Cnt. Upc target, Rie, Inst., Ap., TrqOviSta, TrqOviSta, Perf, Postrajlenable, Cnt. Upc target, TrqOviSta, Perf, DesenvEnable, Cnt. Upc target, Rie, Inst., Ap., TrqOviSta, TrqOviSta, Perf, Postrajlenable, Cnt. Upc target, TrqOviSta, Perf, DesenvEnable, Cnt. Upc target, Rie, Inst., Ap., TrqOviSta, TrqOviSta, Perf, State, Perf, Inst., Perf, State, Perf, Postrajlenable, Cnt. Upc target, TrqOviSta, Perf, DesenvEnable, Cnt. Upc target, Rie, Inst., Ap., TrqOviSta, Perf, TrqOscenable, Cnt. Upc target, TrqOviSta, Perf, TrqOscenable, Cnt. Upc target, TrqOviSta, Perf, Ap. TrqOviSta, Perf, TrqOscenable, Cnt. Upc, value 0 target, TrqOviSta, Perf, LAPARecoverableFaults, Cnt. Upc, value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
larget_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARphbit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARphate_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARstate_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target_TrqOviSta_Per1_LKAState_State_enum target_TrqOviSta_Per1_Discate_State_State_enum target_TrqOviSta_Per1_Discate_State_State_enum target_TrqOviSta_Per1_Discate_State		
target_Rie_Inst_Ap_TrqOviSta Prd_UKARequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta Prd_UKARequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta Prd_UKSE_Prd_UKSE		
larget Rie_Inst Ap_TrqOviSia_TrqOviSia_Perl_MaxSecureVehicleSpeed_Kph_132 larget_TrqOviSia_TrqOviSia_Perl_MaxSecureVehicleSpeed_Kph_132 larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosServEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosServEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosServEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosServEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosTrqEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosTrqEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosTrqEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosTrqEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosTrqEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosTrqEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_PosTrqEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_TosTrqDisTrqOviSia_Perl_PosTrqEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perl_TrqOsCert_Hz_Fiz_Unit_LeverIsinReverse_Cnt_Igo larget_TrqOviSia_Perl_ToqViSia_Perl_TrqOsCert_Hz_Fiz_Unit_LeverIsinReverse_Cnt_Igo larget_TrqOviSia_Perl_ToqViSia_Perl_TrqOsCert_Hz_Fiz_Unit_LeverIsinReverse_Cnt_Igo larget_TrqOviSia_Perl_Ap_ArecoverableFaulis_Cnt_Igo value 0 larget_TrqOviSia_Perl_ApARecoverableFaulis_Cnt_Igo value 0 larget_TrqOviSia_Perl_CoWPosition_HwDeg_fiz_value 0 larget_TrqOviSia_Perl_CoWPosition_HwDeg_fiz_value 0 larget_TrqOviSia_Perl_EoSCeluI_Cnt_Igo_value 0 larget_TrqOviSia_Perl_HxFaul_Cnt_Igo_value 0 larget_TrqOviSia_Perl_HxFaul_Cnt_Igo_value 0 larget_TrqOviSia_Perl_HxFaul_Cnt_Igo_value 0 larget_TrqOviSi		
target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1 MaxSecureVehicleSpeed Kph_132 target_Re_Inst_Ap_TrqOvISta_Per1 MinSecureVehicleSpeed Kph_132 target_Re_Inst_Ap_TrqOvISta_Per1 MinSecureVehicleSpeed Kph_132 target_Re_Inst_Ap_TrqOvISta_Per1 DosSrovHawAngle_HwDeg_132 target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHwAngle_HwDeg_132 target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHwAngle_HwDeg_132 target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_132 target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrajHangle_HwDeg_132 target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrajHangle_HwDeg_132 target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrajHangle_HwDeg_132 target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAnp_MinVm_132 target_Re_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAnp_MinVm_132 target_TrqOvISta_Per1_SWARTrajAngRequest_HwDeg_132 target_TrqOvISta_Per1_SWARTrajAngRequest_HwDeg_132 target_TrqOvISta_Per1_SWARTrajAngRequest_HwDeg_132 target_TrqOvISta_Per1_SWARTrajAngRequest_HwDeg_132 target_TrqOvISta_Per1_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAnp_MinVm_132 target_TrqOvISta_Per1_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAnp_MinVm_132 target_TrqOvISta_Per1_Ap_TrqOvISta_TrqOvISta_Ter1_TrqOsCAnp_MinVm_132 target_TrqOvISta_Per1_Ap_TrqOvISta_Ter1_TrqOsCAnp_MinVm_132 target_TrqOvISta_Per1_Ap_TrqOvISta_Ter1_TrqOsCAnp_MinVm_132 target_TrqOvISta_Per1_Ap_AndrecoverableFaults_Cnt_Igc_value 0 target_TrqOvISta_Per1_Ap_AndrecoverableFaults_Cnt_Igc_value 0 target_TrqOvISta_Per1_CWPOsition_HwDeg_132_value 0 target_TrqOvISta_Per1_CWPOsition_HwDeg_132_value 0 target_TrqOvISta_Per1_ESCFault_Cnt_Igc_value 0 target_TrqOvISta_Per1_ESCFault_Cnt_Igc_value 0 target_TrqOvISta_Per1_ESCFault_Cnt_Igc_value 0 target_TrqOvISta_Per1_HwTqque_Hwn_152_value 0 target_TrqOvISta_Per1_HwTqque_Hwn_152_value 0 target_TrqOvISta_Per1_HwTqque_Hwn_152_value 0 target_TrqOvISta_Per1_HwTqque_Hwn_152_value 0 target		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Per1_PosServEnable_Net_g132 target_Rie_Inst_Ap_TrqOviSta_Per1_PosSrovInAmple_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajEnable_EnwDeg_132 target_Rie_Inst_Ap_TrqOviSta_Per1_PosTrajEnable_RwDeg_132 target_Rie_Inst_Ap_TrqOviSta_Per1_SwARTrqSAngRequest_Mbeg_132 target_Rie_Inst_Ap_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOsCenap_MinNm_132 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOsCenap_MinNm_132 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOsCenap_MinNm_132 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOsCenap_MinNm_132 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOsCenap_MinNm_132 target_TrqOviSta_Per1_APARevorerableFaults_Cnt_Igc_value target_TrqOviSta_Per1_APARevorerableFaults_Cnt_Igc_value target_TrqOviSta_Per1_APARevorerableFaults_Cnt_Igc_value target_TrqOviSta_Per1_CWFound_Cnt_Igc_value target_TrqOviSta_Per1_CWFound_Cnt_Igc_value target_TrqOviSta_Per1_ENCREQuest_Cnt_Igc_value target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value target_TrqOviSta_Per1_ESCRequest_Cnt_Igc_value target_TrqOviSta_Per1_GMOSH_PAMIficanable_Cnt_Igc_value target_TrqOviSta_Per1_HwTorque_HwNm_132_value target_Tr		
target_Rie_Inst_Ap_TrqOVISta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOVISta_Pert_PosSrovHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_SWIARTrgAngRequest_HwDeg_131 target_Rie_Inst_Ap_TrqOVISta_Pert_TrqOSFrag_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_TrqOSFrag_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_TrqOSFrag_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_TrqOSFrag_HwDeg_132 target_Rie_Inst_Ap_TrqOVISta_Pert_TrqOSFrag_HwDeg_132 target_TrqOVISta_Pert_APANGRequest_HwDeg_132 target_TrqOVISta_Pert_APANGRequest_Ric_Ing_Sert_Ric_Inst_132 target_TrqOVISta_Pert_APANGRequest_Ric_Ing_Sert_Ric_Inst_132 target_TrqOVISta_Pert_APANGRequest_Cnt_Ig_cvalue 0 target_TrqOVISta_Pert_APARGRequest_Cnt_Ig_cvalue 1 target_TrqOVISta_Pert_APARGRequest_Cnt_Ig_cvalue 0 target_TrqOVISta_Pert_CWPround_Cnt_Ig_cvalue 1 target_TrqOVISta_Pert_CWPround_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_CWPround_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_ESCFault_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_ESCFault_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_ESCFault_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_ESCFault_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_GMOSH_APAMIgenable_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_GMOSH_APAMIgenable_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_HwDrq_ISQ_Sudue 1 1 target_TrqOVISta_Pert_HwDrq_ISQ_Sudue 1 target_TrqOVISta_Pert_HARAIN_Cnt_Ig_cvalue 1 0 target_TrqOVISta_Pert_HARAIN_Cnt_Ig_cvalue 1 1 target_		
target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Cnt_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Cnt_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Cnt_Igo target_Re_Inst_Ap_TrqOvISta_Pert_PosTrajenable_Cnt_Igo target_Re_Inst_Ap_TrqOvISta_Pert_SorTajenable_Cnt_Igo target_Re_Inst_Ap_TrqOvISta_Pert_SorTajenable_Cnt_Igo target_Re_Inst_Ap_TrqOvISta_Pert_SinItLeverisInReverse_Cnt_Igo target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCarp_Mrithm_f32 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCarp_Mrithm_f32 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCarp_Mrithm_f32 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCarp_Mrithm_f32 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCarp_Mrithm_f32 target_Re_Inst_Ap_TrqOvISta_Pert_TrqOsCarp_Mrithm_f32 target_TrqOvISta_Pert_ApAnnecoverableFaults_Cnt_Igo:Value target_TrqOvISta_Pert_ApAnnecoverableFaults_Cnt_Igo:Value target_TrqOvISta_Pert_ApAnnecoverableFaults_Cnt_Igo:Value target_TrqOvISta_Pert_CWFound_Cnt_Igo:Value target_TrqOvISta_Pert_CWFound_Cnt_Igo:Value target_TrqOvISta_Pert_CWFound_Cnt_Igo:Value target_TrqOvISta_Pert_CWFound_Cnt_Igo:Value target_TrqOvISta_Pert_CWFound_Cnt_Igo:Value target_TrqOvISta_Pert_ESCFault_Cnt_Igo:Value target_TrqOvISta_Pert_ESCFault_Cnt_Igo:Value target_TrqOvISta_Pert_ESCRequest_Cnt_Igo:Value target_TrqOvISta_Pert_ESCRequest_Cnt_Igo:Value target_TrqOvISta_Pert_ESCRequest_Cnt_Igo:Value target_TrqOvISta_Pert_ESCRequest_Cnt_Igo:Value target_TrqOvISta_Pert_ESCRequest_Cnt_Igo:Value target_TrqOvISta_Pert_ESCRequest_Cnt_Igo:Value target_TrqOvISta_Pert_ESCRequest_Cnt_Igo:Value target_TrqOvISta_Pert_ESCRequest_Cnt_Igo:Value target_TrqOvISta_Pert_Mrorque_HwNm_f32_value target_TrqOvISta_Pert		0 = 1 = 1 = 1 = 1
target_Rte_Inst_Ap_TrqOvIsta_Perf_SVMARTrgIAngRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOvIsta_Perf_SWIARTrgIAngRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOvIsta_Perf_SWIARTrgIAngRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOvIsta_Perf_SWIARTrgIAngRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOvIsta_Perf_ShiftLeverIstinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_Perf_TrqOscAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta_Perf_TrqOscAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta_Perf_TrqOscAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta_Perf_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_Perf_TrqOscEnable_Cnt_Igc target_TrqOvIsta_Perf_Ap_And NonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvIsta_Perf_Ap_And NonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvIsta_Perf_Ap_And NonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvIsta_Perf_Ap_And NonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvIsta_Perf_CWPound_Cnt_Igc_value 0 target_TrqOvIsta_Perf_CWPound_Cnt_Igc_value 10 target_TrqOvIsta_Perf_CWPound_Cnt_Igc_value 0 target_TrqOvIsta_Perf_ExCE_VROUND_CNT_Igc_value 10 target_TrqOvIsta_Perf_MOSH_ExMIRE_Rable_Cnt_Igc_value 10 target_TrqOvIsta_Perf_MOSH_ExMIRE_Rable_Cnt_Igc_value 10 target_TrqOvIsta_Perf_InvTorque_HwMm_f32_value 11 target_TrqOvIsta_Perf_HvTorque_HwMm_f32_value 12 target_TrqOvIsta_Perf_InvTorque_HwMm_f32_value 12 target_TrqOvIsta_Perf_InvTorque_HwMm_f32_value 13 target_TrqOvIsta_Perf_InvTorque_HwMm_f32_value 14 target_TrqOvIsta_Perf_InvTorque_HwMm_f32_value 15 target_TrqOvIsta_Perf_InvTorque_HwMm_f32_value 16 target_TrqOvIsta_Perf_InvTorque_HwMm_f32_value 17 target_TrqOvIsta_Perf_InvTorque_HwMm_f32_value 17 target_TrqOvIsta_Perf_InvTor		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SWARTrgtAngRequest_HwDeg_131 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtnNm_132 target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscAmp_MtnNm_132 target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscAmp_MtnNm_132 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtnNm_132 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscFnable_Cnt_Igc target_TrqOvISta_Perl_TrqOscAmp_MtnNm_132 target_TrqOvISta_Perl_TrqOscAmp_MtnNm_132 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscFnable_Cnt_Igc target_TrqOvISta_Perl_TrqOscAmp_MtnNm_132 target_TrqOvISta_Perl_APARONRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Perl_APARecoverableFaults_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_CCWPound_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_CCWPound_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_GMOSH_APAMIgEnable_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_GMOSH_ESCMIgEnable_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value 0 0 target_TrqOvISta_Perl_LKARequest_Cnt_Igc.value	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_ShiftLeverIsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_Mtmm_f32 target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_TrqOvISta_Perl_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_APARequest_Cnt_Igc.value target_TrqOvISta_Perl_APARequest_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_ESCRequest_Cnt_Igc.value target_TrqOvISta_Perl_ESCRequest_Cnt_Igc.value target_TrqOvISta_Perl_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HwTorque_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFrable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFrable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOscFrad_Hz_f32 target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOviSta_Per1_APARequest_Cnt_Igc.value target_TrqOviSta_Per1_APARequest_Cnt_Igc.value target_TrqOviSta_Per1_APARequest_Cnt_Igc.value target_TrqOviSta_Per1_CCWPound_Cnt_Igc.value target_TrqOviSta_Per1_CWFound_Cnt_Igc.value target_TrqOviSta_Per1_CWFound_Cnt_Igc.value target_TrqOviSta_Per1_CWFound_Cnt_Igc.value target_TrqOviSta_Per1_CWFound_Cnt_Igc.value target_TrqOviSta_Per1_CWFound_Cnt_Igc.value target_TrqOviSta_Per1_CWFound_Cnt_Igc.value target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOviSta_Per1_LKAFaut_Cnt_Igc.value target_TrqOviSta_Per1_LKAFaut_Cnt_Igc.value target_TrqOviSta_Per1_LKARequest_Cnt_Igc.value target_TrqOviSta_Per1_LKARequest_Cnt_Igc.value target_TrqOviSta_Per1_LKARequest_Cnt_Igc.value target_TrqOviSta_Per1_LKARequest_Cnt_Igc.value target_TrqOviSta_Per1_LKARequest_Cnt_Igc.value target_TrqOviSta_Per1_LKARequest_Cnt_Igc.value target_TrqOviSta_Per1_LKARequest_Cnt_Igc.value target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc.value	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_PrqTrqOvsEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscFreq_Hz_f32 target_TrqOvlSta_Perl_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Perl_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Perl_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Perl_APARequest_Cnt_Igc.value target_TrqOvlSta_Perl_APARequest_Cnt_Igc.value target_TrqOvlSta_Perl_CWFound_Cnt_Igc.value target_TrqOvlSta_Perl_CWFound_Cnt_Igc.value target_TrqOvlSta_Perl_CWFound_Cnt_Igc.value target_TrqOvlSta_Perl_CWFound_Cnt_Igc.value target_TrqOvlSta_Perl_CWFound_Cnt_Igc.value target_TrqOvlSta_Perl_CWFound_Cnt_Igc.value target_TrqOvlSta_Perl_ESCFault_Cnt_Igc.value target_TrqOvlSta_Perl_ESCFault_Cnt_Igc.value target_TrqOvlSta_Perl_ESCFault_Cnt_Igc.value target_TrqOvlSta_Perl_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Perl_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Perl_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Perl_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvlSta_Perl_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvlSta_Perl_HwTorque_HwNm_f32.value target_TrqOvlSta_Perl_HwTorque_HwNm_f32.value target_TrqOvlSta_Perl_HwTorque_HwNm_f32.value target_TrqOvlSta_Perl_HwTorque_HwNm_f32.value target_TrqOvlSta_Perl_LKAFault_Cnt_Igc.value target_TrqOvlSta_Perl_LKAFault_Cnt_Igc.value target_TrqOvlSta_Perl_LKAFault_Cnt_Igc.value target_TrqOvlSta_Perl_LKAFault_Cnt_Igc.value target_TrqOvlSta_Perl_LKAFault_Cnt_Igc.value target_TrqOvlSta_Perl_LKAFault_Cnt_Igc.value target_TrqOvlSta_Perl_LKAFault_Cnt_Igc.value target_TrqOvlSta_Perl_LKAFault_Cnt_Igc.value target_TrqOvlSta_Perl_IkARequest_Cnt_Igc.value		
target_Rte_inst_Ap_TrqOvISta_Perf_TrqOscFreq_Hz_f32 target_TrqOvISta_Perf_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Perf_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Perf_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Perf_CCWFound_Cnt_lgc.value 0 target_TrqOvISta_Perf_CCWFound_Cnt_lgc.value 0 target_TrqOvISta_Perf_CCWFound_Cnt_lgc.value 107951001 target_TrqOvISta_Perf_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Perf_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Perf_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Perf_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Perf_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Perf_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Perf_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Perf_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Perf_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Perf_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Perf_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Perf_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Perf_HapticRequest_Cnt_lgc.value 0 target_TrqOvISta_Perf_HapticRequest_Cnt_lgc.value 0 target_TrqOvISta_Perf_HapticRequest_Cnt_lgc.value 0 target_TrqOvISta_Perf_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Perf_MaxSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvISta_Perf_PosTrajEnable_Cnt_lgc.value 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1079.51001 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Per1_HntTorque_Hntym_f32.value 0 target_TrqOvISta_Per1_HntTorque_Hntym_f32.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAPequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAPequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOvISta_Per1_MntSecureVehicleSpeed_Kph_f32.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
target_TrqOvISta_Perl_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Perl_APARequest_Cnt_Igc.value 0 target_TrqOvISta_Perl_CCWFound_Cnt_Igc.value 1.079.51001 target_TrqOvISta_Perl_CWPosition_HwDeg_f32.value 1.079.51001 target_TrqOvISta_Perl_CWFound_Cnt_Igc.value 0 target_TrqOvISta_Perl_CWFosition_HwDeg_f32.value 7774.960022 target_TrqOvISta_Perl_CWFosition_HwDeg_f32.value 0 target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value 0 target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value 0 target_TrqOvISta_Perl_ESCRequest_Cnt_Igc.value 0 target_TrqOvISta_Perl_ESCRequest_Cnt_Igc.value 0 target_TrqOvISta_Perl_GMOSH_APAMfgEnable_Cnt_Igc.value 0 target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_Igc.value 0 target_TrqOvISta_Perl_GMOSH_ECM_Igc.value 0 target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value 0 target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value 0 target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value 0 target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value 10 target_TrqOvISta_Perl_HavTorque_HwNm_f32.value 10 target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value 11 target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value 11 target_TrqOvISta_Perl_LKARequest_Cnt_Igc.value 11 target_TrqOvISta_Perl_LKARequest_Cnt_Igc.value 11 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 12 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 13 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 1476.10006 target_TrqOvISta_Perl_PosTrajEnable_Cnt_Igc.value 10		
target_TrqOvISta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1-079.51001 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1-079.51001 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HxAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvISta_Pert_CCWFound_Cnt_Igc.value		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value		
target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 774.960022 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 0 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 0 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 0 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 0 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 10 target_TrqOvISta_Per1_HavTorque_HwNm_f32.value 6.900001 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 10 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 11 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 15 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 176.100006 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 176.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 774.960022 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.900001 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.9000001 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		774.960022
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.9000001 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.900001 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0	· - ·	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.900001 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.9000001 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.9000001 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.9000001 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 476.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0		
		476.100006
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001		
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001

© Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-13.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	75939.7578	75939.7578 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	306594	306594	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11000	11000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995	-3.04999995 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.7999992	22.7999992 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	22.7999992	22.7999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.593999982	0.593999982 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	49.2639999	49.2639999 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

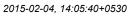
Test Step 2.86 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1612869
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	76739.4375
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	313140
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	13000
TrqOvlSta_Haptictime_Sec_M_f32	174720.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.671999991
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.99000001
k_APAMaxHwTrq_HwNm_f32	8
k_APAMaxVehSpd_Kph_f32	220
k_ESCMaxVehSpd_Kph_f32	113.300003
k_HapticAmplitude_MtrNm_f32	0.600000024
k_HapticDuration_Sec_f32	1.5
k_HapticFreq_Hz_f32	49.7779999
k_HapticReacttime_Sec_f32	1.60000002
k_LKAMaxVehSpd_Kph_f32	190
k_LKAMinVehSpd_Kph_f32	175
k_MaxHapticReqTime_Sec_f32	0.194999993
k_SWARLimiter_HwDeg_f32	16
k_ShiftLevRevTime_Sec_f32	0.0489999987
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.0489999987
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lc	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t	r32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target TrqOvlSta Per1 PosSrvoHwAngle H		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1069.41003		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	779.900024		
	1		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta_Per1_ESCIsLimited Cnt_lgc.value	1		
	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
· - · · ·			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.099999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	471.200012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	481		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-12.3000002		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	13000	13000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	174720.641	174720.641 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc		0 ± 1	•
	0		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08		1 ± 1 1 ± 1	
	1		
TrqOvISta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~

TrqOvlSta_Per1





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.600000024	0.600000024 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	49.7779999	49.7779999 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.87 (Repeat Count = 1)	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1646832
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	77539.1172
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	319686
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	15000
TrqOvlSta_Haptictime_Sec_M_f32	177725
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.683000028
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	1
k APAMaxHwTrg HwNm f32	8.07999992
k_APAMaxVehSpd_Kph_f32	100.360001
k ESCMaxVehSpd Kph f32	118.400002
k_HapticAmplitude_MtrNm_f32	0.606000006
k_HapticDuration_Sec_f32	1.51499999
LapticFreq_Hz_f32	50.2919998
k_HapticReacttime_Sec_f32	1.61600006
k LKAMaxVehSpd Kph f32	191.899994
k_LKAMinVehSpd_Kph_f32	176.75
k MaxHapticReqTime Sec f32	0.0149999997
k_SWARLimiter_HwDeg_f32	16.2999992
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	4.03999996
k StandstillTime Sec f32	0.00100000005
k_Standstiff inte_6c6_f62 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_I	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
© Report created by TESSY V3.1.9, report template V2.1	wassqseac_ or _ mrorque_ man_or

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	· -	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1059.31006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	784.840027		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	476.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	485.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0.119999997		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-10.6999998		
target TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta APAState State M enum	4	4	Result
TrqOvISta_ESCState_State_M_enum	4	4	~
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	-
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	V
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	15000	15000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999	-2.3499999 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.6000004	25.6000004 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	V
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 25.6000004	0 ± 1	*
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.606000006	25.6000004 ± 0.0009765625 0.606000006 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
		50.2919998 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	50.2919998	100.29 19990 ± 0.00097 nanza	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.88 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1680795
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	78338.7969
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	326232
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	180730
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-2 0.407999992
TrgOvlSta LKAFault Cnt M lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	27
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.123000003
k_APAMaxHwTrq_HwNm_f32	6.07999992
k_APAMaxVehSpd_Kph_f32	136.800003
k_ESCMaxVehSpd_Kph_f32	123.5 0.456
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0
k_HapticFreq_Hz_f32	50.8059998
k HapticReacttime Sec f32	1.21599996
k_LKAMaxVehSpd_Kph_f32	144.399994
k_LKAMinVehSpd_Kph_f32	133
k_MaxHapticReqTime_Sec_f32	0.5
k_SWARLimiter_HwDeg_f32	16.6000004
k_ShiftLevRevTime_Sec_f32	0.305000007
k_StandstillThresh_Kph_f32	3.0399996
k_StandstillTime_Sec_f32	0.305000007
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOviSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_inst_Ap_irqovista.irqovista_Peri_GivioShosciliateState_state_enun target Rte Inst Ap TrqoviSta.TrqoviSta Peri GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Ont_igc target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cht_Igc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
targot_rrqOviota_rerr_oovvrouniu_ont_lgc.value	1

2015-02-04, 14:05:40+0530





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1049.20996		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	789.780029		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	481		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	490.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.10000038		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	78338.7969	78338.7969 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	~
TrqOvISta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.456	0.456 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	50.8059998	50.8059998 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.89 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1714758
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79138.4766
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	332778
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	3650
TrqOvlSta_Haptictime_Sec_M_f32	183735
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.3999996

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.134000003
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	128.600006
k_HapticAmplitude_MtrNm_f32	0.462000012 2
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	51.3199997
k HapticReacttime Sec f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	16.8999996
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target TrqOvlSta Per1 APARecoverableFaults Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target TrqOvISta Per1 TrqOscEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1039.10999
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0 704 740074
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	794.719971 0
target_IrqOviSta_Peri_ESCFault_Cnt_igc.value target TrqOviSta_Peri_ESCIsLimited Cnt_igc.value	0
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	485.899994
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	495.700012
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001

© Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79138.4766	79138.4766 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	3650	3650 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.3999996	28.3999996 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	28.3999996	28.3999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.3199997	51.3199997 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.90 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	0	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	
TrqOvlSta_HapticState_State_M_enum	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	25475	
TrqOvlSta_Haptictime_Sec_M_f32	186740.953	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	
TrqOvlSta_LKAState_State_M_enum	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	
k_APAIncludeHaptic_Cnt_lgc	1	
k_APAMaxHwTrqTime_Sec_f32	0.144999996	
k_APAMaxHwTrq_HwNm_f32	6.15999985	
k_APAMaxVehSpd_Kph_f32	138.600006	
k_ESCMaxVehSpd_Kph_f32	133.699997	
k_HapticAmplitude_MtrNm_f32	0.462000012	
k_HapticDuration_Sec_f32	1.55999994	
k_HapticFreq_Hz_f32	51.8339996	
k_HapticReacttime_Sec_f32	1.23199999	
k_LKAMaxVehSpd_Kph_f32	146.300003	
k_LKAMinVehSpd_Kph_f32	134.75	
k_MaxHapticReqTime_Sec_f32	0.600000024	
k_SWARLimiter_HwDeg_f32	17.2000008	
k_ShiftLevRevTime_Sec_f32	0.308999985	
k_StandstillThresh_Kph_f32	3.07999992	
k_StandstillTime_Sec_f32	0.308999985	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_Igc target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_TrqOvlSta_Per1_ESCState_State_en		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enun			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH APAMfgEr		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	~	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc	target TrqOvlSta Per1 GMOSH LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>Igc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	0 = . = =		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_H target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006 -5.9000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta APAState State M enum	1	1	Kesuit
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	79938.1563	79938.1563 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	V
TrqOvlSta_LKAState_State_M_enum	3	3	*
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.91 (Repeat Count = 1)	Innut Value
lame	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	1
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1782684
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	80737.8438
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	345870
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	0
rqOvlSta_Haptictime_Sec_M_f32	189745
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407999992
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	1
qOvlSta_LKAState_State_M_enum	2
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	31.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	1
APAMaxHwTrqTime_Sec_f32	0.156000003
APAMaxHwTrq HwNm f32	6.07999992
APAMaxVehSpd Kph f32	136.800003
ESCMaxVehSpd Kph f32	138.800003
_LapticAmplitude_MtrNm_f32	0.629999995
HapticDuration Sec f32	0.5
HapticFreq Hz f32	52.3479996
HapticReacttime Sec f32	0
_HapticReactiffle_Sec_132 LKAMaxVehSpd Kph f32	199.5
- '-'-	
_LKAMinVehSpd_Kph_f32	183.75
_MaxHapticReqTime_Sec_f32	0.5
_SWARLimiter_HwDeg_f32	17.5
_ShiftLevRevTime_Sec_f32	0.305000007
_StandstillThresh_Kph_f32	3.03999996
_StandstillTime_Sec_f32	0.305000007
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lq	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur	
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	
liget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
irget_Rte_inst_Ap_frqOviSta.FrqOviSta_Feff_HapticRequest_Crit_igc	target_TrqOvISta_Per1_HwTorque_HwNm_f32
1901_110_1131_AP_11qOviola.11qOviola_Pel 1_Hw10lque_HwNIII_132	target_frqoviota_refr_frwforque_frwitin_ioz

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1018.90997		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	804.599976		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvISta_rer1_HapticRequest_Crit_igc.value target TrqOvISta Per1 HwTorque HwNm f32.value	7.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrgOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	495.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	505.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4.30000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	80737.8438	80737.8438 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	31.2000008	31.2000008 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2 4	4	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	2 4 0	4 0 ± 1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	2 4 0 31.2000008	4 0 ± 1 31.2000008 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	2 4 0 31.2000008 0.629999995	4 0 ± 1 31.2000008 ± 0.0009765625 0.629999995 ± 0.0009765625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	2 4 0 31.2000008	4 0 ± 1 31.2000008 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.92 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1816647
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	81537.5234
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	352416
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	3650
TrqOvlSta_Haptictime_Sec_M_f32	192750 -1.6499998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-1.04999990
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	32.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.166999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32 k_HanticAmplitude_MtrNm_f32	143.899994 0.635999978
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0.870000005
k HapticFreq Hz f32	52.8619995
k_HapticReacttime_Sec_f32	2
k_LKAMaxVehSpd_Kph_f32	201.399994
k_LKAMinVehSpd_Kph_f32	185.5
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.7999992
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.308999985 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enum	V - 1
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_irqOvlSta_Per1_minSecurevenicleSpeed_kpn_t32 target_trqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvIsta_Per1_APARecoverablePaulis_Crit_Igc.value target_TrqOvIsta_Per1_APARequest_Cnt_Igc.value	1
target_TrqOvlota_rer1_crWFound_Cnt_lgc.value	1
<u> </u>	<u> </u>

2015-02-04, 14:05:40+0530

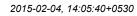


Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1008.81		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	809.539978		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	510.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	-
TrqOvlSta_ESCState_State_M_enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	81537.5234	81537.5234 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	3650	3650 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvISta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.635999978	0.635999978 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	52.8619995	52.8619995 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.93 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1850610	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	82337.2031	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	358962	
TrqOvlSta_HapticState_State_M_enum	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	25475	
TrqOvlSta_Haptictime_Sec_M_f32	195755	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	
TrqOvlSta_LKAState_State_M_enum	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34	

TrqOvlSta_Per1





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.178000003
k_APAMaxHwTrq_HwNm_f32	6.15999985 138.600006
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	149
k_HapticAmplitude_MtrNm_f32	0.64200002
k_HapticDuration_Sec_f32	1.65999997
k_HapticFreq_Hz_f32	53.3759995
k_HapticReacttime_Sec_f32	1.29999995
k_LKAMaxVehSpd_Kph_f32	203.300003
k_LKAMinVehSpd_Kph_f32	187.25
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	18.1000004
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Fer1_GMOSH_LKAMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvIsta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosTrajHwAngle_HwDeg_f32	target TrgOvlSta Per1 PosTrajEnable_UniCigc
target Rte Inst Ap TrqOviota.TrqOviSta Per1 SWARTrgtAngRequest HwDeg f3	, · - · - · · · · · · · · · · · · · · ·
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-998.710022
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	814.47998
target_rrqOvlSta_Per1_ESCFauit_Ont_igc.value	0
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	505.5
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	10.3000002
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007

© Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-1.10000002		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	82337.2031	82337.2031 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	25475	25475 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34	34 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	34	34 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.64200002	0.64200002 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	53.3759995	53.3759995 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.94 (Repeat Count = 1) Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	1884573
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	83136.8828
TrqOvlSta ElapsedStandstillTime Sec M f32	365508
TrgOvISta HapticState State M enum	0
TrgOvISta Hapticdur Cnt M u16	29000
TrgOvlSta Haptictime Sec M f32	198760.641
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-5.5
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.419
TrgOvISta LKAFault Cnt M lgc	1
TrqOvISta LKAPermFault Cnt M Igc	0
TrqOvlSta LKAPermFault Cnt M u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvISta PosSrvoHwAngle HwDeg M f32	35.4000015
TrgOvISta ReadytoPulse Cnt M Igc	1
k APAIncludeHaptic Cnt Igc	0
k APAMaxHwTrqTime Sec f32	0.188999996
k APAMaxHwTrq HwNm f32	8.6400034
k APAMaxVehSpd Kph f32	194.399994
k ESCMaxVehSpd Kph f32	154.100006
k HapticAmplitude MtrNm f32	0.648000002
k HapticDuration Sec f32	1.62
k HapticFreq Hz f32	53.8899994
k HapticReacttime Sec f32	1.72800004
k LKAMaxVehSpd Kph f32	205.199997
k LKAMinVehSpd Kph f32	0
k MaxHapticRegTime Sec f32	0.119999997
k_SWARLimiter_HwDeg_f32	18.3999996
k ShiftLevRevTime Sec f32	0.0289999992
k StandstillThresh Kph f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
	Detected Cnt target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlOta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateSta target TrqOvlSta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target TrqOvISta Per1 GMOSH LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-988.609985		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	819.419983		
target TrgOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrgOvISta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.6999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	510.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	•
TrqOvISta_ESCState_State_M_enum	4	4	_
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	83136.8828	83136.8828 ± 1	Ž
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	~
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	
TrqOviSta_HwTorqueSv_HwWin_w_Str.3v_Dis_132	0	0 ± 1	~
TrqOviSta_EtXi auti_oni_w_gc	0	0±1	
TrqOvISta LKAPermFault Cnt M u08	0	0 ± 1	•
TrqOvISta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.18999998	0.189999998 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
	I .	·=·	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	53.8899994	53.8899994 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.95 (Repeat Count = 1) Name	Input Value
	·
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvISta_APAState_State_M_enum	0
rqOvISta_ESCState_State_M_enum	
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1918536
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83936.5625
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	372054
rqOvISta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	31000
rqOvlSta_Haptictime_Sec_M_f32	201765
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.1500001
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	36.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.200000003
_APAMaxHwTrq_HwNm_f32	8.72000027
_APAMaxVehSpd_Kph_f32	196.199997
_ESCMaxVehSpd_Kph_f32	159.199997
_HapticAmplitude_MtrNm_f32	0.653999984
_HapticDuration_Sec_f32	1.63499999
_HapticFreq_Hz_f32	54.4039993
_HapticReacttime_Sec_f32	1.74399996
_LKAMaxVehSpd_Kph_f32	207.100006
_LKAMinVehSpd_Kph_f32	220
_MaxHapticReqTime_Sec_f32	0.135000005
_SWARLimiter_HwDeg_f32	18.7000008
_ShiftLevRevTime_Sec_f32	0.0329999998
_StandstillThresh_Kph_f32	4.36000013
_StandstillTime_Sec_f32	0.0329999998
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
riget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvlSta Per1 ESCRequest Cnt Igc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enul	
arget Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSH APAMfqEnable Cnt Igc	
arget_Rte_Inst_Ap_TrqOviota.TrqOviota_Fer1_GMOSH_ESCMfgEnable_Cnt_Igc	
riget Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH LKAMfgEnable Cnt Igc	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
aget_rte_inst_rp_frq0viota.frq0viota_FeFf_fwT0fque_fwWifif_i32	talget_fiqOviota_Fell_fiwTolque_fiwMill_joz

2015-02-04, 14:05:40+0530



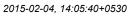
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-978.51001 0		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	824.359985		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOviSta_Per1_ESCNequest_Ont_igc.value target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	30.8999996		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	2.0999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	372054	372054	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	~
TraCulCto Hantistima Coo M 622			
TrqOvlSta_Haptictime_Sec_M_f32	201765	201765 ± 1	~
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	201765 -5.1500001	201765 ± 1 -5.1500001 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	-5.1500001 1	-5.1500001 ± 0.000009 1 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	-5.1500001 1 0	-5.1500001 ± 0.000009 1 ± 1 0 ± 1	*
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	-5.1500001 1 0 2	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	-5.1500001 1 0 2	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1	\rightarrow \right
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	-5.1500001 1 0 2 4 36.7999992	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.7999992 ± 0.0009765625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	-5.1500001 1 0 2 4 36.7999992 0	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.7999992 ± 0.0009765625 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	-5.1500001 1 0 2 4 36.7999992 0	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.79999992 ± 0.0009765625 0 ± 1 0 ± 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	-5.1500001 1 0 2 4 36.7999992 0 0	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.7999992 ± 0.0009765625 0 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	-5.1500001 1 0 2 4 36.7999992 0 0	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.7999992 ± 0.0009765625 0 ± 1 0 ± 1 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	-5.1500001 1 0 2 4 36.7999992 0 0 4 4	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.7999992 ± 0.0009765625 0 ± 1 0 ± 1 4 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	-5.1500001 1 0 2 4 36.7999992 0 0 4 4 4	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.79999992 ± 0.0009765625 0 ± 1 0 ± 1 4 4	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	-5.1500001 1 0 2 4 36.7999992 0 0 4 4 4 2 4	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.79999992 ± 0.0009765625 0 ± 1 0 ± 1 4 4 2 4 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	-5.1500001 1 0 2 4 36.7999992 0 0 4 4 2 4 0 36.7999992	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.7999992 ± 0.0009765625 0 ± 1 0 ± 1 4 4 2 4 0 ± 1 36.7999992 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	-5.1500001 1 0 2 4 36.7999992 0 0 4 4 2 4 0 36.7999992 0.653999984	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.7999992 ± 0.0009765625 0 ± 1 0 ± 1 4 4 2 4 0 ± 1 36.7999992 ± 0.0009765625 0.653999984 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	-5.1500001 1 0 2 4 36.7999992 0 0 4 4 2 4 0 36.7999992	-5.1500001 ± 0.000009 1 ± 1 0 ± 1 2 ± 1 4 36.7999992 ± 0.0009765625 0 ± 1 0 ± 1 4 4 2 4 0 ± 1 36.7999992 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 ChecknointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-





Test Step 2.96 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-4.80000019 0.441000015
TrqOvISta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32 k_HanticAmplitude_MtrNm_f32	164.300003 0.660000026
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	1.6499998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	19
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0370000005 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	V - 1
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_irqOvlSta_Per1_minSecurevenicleSpeed_kpn_t32 target_trqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
	1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-968.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	829.299988		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008	38.2000008 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	38.2000008	38.2000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	~
	4	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.97 (Repeat Count = 1)		~
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	0	
TrqOvlSta_ESCState_State_M_enum	2	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1986462	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	85535.9219	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	385146	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	
TrqOvlSta_Haptictime_Sec_M_f32	207775	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	
TrqOvlSta_LKAState_State_M_enum	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	39.5999985	

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.222000003
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	169.399994
k_HapticDuration_Soc_f32	0.666000009 1.62
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	55.4319992
k HapticReacttime Sec f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	0
k LKAMinVehSpd Kph f32	194.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	19.2999992
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_Results (APADrvrInterventionDetected_Cnt_$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-958.309998
target_rqoviSta_Peri_ccwPosition_Hwbeg_rsz.value target_rrqoviSta_Peri_ccwPosition_Hwbeg_rsz.value	-958.309998 0
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	834.23999
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target TrqOvISta Per1 ESCIsLimited Cnt Igc.value	0
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.6999981
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.8999996
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	51.5
	0
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	U .

© Report created by TESSY V3.1.9, report template V2.1

181





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.30000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	85535.9219	85535.9219 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	207775	207775 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	39.5999985	39.5999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	39.5999985	39.5999985 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.666000009	0.666000009 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.4319992	55.4319992 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrgOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	2020425
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	86335.6016
TrqOvlSta ElapsedStandstillTime Sec M f32	391692
TrgOvlSta HapticState State M enum	2
TrgOvISta Hapticdur Cnt M u16	31000
TrgOvlSta Haptictime Sec M f32	210780
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-5.1500001
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.43000007
TrqOvISta LKAFault Cnt M Igc	1
TrgOvISta LKAPermFault Cnt M lgc	0
TrgOvISta LKAPermFault Cnt M u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.232999995
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	174.5
k_HapticAmplitude_MtrNm_f32	0.671999991
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	55.9459991
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	220
k_LKAMinVehSpd_Kph_f32	196
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	19.6000004
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.032999998

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	:_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-948.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	839.179993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	210780	210780 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	•
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

lame	Input Value
	·
te_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvISta_APAState_State_M_enum	
rqOvISta_ESCState_State_M_enum	4
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2054388
qOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87135.2813
rqOvISta_ElapsedStandstillTime_Sec_M_f32	398238
rqOvlSta_HapticState_State_M_enum	
qOvlSta_Hapticdur_Cnt_M_u16	33000
qOvlSta_Haptictime_Sec_M_f32	213785
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.8000019
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
qOvlSta_LKAFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	3
qOvlSta_LKAState_State_M_enum	0
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	42.4000015
qOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime_Sec_f32	0.244000003
APAMaxHwTrq_HwNm_f32	8.80000019
_APAMaxVehSpd_Kph_f32	198
_ESCMaxVehSpd_Kph_f32	179.600006
_HapticAmplitude_MtrNm_f32	0.677999973
_HapticDuration_Sec_f32	1.64999998
_HapticFreq_Hz_f32	56.4599991
_HapticReacttime_Sec_f32	1.75999999
LKAMaxVehSpd_Kph_f32	105.75
_LKAMinVehSpd_Kph_f32	197.75
_MaxHapticReqTime_Sec_f32	0.150000006
SWARLimiter_HwDeg_f32	19.8999996
_ShiftLevRevTime_Sec_f32	0.0370000005
StandstillThresh_Kph_f32	4.4000001
_StandstillTime_Sec_f32	0.0370000005
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

© Report created by TESSY V3.1.9, report template V2.1

184

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosSrvoHwAngle_H target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-938.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	844.119995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta_Per1_ESCIsLimited Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	51.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	72.0999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0.140000001 8.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	ixesuit
TrgOvISta ESCState State M enum	4	4	·
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	-
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	87135.2813	87135.2813 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	V
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	42.4000015	42.4000015 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	1 ± 1 0 ± 1	*
target_TrqOviSta_Per1_APADrviniterventionDetected_Cnt_igc.value target_TrqOviSta_Per1_APAState_State_enum.value	4	4	-
target_TrqOvISta_Per1_ESCState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
		4	~
	4	4	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value			~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 42.4000015	0 ± 1 42.4000015 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-





Test Step 2.100 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-3.4000001 0.485000014
TrqOvIsta LKAFault Cnt M lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	1
TrgOvISta LKAPermFault Cnt M u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004 0
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	1.71000004
k HapticFreq Hz f32	56.973999
k HapticReacttime Sec f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0529999994 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	· - ·
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_irqOvlSta_Per1_minSecurevenicleSpeed_kpn_t32 target_trqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
	1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	849.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784	404784	-
TrqOvlSta_HapticState_State_M_enum	1	1	-
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	-
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	- − •
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	- − •
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	- − •
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	- − •
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target TrqOvlSta Per1 TrqOscEnable Cnt Igc.value	1	1±1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.101 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2122314
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	88734.6406
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	411330
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	43000
TrqOvlSta_Haptictime_Sec_M_f32	219795.688
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.495999992
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008





	Input Value
, _ , = = = = = = = = = = = = = = =	1
	0
	0.26600003
	9.19999981 207
	19,2999992
2 11 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0.75
- · ·	1.72500002
	57.487999
	1.84000003
k_LKAMaxVehSpd_Kph_f32	218.5
k_LKAMinVehSpd_Kph_f32	201.25
	0.224999994
	20.5
	0.057
	4.599999
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.057
	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvISta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_rer1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
0	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
0 = 1 = = = = = = = = = = = = = = = = =	0
0 _ 1	0
v = 1	1
312 (1 112 1 21 1 1 1 2 2 2 3 1 1 1	0 -917.909973
3.5 1	-917.909973 0
	854
	0
	0
· - ·	0
	0
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0
312 11 112 1 2 1111 1111 2 23 111	1
• - ·	-5.5999999
	0
	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0 72.0999985
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.6999998		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	43000	43000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	219795.688	219795.688 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.31479979	-4.31479979 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	45.2000008	45.2000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	45.2000008	45.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	57.487999	57.487999 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

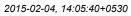
Test Step 2.102 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2156277
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	89534.3203
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	417876
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	45000
TrqOvlSta_Haptictime_Sec_M_f32	222801.016
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	46.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.27700001
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	23
k_HapticAmplitude_MtrNm_f32	0.300000012
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	58.0019989
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.23999995
k_SWARLimiter_HwDeg_f32	20.7999992
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0.0610000007
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Superscript{Aparticles}) and the properties of $	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_M_enum 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 45000 45000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3	Name	Input Value		
Simple Rev. May. Prof. 1006 Temploses Perf. An Antonous Conf. 100	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
Singel Res Mark Age TapOolita Port An ArSilace Sales enum Singel Res Mark Age TapOolita Port ArSilace Sales enum Singel Res Mark Age TapOolita Port ArSilace Sales enum Singel Res Mark Age TapOolita Port Age TapOolita Port Age TapOolita TapOol	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
Barged, Reg. MacA, Part (1998) As Profestion Force Controlled Profestion	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
singed, Rein Jan, Part (1908) and Profit (1908)	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_end	um	
Image, Ray, Jank, Age, Topkosta, Port (Devoting, Cruz)se Image, Ray, Jank, Age, Topkosta, Port (Devoting, Cruz)se Image, Ray, Jank, Age, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Port (Devoting, Laborat, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Topkosta, Topkosta, Port (Devoting, Laborat, Laborat, Cruz)se Image, Ray, Jank, Age, Topkosta, Top	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
Image Rem May TripOstilla Profess Rem Section Cent Section	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Presch_Age_Presched processor_Preschedungs_Presc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
Image, Rev. Part (1904) Star (1904) St				
Image, Reim May, TripOkilla prickings. peril. GMOSI APMISSIANE Crit. gits image, Reim May, TripOkilla prickings. peril. GMOSI APMISSIANE Crit. gits image, Reim May, TripOkilla prickings. peril. GMOSI APMISSIANE Crit. gits image, Reim May, TripOkilla prickings. peril. GMOSI APMISSIANE Crit. gits image, Reim May, TripOkilla prickings. peril. GMOSI APMISSIANE Crit. gits image, Reim May, TripOkilla prickings. peril. GMOSI APMISSIANE Crit. gits image, Reim May, TripOkilla prickings. peril. GMOSI APMISSIANE Crit. gits image, Reim May, TripOkilla prickings. peril. LMORIBATION Sept. peril.				
langer, Rim, Int. A. Drigolista Trock/Osta Part CMMOSH EXCENSION Company				
Langer Reg. Res. Part O-2005 Part CANCER ESCAMBS ESCAMBS ESCAMBS Part CANCER ESCAMBS ESCAMB		0 - 1		
Simple Rev. Part (Proches Tarr) Could Start (Proches Tarr) Could Sta				
Segret TopOSSE Pert TopOSSE Pert TopOSSE T				
Segret Fig. TopOSSE Pert Pert TopOSSE Pert				
stage Re Inst A_CTRO/SSS TROOKS Per L L KARABIT Cnt Lgs stage Re Inst A_CTRO/SSS TROOKS Per L L KARABIT Cnt Lgs stage Re Inst A_CTRO/SSS TROOKS Per L L KARABIT Cnt Lgs stage Re Inst A_CTRO/SSS TROOKS Per L L KARABIT Cnt Lgs stage Re Inst A_CTRO/SSS TROOKS Per L L KARABIT Cnt Lgs stage Re Inst A_CTRO/SSS TROOKS Per L L KASBE (Size crum stage Re Inst A_CTRO/SSS TROOKS Per L INSS SECURITY CORS				
saget Reg. Inst. Ap. Trach/site Trach/site Per LLKAninheit Cnt. go saget Reg. Inst. Ap. Trach/site Trach/site Per LLKAninheit Cnt. go saget Reg. Inst. Ap. Trach/site Trach/site Per LLKAninheit Cnt. go saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASe Site Site , anum saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASe Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASe Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASe Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASe Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASe Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASe Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASe Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Per LLKASE Site , saget Reg. Inst. Ap. Trach/site Per LLKASE Site , saget Reg. Inst.			02	
taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. KARRepuest, Cnt. Ign taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. KARRepuest, Cnt. Ign taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Masscaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Masscaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure/velicle/Speed, Ign. 12 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Stiff.traceristation.com. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Trackors. Pert J. Stiff.traceristation.com. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Trackors. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Trackors. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Trackors. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Trackors. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Trackors. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Pert J. Passcaure, Inst. 20 taged, Rie, Inst. Ap. Track/Sis. Track/Sis. P				
singer, Rep. met. Ap. Tra0/VSSI. Pert. LKASSite. Sinte. enum singer, Rep. met. Ap. Tra0/VSSI. Tra0/VS			c	
taged, Rel. plat. Ap. TracVista TracVista Pert Instancemonal Pert Ins				
Larged, Rev. Bat. Ap. Track/USB. Pert. Ministenser/elhiosSpeed (Aprt.) 22 Imped. Rev. Bat. Ap. Track/USB. Pert. P. Deserver/elhiosCollege Imped. Rev. Bat. Ap. Track/USB. Pert. Deserver/elhiosCollege Imped. Track/USB. Pert. College Imped. Track/USB. Pert. ESCEL Imped. Track/USB. Pert. ESCEL Imped. Track/USB. Pert. ESCEL Imped. Track/USB. Pert. Bat. Bat. Bat. Bat. Bat. Bat. Bat. Ba				
target Reg. Inst. Ap. TracOviSta Prof PostewCrable Cnt. go		0 = . = =		
Ising R. Bin. A. TraOviSta TraOviSta Pert PostropHerbigs Crit Userger River Art Aproxists TraOviSta Pert SWRT TraDviSta Pert Water PostropHerbigs Crit Userger River Art Aproxists TraOviSta Pert SWRT TraDviSta Pert Art Aproxists TraOviSta Pert SWRT TraDviSta Pert TraOviSta Pert				
Langel, Tark, A.D., Tark, Orliss, Part P. Pestaglinable, Cntl. Igo- tagel, Rise, Int. A.D., Tark, Orliss, Part P. Pestaglinable, Cntl. Igo- tagel, Rise, Int. A.D., Tark, Orliss, Part P. Strafferhavege, HMDeg, 12 tagel, Rise, Int. A.D., Tark, Orliss, Part SWARTg/AngRequest, HMDeg, 13 tagel, Rise, Int. A.D., Tark, Orliss, Part SWARTg/AngRequest, HMDeg, 13 tagel, Rise, Int. A.D., Tark, Orliss, Part T. Strafferhavege, Cnt. Igo- tagel, Rise, Int. A.D., Tark, Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Rise, Int. A.D., Tark, Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Rise, Int. A.D., Tark, Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Rise, Int. A.D., Tark, Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Tark, Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Tark, Orliss, Part T. Ark, Park, Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Tark, Orliss, Part T. Ark, Park, Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Tark, Orliss, Part T. Ark, Park, Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Tark, Orliss, Part T. Corlos (Cnt. Igo- tagel, Tark, Orliss, Part T. Orliss, Part T. Tark, Entitlements (Cnt. Igo- tagel, Tark, Orliss, Part T. Orliss, Cnt. Igo- tagel, Tark, Orliss, Part T. Orliss, Cnt. Igo- tagel, Tark, Orliss, Part T. Schall, Cnt. Igo- tagel, Tark, Orliss, Part T. Igo- tagel, Tark, Orl			- *	
Integer, Ret. Int.A.D., Trq:ONSta. Pert. PostTrajt-Mangle-, Molec. 12 Integer. Ret. Int.A.D., Trq:ONSta. Trg:ONSta. Pert. SMRT. PostTrajt-Mangle-guest. Molec. 13 Integer. Ret. Int.A.D., Trq:ONSta. Trq:ONSta. Pert. SMRT. Trq:ONSta. Pert. T			·-	
largeR, linst, Ap, TrqOviSta TrqOviSta Pert TrqOscen, Brittle, vertical News TrqOviSta Pert Trq		target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target, Inst. Ap. Tro/Orista. TrqOrista. Prd. 1.TrqOscAmp. MrNtm. 132 target, Inst. Ap. TrqOvista. TrqOvista. Prd. 1.TrqOscAmp. MrNtm. 132 target, Inst. Ap. TrqOvista. TrqOvista. Prd. 1.TrqOscTroq. Prd. 123 target, TrqOvista. Prd. 1.TrqOscTroq. Prd. 123 target, TrqOvista. Prd. 1.TrqOscTroq. Prd. 123 target, TrqOvista. Prd. 1.Ap. TrqOvista. Prd. 1.TrqOscTroq. Prd. 123 target, TrqOvista. Prd. 1.Ap. TrqOvista. Prd. 1.TrqOscTroq. Prd. 123 target, TrqOvista. Prd. 1.Ap. TrqOvista. Prd. 1.TrqOscTroq. Prd. 124 target, TrqOvista. Prd. 1.Ap. TrqOvista. P	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvISta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
Image Reb. Inst. Ap. TroOvista Prof. TroOpeEnable Cnt. go Image TroOvista Prof. TroOpeEnable Cnt. go Image TroOvista Prof. TroOpeEnable TroOvista Prof. TroOpeEnable TroOpeEnable TroOvista Prof. TroOvista Prof. TroOpeEnable TroOvista Prof. TroOvista Prof. APARocoverableFaults Cnt. go value Image TroOvista Prof. APARocoverableFaults Cnt. go value Image TroOvista Prof. APARocoverableFaults Cnt. go value Image TroOvista Prof. CNVPosition HvDog. R32 value Image TroOvista Prof. CNVPosition R40 value Image TroOvista Prof. CNVPosition R40 value Image TroOvista Prof. CNVPosition R40 value Image TroOvista Prof. May R40 value R40 valu	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target Rr. Inst A.D., TrQOVISIa TrQOVISIA TrQOVISIA Perl _TrqOscFreq_Hz_f32 arget_TrqOVISIa_Perl _APARecoverableFaults_Cnt_lgc_value target_TrqOVISIa_Perl _APARecoverableFaults_Cnt_lgc_value target_TrqOVISIa_Perl _APARecoverableFaults_Cnt_lgc_value target_TrqOVISIa_Perl _APARecoverableFaults_Cnt_lgc_value target_TrqOVISIa_Perl _COWPosition _HWDegf32_value target_TrqOVISIa_Perl _COWPosition _HWDegf32_value target_TrqOVISIa_Perl _COWPosition _HWDegf32_value target_TrqOVISIa_Perl _COWPosition _HWDegf32_value target_TrqOVISIa_Perl _SCWFault_Cnt_lgc_value target_TrqOVISIa_Perl _SCWFault_Cnt_lgc_value target_TrqOVISIa_Perl _SCRED_Trq_VAIVISIA_Perl _SCRED_Trq_V	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	_f32	
target_TrqOvISta_Pert_APARecoverableFaults_Cnt_Igc-value larget_TrqOvISta_Pert_APARecoverableFaults_Cnt_Igc-value otarget_TrqOvISta_Pert_CVWFound_Cnt_Igc-value target_TrqOvISta_Pert_CVWFound_Cnt_Igc-value target_TrqOvISta_Pert_CVWFound_Cnt_Igc-value target_TrqOvISta_Pert_CVWFound_Cnt_Igc-value target_TrqOvISta_Pert_CVWFound_Cnt_Igc-value target_TrqOvISta_Pert_CVWFound_Cnt_Igc-value target_TrqOvISta_Pert_CVWFound_Cnt_Igc-value target_TrqOvISta_Pert_SCREquect_Cnt_Igc-value target_TrqOvISta_Pert_HaptGRequest_Cnt_Igc-value target_TrqOvISta_Pert_HaptGRequest_Cnt_Igc-value target_TrqOvISta_Pert_HaptGRequest_Cnt_Igc-value target_TrqOvISta_Pert_LKAInhibit_Cnt_Igc-value target_TrqOvISta_Pert_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
Integral_TrqOviSta_Pert_APARequest_Crt_Igc_value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
larget_TrqOvISta_Pert_APARequest_Cnt_tgc.value 1	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
larget_TrqOv/Sta_Pert_CCWFound_Cnt_Igc_value	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
larget_TrQOVISIa_Pert_CWFound_Cnt_lgc.value 10 10 10 10 10 10 10 1	target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvSta_Pert_CWFound_Cnt_Igc.value 1	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
larget_TroOVSIa_Pert_ESCFaut_Cnt_govalue 1	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-907.809998		
target_TrqOvIsta_Perrl_ESCIsLImited_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_ESCIsLImited_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_ESCIsLImited_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_GMOSH_ESCIMIgenable_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_GMOSH_ESCIMIgenable_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_GMOSH_ESCIMIgenable_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_MapticRequest_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_HapticRequest_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_HapticRequest_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_HAVTorque_HwNm_f3z_value 8.60000038 target_TrqOvIsta_Perrl_LKAInhibit_Cnt_Igc_value 1 target_TrqOvIsta_Perrl_LKAInhibit_Cnt_Igc_value 0 target_TrqOvIsta_Perrl_LKAInhibit_Cnt_Igc_value 10 target_TrqOvIsta_Perrl_MassecureVehicleSpeed_Kpt_f3z_value 103 target_TrqOvIsta_Perrl_MassecureVehicleSpeed_Kpt_f3z_value 103 target_TrqOvIsta_Perrl_PostrajHwAngle_HwDeg_f3z_value 103 target_TrqOvIsta_Perrl_PostrajHwAngle_HwDeg_f3z_value 103 target_TrqOvIsta_Perrl_PostrajHwAngle_HwDeg_f3z_value 103 target_TrqOvIsta_Perrl_ShiftLeversInReverse_Cnt_Igc_value 104 target_TrqOvIsta_Perrl_ShiftLeversInReverse_Cnt_Igc_value 104 target_TrqOvIsta_Interreverse_Cnt_Igc_value 105 target_TrqOvIsta_Int	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value			
target_TrqOvSta_Pert_ESCRelumited_Cnt_lgc.value 1				
target_TrqOvista_Pert_ESCRequest_Cnt_lgc.value 1 target_TrqOvista_Pert_GMOSH_APAMIgEnable_Cnt_lgc.value 1 target_TrqOvista_Pert_GMOSH_ESCMIgEnable_Cnt_lgc.value 1 target_TrqOvista_Pert_GMOSH_ESCMIgEnable_Cnt_lgc.value 1 target_TrqOvista_Pert_GMOSH_ESCMIgEnable_Cnt_lgc.value 1 target_TrqOvista_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOvista_Pert_LKAPAGUSE_PMOSH_ESCAMIGENABLE		·		
target_TrqOwIsta_Perl_GMOSH_APAMfgEnable_Cnt_Igc.value 1				
target_TrqOvISta_PerI_GMOSH_EKAMIGEnable_Cnt_Igc.value 1 target_TrqOvISta_PerI_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_PerI_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_PerI_HapticRequest_Cnt_Igc.value -8.60000038 target_TrqOvISta_PerI_LKAFautt_Cnt_Igc.value 0 target_TrqOvISta_PerI_LKARequest_Cnt_Igc.value 0 target_TrqOvISta_PerI_LKARequest_Cnt_Igc.value 82.4000015 target_TrqOvISta_PerI_MaxSecureVehicleSpeed_Kph_f32 value 82.4000015 target_TrqOvISta_PerI_MinSecureVehicleSpeed_Kph_f32 value 10 target_TrqOvISta_PerI_PoSTrajHwAngle_HwDeg_f32 value 1 target_TrqOvISta_PerI_SoXRATrigMangRequest_HwDeg_f32 value 1 target_TrqOvISta_PerI_SwARTrigMangRequest_HwDeg_f32 value 1 target_TrqOvISta_PerI_ShiffLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Result TrqOvISta_ParAState_State_M_enum 0 0 0 1 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 1 1 1 1 1 1 1 1 1 1 1 1				
target_TrqOvISta_Per1_BdNOSH_LKAMfgEnable_Cnt_lgc.value				
target_TrqOvISta_Per1_HapticRequest_Cnt_lgc_value -8.60000038 target_TrqOvISta_Per1_LKAFault_Cnt_lgc_value -8.60000038 target_TrqOvISta_Per1_LKAFault_Cnt_lgc_value 0 target_TrqOvISta_Per1_LKARequest_Cnt_lgc_value 0 target_TrqOvISta_Per1_LKARequest_Cnt_lgc_value 0 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32_value 82.4000015 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32_value 103 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc_value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32_value 0.170000002 target_TrqOvISta_Per1_SWARTrgAngRequest_HwDeg_f32_value 13.3000002 target_TrqOvISta_Per1_Shift_everIsinReverse_Cnt_lgc_value 0 Vame Actual Value Expected Value Result TrqOvISta_Per1_Shift_everIsinReverse_Cnt_lgc_value 0 0 Value TrqOvISta_Per1_Shift_everIsinReverse_Cnt_lgc_value 0 0 Value Result TrqOvISta_Per1_Shift_everIsinReverse_Cnt_lgc_value 0 0 Value Result TrqOvISta_ESCState_State_M_enum 3 3 3 3 1 Value Value				
target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value -8.60000038 target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Pert_LKAInhibit_Cnt_Igc.value 0 target_TrqOvISta_Pert_LKAInhibit_Cnt_Igc.value 0 target_TrqOvISta_Pert_MinSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvISta_Pert_MinSecureVehicleSpeed_Kph_f32.value 103 target_TrqOvISta_Pert_PosTrajEnable_Cnt_Igc.value 0.77000002 target_TrqOvISta_Pert_SwaRTrgtAngRequest_HwDeg_f32.value 0.77000002 target_TrqOvISta_Pert_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Result TrqOvISta_PaState_State_M_enum 0 0 0 1 TrqOvISta_ElapsedAPMaxHwTrqTime_Sec_M_f32 0 0 ± 1 2 1 2 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 ± 1 2 1 2 1 2 2 1 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4				
target_TrqOvISta_Pert_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Pert_LKAInhibit_Cnt_lgc.value 0 target_TrqOvISta_Pert_LKARequest_Cnt_lgc.value 0 target_TrqOvISta_Pert_LKARequest_Cnt_lgc.value 82.4000015 target_TrqOvISta_Pert_MinSecureVehicleSpeed_Kph_f32.value 103 target_TrqOvISta_Pert_PosTrajHwAngle_HwDeg_f32.value 1 target_TrqOvISta_Pert_PosTrajHwAngle_HwDeg_f32.value 13.3000002 target_TrqOvISta_Pert_ShiffLeverIsInReverse_Cnt_lgc.value 0 Name Actual Value Expected Value Result TrqOvISta_PAState_State_M_enum 0 0 4 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 4 TrqOvISta_ElapsedStandstiliTime_Sec_M_f32 0 0 ± 1 4 TrqOvISta_Hapticatie_State_M_enum 0 0 0 0 TrqOvISta_ElapsedStandstiliTime_Sec_M_f32 0 0 ± 1 0 0 0 TrqOvISta_Hapticatie_State_M_enum 0 0 0 0 0 0 0 0 0 0 0 0 0 0 <td></td> <td></td> <td></td> <td></td>				
target_TrqOvISta_Perl_LKAInhibit_Cnt_Igc.value 0 target_TrqOvISta_Perl_LKARequest_Cnt_Igc.value 0 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32.value 103 target_TrqOvISta_Perl_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Perl_PosTrajHwAngle_HwDeg_f32.value 13.3000002 target_TrqOvISta_Perl_SMRTrgtAngRequest_HwDeg_f32.value 13.3000002 target_TrqOvISta_Perl_ShiffLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Result TrqOvISta_Pastate_State_Menum 0 0 V TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 V TrqOvISta_ElapsedShiffttevRevTime_Sec_M_f32 0 0 ± 1 V TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 ± 1 V TrqOvISta_Hapticdute_Cnt_M_u16 45000 45000 ± 1 V TrqOvISta_Hapticdute_Sec_M_f32 222801.016 ± 1 222801.016 ± 1 V TrqOvISta_HwTroqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.00009 V TrqOv				
target_TrqOvISta_Perl_LKARequest_Ont_Igc.value 0 target_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32.value 103 target_TrqOvISta_Perl_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Perl_PosTrajHwAngle_HwDeg_f32.value 0.170000002 target_TrqOvISta_Perl_ShiftLeverIsInReverse_Cnt_Igc.value 13.3000002 target_TrqOvISta_Perl_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Result TrqOvISta_APAState_State_M_enum 0 0 0 V TrqOvISta_ElapsedAPAMAxHwTrqTime_Sec_M_f32 0 0 ± 1 V TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 V TrqOvISta_HapticState_State_Menum 0 0 V TrqOvISta_HapticState_State_M_enum 0 0 0 V TrqOvISta_Hapticidur_Cnt_M_u16 45000 45000 ± 1 V TrqOvISta_Hapticime_Sec_M_f32 222801.016 222801.016 ± 1 V TrqOvISta_LKAFaut_Cnt_M_Igc 1 1 ± 1 1				
target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32.value 103 target_TrqOvISta_Perl_PosTrajHwAngle_HwDeg_f32.value 0.170000002 target_TrqOvISta_Perl_PosTrajHwAngle_HwDeg_f32.value 13.3000002 target_TrqOvISta_Perl_SWARTrgtAngRequest_HwDeg_f32.value 13.3000002 Name Actual Value Expected Value Result TrqOvISta_APAState_State_M_enum 0				
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 103 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.170000002 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 13.3000002 target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Result TrqOvISta_PAState_State_Menum 0 0 0 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOvISta_HapticState_State_M_enum 0 0 0 0 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 0 0 0 TrqOvISta_HapticState_State_M_enum 0 <t< td=""><td></td><td></td><td></td><td></td></t<>				
target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 0.170000002 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 13.3000002 target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_lgc.value 0 Name Actual Value Expected Value Result TrqOvISta_APAState_State_M_enum 0 0 0 TrqOvISta_ESCState_State_M_enum 3 3 ✓ TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_HapticState_State_M_enum 0 0 ✓ TrqOvISta_HapticCont_M_u16 45000 45000 ± 1 ✓ TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 ✓ TrqOvISta_Haptictime_Sec_M_f32 2.70000005 -2.70000005 ± 0.000009 ✓ TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lu08 3 3 ± 1 <				
target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.170000002 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 13.3000002 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Result TrqOvISta_APAState_State_M_enum 0 0 0 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 ± 1 0 TrqOvISta_HapticState_State_M_enum 0 0 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 45000 45000 ± 1 0 0 TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 0 0 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 0 0 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 1 1 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 0.170000002 ± 0.0000765625 0.170000002 ± 0.				
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 13.3000002 Name Actual Value Expected Value Result TrqOvlSta_APAState_State_M_enum 0 0 ✓ TrqOvlSta_ESCState_State_M_enum 3 3 ✓ TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 0 ✓ TrqOvlSta_HapticState_State_M_enum 0 0 ✓ ✓ TrqOvlSta_HapticOtate_State_M_enum 0 0 ✓				
Name				
Name Actual Value Expected Value Result TrqOvISta_APAState_State_Menum 0 0 ✓ TrqOvISta_ESCState_State_Menum 3 3 ✓ TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0±1 ✓ TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0±1 ✓ TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 ✓ TrqOvISta_HapticState_State_Menum 0 0 ✓ TrqOvISta_Hapticdur_Cnt_M_u16 45000 45000 ±1 ✓ TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ±1 ✓ TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ±0.000009 ✓ TrqOvISta_LKAFault_Cnt_M_lgc 1 1±1 ✓ TrqOvISta_LKAPermFault_Cnt_M_u08 3 3±1 ✓ TrqOvISta_LKAState_State_M_enum 3 3 3 ✓ TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ±0.0009765625 ✓				
TrqOvISta_APAState_State_Menum 0 0 V TrqOvISta_ESCState_State_Menum 3 3 V TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 V TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 V TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 V TrqOvISta_HapticState_State_Menum 0 0 V TrqOvISta_Hapticdur_Cnt_M_u16 45000 45000 ± 1 V TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 V TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 V TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 V TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 V TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 V TrqOvISta_LKAState_State_Menum 3 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625 V		Actual Value	Expected Value	Result
TrqOvISta_ESCState_State_Menum 3 3 0 0 ± 1 1			•	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_M_enum 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 45000 45000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625				~
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_M_enum 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 45000 45000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625				
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 ✓ TrqOvISta_HapticState_State_M_enum 0 0 ✓ TrqOvISta_Hapticdur_Cnt_M_u16 45000 45000 ± 1 ✓ TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 ✓ TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 ✓ TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 ✓ TrqOvISta_LKAState_State_M_enum 3 3 3 ✓ TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625 ✓				✓
TrqOvISta_HapticState_State_M_enum 0 0 ✓ TrqOvISta_Hapticdur_Cnt_M_u16 45000 45000 ± 1 ✓ TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 ✓ TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 ✓ TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 ✓ TrqOvISta_LKAState_State_M_enum 3 3 3 ✓ TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625 ✓				~
TrqOvISta_Hapticdur_Cnt_N_u16 45000 45000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625		0	0	✓
TrqOvISta_Haptictime_Sec_M_f32 222801.016 222801.016 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -2.70000005 -2.70000005 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625		45000	45000 ± 1	~
TrqOvlSta_LKAFault_Cnt_M_lgc 1 1±1 TrqOvlSta_LKAPermFault_Cnt_M_lgc 1 1±1 TrqOvlSta_LKAPermFault_Cnt_M_u08 3 3±1 TrqOvlSta_LKAState_State_M_enum 3 3 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002±0.0009765625		222801.016	222801.016 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc 1 1±1 * TrqOvlSta_LKAPermFault_Cnt_M_u08 3 3±1 * TrqOvlSta_LKAState_State_M_enum 3 3 * TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002±0.0009765625 *	TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	~
TrqOvlSta_LKAPermFault_Cnt_M_u08 3 3±1 TrqOvlSta_LKAState_State_M_enum 3 3 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002±0.0009765625	TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum 3 3 3 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625 ✓	TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.170000002 0.170000002 ± 0.0009765625 ✓	TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
	TrqOvlSta_LKAState_State_M_enum	3	3	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 ✓				~
	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	

TrqOvlSta_Per1





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	58.0019989	58.0019989 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	✓
Ric Jane A.D. TriQOVSIa State M. enum 1 1 1 1 1 1 1 1 1	
TrigOASIa_ESCState_State_M_enum	
TropOxIsia_EspasedAMaxishtmTqTime_Sec_M_132 200344 TropOxIsia_EspasedShriftLevFevTime_Sec_M_132 200334 TropOxIsia_EspasedShriftLevFevTime_Sec_M_132 200334 TropOxIsia_EspasedShriftLevFevTime_Sec_M_132 424422 TropOxIsia_EspasedShriftLevFevTime_Sec_M_132 424422 TropOxIsia_Hapitodar_Cnf_M_116 40000 TropOxIsia_Hapitodar_Cnf_M_116 40000 TropOxIsia_Hapitodar_Cnf_M_116 40000 TropOxIsia_Hapitodar_Cnf_M_116 40000 TropOxIsia_Hapitodar_Cnf_M_116 40000 TropOxIsia_Hapitodar_Cnf_M_116 40000 TropOxIsia_HavTorqueSV_HavNin_M_Str_SV_Uis_132 3049899952 TropOxIsia_HAVTorqueSV_HavNin_M_Str_SV_Uis_132 3049899952 TropOxIsia_LKAPItor_Gnf_M_106 1 1 TropOxIsia_LKAPItor_Gnf_M_106 1 1 TropOxIsia_LKAPItor_Gnf_M_106 1 1 TropOxIsia_LKAPItor_Gnf_M_106 4 4 TropOxIsia_LKAPItor_G	
TropVisia Eagsed APAMast Mirror TropVisia Eagsed Shittlewither Time Sec M. 152 98334	
TriQOVISIa ElapsedSinitLuckPorTime, Sec. M, 132 TriQOVISIa ElapsedSinitLuckPorTime, Sec. M, 132 TriQOVISIa Hapticotaire, State, M, enum 0 TriQOVISIa Hapticotaire, State, M, enum 0 TriQOVISIa Hapticotaire, Sec. M, 132 225806.359 TriQOVISIa Hapticotaire, Sec. M, 132 225806.359 TriQOVISIA HAPTICOLIENS, SEC. M, 132 225806.359 TriQOVISIA HAPTICOLIENS, SEC. M, 132 2480999995 TriQOVISIA LINEAR M, SIT, KU, US, 132 2480999995 TriQOVISIA LINEAR M, SIT, KU, US, 132 2480999992 TriQOVISIA LINEAR M, LIN	
TripONSIs_HappicStandstillTime_Sec_M_132	
TrqOv/Sis JapticState_State_M_enum	
TrqOVISia_Hapticture_Sec_M_TS2 TrqOVISia_Hapticture_Sec_M_TS2 TrqOVISia_HartoqueSV_HwNm_M_Sir_SV_Uis_TS2 3.04999995 TrqOVISia_HartoqueSV_HwNm_M_Sir_SV_Uis_TS2 3.04999995 TrqOVISia_HartoqueSV_HwNm_M_Sir_SV_Uis_TS2 0.49999992 TrqOVISia_LKAPermFault_Cnt_M_lge 1 TrqOVISia_LKAPermFault_Cnt_M_lge 1 TrqOVISia_LKAPermFault_Cnt_M_lge 1 TrqOVISia_LKAPermFault_Cnt_M_lge 4 TrqOVISia_LKAPermFault_Cnt_M_lge 4 TrqOVISia_LKAPermFault_Cnt_M_lge 1 TrqOVISia_LKAPermFault_Cnt_M_lge 1 TrqOVISia_PosSnotHwAngle_HWDeg_M_TS2 TrqOVISia_PosSnotHwDeg_M_TS2	
TrqOviSta Hapticitime See, M, 132 TrqOviSta HwTorquesV Hwhm, M, Str.SV_Uls_132	
TrigOVISIa I-WTorqueSV_HwNm_M_Str.K_UIs_f32	
TrqOvISta_HwTorqueSV_HwNm_M_Sir.K_Uis_f32	
TrqOv/Sla_LKAFault_Cnt_M_lgc	
TrqOVISIa_LKAPermFault_Ont_M_u08	
TrqOviSta_LKAState_State_M_enum	
TrqOviSta_LKAState_State_M_enum	
Trigovista_PossvorthwAngle_HwDeg_M_i32	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	
k APANacHedraptic Cnt. Igc k APANacHevTrq Time, Sec, f32 k APANacHevTrq HwNm, f32 k APANacHevTrq HwNm, f32 k APANacHevTrq HwNm, f32 k APANacVehSpd (kph, f32 k ESCNacVehSpd (kph, f32 k ESCNacVehSpd (kph, f32 k HapticDuration, Sec, f32 k HapticDuration, Sec, f32 k HapticPreq, Hz, f32 k LKAMinVehSpd (kph, f32 k L	
K_APAMaxHwTrq Time_Sec_f32	
K. APAMaxHwTrq_HwNm_f32 k. APAMaxVehSpd_Kph_f32 k. APAMaxVehSpd_Kph_f32 k. ESCMaxVehSpd_Kph_f32 k. HapticAmplitude_IntrNm_f32 k. LKAMinVehSpd_Kph_f32 k. LKAMinVehSpd_Kph_f32 k. LKAMinVehSpd_Kph_f32 k. LKAMinVehSpd_Kph_f32 k. LKAMinVehSpd_Kph_f32 k. SandstillTime_Sec_f32 lt_IntrNm_f32 lt_	
K_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32 k_HapticAmplitude_MtrNm_f32 k_LKAMaxVehSpd_Kph_f32 k_LKAMaxVehSpd_Kph_f32 k_LKAMaxVehSpd_Kph_f32 k_LKAMirVehSpd_Kph_f32 k_LKAMirVehSpd_Kph_f32 k_SMart_Matter_MtrNm_Sec_f32 d_1.25	
K_ESCMaxVehSpd_Kph_f32 k_HapticAmplitude_MtNm_f32 l	
K HapticDuration_Sec_f32 k HapticDuration_Sec_f32 k HapticDuration_Sec_f32 k HapticDuration_Sec_f32 k HapticDuration_Sec_f32 k HapticReacttime_Sec_f32 k LKAMaxVehSpd_Kph_f32 k LKAMaxV	
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32 k_HapticFreq_Hz_f32 k_HapticFreq_Hz_f32 k_HapticFreq_Hz_f32 k_HapticFreq_Hz_f32 k_HapticFreq_Hz_f32 k_LKAMaxVehSpd_Kph_f32 218.5 k_LKAMaxVehSpd_Kph_f32 218.5 k_LKAMinVehSpd_Kph_f32 201.25 k_MaxHapticReqTime_Sec_f32 k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32 k_SWARLimiter_HwDeg_f32 k_StandstillTimes_kcp_f32 k_StandstillTimes_kcp_f32 k_StandstillTimes_kcp_f32 k_StandstillTimes_Sec_f32 c_f32 k_StandstillTimes_Sec_f32 c_f32 k_StandstillTimes_Sec_f32 c_f32 k_StandstillTimes_Sec_f32 c_f32 k_StandstillTimes_Sec_f32 c_f32 c_f32 c_f32 c_f42 c_f42 c_f42 c_f42 c_f42 c_f43 c_f44 c_f	
k_HapticFreq_Hz_f32	
k_HapticReacttime_Sec_f32 k_LKAMaxVehSpd_Kph_f32 218.5 k_LKAMinVehSpd_Kph_f32 201.25 k_MaxHapticReqTime_Sec_f32 k_MaxHapticReqTime_Sec_f32 k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32 21.1000004 k_ShiffLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32 k_StandstillThresh_Kph_f32 k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32 lo.057 k_StandstillTime_Sec_f32 lo.057 target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_target_Rel_Inst_Ap_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APAState_State_enum target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APAState_State_enum target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rel_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_TrqOvISta_Per1_ESCReques	
k_LKAMaxVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32 k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32 k_StandstillTime_f1_FAPADrrInterventionDetected_Cnt_gc target_TrqOviSta_Per1_EVFound_Cnt_lgc target_TrqOviSta_Per1_EVFound_Cnt_lgc target_TrqOviSt	
k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32 k_MaxHapticReqTime_Sec_f32 k_ShiffLevRevTime_Sec_f32 k_ShiffLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32 d_cost target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_	
k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32 k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kpt_f32 k_StandstillThresh_Kpt_f32 k_StandstillThresh_Kpt_f32 k_StandstillTime_Sec_f32 k_StandstillTime_Sec_f32 lo.057 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_ESCBate_State_enum target_TrqOvlSta_Per1_ESCBate_State_enum target_TrqOvlSta_Per1_ESCBate_State_enum target_TrqOvlSta_Per1_ESCBate_State_enum target_TrqOvlSta_Per1_ESCBate_State_enum target_TrqOvlSta_Per1_ESCBate_State_enum	
k_SWARLimiter_HwDeg_f32 k_StandstillThresh_Kph_f32 k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32 d.5999999 k_StandstillTime_Sec_f32 d.5999999 k_StandstillTime_Sec_f32 d.5999999 k_StandstillTime_Sec_f32 d.5999999 k_StandstillTime_Sec_f32 d.5999999 k_StandstillTime_Sec_f32 d.5999999 d.599999 d.5999999 d.5999999 d.5999999 d.5999999 d.5999999 d.599999 d.5999999 d.5999999 d.5999999 d.599999 d.5999999 d.599999 d.59999 d.599999 d.599999 d.599999 d.599999 d.599999 d.599999 d.599999 d.599999 d.599999 d.59999 d.599999 d.599999 d.59999 d.599999 d.59999 d.599999 d.59999 d.599999 d.59999 d.59999 d.599999 d.59999 d.59999 d.599999 d.59999 d.599999 d.59999 d.5999	
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32 0.057 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_target_TrqOvlSta_Per1_CovlSta_Per1_CovlSta_Per1_CovlSta_Per1_CovlSta_Per1_CovlSta_Per1_CovlSta_Per1_Eovl	
k_StandstillThresh_Kph_f32	
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta	
target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_PrqOvlS	
target_Rte_Inst_Ap_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_TrqOviSta_Per1_GMOSHOScillateState_State_enum	
target_Rte_Inst_Ap_TrqOviSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_TrqOviSta_Per1_GMOSHOScillateState_State_enum	
target_Rte_Inst_Ap_TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCIs_Limited_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_TrqOviSta_Per1_GMOSHOScillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOScillateState_State_enum	
target_Rte_Inst_Ap_TrqOviSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSHOScillateState_State_enum	
target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSHOscillateState_State_enum target_TrqOviSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_enum	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_enum	
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_TrqOvlSta_Per1_GMOSHOscillateState_enum	
mast_rio_no_no_no_no_notic_i di _omoon_ni Anangenable_on_go maget_iidonota_reii_omoon_nramigenable_ont_go	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_HwTorque_HwNm_f32	
augo_no_no_ny_nyonota_ren_nwinn_oz	

© Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-897.710022 0		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	863.880005		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_rer1_ESCFault_Crit_tgc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvISta_rer1_ESCISEIIIIIted_Off_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-5.5999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	92.6999969		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	113.300003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	14.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	43000	43000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.31479979	-4.31479979 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	
TrqOvISta_LKAState_State_M_enum	4	4	*
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	48	48 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target TrqOvlSta Per1 APADrvrInterventionDetected Cnt lgc.value	0	1 ± 1 0 ± 1	·
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	-
	0	0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	V
target_rqovista_Peri_LkAstate_state_enum.value target_TrqOviSta_Peri_LkAstate_state_enum.value	0	0 ± 1	-
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	48	48 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	10		
	0.349999994	0.349999994 ± 0.0009765625	_
target TrgOvlSta Per1 TrgOscEnable Cnt lgc.value	0.349999994	0.349999994 ± 0.0009765625 0 ± 1	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	0.349999994 0 58.5159988	0.349999994 ± 0.0009765625 0 ± 1 58.5159988 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-





Test Step 2.104 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	45000
TrqOvlSta_Haptictime_Sec_M_f32	228811.703
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-2.70000005 0.507000029
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.29899995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996 0.460000008
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	1.74000001
k HapticFreq Hz f32	59.0299988
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0610000007 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_inst_Ap_irq0vista.irq0vista_Peri_minsecureveniciespeed_kpn_132 target_Rte_inst_Ap_Trq0viSta.Trq0viSta_Peri_PosServEnable_Cnt_lgc	target_irqOvlSta_Peri_minSecurevenicleSpeed_kpn_t32 target_trqOvlSta_Peri_minSecurevenicleSpeed_kpn_t32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
	<u> </u>

2015-02-04, 14:05:40+0530



TIQOVISIA_T ELT			
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	103		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.105 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.7999992

2015-02-04, 14:05:40+0530



Name Input Value TrqOvlSta_ReadytoPulse_Cnt_M_lgc 1 k_APAIncludeHaptic_Cnt_lgc 0	
, _ ,	
k APAIngludgHaptic Cnt Igo	
K_AF Allicidder laptic_Crit_igc	
k_APAMaxHwTrqTime_Sec_f32 0.36500001	
k_APAMaxHwTrq_HwNm_f32 8.64000034	
k APAMaxVehSpd Kph f32 194.399994	
k_ESCMaxVehSpd_Kph_f32 34.0999985	
k HapticAmplitude MtrNm f32 0.648000002	
k_HapticDuration_Sec_f32 1.62	
k_HapticFreq_Hz_f32 3	
k_HapticReacttime_Sec_f32 1.72800004	
k_LKAMaxVehSpd_Kph_f32 205.199997	
k LKAMinVehSpd Kph f32 0	
k_SWARLimiter_HwDeg_f32 21.7000008	
k_ShiftLevRevTime_Sec_f32 0.0289999992	
k_StandstillThresh_Kph_f32 4.32000017	
k_StandstillTime_Sec_f32 0.0289999992	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_TrqOvlSta	
	a_Per1_APANonRecoverableFaults_Cnt_lgc
	a_Per1_APARecoverableFaults_Cnt_lgc
	a_Per1_APARequest_Cnt_lgc
	a_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta	a_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta	a_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta	a_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta	a_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	a_Per1_ESCFault_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc \\ target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc \\ target_$	a_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta	_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta	a_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_TrqOvlSta	_Per1_GMOSHOscillateState_State_enum
	a_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	a_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	a_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	a_Per1_HapticRequest_Cnt_lgc
	a_Per1_HwTorque_HwNm_f32
	a_Per1_LKAFault_Cnt_lgc
	n_Per1_LKAInhibit_Cnt_lgc
	n_Per1_LKARequest_Cnt_lgc
	Per1 LKAState State enum
	n_Per1_MaxSecureVehicleSpeed_Kph_f32
	n_Per1_MinSecureVehicleSpeed_Kph_f32
	n_Per1_PosServEnable_Cnt_lgc
	n_Per1_PosSrvoHwAngle_HwDeg_f32
	a_Per1_PosTrajEnable_Cnt_lgc
	a_Per1_PosTrajHwAngle_HwDeg_f32
	n_Per1_SWARTrgtAngRequest_HwDeg_f32
	a_Per1_ShiftLeverIsInReverse_Cnt_lgc
	n_Per1_TrqOscAmp_MtrNm_f32
	a_Per1_TrqOscEnable_Cnt_lgc
	a_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -877.51001	
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 873.76001	
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0	
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value 0	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 0	
target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value 0	
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	
target TrqOvlSta_Fer1_HapticKequest_Crit_igc.value 1 target TrqOvlSta_Per1_HwTorque HwNm f32.value 8.69999981	
target_TrqOvlSta_Per1_Hw10rque_Hwnin_isz.value 0.659999961 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.400000006	
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 133.899994	
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001	



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	18.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508	365508	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.7999992	50.7999992 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	50.7999992	50.7999992 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.106 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1918536
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83936.5625
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	372054
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	31000
TrqOvlSta_Haptictime_Sec_M_f32	201765
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	52.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.375999987
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	37.7999992
k_HapticAmplitude_MtrNm_f32	0.653999984
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	100
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	207.100006
k_LKAMinVehSpd_Kph_f32	220
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	22
k_ShiftLevRevTime_Sec_f32	0.0329999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_RecoverableFau$	g target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc		target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwD	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_lgc	~ 400	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDe	g_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	laa	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_ target_TrqOvlSta_Per1_ESCRequest_Cnt_I	-	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_ei		
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSHOscillateState State enu			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo		,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_	<u>f</u> 32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3def{MaxSecureVehicleSpeed} \\$	2 target_TrqOvlSta_Per1_MaxSecureVehicles	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	-·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3:	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-867.409973		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	878.700012		
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	144.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	19.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	•
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	201765	201765 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	•
TrqOvlOta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2±1	
TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSpyoHwAngle_HwDeq_M_f32	0.109999999	0 0.109999999 ± 0.0009765625	Ž
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PosdvtoPulse_Cnt_M_lgg			
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.653999984	0.653999984 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	100	100 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	•
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.107 (Repeat Count = 1) Name	Input Value
	•
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	2
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	33000
rqOvlSta_Haptictime_Sec_M_f32	204770
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	3
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	53.5999985
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.386999995
_APAMaxHwTrq_HwNm_f32	8.80000019
_APAMaxVehSpd_Kph_f32	198
_ESCMaxVehSpd_Kph_f32	41.5
_HapticAmplitude_MtrNm_f32	0.660000026
HapticDuration_Sec_f32	1.64999998
_HapticFreq_Hz_f32	60.5719986
	1.75999999
 _LKAMaxVehSpd_Kph_f32	209
LKAMinVehSpd_Kph_f32	120.220001
	0.150000006
SWARLimiter HwDeg f32	22.2999992
ShiftLevRevTime_Sec_f32	0.0370000005
standstillThresh_Kph_f32	4.400001
_StandstillTime_Sec_f32	0.0370000005
_oraniostiffinio_oco_ioz arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lg	
	target TrgOvISta Per1 APARecoverableFaults Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-857.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	883.640015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target TrqOvlSta_Per1_ESCRequest Cnt_lqc.value	0		
target_TrqOviSta_Per1_ESCRequest_Crit_igc.value target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value	0		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	0		
target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrgOvlSta Per1 HwTorque HwNm f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	133.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	154.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	21.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	Y
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	
TrqOvISta_LKAState_State_M_enum	53.5999985	4	*
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	53.5999985 ± 0.0009765625 1 ± 1	
	0	0 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
	4	4	-
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	-
target TrqOviSta_Per1_LRAState_State_enum.value target TrqOviSta Per1_PosServEnable Cnt lgc.value	0	0 ± 1	
target TrqOvlSta Per1 PosServEhable_Ont_igc.value	53.5999985	53.5999985 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	60.5719986	60.5719986 ± 0.0009765625	
O _	1 1111		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.108 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.26001
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	860.26001
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	860.26001
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	35000
TrqOvlSta_Haptictime_Sec_M_f32	860.26001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-4.44999981 0.451999992
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	62
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.398000002
k_APAMaxHwTrq_HwNm_f32	0.100000001
k_APAMaxVehSpd_Kph_f32	199.800003
k_ESCMaxVehSpd_Kph_f32 k_HapticAmplitude_MtrNm_f32	63.7000008 0.119999997
k HapticDuration Sec f32	1.66499996
k_HapticFreq_Hz_f32	3.5
k_HapticReacttime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	0.400000006
k_MaxHapticReqTime_Sec_f32	0.165000007
k_SWARLimiter_HwDeg_f32	24.1000004
k_ShiftLevRevTime_Sec_f32	0.037999988
k_StandstillThresh_Kph_f32	4.4400006
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0209999997 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAlnhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_IrqOviSta.IrqOviSta_Peri_MinSecurevenicieSpeed_Rpn_132 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Peri_PosServEnable_Cnt_lgc	target_irqOvlSta_Peri_minSecurevenicleSpeed_kpn_f32 target_TrqOvlSta_Peri_minSecurevenicleSpeed_kpn_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServErlable_Unit_gc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	0
	I'

2015-02-04, 14:05:40+0530



i rqUvista_Peri		(WA)	CILAB
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-847.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	888.580017		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	216.300003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	30.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.26001	860.26001 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	860.26001	860.26001	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	35000	35000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.37099981	-6.37099981 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	62	62 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value		60 + 0 0000765605	✓
20.20-7: 40 ::00-20-7: 0: 00-1: 00-1: 00-2: 00-1	62	62 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	62 0.119999997	0.119999997 ± 0.0009765625	-
· - · · · - · · · · · · · · · · · ·			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.109 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1345
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1345
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1345
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	37000
TrqOvlSta_Haptictime_Sec_M_f32	1345
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.463
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	63.4000015





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.409000009
k_APAMaxHwTrq_HwNm_f32	0.0799999982
k APAMaxVehSpd Kph f32	201.600006
k_ESCMaxVehSpd_Kph_f32	67.4000015
k HapticAmplitude MtrNm f32	0.140000001
k_HapticDuration_Sec_f32	1.67999995
k HapticFreq Hz f32	4.1999981
k HapticReacttime Sec f32	0.0160000008
k_LKAMaxVehSpd_Kph_f32	1.8999998
k LKAMinVehSpd Kph f32	1.75
k_MaxHapticReqTime_Sec_f32	0.180000007
k SWARLimiter HwDeg f32	24.3999996
k_ShiftLevRevTime_Sec_f32	0.0390000008
k StandstillThresh Kph f32	4.4800002
k_StandstillTime_Sec_f32	0.0250000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enuncer.$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	V = 1
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrgOvlSta Per1 ShiftLeverIsInReverse Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-837.109985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	893.52002
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.30000019
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	514.799988
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	226.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	32.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	✓
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1345	1345 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	37000	37000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	1345	1345 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.0999999	-4.0999999 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.140000001	0.140000001 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.19999981	4.19999981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrgOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	1830.43005
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	1830.43005
TrgOvlSta ElapsedStandstillTime Sec M f32	1830.43005
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	39000
TrqOvlSta_Haptictime_Sec_M_f32	1830.43005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.75
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.474000007
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	64.8000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.419999987
k_APAMaxHwTrq_HwNm_f32	0.159999996
k_APAMaxVehSpd_Kph_f32	203.399994
k_ESCMaxVehSpd_Kph_f32	71.0999985
k_HapticAmplitude_MtrNm_f32	0.159999996
k_HapticDuration_Sec_f32	1.69500005
k_HapticFreq_Hz_f32	4.900001
k_HapticReacttime_Sec_f32	0.0320000015
k_LKAMaxVehSpd_Kph_f32	3.79999995
k_LKAMinVehSpd_Kph_f32	3.5
k_MaxHapticReqTime_Sec_f32	0.194999993
k_SWARLimiter_HwDeg_f32	24.7000008
k_ShiftLevRevTime_Sec_f32	0.039999991
k_StandstillThresh_Kph_f32	4.51999998
k_StandstillTime_Sec_f32	0.0289999992

2015-02-04, 14:05:40+0530



· -			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverate	·	bleFaults Cnt loc	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFa			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APAState State e			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Ig	target_TrqOvISta_Per1_CCWFound_Cnt_		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWPosition HwD			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igo	target_TrqOvISta_Per1_CWFound_Cnt_lc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDe			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lg		
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCIsLimited_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_		- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_e			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateS			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgE			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgB			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgE			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cn			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_		_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_er			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicle	eed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicl	eSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSta_Per1_MinSecureV$			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cr	lgc target_TrqOvlSta_Per1_PosServEnable_0	Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_Inst_Ap_TrqOvlSta_P$	/Deg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle	_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cn	gc target_TrqOvlSta_Per1_PosTrajEnable_C	nt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Harget_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_Rte_Inst_Ap_TrqOvlSta_Per1_P$	Deg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngReq	st_HwDeg_f3. target_TrqOvlSta_Per1_SWARTrgtAngRe	quest_HwDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverlation the property of the pr$	e_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverIsInRev	erse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNr	f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrN	m_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt	gc target_TrqOvlSta_Per1_TrqOscEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f3	target_TrqOvlSta_Per1_TrqOscFreq_Hz_	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target TrgOvlSta Per1 APAReguest Cnt lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target TrqOvlSta Per1 CCWFound Cnt lqc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	0 -827.01001		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0 -827.01001 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0 -827.01001 0 898.460022		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0 -827.01001 0 898.460022 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0 -827.01001 0 898.460022 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 -7.9000001 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 -7.9000001 0 0 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 -7.9000001 0 0 0 250.600006		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_Havorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MavSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001		
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HAPTiorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998		
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HAPTiorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_HAPTiorque_HwNm_f32.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0		
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HAPTiorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value	· ·	Result
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HAPTiorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_HAPTiorque_HwNm_f32.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value	4	Result ✓
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HavTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_HavSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value Name	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value	· ·	Result ✓
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_APAState_State_M_enum	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value	4	~
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCISLimited_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4	4	✓
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCISLimited_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_Minibit_Cnt_Igc.value target_TrqOvISta_Per1_Minibit_Cnt_Igc.value target_TrqOvISta_Per1_Minibit_Cnt_Igc.value target_TrqOvISta_Per1_Minibit_Cnt_Igc.value target_TrqOvISta_Per1_Minibit_Cnt_Igc.value target_TrqOvISta_Per1_SmaySecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_SmaySecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_SwarTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SwarTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4	4 4 0±1	✓
target_TrqOvISta_Per1_CCWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_ESCFault_Cnt_igc.value target_TrqOvISta_Per1_ESCFault_Cnt_igc.value target_TrqOvISta_Per1_ESCISLimited_Cnt_igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_LKAFault_Cnt_igc.value target_TrqOvISta_Per1_LKAFault_Cnt_igc.value target_TrqOvISta_Per1_LKARequest_Cnt_igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4 4	4 4 0±1 0±1	\rightarrow \right
target_TrqOvISta_Per1_CCWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_ESCFault_Cnt_igc.value target_TrqOvISta_Per1_ESCFault_Cnt_igc.value target_TrqOvISta_Per1_ESCISLimited_Cnt_igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_HAPTorque_HwNm_f32_value target_TrqOvISta_Per1_LKAFault_Cnt_igc.value target_TrqOvISta_Per1_LKARequest_Cnt_igc.value target_TrqOvISta_Per1_LKARequest_Cnt_igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32_value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32_value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32_value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32_value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_EasedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.900001 0 0 250.600006 236.89994 0 0.10000001 10.1999998 0 Actual Value 4 4	4 4 0 ± 1 0 ± 1 0	\rightarrow \right
target_TrqOvISta_Per1_CCWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_ESCFault_Cnt_igc.value target_TrqOvISta_Per1_ESCIsLimited_Cnt_igc.value target_TrqOvISta_Per1_ESCIsLimited_Cnt_igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_HAPTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_igc.value target_TrqOvISta_Per1_LKARequest_Cnt_igc.value target_TrqOvISta_Per1_LKARequest_Cnt_igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_igc.value target_TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4 4 4 0 0 0 0	4 4 0 ± 1 0 ± 1 0 2	\rightarrow \right
target_TrqOvISta_Per1_CCWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCISLimited_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HAPTicRequest_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShifftLeverIsInReverse_Cnt_lgc.value target_TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 0 -7.900001 0 0 250.600006 236.89994 0 0.10000001 10.1999998 0 Actual Value 4 4 0 0 0 0 2 39000	4 4 0 ± 1 0 ± 1 0 2 39000 ± 1	\rightarrow \right
target_TrqOvISta_Per1_CCWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_CWFound_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCISLimited_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Ent_lgc.value target_TrqOvISta_Per1_PosTrajEnable_Ent_lgc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 0 -7.900001 0 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4 4 0 0 0 0 2 39000 0 -3.75	4 4 0 ± 1 0 ± 1 0 2 39000 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value target_TrqOvISta_Per1_Shif	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 0 -7.900001 0 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4 4 0 0 0 0 2 39000 0 -3.75 0	4 4 0±1 0±1 0 2 39000±1 0±1 -3.75±0.000009 0±1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCSIsLimited_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value TrqOvISta_ElapsedShandstilTime_Sec_M_f32 TrqOvISta_ElapsedShandstilTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAFault_Cnt_M_Igc	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4 4 4 0 0 0 0 2 39000 0 -3.75 0	4 4 0±1 0±1 0 2 39000±1 0±1 -3.75±0.000009 0±1 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_CCWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_CWFound_Cnt_igc.value target_TrqOvISta_Per1_ESCFault_Cnt_igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_igc.value target_TrqOvISta_Per1_LKAFault_Cnt_igc.value target_TrqOvISta_Per1_LKARequest_Cnt_igc.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_igc.value TrqOvISta_APAState_State_M_enum TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedShandstillTime_Sec_M_f32 TrqOvISta_ElapsedShandstillTime_Sec_M_f32 TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_igc TrqOvISta_LKAPermFault_Cnt_M_igc TrqOvISta_LKAPermFault_Cnt_M_igc TrqOvISta_LKAPermFault_Cnt_M_igc	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4 4 4 0 0 0 0 2 39000 0 -3.75 0 0 0 2	4 4 0±1 0±1 0 2 39000±1 0±1 -3.75±0.000009 0±1 0±1 2±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvISta_APAState_State_M_enum TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_EscState_State_M_enum TrqOvISta_ElapsedShandstillTime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4 4 4 0 0 0 0 2 39000 0 -3.75 0 0 0 2 4	4 4 0±1 0±1 0 2 39000±1 0±1 -3.75±0.000009 0±1 0±1 2±1 4	· · · · · · · · · · · · · · · · · · ·
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value TrqOvISta_APAState_State_M_enum TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedShandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticHort_Cnt_M_16 TrqOvISta_HapticHort_Cnt_M_16 TrqOvISta_LKAFault_Cnt_M_16	0 -827.01001 0 898.460022 0 0 0 0 0 0 0 0 0 0 -7.9000001 0 0 0 250.600006 236.899994 0 0.100000001 10.1999998 0 Actual Value 4 4 4 0 0 0 0 2 39000 0 -3.75 0 0 0 2	4 4 0±1 0±1 0 2 39000±1 0±1 -3.75±0.000009 0±1 0±1 2±1	***************************************

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	64.8000031	64.8000031 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.159999996	0.159999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.9000001	4.9000001 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.111 (Repeat Count = 1) Iame Ite_Inst_Ap_TrqOvISta IrqOvISta_APAState_State_M_enum IrqOvISta_ESCState_State_M_enum IrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 IrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	Input Value target_Rte_Inst_Ap_TrqOvlSta 3
tte_Inst_Ap_TrqOvlSta rqOvlSta_APAState_State_M_enum rqOvlSta_ESCState_State_M_enum rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	target_Rte_Inst_Ap_TrqOvlSta 3
rqOvISta_APAState_State_M_enum rqOvISta_ESCState_State_M_enum rqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3
rqOvlSta_ESCState_State_M_enum rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1
	2315.65991
14011010_E10p000011111E01110111110_000_111_102	2315.65991
rqOvlSta ElapsedStandstillTime Sec M f32	2315.65991
rqOvlSta_HapticState_State_M_enum	0
rgOvISta Hapticdur Cnt M u16	41000
rqOvISta_Haptictime_Sec_M_f32	2315.65991
rgOviSta HwTorqueSV HwNm M Str.SV Uls f32	-3.400001
rqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.485000014
rqOviSta LKAFault Cnt M lgc	1
rqOviSta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	3
rqOviSta_LKAState_State_M_enum	3
rqOvlSta_Erx-State_state_w_entitiii rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	66.1999969
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
APAMaxHwTrqTime Sec f32	0.430999994
APAMaxHwTrq HwNm f32	0.239999995
APAMaxVehSpd Kph f32	205.199997
ESCMaxVehSpd Kph f32	74.800031
LapticAmplitude MtrNm f32	0.180000007
HapticDuration_Sec_f32	1.71000004
_HapticFreq_Hz_f32	5.5999999
HapticReacttime_Sec_f32	0.0480000004
LKAMaxVehSpd Kph f32	5.6999981
rvalvaxverispu_rpn_is2 KAMinVehSpd_Kph_f32	5.25
MaxHapticReqTime_Sec_f32	0.209999993
SWARLimiter HwDeg f32	25
_ShiftLevRevTime_Sec_f32	0.0410000011
_StandstillThresh_Kph_f32	4.55999994
_StandstillTime_Sec_f32	0.032999998
_standstill inite_sec_ts2 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvista_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvIsta_Per1_APARequest_Cnt_Igc
arget Rte Inst Ap TrqOvISta.TrqOvISta_FeT_AFARequest_Cit_gc	
	target_TrqOvISta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt lgc	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvISta.TrqOvISta_PerT_ESCFauit_Crit_igc arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
	target TrqOvISta Per1 ESCRequest Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	
arget_Rte_inst_Ap_1rqOviSta.1rqOviSta_Per1_ESCState_State_enum arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enu	target_TrqOvISta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvISta_Per1_LKARequest_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3: target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvISta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_l		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-816.909973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	903.400024		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-7.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	190.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.3000002		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1	1	1
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	· ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2315.65991	2315.65991 ± 1	
	2315.65991	2315.65991 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	2315.65991	2315.65991 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.38850021	-5.38850021 ± 0.000009	
TrqOvlSta LKAFault Cnt M lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value	1	1 ± 1	· ·
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.109999999 0.180000007	0.109999999 ± 0.0009765625 0.180000007 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.180000007	0.180000007 ± 0.0009763625	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.5999999	5.5999999 ± 0.0009765625	
		1.1110000 1 0.0000 00000	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 2.112 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2800.93994
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2800.93994
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	2800.93994
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	43000
TrqOvlSta_Haptictime_Sec_M_f32	2800.93994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-3.04999995 0.495999992
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	67.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.442000002
k_APAMaxHwTrq_HwNm_f32	0.319999993
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	207 78.5
k_HapticAmplitude_MtrNm_f32	0.20000003
k HapticDuration Sec f32	1.72500002
k_HapticFreq_Hz_f32	6.30000019
k_HapticReacttime_Sec_f32	0.064000003
k_LKAMaxVehSpd_Kph_f32	7.5999999
k_LKAMinVehSpd_Kph_f32	7
k_MaxHapticReqTime_Sec_f32	0.224999994
k_SWARLimiter_HwDeg_f32	25.2999992
k_ShiftLevRevTime_Sec_f32	0.0419999994
k_StandstillThresh_Kph_f32	4.599999 0.037000005
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_ESCRequest_Cnt_Igc	target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ESCState State enum	target TrqOvISta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	· - ·
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. \\$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0
target_TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlota_rer1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
·	

2015-02-04, 14:05:40+0530



TrqOviSta_Peri		IGA	CIMO
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-806.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	908.340027		
target TrqOvlSta Per1 ESCFault Cnt lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-7.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	200.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	511		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	12.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	43000	43000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.04999995	-3.04999995 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	67.5999985	67.5999985 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	67.5999985	67.5999985 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.30000019	6.30000019 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.113 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3285.03003
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3285.03003
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3285.03003
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	45000
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	69





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.453000009
k_APAMaxHwTrq_HwNm_f32	0.40000006
k APAMaxVehSpd Kph f32	208.800003
k_ESCMaxVehSpd_Kph_f32	82.1999969
k HapticAmplitude MtrNm f32	0.219999999
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	7
k_HapticReacttime_Sec_f32	0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k LKAMinVehSpd Kph f32	8.75
k_MaxHapticReqTime_Sec_f32	0.239999995
k SWARLimiter HwDeg f32	25.6000004
k_ShiftLevRevTime_Sec_f32	0.0430000015
k StandstillThresh Kph f32	4.63999987
k_StandstillTime_Sec_f32	0.0410000011
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target TrqOvISta Per1 APADrvrInterventionDetected Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWPosition HwDeg f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrqOvISta Per1 CWFound Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt lgc	target TrqOvlSta Per1 PosServEnable Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1
target TrqOvlSta Per1 APARecoverableFaults Cnt Igc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target TrgOvlSta Per1 CCWPosition HwDeg f32.value	-796.710022
target TrgOvlSta Per1 CWFound Cnt Igc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	913.280029
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.69999981
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	211.199997
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	255.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	45000	45000 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.219999999	0.219999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7	7 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.114 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3770.09009
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3770.09009
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3770.09009
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	47000
TrqOvlSta_Haptictime_Sec_M_f32	3770.09009
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.3499999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.518000007
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.463999987
k_APAMaxHwTrq_HwNm_f32	0.479999989
k_APAMaxVehSpd_Kph_f32	210.600006
k_ESCMaxVehSpd_Kph_f32	85.9000015
k_HapticAmplitude_MtrNm_f32	0.23999995
k_HapticDuration_Sec_f32	1.755
k_HapticFreq_Hz_f32	7.69999981
k_HapticReacttime_Sec_f32	0.0960000008
k_LKAMaxVehSpd_Kph_f32	11.3999996
k_LKAMinVehSpd_Kph_f32	10.5
k_MaxHapticReqTime_Sec_f32	0.254999995
k_SWARLimiter_HwDeg_f32	25.8999996
k_ShiftLevRevTime_Sec_f32	0.0439999998
k_StandstillThresh_Kph_f32	4.67999983
k_StandstillTime_Sec_f32	0.0450000018
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_In$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	•	Faults Cnt loc		
	target TrqOvISta Per1 APARecoverableFau			
	target_TrqOvlSta_Per1_APARequest_Cnt_lg			
	target TrqOvISta Per1 APAState State enu			
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc			
	target TrqOvISta Per1 CCWPosition HwDe			
·	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	9		
	target_TrqOvISta_Per1_CWPosition_HwDeg	f32		
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc			
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	ac		
	target TrqOvlSta Per1 ESCRequest Cnt Ig			
	target_TrqOvlSta_Per1_ESCState_State_enu			
	target_TrqOvlSta_Per1_GMOSHOscillateStar			
	target TrgOvISta Per1 GMOSH APAMfgEn			
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn			
	target TrqOvlSta Per1 GMOSH LKAMfgEn			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f3	32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
	target TrqOvlSta Per1 LKARequest Cnt Igo	3		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enu	ım		
	target TrqOvlSta Per1 MaxSecureVehicleSp			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed Kph f32		
	target TrgOvlSta Per1 PosServEnable Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_Hv	wDeg_f32		
	target TrqOvlSta Per1 PosTrajEnable Cnt			
	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw	/Deg f32		
	target_TrqOvISta_Per1_SWARTrgtAngReque			
	target TrqOvlSta Per1 ShiftLeverlsInRevers			
	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_	f32		
	target TrqOvlSta Per1 TrqOscEnable Cnt I			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32			
	0			
	0			
	0			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-786.609985			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	918.219971			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.30000019			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0			
	221.5			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	190.600006			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	14.6000004			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	4	4	~	
	4	4	~	
, = = ==	3770.09009	3770.09009 ± 1	~	
	0	0 ± 1	~	
	0	0	~	
	0	0	~	
	47000	47000 ± 1	~	
	0	0 ± 1	~	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.39610004	-4.39610004 ± 0.000009	~	
	0	0 ± 1	~	
	0	0 ± 1	~	
	1	1 ± 1	~	
	4	4	~	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.4000015	70.4000015 ± 0.0009765625	~	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~	





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	70.4000015	70.4000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.23999995	0.239999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.69999981	7.69999981 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.115 (Repeat Count = 1) ✓	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvISta ESCState State M enum	0
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	425564
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	425564
TrqOvlSta ElapsedStandstillTime Sec M f32	425564
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta Hapticdur Cnt M u16	49000
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-2
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.528999984
TrqOvlSta LKAFault Cnt M lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	71.8000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.474999994
k APAMaxHwTrq HwNm f32	0.560000002
k APAMaxVehSpd Kph f32	212.399994
k ESCMaxVehSpd Kph f32	89.5999985
k_HapticAmplitude_MtrNm_f32	0.25999999
k_HapticDuration_Sec_f32	1.76999998
k_HapticFreq_Hz_f32	8.39999962
k_HapticReacttime_Sec_f32	0.112000003
k LKAMaxVehSpd Kph f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.270000011
k SWARLimiter HwDeg f32	26.2000008
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	4.71999979
k_StandstillTime_Sec_f32	0.048999987
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Irist_Ap_TrqOvIsta.TrqOvIsta_Fer1_AFARequest_Crit_igc target_Rte_Irist_Ap_TrqOvIsta.TrqOvIsta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APARequest_Ont_gc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_Entitin target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_lgc	target_TrqOvIsta_Per1_APAstate_state_enum target_TrqOvIsta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_PerT_CCWPound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWFound_Cnt_Igc	target_TrqOvIsta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvIsta_Per1_CWPosition_HwDeg_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target TrgOvISta Per1 ESCFault Cnt Igc
target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_reiT_ESCFatit_Crit_igc target_Rte_Irist_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Crit_igc	target_TrqOvlSta_Per1_ESCFault_Ont_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCRequest_Cnt_lgc	target TrgOvISta Per1 ESCRequest Cnt Igc
target_Rte_Irist_Ap_TrqOvIsta.TrqOvIsta_Fe11_E3CRequest_Crit_igc target_Rte_Irist_Ap_TrqOvIsta.TrqOvIsta_Pe11_E3CRequest_Crit_igc	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_linst_Ap_mqovista.mqovista_reri_E3cstate_state_entilin target Rte Inst Ap Trq0vlSta.Trq0vlSta Per1 GM0SH0scillateState State enur	
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSHOScillateState_State_entit target Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMilgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMigEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_GMOSH_ESCMIgEnable_Ctt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_PerT_GMOSH_LKAMfgEnable_Ctt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMigEnable_Crit_lgc
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GiviOSH_ExAwiigEnable_Crit_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
anger the met Ap Tryonola.Tryonola FCH Habile/Cuucer Oil Iuc	talgot_1140 nota_1 of 1_1 aption of acst_ont_igo
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	t_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3		_ <u>-</u>		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	1			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	776 54004			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-776.51001			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	923.159973			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1			
target_rrqOvlSta_rer1_ESCFault_Crit_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1			
target_TrqOvlSta_Fer1_ESCISEIIIIIted_Grt_Igc.value target_TrqOvlSta_Fer1_ESCRequest_Cnt_Igc.value	1			
target_TrqOvlSta_Per1_ESCRequest_Clit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1			
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1			
target TrqOvlSta Per1 HwTorque HwNm f32.value	-5.9000001			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	200.899994			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15.6999998			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	3	3	~	
TrqOvlSta_ESCState_State_M_enum	3	3	~	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	425564	425564 ± 1	~	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	425564	425564	~	
TrqOvlSta_HapticState_State_M_enum	1	1	~	
TrqOvlSta_Hapticdur_Cnt_M_u16	49000	49000 ± 1		
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	~	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2	-2 ± 0.000009	~	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	~	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1		
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1		
TrqOvISta_LKAState_State_M_enum	3	3	V	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625		
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0	1±1 0±1	~	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	3	3		
	3	3	-	
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-	
target_IrqOviSta_Peri_GMOSHOscillateState_state_enum.value target_TrqOviSta_Peri_LKAState_State_enum.value	3	3	-	
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1		
target TrqOvlSta Per1 PosServErlable_Cht_lgc.value target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	-	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.25999999	0.25999999 ± 0.0009765625		
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	8.3999962	8.39999962 ± 0.0009765625	-	
J ,	1			

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Fest Step 2.116 (Repeat Count = 1)	Input Value
	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
	1
	4740.43018
	4740.43018 4740.43018
	4740.43018
	2
	51000
	4740.43018
	-1.64999998
	0.540000021
	0
	0
	3
	3
	73.1999969
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
. = =:	0.486000001
·	0.639999986
_APAMaxVehSpd_Kph_f32	214.199997
_ESCMaxVehSpd_Kph_f32	93.3000031
_HapticAmplitude_MtrNm_f32	0.280000001
_HapticDuration_Sec_f32	1.78499997
_HapticFreq_Hz_f32	9.10000038
_HapticReacttime_Sec_f32	0.128000006
_LKAMaxVehSpd_Kph_f32	15.1999998
_LKAMinVehSpd_Kph_f32	14
_MaxHapticReqTime_Sec_f32	0.284999996
_SWARLimiter_HwDeg_f32	26.5
_ShiftLevRevTime_Sec_f32	0.0460000001
	4.76000023
	0.0529999994
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvlSta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSH.APAMfqEnable_Cost_Inc
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
0 = = = 1	target_IrqOvlSta_Per1_HapticRequest_Cnt_igc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	target_TrqOvlSta_Per1_HW10rque_HWNIII_IS2 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
	target_rrqOvlSta_Per1_EKArimibit_Crit_igc target TrqOvlSta Per1_EKARequest Cnt_lgc
	target_TrqOvISta_PerT_EKARequest_Ont_igc
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32

2015-02-04, 14:05:40+0530



TrqOviSta_Per1		Ida	CILAB
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-766.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	928.099976		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	0		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-5.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	242.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	211.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	51000	51000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	73.1999969	73.1999969 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc		0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 1 1	
torrect TerrOulCto Dard ADACtate Ctate anum value	0 0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value			· ·
target_TrqOvlSta_rer1_AFAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0±1 4 4	~
	0 4	0 ± 1 4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	0 4 4	0±1 4 4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 4 4 2	0±1 4 4 2	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0 4 4 2 4	0±1 4 4 2 4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 4 4 2 4 0	0±1 4 4 2 4 0±1	· · · · · · · · · · · · · · · · · · ·
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 4 4 2 4 0 73.1999969	0 ± 1 4 4 2 4 0 ± 1 73.1999969 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.117 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	4	
TrqOvlSta_ESCState_State_M_enum	2	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5225.33984	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5225.33984	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	5225.33984	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	53000	
TrqOvlSta_Haptictime_Sec_M_f32	5225.33984	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.29999995	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.550999999	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	
TrqOvlSta_LKAState_State_M_enum	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	74.5999985	

2015-02-04, 14:05:40+0530



Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.497000009
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	216
k_ESCMaxVehSpd_Kph_f32	97
k_HapticAmplitude_MtrNm_f32	0.300000012
k_HapticDuration_Sec_f32	1.79999995
k_HapticFreq_Hz_f32	9.80000019
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.100004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.300000012
k_SWARLimiter_HwDeg_f32	26.7999992
k_ShiftLevRevTime_Sec_f32	0.0469999984
k_StandstillThresh_Kph_f32	4.80000019
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt Igc	target_TrqOvISta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target TrgOvlSta Per1 CCWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc	target_TrqOvIsta_Per1_CWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target TrgOvlSta Per1 CWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviota_TrqOviSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvIsta_Per1_LKARequest_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAState State enum	
	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	· - · · · · - · - · -
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-756.309998
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	933.039978
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-5.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	252.399994
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	221.5
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	17.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5225.33984	5225.33984 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5225.33984	5225.33984 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	53000	53000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	5225.33984	5225.33984 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.39380002	-3.39380002 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	9.80000019	9.80000019 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.118 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	5710.16992
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5710.16992
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	5710.16992
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	55000
TrqOvlSta_Haptictime_Sec_M_f32	5710.16992
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.94999988
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.561999977
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	76
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.508000016
k_APAMaxHwTrq_HwNm_f32	0.800000012
k_APAMaxVehSpd_Kph_f32	217.800003
k_ESCMaxVehSpd_Kph_f32	100.699997
k_HapticAmplitude_MtrNm_f32	0.319999993
k_HapticDuration_Sec_f32	1.81500006
k_HapticFreq_Hz_f32	10.5
k_HapticReacttime_Sec_f32	0.159999996
k_LKAMaxVehSpd_Kph_f32	19
k_LKAMinVehSpd_Kph_f32	17.5
k_MaxHapticReqTime_Sec_f32	0.314999998
k_SWARLimiter_HwDeg_f32	27.1000004
k_ShiftLevRevTime_Sec_f32	0.0480000004
k_StandstillThresh_Kph_f32	4.84000015
k_StandstillTime_Sec_f32	0.0610000007
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntarget_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntarget_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntarget_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntarget_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Ap_TrqOvlSta_Per1_Apa_Rte_Inst_Apa$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lc	target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lq	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_l	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEi	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvIsta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ac	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleS		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	iest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-746.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	937.97998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 HapticRequest Cnt lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-4.69999981		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKAFatilt_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	262.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	231.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	19		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	/ v
TrqOvISta_ESCState_State_M_enum	4	4	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	_
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvISta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	55000	55000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.94999988	-0.949999988 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
		0.4	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	0 0	0 ± 1	
			✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	76	76 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.319999993	0.319999993 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	10.5	10.5 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6195
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	6195
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	6195
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	57000
TrqOvlSta_Haptictime_Sec_M_f32	6195
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.573000014
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	77.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.518999994
k_APAMaxHwTrq_HwNm_f32	0.879999995
k_APAMaxVehSpd_Kph_f32	219.600006
k_ESCMaxVehSpd_Kph_f32	104.400002
k_HapticAmplitude_MtrNm_f32	0.340000004
k_HapticDuration_Sec_f32	1.83000004
k_HapticFreq_Hz_f32	11.1999998
k_HapticReacttime_Sec_f32	0.175999999
k LKAMaxVehSpd Kph f32	20.8999996
k_LKAMinVehSpd_Kph_f32	19.25
k MaxHapticRegTime Sec f32	0.330000013
k_SWARLimiter_HwDeg_f32	27.3999996
k_ShiftLevRevTime_Sec_f32	0.0489999987
k_StandstillThresh_Kph_f32	4.88000011
StandstillTime Sec f32	0.0649999976
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Ci	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_Igc	target_TrqOvIsta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APAState_State_enum	target_TrqOvIsta_Per1_APAState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvIsta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fer1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
	target TrqOvISta Per1 CWPosition HwDeg f32
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target TrqOvlSta Per1_ESCRequest Cnt lqc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	0 - 1 1
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_6	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3		_ <u>-</u> _ <u>-</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	726 100005		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-736.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	942.919983		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_rer1_ESCFatit_Crit_tgc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_rer1_ESCISEIIIIIted_Off_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lqc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-4.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	273		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	242.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	6195	6195 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	57000	57000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	6195	6195 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.600000024	-0.600000024 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	- 4
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3 4	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1 ± 1	-
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.340000004	0.340000004 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	11.1999998	11.1999998 ± 0.0009765625	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Namo	<u> </u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6679.83008
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	6679.83008
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	6679.83008
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	59000
TrqOvlSta_Haptictime_Sec_M_f32	6679.83008
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.25
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.583999991
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	2 2
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	78.8000031
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.529999971
k_APAMaxHwTrq_HwNm_f32	0.959999979
k APAMaxVehSpd Kph f32	0.20000003
k_ESCMaxVehSpd_Kph_f32	108.099998
k_HapticAmplitude_MtrNm_f32	0.360000014
k_HapticDuration_Sec_f32	1.84500003
k_HapticFreq_Hz_f32	11.8999996
k_HapticReacttime_Sec_f32	0.192000002
k_LKAMaxVehSpd_Kph_f32	22.7999992
k_LKAMinVehSpd_Kph_f32	21
k_MaxHapticReqTime_Sec_f32	0.344999999
k_SWARLimiter_HwDeg_f32	27.7000008
k_ShiftLevRevTime_Sec_f32	0.00100000005
k_StandstillThresh_Kph_f32	4.92000008
k_StandstillTime_Sec_f32	0.068999983
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCIsLimited Cnt Igc	target TrqOvlSta Per1 ESCIsLimited Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrgOvISta Per1 TrgOscFreg Hz f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	

2015-02-04, 14:05:40+0530



TIQOVISIA_T ELT			
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-726.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	947.859985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-3.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	252.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	21.2000008		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	1100011
TrgOvlSta ESCState State M enum	4	4	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6679.83008	6679.83008 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	-
TrqOvlSta ElapsedStandstillTime Sec M f32	6679.83008	6679.83008	
TrgOvISta HapticState State M enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	59000	59000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.38160014	-2.3815999 ± 0.000009	
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrgOvlSta LKAPermFault Cnt M lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	
TrqOvISta_LKAState_State_M_enum	4	4	
TrqOvISta PosSrvoHwAngle HwDeg M f32	78.8000031	78.8000031 ± 0.0009765625	
TrqOvlSta ReadytoPulse Cnt M lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target TrqOvISta Per1 ESCState State enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target TrqOvISta Per1 LKAState State enum.value	4	4	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	78.8000031	78.8000031 ± 0.0009765625	
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.360000014	0.360000011 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.300000014	0.5000000 14 ± 0.00097 05025	
target_TrqOvlSta_Per1_TrqOscEriable_Cit_gc.value	11.8999996	11.8999996 ± 0.0009765625	
target_11qOviota_F 611_11qOs6116q_11z_10z.value	11.0999990	11.0999990 ± 0.0009703023	

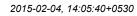
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.121 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7164.66016
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	7164.66016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	7164.66016
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	61000
TrqOvlSta_Haptictime_Sec_M_f32	7164.66016
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.595000029
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	80.1999969





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.541000009
k_APAMaxHwTrq_HwNm_f32	1.03999996 1.79999995
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	111.800003
k_HapticAmplitude_MtrNm_f32	0.379999995
k_HapticDuration_Sec_f32	1.86000001
k_HapticFreq_Hz_f32	12.6000004
k_HapticReacttime_Sec_f32	0.208000004
k_LKAMaxVehSpd_Kph_f32	24.7000008
k_LKAMinVehSpd_Kph_f32	22.75
k_MaxHapticReqTime_Sec_f32	0.360000014
k_SWARLimiter_HwDeg_f32	28
k_ShiftLevRevTime_Sec_f32	0.0049999989
k_StandstillThresh_Kph_f32	4.96000004
k_StandstillTime_Sec_f32	0.0729999989
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_linst_Ap_mqovista.mqovista_ren_ArARequest_cin_igc target_Rte_linst_Ap_mqovista.TrgOviSta_Per1_APAState_State_enum	target TrqOvlSta Per1 APAState State enum
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enunceters$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target TrqOvlSta Per1 TrqOscFreq Hz f32
target_Rte_lirst_Ap_TriqOvista.TriqOvista_FeTi_TriqOscFreq_R2_is2 target_TriqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-715.909973
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	952.799988
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_Haptickequest_Cnt_igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	-3.5
target_TrqOviSta_Per1_hw10rque_nw10ri_isz.value target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	-3.5 1
target_TrqOvIsta_FeT1_EKAFatit_Cftt_igc.value target_TrqOvIsta_Per1_EKAFatit_Cftt_igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
	293.600006
target_frqOviSta_Peri_MaxSecureverlicleSpeed Kpri i32.value	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	262.700012
	262.700012 1





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	7164.66016	7164.66016 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	61000	61000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	7164.66016	7164.66016 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.100000001	0.100000001 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.379999995	0.379999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	12.6000004	12.6000004 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.122 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7649.49023
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	7649.49023
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	7649.49023
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	63000
TrqOvlSta_Haptictime_Sec_M_f32	7649.49023
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449999988
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.606000006
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	81.5999985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.551999986
k_APAMaxHwTrq_HwNm_f32	1.12
k_APAMaxVehSpd_Kph_f32	3.5999999
k_ESCMaxVehSpd_Kph_f32	115.5
k_HapticAmplitude_MtrNm_f32	0.400000006
k_HapticDuration_Sec_f32	1.875
k_HapticFreq_Hz_f32	13.3000002
k_HapticReacttime_Sec_f32	0.224000007
k_LKAMaxVehSpd_Kph_f32	26.6000004
k_LKAMinVehSpd_Kph_f32	24.5
k_MaxHapticReqTime_Sec_f32	0.375
k_SWARLimiter_HwDeg_f32	28.2999992
k_ShiftLevRevTime_Sec_f32	0.00899999961
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.0769999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>j_</u> f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr target_TrqOvlSta_Per1_GMOSH_LKAMfgEr			
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_t			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt Igc	02		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_en			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32				
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-705.809998			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	957.73999			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	0			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0			
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0			
target TrgOvlSta Per1 HwTorque HwNm f32.value	-3.0999999			
target TrgOvlSta Per1 LKAFault Cnt Igc.value	0			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	303.899994			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	273			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	23.3999996			
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0			
Name	Actual Value	Expected Value	Result	
TrgOvlSta APAState State M enum	4	4		
TrqOvISta_ESCState_State_M_enum	4	4	·	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1		
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~	
TrqOvlSta_HapticState_State_M_enum	2	2	~	
TrqOvlSta_Hapticdur_Cnt_M_u16	63000	63000 ± 1	~	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32		0.449999988 ± 0.0000009	~	
TrqOvlSta_LKAFault_Cnt_M_lgc	0.449999988			
TrqOviota_Eron aut_Ont_ivi_igc	0.449999988	0 ± 1	~	
TrqOviSta_LKAPermFault_Cnt_M_igc			•	
	0	0 ± 1	~	
TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	0 0 4 4	0 ± 1 0 ± 1 4 ± 1 4	· · · · · · · · · · · · · · · · · · ·	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 0 4	0 ± 1 0 ± 1 4 ± 1	·	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
Maine	Actual Value	Expected value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	81.5999985	81.5999985 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.40000006	0.400000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	13.3000002	13.3000002 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.123 (Repeat Count = 1) lame	
	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrgOvlSta
rqOvISta_APAState_State_M_enum	0
rqOvISta_ESCState_State_M_enum	3
rgOvISta ElapsedAPAMaxHwTrqTime Sec M f32	8134.31982
rqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	8134.31982
rqOvlSta ElapsedStandstillTime Sec M f32	8134.31982
rqOvISta_HapticState_State_M_enum	0
rgOvlSta Hapticdur Cnt M u16	1596
rqOvISta_Haptictime_Sec_M_f32	8134.31982
rgOvISta HwTorqueSV HwNm M Str.SV Uls f32	0.800000012
rqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.616999984
rgOviSta LKAFault Cnt M lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	0
rqOvISta_LKAState_State_M_enum	0
rqOviSta_ErxAstate_state_w_entitiii rqOviSta_PosSrvoHwAngle_HwDeg_M_f32	83
rqOviSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
APAMaxHwTrqTime Sec f32	0.563000023
: APAMaxHwTrq HwNm f32	1.2000005
APAMaxVehSpd Kph f32	5.4000001
ESCMaxVehSpd_Kph_f32	119.199997
	0.419999987
HapticDuration_Sec_f32	1.8899999
HapticFreq_Hz_f32	14
_HapticReacttime_Sec_f32	0.239999995
LKAMaxVehSpd Kph f32	28.5
_LKAMinVehSpd_Kph_f32	26.25
MaxHapticReqTime_Sec_f32	0.389999986
: SWARLimiter HwDeg f32	28.6000004
_SWARLINITIEL_NWDeg_132 _ShiftLevRevTime_Sec_f32	0.0130000003
_StandstillThresh_Kph_f32	0.039999991
_StandstillTime_Sec_f32	0.0049999989
standstiff inte_Sec_roz arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rer1_APARecoverableFaults_Cnt_igc	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_rerr_ArARecoverabler autis_Ont_igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOviSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target TrqOvlSta Per1 CWFound Cnt lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target TrqOvISta Per1 CWPosition HwDeg f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_rrqOviSta_Peri_CvvPosition_Hwbeg_is2 target_rrqOviSta_Peri_EsCFault_Cnt_lgc
arget_Rte_inst_Ap_rrqOviSta.rrqOviSta_Perr_ESCPault_Cnt_igc arget_Rte_inst_Ap_rrqOviSta.TrqOviSta_Perr_ESCIsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCFatilt_Ont_lgc target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
· · · · ·	target TrqOvISta Per1_ESCISLIMILEd_CNI_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enu	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	Larger Tryonola Fell Invitique Invitil 132





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_Fe11_TrqOscFreq Hz f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-695.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	962.679993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.70000005		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	314.200012 283.299988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	3	3	✓
TrgOvlSta ESCState State M enum	3	3	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	8134.31982	8134.31982 ± 1	-
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	8134.31982	8134.31982 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	1596	1596 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	8134.31982	8134.31982 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.35949993	-1.35950005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	V
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	, , a
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0.120000005	1±1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.129999995 0.419999987	0.129999995 ± 0.0009765625 0.419999987 ± 0.0009765625	-
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	0.419999967	0.419999967 ± 0.0009765625	-
target_TrqOviSta_Per1_TrqOscEriable_Crit_igc.value target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	14	14 ± 0.0009765625	
targot_rigoviota_r or i_rigosor roq_riz_loz.value	17	1 1 0.00001 00020	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.124 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8619.15039
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	8619.15039
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	8619.15039
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	2578
TrqOvlSta_Haptictime_Sec_M_f32	8619.15039
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.1499998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.628000021
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_W_gc	1
TrqOvISta_LKAState_State_M_enum	1
TrgOvlSta PosSrvoHwAngle HwDeg M f32	84.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.574000001
k_APAMaxHwTrq_HwNm_f32	1.27999997
k_APAMaxVehSpd_Kph_f32	7.19999981
k_ESCMaxVehSpd_Kph_f32	122.900002
k_HapticAmplitude_MtrNm_f32	0.439999998
k_HapticDuration_Sec_f32	1.90499997
k_HapticFreq_Hz_f32	14.699998
k_HapticReacttime_Sec_f32	0.256000012
k_LKAMaxVehSpd_Kph_f32	30.3999996
k_LKAMinVehSpd_Kph_f32	28
k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32	0.405000001 28.8999996
k ShiftLevRevTime Sec f32	0.0170000009
k_StandstillThresh_Kph_f32	0.0799999982
k_StandstillTime_Sec_f32	0.00899999961
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfqEnable Cnt lqc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOviSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosSrvoHwAngie_HwDeg_tsz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_ts2 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Ont_ig0 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
g	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 SWARTrgtAngRequest HwDeg f3	0 - 1
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt lqc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0 0
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0

TrqOvlSta_Per1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-685.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	967.619995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	324.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	293.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	25.6000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	2578	2578 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.14999998	1.14999998 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	84.4000015	84.4000015 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
		4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4		
target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
		1 4	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1		-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	1 4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1 4 0	4 0 ± 1	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 4 0 84.4000015	4 0 ± 1 84.4000015 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.125 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	9103.98047
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	9103.98047
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	9103.98047
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	3560
TrqOvlSta_Haptictime_Sec_M_f32	9103.98047
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.638999999
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	85.8000031





Name		
k. APAMachaterine (Time. Sec. 12) k. Scolkavethold, Knih. 102 k. Scolkavethold, Knih. 102 k. Hapicholdson (Sec. 12) k. Hapicholdson (Sec. 1	Name	Input Value
K. APAMAMATVI Trime, Sec., 132 K. APAMAMATVI KHAMI, 132 K. SWANATHIMITER HIMBE, 132 Larget, Rein, 134 K. THOWSIS, PORT J. APAMOMINEROROWERSHERSHE, 132 Larget, Rein, 134 K. THOWSIS, PORT J. APAMOMINEROROWERSHERSHE, 134 Larget, Rein, 134 K. THOWSIS, PORT J. APAMOMINEROROWERSHERSHE, 134 Larget, Rein, 134 K. THOWSIS, PORT J. APAMOMINEROR HIMBE, 132 Larget, Rein, 134 L	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
K.APAMAWAYEN, HANNI, 152 126,59998 1	k_APAIncludeHaptic_Cnt_lgc	0
K.APAMAWAYEN, HANNI, 152 126,59998 1	k APAMaxHwTrqTime Sec f32	0.584999979
k, APAMAVeNSpd, Kph, 132 k, Haptokranjitude, MinVm, 132 k, Hap		1.36000001
K. HapicDuraino, Sec. 932 K. LKAMarvishSpd, Krp. 152 S. 2975 K. LKAMarvishSpd, Krp. 152 S. 2976 K. LKAMarvishSpd, Krp. 152 S. 2976 K. LKAMarvishSpd, Krp. 152 S. 2976 K. Januari, M. Kawarvish, Krp. 152 K. SWAMarlaniter, HvObeg, 132 K. SWAMar		9
K. HapicDuraino, Sec. 932 K. LKAMarvishSpd, Krp. 152 S. 2975 K. LKAMarvishSpd, Krp. 152 S. 2976 K. LKAMarvishSpd, Krp. 152 S. 2976 K. LKAMarvishSpd, Krp. 152 S. 2976 K. Januari, M. Kawarvish, Krp. 152 K. SWAMarlaniter, HvObeg, 132 K. SWAMar	k ESCMaxVehSpd Kph f32	126.599998
K. HapticPreq.Hz, [72 K. HapticPred.Hz, [72 K. H		
K. HapforReacthime, Sec., 132 0.272000015 K. LKAMArvichSpG Kph, 132 32.299992 K. LKAMArvichSpG Kph, 132 29.75 K. LKAMArvichSpG Kph, 132 29.75 K. MachapforReaffime, Sec., 132 0.41999987 K. Sandastliffime, Sec., 132 0.020999997 K. Sandastliffime, Sec., 132 0.020999997 K. Sandastliffime, Sec., 132 0.020999997 K. Sandastliffime, Sec., 132 0.01999997 K. Sandastliffime, Sec., 132 0.019999997 K. Sandastliffime, Sec., 132 0.019999997 K. Sandastliffime, Sec., 132 0.013000000 K. Sandastliffire, Sec., 132 0.0130000000 K. Sandastliffire, Sec., 132 0.01300000000000000000 K. Sandastliffire, Sec., 132 0.0130000000000000000000000000000000000		
LAMANAYENSPA KPL 782 32.299992		
k_LKAMinvehSpd_Kph_132 k_LKAMinvehSpd_Kph_132 k_LKAMinvehSpd_Kph_132 k_SMARABLmitter_HwDeg_132 k_SMARABLmitter_HwDeg_132 k_SMARABLmitter_HwDeg_132 k_SindställTime_Sec_152 0.0000008 k_SMARABLmitter_HwDeg_132 k_SindställTime_Sec_152 0.119999997 k_SindställTime_Sec_152 0.119999997 0.119999997 0.1199999999 0.1199999997 0.1199999997 0.1199999997 0.1199999997 0.1199999997 0.1199999997 0.1199999997 0.1199999997 0.1199999997 0.1199999997 0.11999999997 0.11999999997 0.11999999999 0.11999999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.1199999999 0.11999999 0.119999999 0.119999999 0.119999999 0.119999999 0.11999999 0.119999999 0.119999999 0.119999999 0.119999999 0.119999999 0.119999999 0.119999999 0.119999999 0.119999999 0.119999999 0.11999999 0.119999999 0.1199999 0.11999999 0.11999999 0.11999999 0.11999999 0.11999999 0.1199999 0.11999999 0.		
k_KAMinvehSpd_Kph_fi32 k_MaxHapticKeqTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 c_modelliThreah_Kph_f32 c_modelliThreah_Kph_		
k_SWARLmiter_HvDeg_fis2		
K_SMRKLmiter HwDeg_132 K_Standstill*Time_Sec_132 O.020999997 K_Standstill*Time_Sec_132 O.0130000003 A STANDSTANDSTANDSTANDSTANDSTANDSTANDSTAND		
K_StandstillTriesh_Kph_T32 k_StandstillTriesh_Kph_T32 larget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APANonRecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APANonRecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APANonRecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APANonRecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APANecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APANecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableFaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableGaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_APARecoverableGaults_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta_Pert_CowFound_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_CowFound_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_GMOSH_APAMigenable_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_GMOSH_APAMigenable_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Pert_IMAFault_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta_Pert_IMAFault_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta_Pert_IMAFault_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta_Pert_IMAFault_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta_Pert_IMAFault_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta_Pert_IMAFault_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta_Pert_IMAFault_Cnt_ignotaget_Rie_Inst_Ap_TrqOviSta_Pert_IMASecurevehideSpeed_Kph_732 Iarget_Rie_		
k. StandstillTimes Sh. Kph. 132 k. StandstillTimes Sec. [32] k. Standstill Sec. [32] k. Stand		
K. StandstillTime, Sec. f.32 target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. APADorviInterventionDetected, Cnt. target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. APARonRecoverableFaults, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. APARecoverableFaults, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. APARecoverableFaults, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. APARecoverableFaults, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. APARestale, State enum target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. CWFootid, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. CWFootid, Derl. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. CWFootid, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. CWFootid, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. ESCFault, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. ESCFault, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. ESCS tale. Inst. Qp. TrqOViSta. TrqOViSta. Perl. GMOSH APAMIgEnable, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. GMOSH APAMIgEnable, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. GMOSH APAMIgEnable, Cnt. Igc target, Rie. Inst. Ap. TrqOViSta. TrqOViSta. Perl. InstOviSta. P		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_Pert_APARcoverableFaulis_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_APARceoverableFaulis_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_APARceoverableFaulis_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_APARceoverableFaulis_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_APARceoverableFaulis_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_APARceoverableFaulis_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_APARceoverableFaulis_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_CCWPoontd_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_CCWPoontd_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_CCWPoontd_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_CWPosition_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Pert_CWPosition_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Pert_ESCFauli_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCFauli_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCRequest_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCScate_State_enum target_Rie_Inst_Ap_TrqOviSta_Pert_GWOSH_APARGenable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCRequest_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCRequest_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_EMOSH_APARGenable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_EMOSH_APARGenable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_EMOSH_APARGenable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_EMOSH_APARGenable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_EMOSH_APARGenable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_EMOSH_APARGenable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_HWTorque_HwNm_f32 target_Rie_Inst_Ap_TrqOviSta_Pert_HKAPargineRedues_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_HistorqoviSta_Pert_Histo		
target Rie _inst_Ap_TrqOviSta_TrqOviSta_Pert_APANenRecoverableFaults_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_APARecoverableFaults_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARecoverableFaults_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APARecoverableFaults_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_APAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CVCPround_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CVCPround_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CVCPround_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CVCPround_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_EMOSH_ESCMfigEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_AphicRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HarpicRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LARARecoverable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LARARecoverable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LARARecoverable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LARARecoverable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LARARecoverable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LARARecoverable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrafienble_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Postrafienble_Cnt_lgc target_Rie_In		
target_Rte_Inst_Ap_TrqOviSta_Pert_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSHOSHOSHIdeState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSHOSHOSHIdeState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMIgfanable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMIgfanable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMIgfanable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAShet_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqO		
target_Rie_Inst_Ap_TrqOv/Sta_Pref_APARequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_APAState_State_enum target_Rie_Inst_Ap_TrqOv/Sta_Pref_CCWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CCWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_GMOSH_ESCMfgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_GMOSH_ESCMfgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_GMOSH_ESCMfgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_SCSRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOv/Sta_Pref_S		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCStatulted_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCSMapenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_EMSCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LAKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_Po		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_Pert_ESCReuts_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Des		
target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_ESCRatut_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_GMOSH-OSCIIIateState_State_enum target_Rte_Inst_Ap_TrqOv/Sta.TrqOv/Sta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Per1_StrajHwAngle		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_GMOSH_OSCIIIateState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_GMOSH_OSCIIIateState_Enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_GMOSH_OSCIIIateState_Enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_HapficRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_HapficRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_HapficRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_HavIForque_HmVm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_InstOvISta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CSCPault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LAPAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LAPAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_In		
target_Rte_Inst_Ap_TrqOvlSta_Prerl_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_PosSrvchwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_PosSrvchwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Prerl_SosTrqindengent_Sort_IndovlSta_Prerl_SosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_PosTrqindengent_Sort_IndovlSta_Prerl_Sort_IndovlSta_Prerl_Sort_Indov		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_OScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-ault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-ault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-ault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAR-qulset_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTrajHayRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTrajHayRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTrajHayRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorTrajHayRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInRe	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_inst_Ap_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_inst_Ap_TrqOviSta_Per1_ESCState_State_enum target_Rte_inst_Ap_TrqOviSta_Per1_GMOSHOsciliateState_State_enum target_Rte_inst_Ap_TrqOviSta_Per1_GMOSHOsciliateState_State_enum target_Rte_inst_Ap_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_HpticRequest_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_HpticRequest_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_HyTorque_HwNm_f32 target_Rte_inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_LKARpiult_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_LKARpiult_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_LKARpiult_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_inst_Ap_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_inst_Ap_TrqOviSta_Per1_DesServEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_PosServInable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_TrqOviSta_Per1_DosTrajEnable_Cnt_lgc target_Tr	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHabricRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHabricRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHabricRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_Igc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_HADTORQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_HADTORQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_HADTORQUEST_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosTrajHwAngle_HwDeg_f	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqO	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFacquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFacquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFacquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFacquest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate_state_enuncestate$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_MinSecureVehicleSpeed_tpl target_TrqOvlSta_Per1_PosSrvehue_InsterverNellgc target_TrqOvlSta_Per1_PosSrvehue_InsterverNellgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt lgc	target TrgOvlSta Per1 LKAFault Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_Cnt_Igc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_SwiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_SigtLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShifftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShifftLeverlsInReverse_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc		
target Rte inst Ap Irquvista.Irquvista Peri Irquscamp ivitrim 132 - target Irquvista Peri Irquscamp ivitrim 132		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -675.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 972.559998	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	972.559998
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -1.89999998		-1.89999998
target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value 1	· · · · · · · · · · · · · · · · · · ·	
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value		
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1	~ - ,	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 303.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.200000003		
	5 4a	j .



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	9103.98047	9103.98047 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	3560	3560 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	9103.98047	9103.98047 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5	1.5 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.3999996	15.3999996 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.126 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	9588.80957
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	9588.80957
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	9588.80957
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	4542
TrqOvlSta_Haptictime_Sec_M_f32	9588.80957
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.85000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.64999976
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	87.199969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.596000016
k_APAMaxHwTrq_HwNm_f32	1.44000006
k_APAMaxVehSpd_Kph_f32	10.8000002
k_ESCMaxVehSpd_Kph_f32	130.300003
k_HapticAmplitude_MtrNm_f32	0.47999989
k_HapticDuration_Sec_f32	1.93499994
k_HapticFreq_Hz_f32	16.1000004
k_HapticReacttime_Sec_f32	0.287999988
k_LKAMaxVehSpd_Kph_f32	34.2000008
k_LKAMinVehSpd_Kph_f32	31.5
k_MaxHapticReqTime_Sec_f32	0.435000002
k_SWARLimiter_HwDeg_f32	29.5
k_ShiftLevRevTime_Sec_f32	0.0250000004
k_StandstillThresh_Kph_f32	0.159999996
k_StandstillTime_Sec_f32	0.0170000009
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnd$	t_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	eg_t32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	f22	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	_132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt I	ac .	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlOta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target TrqOvlSta Per1 LKARequest Cnt lg	6	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 LKAState State enum	target_TrqOvISta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- ' -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_ target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-665.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	977.5		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target TrqOvlSta Per1 LKARequest Cnt lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	345.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	314.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.0099999978		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	27.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	*
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1 0	
TrqOvISta_ElapsedStafidstill*Iffle_Sec_Ivi_132 TrqOvISta_HapticState_State_M_enum	0	0	-
TrgOvlSta Hapticdur Cnt M u16	4542	4542 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-0.327499747	-0.327499986 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvISta_LKAState_State_M_enum	4	4	V
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	87.1999969 0	87.1999969 ± 0.0009765625 0 ± 1	*
TrqOvlSta_ReadytoPulse_Cnt_M_lgc			





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	87.1999969	87.1999969 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.479999989	0.479999989 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	16.1000004	16.1000004 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Fest Step 2.127 (Repeat Count = 1)	Innut Value
lame	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	4
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	10073.6396
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	10073.6396
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	10073.6396
rqOvlSta_HapticState_State_M_enum	1
rqOvlSta_Hapticdur_Cnt_M_u16	5524
rqOvlSta_Haptictime_Sec_M_f32	10073.6396
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.661000013
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_u08	4
rqOvlSta_LKAState_State_M_enum	4
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	88.5999985
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
_APAMaxHwTrqTime_Sec_f32	0.606999993
APAMaxHwTrq HwNm f32	1.51999998
APAMaxVehSpd Kph f32	12.6000004
- , -, -	134
_ESCMaxVehSpd_Kph_f32	
_HapticAmplitude_MtrNm_f32	0.5
_HapticDuration_Sec_f32	1.95000005
_HapticFreq_Hz_f32	16.7999992
_HapticReacttime_Sec_f32	0.30399999
_LKAMaxVehSpd_Kph_f32	36.0999985
_LKAMinVehSpd_Kph_f32	33.25
_MaxHapticReqTime_Sec_f32	0.44999988
_SWARLimiter_HwDeg_f32	29.7999992
_ShiftLevRevTime_Sec_f32	0.0289999992
_StandstillThresh_Kph_f32	0.200000003
_StandstillTime_Sec_f32	0.020999997
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvlSta Per1 ESCState State enum
rget_Rte_inst_Ap_frqOviSta_frqOviSta_Perf_ESCState_State_enum rget_Rte_inst_Ap_frqOviSta_TrqOviSta_Perf_GMOSHOscillateState_State_enu	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_Fe11_TrqOscFreq Hz f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-655.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	982.440002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-1.10000002		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	355.399994 324.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-34.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	3	3	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	-
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	10073.6396	10073.6396 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5524	5524 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	10073.6396	10073.6396 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005	2.20000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.100000001	0.100000001 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	- 4
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	V
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	_
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	*
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.100000001 0.5	0.100000001 ± 0.0009765625 0.5 ± 0.0009765625	-
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value	1	1 ± 1	-
target_TrqOviSta_Per1_TrqOscEriable_Crit_igc.value target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	16.7999992	16.7999992 ± 0.0009765625	
		500002 ± 0.00007 00020	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-





Name	Test Step 2.128 (Repeat Count = 1)	→
Mag. Dec. April	Name	Input Value
TOPOSES, ESCHAROL SENS, M. Ammin TOPOSES, Especial Sens, M. 20 TOPOSES, ESCHAROL SENS,		·
TROMOSE Beyond Anter Art Prince Set M. 192 TROMOSE Several Prince Prince From Set M. 192 TROMOSE Several Prince Prince Prince From Set M. 192 TROMOSE SEVERAL PRINCE FROM SET M. 192 TROMOSE SET	TrqOvlSta_APAState_State_M_enum	
TREORNER Begreschialtricker-Irris Ser. M. 192 TREORNER Begreschialtrian See. M. 192 TREORNER Statesticher, States Aug. yourne TreOrder Begreschialtrian See. M. 192 TREORNER Statesticher, States Aug. yourne TreOrder Begreschialtrian See. M. 192 103064877 TREORNER STATESTICH M. 198 TREORNER S	TrqOvlSta_ESCState_State_M_enum	3
Track/Mars September Sep	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	10558.4697
TROMOSA Spikolos Siske Mr. evan TROMOSA Spikolos CV M. 216 TROMOSA Spikolos CV M. 216 TROMOSA SPIKOLOS CV M. 216 TROMOSA SPIKOLOS MANNE M. 255 V. Ull. 152 2.5669995 TROMOSA SPIKOLOS MANNE M. 255 V. Ull. 152 2.5669995 TROMOSA SPIKOLOS CV M. 152 2.5669955 TROMOSA SPIKOLOS	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	10558.4697
TROONES application Coli. M. 198 TROONES application See M. 192 TROONES AMPTORUSEY HAMBUR, M. S. 197. UBL 192 TROO	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	
TROONED HAPPORES THAME AS SEY US DE 2 599995 TROONED HAPPORES THAME AS SEY US DE 2 599995 TROONED HAPPORES THAME AS SEY US DE 2 599995 TROONED HAPPORES TO ME SEY US DE 2 599995 TROONED HAPPORES TO M		
TROM/SEA Hofforway No. Hoffor M. Sci. SV. Us. 152		
TROMPS MATORIUS / Holm M, Str.Y, Us. 92 TORONES DATE MATORIUS CM M, Us. 90 TORONES PASSAGE M, Drum 10 TORONES PASSAGE M, Drum 11 TORONES PASSAGE M, Drum 11 TORONES PASSAGE M, Drum 12 TORONES PASSAGE M, Drum 13 TORONES PASSAGE M, Drum 14 TORONES PASSAGE M, Drum 15 TORONES PASSAG		
TOROMS LAWFame Crit M, top TOROMS LAWFame and LOR M, tips TOROWS LAWFame and LOR M, tips TO		
TROMOSIS JANSHIES STARK M. Jemin TOROSIS PASSHORA JANSHIES STARK M. JANSHIES ST		
TopCrisits Pass Northweigh Hallong M. 10	TrqOvlSta_LKAPermFault_Cnt_M_lgc	
1099998	TrqOvISta_LKAPermFault_Cnt_M_u08	0
Trock/iss ReadynChies Crit 10	TrqOvlSta_LKAState_State_M_enum	0
A. APAlloach VTC, IEANNE, 12	TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	
A PAPAMANTOTION, Sec. 152 A PAPAMANTOTION, Sec. 152 A PAPAMANTOTION, HANDING 2 A PA	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	i a constant a constan
A.PAMAWNISTAL KYN. 122		
L. ADAMANY-RISKE LYDIN 1.4 9999996		
Agendaministro Minn.		
k. Hapticharion Sec. 173		
Applichage App	k_HapticAmplitude_MtrNm_f32	
k, Habou/Rescitime, Sec, 132 k, LKAMainvehispel Kpin, 152 k, Sandställrined, Sec, 152 k, Sandställrined, Kpin, 152 d, Sandställrined	k_HapticDuration_Sec_f32	
LickMan/verbspd (kgn. 132 k. LikMan/verbspd (kgn. 132 k. Likman, kgn.	k_HapticFreq_Hz_f32	17.5
k, DAKAII/WebSpd. Kgh. 12 k, Mohtspit/RecTime, Sec. 132 k, ShiftLerKerTime, Sec. 132 k, ShiftLerKerTime, Sec. 133 k, SandstillTreals, Kgh. 152 d) 0.355000004 d) 0.3550000004 d) 0.355000004 d) 0.3550000004 d) 0.355000004 d) 0.355000	k_HapticReacttime_Sec_f32	0.319999993
k, Joseffactimer HwDog, 152 k, Standsulffreen, Kpn, 92 k, Standsulffreen, Mpn, 92 k, Standsulffreen, M	k_LKAMaxVehSpd_Kph_f32	
K, SMILLEVRYTIME, Sec. 152 A, StandstillTime, Sec. 152 A, TrigOviSta, TrigOviSta, Perl _APARom/RecoverableFaults, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _APARomecoverableFaults, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _APARequest, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _APARequest, Col. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _APARequest, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _APARequest, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _CWPosition, Hwbeg, J32 target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _APARequest, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _CWPosition, Hwbeg, J32 target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _ESCRequest, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _ESCRequest, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _ESCRequest, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _AMASter, State, enum target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _AMASter, State, Enum target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _AMASter, State, Enum target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _AMASter, APAMIgenable, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _AMASter, APAMIgenable, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, TrigOviSta, Perl _AMASter, APAMIgenable, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _AMASter, APAMIgenable, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _AMASter, APAMIgenable, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _AMASter, APAMIgenable, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _AMASter, APAMIgenable, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _AMASter, APAMigenable, Cnt. Igo target, Rie. Inst. Ap. TrigOviSta, Perl _AMASt	k_LKAMinVehSpd_Kph_f32	
ik, SindraksliTimesin, Kph. Ji 22 X, SlandskilTimesin, Kph. Ji 23 X, SlandskilTimesin, Kph. Ji 24 X, SlandskilTimesin, Kph. Ji 24		
SandstillTrines, Sec., 152 target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _APADrvinterventionDetected_ Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _APADrvinterventionDetected_ Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _APADrvinterventionDetected_ Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _APADrvinterventionDetected_ Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _APADrvinterventionDetected_ Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _APADrvinterventionDetected_ Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _APADrvinterventionDetected_ Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CCWPound_ Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CCWPosition_ HwDeg, 132 target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CCWPosition_ HwDeg, 132 target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CCWPosition_ HwDeg, 132 target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _ESCISta, Into, Into, Intaget, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _ESCiSta, Into, Intaget, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CMPOSISD_ Perl _ESCiSta, Intaget, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CMPOSISD_ Perl _ESCiSta, Intaget, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CMPOSISD_ Perl _CMPOSISD_ Perl _ESCISta, Intaget, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CMPOSISD_ Perl _CMPOSISD_ Perl _ESCISta, Intage, Cnt. target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _CMPOSISD_ Perl _CM		
ik, Sandstill'ime, Sec., 122 target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. APADovrinterventionDetected Crit. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. APARocoverableFaults, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. APARocoverableFaults, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. APARocoverableFaults, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. APARocoverableFaults, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. APARocoverableFaults, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. APARocoverableFaults, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. CCWPound, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. CCWPound, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. ESCFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. ESCFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. ESCFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. ESCFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. ESCReques, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. ESCReques, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. GMOSH, APAMfgEnable, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. GMOSH, APAMfgEnable, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. JapticRequest, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. LKAFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. LKAFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. LKAFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. LKAFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. LKAFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. LKAFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. LKAFault, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. Secretable, Crit. [gr. target, Rie, Inst. Ap, TrqOvista, TrqOwSta, Perl. Secr		
target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl APADorvintervereitonDetected. Cnt. I target, Rie, Inst, Ap. TrqOVista, Perl APADorvintervereitonDetected. Cnt. I got target, Rie, Inst, Ap. TrqOVista, Perl APARecoverableFaults, Cnt. I got target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl APARecoverableFaults, Cnt. I got target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl APARecoverableFaults, Cnt. I got target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl APARecoverableFaults, Cnt. I got target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl CCWPosition, HwDeg, 32 target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl CCWPosition, HwDeg, 32 target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl CWPosition, HwDeg, 32 target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl CWPosition, HwDeg, 32 target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl CWPosition, HwDeg, 32 target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl CWPosition, Involved, I got target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl CWPosition, Involved, I got target, Rie, Inst, Ap. TrqOVista, TrqOVista, Perl CWPOsition, Involved, Perl CWPOsition, Involv		
target, Rie, Inst. Ap. TrqOv/Sta TrqOv/Sta. Perf. APARceoverableFaults, Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. Perf. APARceoverableFaults, Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. Perf. APARceoverableFaults, Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. Perf. APARceoverableFaults, Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. Perf. CVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. Perf. CVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. CVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. CVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. CVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. EVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. EVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. EVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. EVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. EVPOVEND. Perf. EVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. EVPOVEND. Cnt. Igs target, Rie, Inst. Ap. TrqOv/Sta. TrqOv/Sta. Perf. EVPOVEND.		
target, Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_APARequest_Cnt_Igo target Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFooting_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFooting_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFooting_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFooting_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFooting_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFooting_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFooting_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCFout_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCSIate_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCSIate_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCSIate_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SCMSS_Merl_ScMS_Merl_ScMS_Merl_SCSIate_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_APAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Moosh_LAPAMfgEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PooservEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PooservEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PooservEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PooservEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PooservEnable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta_TrqOviSt		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_Per1_CWFound_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCSLImited_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCSLImited_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCSlate_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ApATisequet_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ApATisequet_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARPault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARPault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARPault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARPault_Cnt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevfEnable_Cnt_Igo ta	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCStall_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCStall_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCStall_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCStall_Esta_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCMgenable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_DScallateState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_DScallateState_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_RASecureVehicleSpeed_Kph_G32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_RASecureVehicleSpeed_Kph_G32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrqiDus_PubB_g32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrqiDus_PubB_g32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrqiDus_PubB_g32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrqiDus_PubB_g32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrqiDus_PubB_g32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SosTrqiDus_PubB_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWPosition_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Ent_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Ent_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCReques_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_EMOSH_Dev1_ESCMIgenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_EMOSH_Dev1_ENCOR_Dev1_Inst_Ap_TrqOviSta_Per1_EMOSH_Dev1_ENCOR_Dev1_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Inst_Inst_Ap_IrqOviSta_Inst_Ap_IrqOviSta_Per1_EMOSH_Dev1_End_Inst_Inst_Inst_Inst_Inst_Inst_Inst_Inst	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWFound_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_CWFouston_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_DAPAMIgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_DAPAMIgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_LESCMigenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_AphticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MavSecurevehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MinSecurevehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvoHawAngie_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHawAngie_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHawAngie_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLeverisinReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLeverisinReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHawAngie_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHawAngie_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHawAngie_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHawAngie_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHawAngie_HwDeg_f32 target_Rie_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MoSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MoSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapfoRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MoSecurevehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecurevehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_DoServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_I		
target_Rie_Inst_Ap_TrqOviSta_Pert_ESCFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCISLimited_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCISLimited_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCISLimited_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_ESCISLimited_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_GMOSH_ScillateState_state_enum target_Rie_Inst_Ap_TrqOviSta_Pert_GMOSH_ScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta_Pert_GMOSH_ScillateState_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Hwforque_Hwhm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Lie_RABecurevery RingoviSta_Pert_Lie_RABecurevery RingoviSta_Pert_Lie_RABecurevery RingoviSta_Pert_Lie_RABecurevery RingoviSta_Pert_PosSevenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajenable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_Sinft(everisinReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Sinft(everisinReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Sinft(everisinReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCenp_MtrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCenp_MtrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCenp_MtrNm_f32 target_Rie_Inst_Ap		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShriftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShriftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShriftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShriftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_ShriftLeverIsInReverse_Cnt_Igc target_TrqOviSta_Per1_ShriftLeverI		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARAult_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortIngEnable_Cnt_lgc targ		
target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_HybricRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_HybricRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_State_enum target_TrqOvIsta_Pert_LAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_LKAFatult_State_enum target_TrqOvIsta_Pert_LAFAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_DasservEvAficleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_StriftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Pert_TrqOsCEnable_Cnt_Igc target_TrqOvIsta_Pert_TrqOsCEnable_Cnt_Igc target_TrqOvIsta_Pert_APANeoneesoverableFaults_Cnt_Igc.value 0 target_TrqOvIst		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_SorTajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOviSta_Per1_APARequest_Cnt_lgc.value	target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	
target_Rte_Inst_Ap_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Perl_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_TrqOsCenable_Cnt_lgc target_TrqOvISta_Perl_TrqOsCenable_Cnt_lgc target_TrqOvISta_Perl_TrqOsCenable_Cnt_lgc target_TrqOvISta_Perl_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Perl_APARequest_Cnt_lgc.value 0 target_TrqOvISta_Perl_APARequest_Cnt_lgc.value	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKASItat_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKASitat_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKASitat_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SorAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOsCEnable_Cnt_lgc target_TrqOvlSta_Per1_APANcoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARceoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARceoverableFaults_Cnt_lgc.value	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_DesServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANenRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAnp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 0	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_DosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Tte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_igc target_Tte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TtqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TtqOvlSta_Per1_APARecoverableFaults_Cnt_igc.value target_TtqOvlSta_Per1_APARequest_Cnt_igc.value target_TtqOvlSta_Per1_LKAState_State_enum target_TtqOvlSta_Per1_LKAState_State_enum target_TtqOvlSta_Per1_LKAState_State_enum target_TtqOvlSta_Per1_LKAState_State_enum target_TtqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_PosTrajMaxSecureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_PosTrajPwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajMwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajDwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajDwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajDwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajDwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajDwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajDwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajDwasceureVehicleSpeed_Kph_f32 target_TtqOvlSta_Per1_SosTrajDwasceureVehic		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiffLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscErable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 0	target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530



TIQOVISIA_T ELL			TOIL CTUOID
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-645.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	987.380005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-0.69999988		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	365.700012		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	334.799988		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	6506	6506 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.54999995	2.54999995 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	10.1999998	10.1999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	10.1999998	10.1999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.519999981	0.519999981 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	17.5	17.5 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.129 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11043.2998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11043.2998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	11043.2998
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	7488
TrqOvlSta_Haptictime_Sec_M_f32	11043.2998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.9000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.683000028
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	20.2000008





APPANDATION		
A.P.A.Masteristric Sec. 92	Name	Input Value
APANData Front Test Park 192	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
A APAMentain Terf ten Ser D2 A D2	k_APAIncludeHaptic_Cnt_lgc	0
		0.629000008
ESCHARACHERY LENGTH 13.399994 1.3399994 1.3399994 1.3399994 1.399979979 1.399979979 1.399979979 1.399979979 1.399979999 1.3999799999 1.3999799999 1.3999799999 1.3999799999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.39997999999 1.399979999999 1.399979999999 1.399979999999 1.399979999999 1.399979999999 1.39997999999999999999999999999999999999		
L. Lapschunders, Sept. 202 1. Hapschord plat. 12. Ca. 1. Local Carlot plat	_	
Lignories Lign		
L. Liganite Residence (Res. C.) L. Li Mondarde (Sept. 192. 12. 19. 19. 19. 19. 19. 19. 19. 19. 19. 19		
L. Marken/Projects 1975		
L. L. Marksprücker		
L. Marchigen-Fragrine, Sec., 122 L. Schell-schred/mine, Sec., 123 L. Sch		
S. SWARLEWART Free, Sep. 12 S. Standard Tree, Sep. 13 Stanger, R.P. Jand, A.P. Tro-Ostic Tro-Ostic Part / APARoverseverisher Static, City is stager, R.P. Jand, A.P. Tro-Ostic Tro-Ostic Part / APARoverseverisher Static, City is stager, R.P. Jand, A.P. Tro-Ostic Tro-Ostic Part / APARoverseverisher Static, City is stager, R.P. Jand, A.P. Tro-Ostic Tro-Ostic Part / APARoverse Stater, State,		
N. Springer Times, Soc. 122 Note of the Times of the Springer Times of		
A SpinsdallThree Sec 122 unget, Rin, Ind. A.D., 190/Sta. TroVista. Port J. APADorniterventonDecictod, Cet., target, TropVista. Port J. APADorniterventonDecictod, Cet., target, TropVista. ProVista. ProVista		
N. Sandard Time, Sec., 192 straiger, R.R. Innick, A.D. (TION/Sta. Tro/Sta.) Pert. APADominservanionDeaded Cott. Igo targer, R.R. Innick, A.D. (TION/Sta.) Tro/Sta. Pert. APADominservanionDeaded Cott. Igo targer, R.R. Innick, A.D. (TION/Sta.) Tro/Sta. Pert. APADominservanionDeaded Cott. Igo targer, R.R. Innick, A.D. (TION/Sta.) Tro/Sta. Pert. APADAGenes, Cott. Igo targer, R.R. Innick, A.D. (TION/Sta.) Tro/Sta. Pert. APADAGenes, Cott. Igo targer, R.R. Innick, A.D. (TION/Sta.) Tro/Sta. Pert. APADAGenes, Cott. Igo targer, R.R. Innick, A.D. (TION/Sta.) Tro/Sta. Pert. COVERS and Innick. Innick. State of the Cover o		
James J. Rein J. A.D. (TOX)555. Prof. JOX/555. Prof. JAPACON retirement condected. Crit. James J. Rein J. A.D. (TOX)555. Prof. JAPACON		
James R. Rimula A.D. (TOX)NSB TOX NSB pert J. APARAMORE convenible Faults. Chill gis target, Rise, Inst. A.D. (TOX)NSB TOX NSB pert J. APARAMORE contentions in child growth of the process of the proces		
sanget, Riss, Inst. Ap. TROM/Sist Prof. IAPAR Recoverable Faults on Ligo sanget, Riss, Inst. Ap. TROM/Sist Prof. IAPAR Recoverable Faults on Ligo sanget, Riss, Inst. Ap. TROM/Sist TrOM/Sist. Pert. APAR Sist. Sist. enum sanget, Riss, Inst. Ap. TROM/Sist TrOM/Sist. Pert. APAR Sist. Sist. enum sanget, Riss, Inst. Ap. TROM/Sist TrOM/Sist. Pert. APAR Sist. Sist. enum sanget, Riss, Inst. Ap. TROM/Sist. TrOM/Sist. Pert. DAVIS sist. Coll. Sig. sanget, Riss, Inst. Ap. TROM/Sist. TrOM/Sist. Pert. DAVIS sist. Coll. Sig. sanget, Riss, Inst. Ap. TROM/Sist. TrOM/Sist. Pert. DAVIS sist. Coll. Sig. sanget, Riss, Inst. Ap. TROM/Sist. TrOM/Sist. Pert. DAVIS sist. Coll. Sig. sanget, Riss, Inst. Ap. TROM/Sist. TroM/Sist. Pert. DAVIS sist. Coll. Sig. sanget, Riss, Inst. Ap. TROM/Sist. TroM/Sist. Pert. Sist. Sist. Enum sanget, Riss, Inst. Ap. TroM/Sist. TroM/Sist. Pert. Sist. Sist. enum sanget, Riss, Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss. Inst. Ap. TroM/Sist. TroM/Sist. Pert. LASS sist. enum sanget. Riss		
saget Rin Inst. Ap. TropOviSta TropOviSta Pert J. APARoquest Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta Pert J. APASitane; State, enum target, Rin Inst. Ap. TropOviSta TropOviSta Pert J. APASitane; State, enum target, Rin Inst. Ap. TropOviSta TropOviSta Pert J. COVIPS and Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta Pert J. COVIPS and Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta Pert J. COVIPS and Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. SSCP State, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. SSCP State, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. SSCP State, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. SSCP State, Cnt. Igo target, Rin Inst. Ap. TropOviSta, TropOviSta, Pert J. SSCP State, Cnt. Igo target, Rin Inst. Ap. TropOviSta, TropOviSta, Pert J. SSCP State, Cnt. Igo target, Rin Inst. Ap. TropOviSta, TropOviSta, Pert J. SSCP State, Cnt. Igo target, Rin Inst. Ap. TropOviSta, TropOviSta, Pert J. SSCP State, State, and target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. SSCP State, State, and target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. SSCP State, State, and target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. SSCP State, State, and target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, State, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cot. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J. ASSCRIPT, Cate, Cnt. Igo target, Rin Inst. Ap. TropOviSta TropOviSta, Pert J		
sanger, Riss, Inst. Ap. TROM/SIS INTO/SIS Perf. DEVIEW DRIVE OR 10, 19 states, Riss, Inst. Ap. TROM/SIS TROMSIS, Perf. COMPosition, Handley, 122 states, Riss, Inst. Ap. TROM/SIS TROMSIS, Perf. COMPosition, Handley, 122 states, Riss, Inst. Ap. TROM/SIS TROMSIS, Perf. COMPosition, Handley, 122 states, Riss, Inst. Ap. TROM/SIS TROMSIS, Perf. COMPosition, Handley, 122 states, Riss, Inst. Ap. TROM/SIS TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROM/SIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL, Cri. 195 states, Riss, Inst. Ap. TROMSIS, TROMSIS, Perf. SCORGUL,		
target, Rin, Inst. Ap. Trophists Trophists pert COVENIAND Cell (go target, Rin, Inst.) Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 22 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 22 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 23 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 23 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 23 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 23 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 24 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 25 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN INDEAD, 25 target, Rin, Inst. Ap. Trophists Trophists pert CoVENIAND IN Inst. Trophists Trophists Trophists pert CoVENIAND IN Inst. Trophists Trophists Trophists pert CoVENIAND IN Inst. Trophists pert Likeling Critical Inst. Des. Trophists Trophists Trophists Pert Likeling Critical Inst. Des. Trophists Trophists Trophists Trophists Trophists Trophists Pert Likeling Critical Inst. Des. Trophists Trophis		
tanget, Rise, Lad., Ap. Trajovista Trajovista, Pert. COVPosition, Ne/Deg. (IZ) tanget, Rise), Lad., Ap. Trajovista Trajovista, Pert. CWFosition (Art.) go tanget, Rise, Lad., Ap. Trajovista Trajovista, Pert. CWFosition (Art.) go tanget, Rise, Lad., Ap. Trajovista Trajovista, Pert. ESCalat.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert. ESCalat.Inted. Crit.) go tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert. ESCalat.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert. ESCalat.Inted. Crit.) go tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert. ESCalat.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) ESCalat.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) ESCALAT.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) ESCALAT.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) ESCALAT.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) ESCALAT.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) ESCALAT.Inted. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Pert. Ladioset. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Pert. Ladioset. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Pert. Ladioset. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget, Rise, Lad., Ap. Trajovista, Trajovista, Pert.) Escalat. Crit. (pc tanget,		
tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. CVPTound Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. BESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ABCOST ESCR and Cnt. Jos tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. ABCOST ESCR and Estate enum tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. JAROST ESCR and Estate Enum tanget, Rie, Inst. Ap. TrigOVSta TrigOVSta Pert. JAROST ESCR ANDIST ESCR A		
tampet, Rie, May, A. TrigoVisia TrigoVisia pert, ECPard (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, ESCisia (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, ESCisia (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, ESCisia (State, graum tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, ESCisia (State, graum tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, ESCisia (State, graum tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, ESCisia (State, graum tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, GINOSH APAMighrable, Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, GINOSH APAMighrable, Cnt. Igo tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia pert, Baptic Republic, Inst., Ap. TrigoVisia TrigoVisia pert, Baptic Republic, Inst., Ap. TrigoVisia TrigoVisia pert, Baptic Republic, Inst., Ap. TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go value tampet, Rie, Inst., Ap. TrigoVisia TrigoVisia Pert, Individual (Cnt.) go value tampet, TrigoVisia Pert, Lower (Cnt.) go value tampet,	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	
tanget, Rien, Hat, Ap, TropOvisia, TropOvisia, Pert, ESCFault, Cirt. Joe tanget, Rien, Hat, Ap, TropOvisia, TropOvisia, Pert, ESCFault, Cirt. Joe tanget, Rien, Inst. Ap, TropOvisia, TropOvisia, Pert, ESCRequest, Crit. Joe tanget, Rien, Inst. Ap, TropOvisia, TropOvisia, Pert, ESCRequest, Crit. Joe tanget, Rien, Inst. Ap, TropOvisia, TropOvisia, Pert, ESCRequest, Crit. Joe tanget, Rien, Inst. Ap, TropOvisia, TropOvisia, Pert, ESCRequest, Crit. Joe tanget, Rien, Inst. Ap, TropOvisia, TropOvisia, Pert, MOSH, APAMIlighanible, Cirt. Joe tanget, Rien, Inst. Ap, TropOvisia, TropOvisia, Pert, MOSH, APAMIlighanible, Cirt. Joe tanget, Rien, Inst. Ap, TropOvisia, TropOvisia, Pert, Haviorage, Perkin, Inst. Ap, TropOvisia, TropOvisia, Pert, Javiorage,		
target, Rie, Mat. An. Tragovisia. Part I. SSCistalimied, Ont. Igo target, Rie, Ind. Ap. Tragovisia. Part I. SSCistalimied, Ont. Igo target, Rie, Ind. Ap. Tragovisia. Part I. MONSH-Oscillate/State, enum target, Rie, Ind. Ap. Tragovisia. Part I. MONSH-Oscillate/State, Estate, enum target, Rie, Ind. Ap. Tragovisia. Part I. MONSH-Oscillate/State, Crit. Igo target, Rie, Ind. Ap. Tragovisia. Part I. MONSH-Oscillate/State, Crit. Igo target, Rie, Ind. Ap. Tragovisia. Part I. MONSH-ESCMigrinable, Crit. Igo target, Rie, Ind. Ap. Tragovisia. Part I. Monsh. I. HapticRequent, Crit. Igo target, Rie, Ind. Ap. Tragovisia. Part I. Monsh. I. Crit. Igo target, Rie, Ind. Ap. Tragovisia. Part I. Monsh. I. Crit. Igo target, Rie, Ind. Ap. Tragovisia. Part I. Monsh. I. Crit. Igo target, Rie, Ind. Ap. Tragovisia. Part I. Monsh. I. Crit. Igo target, Rie, Ind. Ap. Tragovisia. Tragovisia. Part I. Monsh. I. Crit. Igo target, Rie, Ind. Ap. Tragovisia. Tragovisia. Part I. Monsh. I. Crit. Igo target, Rie, Ind. Ap. Tragovisia. Tragovisia. Part I. Monsh. I. Crit. Igo target, Rie, Ind. Ap. Tragovisia. Tragovisia. Part I. Monsecure/enice/Speed J. Kryf. 22 target, Rie, Ind. Ap. Tragovisia. Tragovisia. Part I. Monsecure/enice/Speed J. Kryf. 23 target, Rie, Ind. Ap. Tragovisia. Tragovisia. Part I. Monsecure/enice/Speed J. Kryf. 23 target, Rie, Ind. Ap. Tragovisia. Tragovisia. Part I. Monsecure/enice/Speed J. Kryf. 23 target, Rie, Ind. Ap. Tragovisia. Tragovisia. Part I. Pass Tragovisia. Part I. Tragovisia. Part I. Pass Tragovisia. Part I. Monsecure/enicelospeed J. Tragovisia. Part I. Monsecure/enicelosp	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCRequest, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCRequest, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCRequest, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCState, State, enum traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCState, State, enum traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCSTate, Inst. Rev. traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Ont. Jos traget, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Developed, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Developed, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Developed, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J ESCMIpstable, Developed, Rie, Inst. Ap. TrqOvSta TrqOvSta Pert J Escal TrqOvSta Pert J Esc		
target, Reit, Jan. A. TriqOVISIa (TriqOVISIa Perf. LISCSIate State, enum target, Reit, Jan. A. TriqOVISIa (TriqOVISIa Perf. LISCSIate) Perf. LIGNOSH, Perf.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target, Rie, Inst. Ap. TrqOviSta TrqOviSta, Pert. (MOSH APAM(gEnable, Cnt. Igo target, Rie, Inst. Ap. TrqOviSta, Pert. (MOSH, PAMM(gEnable, Cnt. Igo target, Rie, Inst. Ap. TrqOviSta, Pert. (MOSH, ESCM(gEnable, Cnt. Igo target, Rie, Inst. Ap. TrqOviSta, Pert. (MOSH, ESCM(gEnable, Cnt. Igo target, Rie, Inst. Ap. TrqOviSta, Pert. (MOSH, ESCM(gEnable, Cnt. Igo target, Rie, Inst. Ap. TrqOviSta, Pert. (MOSH, ESCM(gEnable, Cnt. Igo target, Rie, Inst. Ap. TrqOviSta, Pert. (MOSH, PERT, MOSH, PERT, PERT, MOSH, PERT, MOSH, PERT, MOSH, PERT, MOSH, PERT, MOSH, PERT, MOSH	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
tanget, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. (SMOSH, APAMIGEnable, Cnt. Igo: tanget, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. (SMOSH, LEXCMIgEnable) Cnt. Igo: tanget, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. (SMOSH, LEXCMIgEnable) Cnt. Igo: tanget, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. HaptcRequest, Cnt. Igo: tanget, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. HaptcRequest, Cnt. Igo: tanget, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Perl. LixCoviSta. Perl. LixCoviSta	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_state_enum
target, Rie, Inst. Ap. TrqOvista TrqOvista. Peri CMOSH, ESCM/genable, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri CMOSH, LKAMgenable, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LyberkPropused, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LyberkPropused, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LyberkPropused, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LyberkPropused, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LykAnhabit, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LykAnhabit, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LykAnhabit, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LykAnhabit, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LykAnbabit, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LykAnbabit, Cnt. Joc target, Rie, Inst. Ap. TrqOvista. TrqOvista. Peri LyberkPropused, Peri	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enun$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl JahplicRequest, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl JahplicRequest, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl JahplicRequest, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J.KAFault, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl P. Post Tollehovide, Perl J. Variance, P. TrqOviSta, TrqOviSta, TrqOviSta, Perl J. Variance, P. TrqOviSta, TrqOviSta, TrqOviSta, Perl J. Variance, P. TrqOviSta, TrqOviSta, TrqOviSta, Perl J. Variance, P. TrqOviSta, TrqOviSta, TrqOviSta, Perl J. Variance, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J. TrqOviSta, Perl J. TrqOviSta, Perl J. Variance, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J. TrqOviSta, Perl J. Variance, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J. TrqOviSta, Perl J. Variance, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J. TrqOviSta, Perl J. Variance, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J. TrqOviSta, Perl J. Variance, Chil.go tanget, Rie, Inst, Ap. TrqOviSta, TrqOviSta, Perl J. TrqOviSta, Perl J. Variance, Chil.go tanget, TrqOviSta, Perl J. Variance, Chil.go tanget, TrqOviSta, Perl J	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rie Inst. Ap. TrqOVSta TrqOVSta Perl HablicRequest, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl HwTorque HwNm. Ig32 target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl LKAFaut, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl LKAFaut, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl LKAFaut, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl LKAFaut, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl LKAFaut, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl LKAState, State, enum target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl LKAState, State, enum target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl MaxSecureVehicleSpeed Kph. Ig32 target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl MaxSecureVehicleSpeed Kph. Ig32 target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl Pers TrqIpanate, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl Pers TrqIpanate, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl Pers TrqIpanate, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl Pers TrqIpanate, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl Pers TrqIpanate, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl Pers TrqIpanate, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl ShiffLeverlainReverse, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl TrqOscEnable, Cnt. Igo target, Rie Inst. Ap. TrqOVSta TrqOVSta Perl TrqOscEnable, Cnt. Igo target, Rie Inst. Ap. TrqOVSta Perl Pers TrqOscEnable, Cnt. Igo target, Rie Inst. Ap. TrqOVSta Perl TrqOscEnable, Cnt. Igo target, Rie Inst. Ap. TrqOvSta TrqOvSta Perl TrqOscEnable, Cnt. Igo target, Rie Inst. Ap. TrqOvSta TrqOvSta Perl TrqOscEnable, Cnt. Igo target, Rie Inst. Ap. TrqOvSta Perl ShiffLeverlainReverse, Cnt. Igo target, Rie Inst. Ap. TrqOvSta Perl CWPoston, InstWing target, TrqOvSta Perl ShiffLeverlainReverse, Cnt. Igo target, TrqOvSta Pe	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. LKAFault, Cnt. Igo target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. LKAFault, Cnt. Igo target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. LKAFault, Cnt. Igo target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. LKAFault, Cnt. Igo target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. LKAFault, Cnt. Igo target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. LKAFault, State, State, enum target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. Post. State, Enum target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. Post. State, Enum target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. Post. State, Enum target, TrqOvStia. Perl. LKAFault, Ext. Inst. Ap. TrqOvStia. TrqOvStia. Perl. Postsov-Enable, Cnt. Igo target, TrqOvStia. Perl. JKASfate, State, enum target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. Postsov-Instance, Cnt. Igo target, TrqOvStia. Perl. JKASfate, State, enum target, TrqOvStia. Perl. JKASfate, State, enum target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. Postsov-Instance, Cnt. Igo target, TrqOvStia. Perl. JKASfate, State, enum target, TrqOvStia. Perl. JKASfate, State, enum target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. Postsov-Instance, Cnt. Igo target, TrqOvStia. Perl. JKASfate, State, enum target, TrqOvStia. Perl. Postsov-Instance, Cnt. Igo target, Riel, Inst. Ap. TrqOvStia. TrqOvStia. Perl. Postsov-Instance, Cnt. Igo target, TrqOvStia. Perl. State, Cnt. Igo, value target, TrqOvStia. Perl. State, Cnt. Igo, value target, TrqOvStia. Perl. TrqOvStia. Perl. Greate, Cnt. Igo, v	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_LKAFault_Cnt_lgc target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_LKAInshipt_Cnt_lgc target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_LKAInshipt_Cnt_lgc target_Inst_Ap_TrqOvSta_Pert_LKAInshipt_Cnt_lgc target_Inst_Ap_TrqOvSta_Pert_LKAInshipt_Cnt_lgc target_Inst_Ap_TrqOvSta_Pert_LKAInshipt_Cnt_lgc target_Inst_Ap_TrqOvSta_Pert_MKAScale_State_enum target_Inst_Ap_TrqOvSta_Pert_MKAScale_State_enum target_Inst_Ap_TrqOvSta_Pert_MKAScale_State_enum target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_MKAScale_State_enum target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_MKAScale_State_enum target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_PosServEnable_Cnt_lgc target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_PosServEnable_Cnt_lgc target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_PosServEnable_Cnt_lgc target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_PosTrqIehAngle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_State_Part_MkANGle_HvDeg_G22 target_Inst_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Inst_Ap_TrqOvSta_Pert_Inst_Ap_TrqOvSta_Pert_Inst_Ap_TrqOvSta_Pert_Inst_Ap_TrqOvSta_Pert_Inst_Ap_TrqOvSta_Pert_Inst_Ap_TrqOvSta_Pert_Inst_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Inst_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_TrqOvSta_Pert_Ap_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_LKARibite, Cnt. Igc target, Rite, Inst. Ap. TrqOv/Sla Perl_LKARibite, State, enum target, Rite, Inst. Ap. TrqOv/Sla Perl_MasSecureVehicleSpeed, Kph. 132 target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_MasSecureVehicleSpeed, Kph. 132 target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_MinSecureVehicleSpeed, Kph. 132 target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_MinSecureVehicleSpeed, Kph. 132 target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_PosservEnable, Cnt. Igc target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_PosservEnable, Cnt. Igc target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_PosservEnable, Cnt. Igc target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_Sperl_Sperl_Sperl_Sperl_Sperl_PosservEnable, Cnt. Igc target, Rite, Inst. Ap. TrqOv/Sla TrqOv/Sla Perl_Spe	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Pert_UKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Pert_OviSta_Pert_McSearcevethicleSpeed_Kpt_122 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecurevethicleSpeed_Kpt_122 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajHwAnget_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchable_Cnt_lgc target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchable_Cnt_lgc target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_TrqOviSta_Pert_SvARTrgdAngRequest_HwDeg_132 target_TrqOviSta_Pert_SvARTrgdAngRequest_Ent_gcvalue 1 target_TrqOviSta_Pert_SvARTrgdAngRequest_Cnt_lgcvalue 1 target_TrqOviSta_Pert_TrqOviSta_Pert_SvARTrgdAngRequest_Cnt_lgcvalue 1 target_TrqOviSta_Pert_SvARTrgdAngRequest_Cnt_lgcvalue 1 target_TrqOviSta_Pert_SvARTrgdAngRequest_Cnt_lgcvalue 1 target_TrqOviSta_Pert_SvARTrgdAngRequest_Cnt_lgcvalue 1 target_TrqOviSta_Pert_SvARTrgdAngRequest_Cnt_lgcvalue 1 target_TrqOviSta_Pert_SvARTrgdAngRequest_Cnt_lgcvalue 1 target_TrqOviSta_Pert_Margat_Cnt_lgcvalue 1 target_TrqOviSta_Pert_LKARequest_Cnt_l	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _LKAState_State_enum target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _MaxSecureVehicleSpeed _KPh_32 target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosServEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosServEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosServEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosServEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosSrajEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _TrqOscAmp. Mirthm32 target. Ries. Inst. Ap. TrqOviSta. TrqOviSta. Perl _TrqOscAmp. Mirthm32 target. TrqOviSta. Perl _Ap. TrqOviSta. Perl _Ap. TrqOviSta. Perl _TrqOscAmp. Mirthm32 target. TrqOviSta. Perl _Ap. TrqOviSta. Perl _TrqOscAmp. Mirthm32 target. TrqOviSta. Perl _Ap. TrqOviSta. Perl	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAmp_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAmp_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAmp_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAmp_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAmp_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAmp_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOsCeAmp_Mintm_132 target_Rie_Inst_Ap_TrqOviSta_Perl_APARecoverableFaults_Cnt_Igc.value target_TrqOviSta_Perl_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Perl_CCWPosition_HwDeg_132.value 1 target_TrqOviSta_Perl_CCWPosition_HwDeg_132.value 1 target_TrqOviSta_Perl_CCWPosition_HwDeg_132.value 1 target_TrqOviSta_Perl_ESCFaul_Cnt_Igc.value 1 target_TrqOviSta_Perl_ESCFaul_Cnt_Igc.value 1 target_TrqOviSta_Perl_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta_Perl_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta_Perl_HwTrque_Hwnm_132.value 1 target_TrqOviSta_Perl_HwTrque_Hwnm_132.value 1 target_TrqOviSta_Perl_MASCeureVehicleSpeed_Kph_132.value 1 target_TrqOviSta_Perl_MASCeureVehicleSpeed_Kph_132.value 1 target_TrqOviSta_Perl_MASCeureVehicleSpeed_Kph_132.value 1 target_TrqOviSta_Perl_DosTrajEnabl	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target Rie Inst Ap_TrqOviSta_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovEhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovEhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrovEhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwRATrqiAngRequest_Hwbeg_g132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSer.App_Gnt_lgc target_TrqOviSta_Per1_ApAnnonRecoverableFaults_Cnt_lgc_value target_TrqOviSta_Per1_ApAnnonRecoverableFaults_Cnt_lgc_value target_TrqOviSta_Per1_CWFound_Cnt_lgc_value target_TrqOviSta_Per1_CWFound_Cnt_lgc_value target_TrqOviSta_Per1_CWFound_Cnt_lgc_value target_TrqOviSta_Per1_CWFound_Cnt_lgc_value target_TrqOviSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOviSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOviSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_lgc_value target_TrqOviSta_Per1_GMOSH_ESCMgEnable_Cnt_lgc_value target_TrqOviSta_Per1_Mosh_ApMgEnable_Cnt_lgc_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSta_Per1_HaviTrque_Hwhm_gsz_value target_TrqOviSt	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrovhwAngle_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenap_MitnMm_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenap_MitnMm_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOvlSta_Pert_ApANnonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Pert_APANecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Pert_APANecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Pert_CoWFound_Cnt_Igc.value target_TrqOvlSta_Pert_CoWFound_Cnt_Igc.value target_TrqOvlSta_Pert_CoWFound_Cnt_Igc.value target_TrqOvlSta_Pert_CoWFound_Cnt_Igc.value target_TrqOvlSta_Pert_ESCRautt_Cnt_Igc.value target_TrqOvlSta_Pert_ESCRautt_Cnt_Igc.value target_TrqOvlSta_Pert_ESCRautt_Cnt_Igc.value target_TrqOvlSta_Pert_ESCRautt_Cnt_Igc.value target_TrqOvlSta_Pert_GMOSH_APAMfgenable_Cnt_Igc.value target_TrqOvlSta_Pert_GMOSH_APAMfgenable_Cnt_Igc.value target_TrqOvlSta_Pert_GMOSH_APAMfgenable_Cnt_Igc.value target_TrqOvlSta_Pert_Horoque_Hnkhm_132_value -0.30000012 target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_P	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosServEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrovhwAngle_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrepEnable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HNDeg_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenap_MitnMm_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenap_MitnMm_132 target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_Re_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCenable_Cnt_Igc target_TrqOvlSta_Pert_ApANnonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Pert_APANecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Pert_APANecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Pert_CoWFound_Cnt_Igc.value target_TrqOvlSta_Pert_CoWFound_Cnt_Igc.value target_TrqOvlSta_Pert_CoWFound_Cnt_Igc.value target_TrqOvlSta_Pert_CoWFound_Cnt_Igc.value target_TrqOvlSta_Pert_ESCRautt_Cnt_Igc.value target_TrqOvlSta_Pert_ESCRautt_Cnt_Igc.value target_TrqOvlSta_Pert_ESCRautt_Cnt_Igc.value target_TrqOvlSta_Pert_ESCRautt_Cnt_Igc.value target_TrqOvlSta_Pert_GMOSH_APAMfgenable_Cnt_Igc.value target_TrqOvlSta_Pert_GMOSH_APAMfgenable_Cnt_Igc.value target_TrqOvlSta_Pert_GMOSH_APAMfgenable_Cnt_Igc.value target_TrqOvlSta_Pert_Horoque_Hnkhm_132_value -0.30000012 target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_Pert_HARAmit_Cnt_Igc.value target_TrqOvlSta_P	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StrajEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ShintLeverisinReverse_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_Rie_Inst_Ap_TrqOviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_TrqOscEnable_Cnt_igc target_TrqOviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Art_OviSta_Pert_Ap_Ar		
target_Rie_Inst_Ap_TrqOviSta_Perl_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Perl_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SWARTrgAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_MitrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_MitrNm_f32 target_Rie_Inst_Ap_TrqOviSta_Perl_TrqOscAmp_MitrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscFreq_Hz_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscFreq_Hz_f32 target_TrqOviSta_Perl_APRAnglest_Cnt_Igc_value target_TrqOviSta_Perl_APRARecoverableFaults_Cnt_Igc_value target_TrqOviSta_Perl_APRARequest_Cnt_Igc_value target_TrqOviSta_Perl_CWFound_Cnt_Igc_value target_TrqOviSta_Perl_CWFound_Cnt_Igc_value target_TrqOviSta_Perl_CWFound_Cnt_Igc_value target_TrqOviSta_Perl_CWFound_Cnt_Igc_value target_TrqOviSta_Perl_CWFound_Cnt_Igc_value target_TrqOviSta_Perl_ESCFault_Cnt_Igc_value target_TrqOviSta_Perl_ESCFault_Cnt_Igc_value target_TrqOviSta_Perl_ESCFault_Cnt_Igc_value target_TrqOviSta_Perl_GMOSH_APAMfgEnable_Cnt_Igc_value target_TrqOviSta_Perl_GMOSH_APAMfgEnable_Cnt_Igc_value target_TrqOviSta_Perl_GMOSH_APAMfgEnable_Cnt_Igc_value target_TrqOviSta_Perl_GMOSH_ESCMfgEnable_Cnt_Igc_value target_TrqOviSta_Perl_HwTorque_HwNm_f32_value target_TrqOviSta_Perl_HwTorque_HwNm_f32_value target_TrqOviSta_Perl_LKAFault_Cnt_Igc_value target_TrqOviSta_Perl_MASecureVehicleSpeed_Kph_f32_value 376 target_TrqOviSta_Perl_MASecureVehicleSpeed_Kph_f32_value target_TrqOviSta_Perl_DosTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_DosTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_DosTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_DosTrajEnable_Cnt_Igc_value target_TrqOviSta_Perl_MASecureVehicleSpeed_Kph_f32_value		target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOviSta_Per1_SVRARTrgIAhgRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_SWRARTrgIAhgRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SMRARTrgIAhgRequest_Hwbeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFanable_Cnt_Ugc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_ApArtoquest_Ap_Artoquest_ApAnthecoverableFaults_Cnt_Ugc value target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Ugc value target_TrqOviSta_Per1_ApARecoverableFaults_Cnt_Ugc value target_TrqOviSta_Per1_CCWPound_Cnt_Ugc value target_TrqOviSta_Per1_CCWPound_Cnt_Ugc value target_TrqOviSta_Per1_CCWPosition_Hwbeg_f32_value target_TrqOviSta_Per1_CCWPosition_Hwbeg_f32_value target_TrqOviSta_Per1_ESCFault_Cnt_Ugc value target_TrqOviSta_Per1_ESCFault_Cnt_Ugc value target_TrqOviSta_Per1_ESCRequest_Cnt_Ugc value target_TrqOviSta_Per1_ESCRequest_Cnt_Ugc value target_TrqOviSta_Per1_ESCRequest_Cnt_Ugc value target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Ugc value target_TrqOviSta_Per1_GMOSH_ESCRMfgEnable_Cnt_Ugc value target_TrqOviSta_Per1_GMOSH_ESCRMfgEnable_Cnt_Ugc value target_TrqOviSta_Per1_MoSH_ESCRMfgEnable_Cnt_Ugc value target_TrqOviSta_Per1_HvTorque_Hwhm_f32_value target_TrqOviSta_Per1_HvTorque_Hwhm_f32_value target_TrqOviSta_Per1_HvTorque_Hwhm_f32_value target_TrqOviSta_Per1_HvTorque_Hwhm_f32_value target_TrqOviSta_Per1_HxApthecquest_Cnt_Ugc value target_TrqOviSta_Per1_HxApthecquest_Cnt_Ugc valu		
target_Re_Inst_Ap_TrqOvIsta.TrqOvIsta_Perf_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Perf_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Perf_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Perf_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Perf_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Perf_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Perf_TrqOscAmp_MtrNm_f32 target_TrqOvIsta_Perf_DrqOvIsta.TrqOvIsta_Perf_TrqOscFreq_Hz_f32 target_TrqOvIsta_Perf_APANonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Perf_APANonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Perf_APARequest_Cnt_Igc_value target_TrqOvIsta_Perf_CWFound_Cnt_Igc_value target_TrqOvIsta_Perf_CWFound_Cnt_Igc_value target_TrqOvIsta_Perf_CWFound_Cnt_Igc_value target_TrqOvIsta_Perf_CWFound_Cnt_Igc_value target_TrqOvIsta_Perf_CWFound_Cnt_Igc_value target_TrqOvIsta_Perf_ESCFault_Cnt_Igc_value target_TrqOvIsta_Perf_ESCFault_Cnt_Igc_value target_TrqOvIsta_Perf_ESCFault_Cnt_Igc_value target_TrqOvIsta_Perf_ESCRequest_Cnt_Igc_value target_TrqOvIsta_Perf_GMOSH_ESCMIgenable_Cnt_Igc_value target_TrqOvIsta_Perf_GMOSH_ESCMIgenable_Cnt_Igc_value target_TrqOvIsta_Perf_MOSH_LKAMIgenable_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_HapticRequest_Cnt_Igc_value target_TrqOvIsta_Perf_Hap		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_TrqOvISta_Pert_Ap_TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_TrqOvISta_Pert_Ap_Arraget_Int_Scatt_Igc.value target_TrqOvISta_Pert_Ap_Arraget_Int_Scatt_Igc.value target_TrqOvISta_Pert_Ap_Arraget_Int_Scatt_Igc.value target_TrqOvISta_Pert_Ap_Arraget_Int_Igc.value target_TrqOvISta_Pert_CWPound_Cnt_Igc.value target_TrqOvISta_Pert_CWPound_Cnt_Igc.value target_TrqOvISta_Pert_CWPound_Cnt_Igc.value target_TrqOvISta_Pert_CWPound_Cnt_Igc.value target_TrqOvISta_Pert_CWPound_Cnt_Igc.value target_TrqOvISta_Pert_CWPound_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc.value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc.value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HwTorque_Hw		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Pert_TrqOscFreq_Hz_f32 target_TrqOvISta_Pert_ApAnonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_ApARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_ApARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_ApARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_ApARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_ExcFault_Cnt_Igc.value target_TrqOvISta_Pert_ExcFault_Cnt_Igc.value target_TrqOvISta_Pert_ExcRequest_Cnt_Igc.value target_TrqOvISta_Pert_ExcRequest_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_APAMrgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_APAMrgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ExcMrgEnable_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_IkARequest_Cnt_Igc.value target_TrqOvISta_Pert_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Pert_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value		
target_Rte_Inst_Ap_TrqOviSta_Pert_TrqOsEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Pert_TrqOviSta_Pert_TrqOsEnable_Cnt_Igc target_TrqOviSta_Pert_APANonRecoverableFaults_Cnt_Igc.value target_TrqOviSta_Pert_APARecoverableFaults_Cnt_Igc.value target_TrqOviSta_Pert_APARequest_Cnt_Igc.value target_TrqOviSta_Pert_APARequest_Cnt_Igc.value target_TrqOviSta_Pert_APARequest_Cnt_Igc.value target_TrqOviSta_Pert_CoWPosition_HwDeg_f32.value target_TrqOviSta_Pert_CoWPosition_HwDeg_f32.value target_TrqOviSta_Pert_CwDosition_HwDeg_f32.value target_TrqOviSta_Pert_CwDosition_HwDeg_f32.value target_TrqOviSta_Pert_ExcFault_Cnt_Igc.value target_TrqOviSta_Pert_ExcFault_Cnt_Igc.value target_TrqOviSta_Pert_ExcFault_Cnt_Igc.value target_TrqOviSta_Pert_ExcFault_Cnt_Igc.value target_TrqOviSta_Pert_ExcRequest_Cnt_Igc.value target_TrqOviSta_Pert_ExcRequest_Cnt_Igc.value target_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOviSta_Pert_GMOSH_ExcMfgEnable_Cnt_Igc.value target_TrqOviSta_Pert_HwTorque_HwNm_f32.value target_TrqOviSta_Pert_HwTorque_HwNm_f32.value target_TrqOviSta_Pert_HwTorque_HwNm_f32.value target_TrqOviSta_Pert_LKAFault_Cnt_Igc.value target_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOviSta_Pert_PostrajEnable_Cnt_Igc.value target_TrqOviSta_Pert_PostrajEnable_Cnt_Igc.value		
target_Re_Inst_Ap_TrqOvISta_Pert_TrqOvISta_Pert_TrqOscFreq_Hz_f32 target_TrqOvISta_Pert_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_APARequest_Cnt_Igc.value target_TrqOvISta_Pert_APARequest_Cnt_Igc.value target_TrqOvISta_Pert_CCWFound_Cnt_Igc.value target_TrqOvISta_Pert_CCWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_CWFound_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc.value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_MaxSecureVehicleSpeed_Kph_f32.value 345 target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value		
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFosition_HwDeg_f32_value 635.109985 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCR_count_Igc.value 1 target_TrqOvISta_Per1_ESCR_count_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HavTorque_HwNm_f32_value 0.030000012 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 376 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32_value 345.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1		
target_TrqOvISta_Perl_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Perl_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Perl_CCWFound_Cnt_Igc.value 635.109985 target_TrqOvISta_Perl_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Perl_CWFound_Cnt_Igc.value 992.320007 target_TrqOvISta_Perl_CWFosition_HwDeg_f32.value 992.320007 target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Perl_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_HwTorque_HwNm_f32.value 1 target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Perl_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvISta_Perl_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 345.100006	0 = = = 1	
target_TrqOvISta_Per1_APARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_igc.value 635.109985 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 992.320007 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 376 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_igc.value 1	· · · · · · · · · · · · · · · · · · ·	
target_TrqOvISta_Per1_CCWFound_Cnt_igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 635.109985 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 992.320007 target_TrqOvISta_Per1_ESCFault_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_HwTorque_HwMm_f32.value -0.300000012 target_TrqOvISta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 376 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 1		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value		
target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 992.320007 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 2-0.300000012 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCIsL:imited_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 1	· · · · · · · · · · · · · · · · · · ·	
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value 1. target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.30000012 target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value 1. target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1. target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1. target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1.		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value -0.300000012 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 376 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 345.100006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	376
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	345.100006
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.119999997	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11043.2998	11043.2998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	7488	7488 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	11043.2998	11043.2998 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.714400053	0.714399993 ± 0.0000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.119999997	0.119999997 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.119999997	0.119999997 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	18.2000008	18.2000008 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.130 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11528.1299
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	11528.1299
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	11528.1299
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	8470
TrqOvlSta_Haptictime_Sec_M_f32	11528.1299
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.25
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.694000006
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	30.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.639999986
k_APAMaxHwTrq_HwNm_f32	1.75999999
k_APAMaxVehSpd_Kph_f32	18
k_ESCMaxVehSpd_Kph_f32	145.100006
k_HapticAmplitude_MtrNm_f32	0.560000002
k_HapticDuration_Sec_f32	1.995
k_HapticFreq_Hz_f32	18.8999996
k_HapticReacttime_Sec_f32	0.351999998
k_LKAMaxVehSpd_Kph_f32	41.7999992
k_LKAMinVehSpd_Kph_f32	38.5
k_MaxHapticReqTime_Sec_f32	0.495000005
k_SWARLimiter_HwDeg_f32	30.7000008
k_ShiftLevRevTime_Sec_f32	0.0410000011
k_StandstillThresh_Kph_f32	0.319999993
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_ig	•	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	:	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr target TrqOvlSta Per1 GMOSH LKAMfgEr		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc	52	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKARequest Cnt Igc	target TrgOvISta Per1 LKARequest Cnt Ig	C	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrgOvISta Per1 LKAState State en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	: -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lg</u> c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-625.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	997.26001		
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvIsta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	0		
target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	0		
target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.100000001		
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0		
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	355.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Resu
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	1	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	8470	8470 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	3.25	3.25 ± 0.000009	
	0	0 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc		0 ± 1	
TrqOviSta_LKAFault_Cnt_M_lgc TrqOviSta_LKAPermFault_Cnt_M_lgc	0	011	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc			

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	30.2000008	30.2000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.560000002	0.560000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	18.8999996	18.8999996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Input Value
target_Rte_Inst_Ap_TrqOvlSta
3
1
12012.96
12012.96
12012.96
2
9452
12012.96
3.599999
0.704999983
1
1
3
3
40.2000008
1
0
0.651000023
1.84000003
19.7999992
148.800003
0.579999983
0.50999999
19.6000004
0.368000001
43.7000008
40.25
0.50999999
31
0.0450000018
0.360000014
0.0370000005
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc
g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_TrqOvISta_Per1_APARequest_Cnt_lgc
target_TrqOvISta_Per1_APAState_State_enum
target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_TrqOvISta_Per1_CWFound_Cnt_Igc
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target TrqOvlSta Per1 ESCRequest Cnt Iqc
target_TrqOvlSta_Per1_ESCState_State_enum
n target_TrqOvlSta_Per1_GMOSHOscillateState_enum
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1



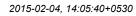


Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	,_	
	target_TrqOvlSta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_l		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_Fer1_TrqOscFreq Hz f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-614.909973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1002.20001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	396.600006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	365.700012 1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-25.6000004		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	3	3	- Court
TrqOvISta_ESCState_State_M_enum	3	3	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	_
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	12012.96	12012.96 ± 1	~
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	9452	9452 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	12012.96	12012.96 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.5999999	3.5999999 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.140000001	0.140000001 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	V
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	, , a
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0.140000001	1±1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.140000001 0.579999983	0.140000001 ± 0.0009765625 0.579999983 ± 0.0009765625	-
wigot_irqoviota_i oi i_irqosoAmp_ivitirim_ioz.value			· •
target TrgOvISta Per1 TrgOscEnable Cnt lgc value			
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	0 19.6000004	0 ± 1 19.6000004 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Fest Step 2.132 (Repeat Count = 1)	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	4
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12497.79
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12497.79
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	12497.79
TrqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	10434
rqOvlSta_Haptictime_Sec_M_f32 rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	12497.79 3.95000005
rqOviSta HwTorqueSV HwNm M Str.K Uls f32	0.0020000009
rqOvISta LKAFault Cnt M lgc	0
rqOviSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	4
rqOvlSta_LKAState_State_M_enum	4
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.662 1.91999996
_APAMaxHwTrq_HwNm_f32 _APAMaxVehSpd_Kph_f32	21.6000004
ESCMaxVehSpd Kph f32	152.5
_HapticAmplitude_MtrNm_f32	0.60000024
_HapticDuration_Sec_f32	0.0149999997
HapticFreq_Hz_f32	20.2999992
_HapticReacttime_Sec_f32	0.384000003
_LKAMaxVehSpd_Kph_f32	45.5999985
_LKAMinVehSpd_Kph_f32	42
_MaxHapticReqTime_Sec_f32	0.524999976
_SWARLimiter_HwDeg_f32	31.2999992
_ShiftLevRevTime_Sec_f32 StandstillThresh Kph f32	0.0489999987 0.40000006
StandstillTime Sec f32	0.0410000011
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOviSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32 arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt lgc	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target TrqOvISta Per1 ESCIsLimited Cnt Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrqOvlSta Per1 ESCRequest Cnt lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
${\tt rrget_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc}$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
riget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
riget Rte Inst Ap TrqOviSta.TrqOviSta Per1 LKARequest Cnt Igc	target_TrqOviSta_Per1_LKARequest_Cnt_lgc
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAState State enum	target_TrqOvISta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	2 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3	
Irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 rrget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
irget_Rte_inst_Ap_frqOviSta_frqOviSta_Per1_frqOscEnable_Cnt_igc irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvIsta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-604.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1007.14001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.89999976		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	406.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	376		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-32.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12497.79	12497.79 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	10434	10434 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.94390011	3.94390011 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.2000008	50.2000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	50.2000008	50.2000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.600000024	0.600000024 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value target TrqOvlSta Per1 TrqOscFreq Hz f32.value	0 20.2999992	0 ± 1 20,2999992 ± 0,0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.133 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	12982.6201
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12982.6201
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	12982.6201
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	11416
TrqOvlSta_Haptictime_Sec_M_f32	12982.6201
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.30000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00419999985
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	60.2000008





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.672999978
k_APAMaxHwTrq_HwNm_f32	2
k APAMaxVehSpd Kph f32	23.3999996
k_ESCMaxVehSpd_Kph_f32	156.199997
k HapticAmplitude MtrNm f32	0.620000005
k_HapticDuration_Sec_f32	0.029999993
k HapticFreq Hz f32	21
k HapticReacttime Sec f32	0.40000006
k_LKAMaxVehSpd_Kph_f32	47.5
k LKAMinVehSpd Kph f32	43.75
k_MaxHapticReqTime_Sec_f32	0.540000021
k SWARLimiter HwDeg f32	31.6000004
k_ShiftLevRevTime_Sec_f32	0.0529999994
k StandstillThresh Kph f32	0.43999998
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_enumerator and the property of the property $	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrgOvlSta Per1 PosTrajHwAngle HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ShiftLeverIsInReverse Cnt Igc	target TrgOvlSta Per1 ShiftLeverlsInReverse Cnt Igc
target Rte Inst Ap TrqOvISta.TrqOvISta Per 1 TrqOscAmp MtrNm f32	
	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-594.710022
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1012.08002
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.29999995
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	
	1
target TrgOvlSta Per1 LKARequest Cnt lac.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1 417.200012
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1 417.200012 386.299988
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1 417.200012





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	12982.6201	12982.6201 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvISta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11416	11416 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	12982.6201	12982.6201 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.30000019	4.30000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.620000005	0.620000005 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	21	21 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.134 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13467.4502
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	13467.4502
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13467.4502
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	12398
TrqOvlSta_Haptictime_Sec_M_f32	13467.4502
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.6500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00639999984
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.1999969
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.684000015
k_APAMaxHwTrq_HwNm_f32	2.07999992
k_APAMaxVehSpd_Kph_f32	25.2000008
k_ESCMaxVehSpd_Kph_f32	159.899994
k_HapticAmplitude_MtrNm_f32	0.63999986
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	21.7000008
k_HapticReacttime_Sec_f32	0.416000009
k_LKAMaxVehSpd_Kph_f32	49.4000015
k_LKAMinVehSpd_Kph_f32	45.5
k_MaxHapticReqTime_Sec_f32	0.555000007
k_SWARLimiter_HwDeg_f32	31.8999996
k_ShiftLevRevTime_Sec_f32	0.057
k_StandstillThresh_Kph_f32	0.479999989
k_StandstillTime_Sec_f32	0.0489999987
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntore and the property of the prope$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 _ 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target TrqOvISta Per1 LKAState State enu		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosSrvoHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target TrqOvlSta Per1 PosTrajEnable Cnt	·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1140.10999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target TrqOvlSta Per1 HwTorque HwNm f32.value	1.70000005		
target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ExArcequest_Crit_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	427.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	396.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.700008		
target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	4	4	
TrqOvISta_ESCState_State_M_enum	4	4	V
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	12398	12398 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.6500001	4.6500001 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	70.1999969	70.1999969 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	70.1999969	70.1999969 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.639999986	0.639999986 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	21.7000008	21.7000008 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.135 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13952.2803
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	13952.2803
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	13952.2803
TrqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	13380
rqOvlSta_Haptictime_Sec_M_f32	13952.2803
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	80.1999969
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.694999993
_APAMaxHwTrq_HwNm_f32	2.16000009
c_APAMaxVehSpd_Kph_f32	27
z_ESCMaxVehSpd_Kph_f32	163.600006
HapticAmplitude_MtrNm_f32	0.660000026
_HapticDuration_Sec_f32	0.059999987
	22.3999996
c_HapticReacttime_Sec_f32	0.432000011
	51.2999992
 LKAMinVehSpd_Kph_f32	47.25
	0.569999993
SWARLimiter HwDeg f32	32.2000008
ShiftLevRevTime_Sec_f32	0.0610000007
StandstillThresh_Kph_f32	0.519999981
s_StandstillTime_Sec_f32	0.0529999994
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_Ig	
	target TrqOvISta Per1 APARecoverableFaults Cnt Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



· -			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_trqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvISta_Per1_TrqOscFreq_Hz_f3;		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1	<u>-</u>	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1100.20996		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target TrqOvlSta Per1 CWPosition HwDeg f32.value	1440.10999		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	406.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta ESCState State M enum			~
	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13952.2803	3 13952.2803 ± 1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	13952.2803 13952.2803	3 13952.2803 ± 1 13952.2803 ± 1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13952.2803 13952.2803 13952.2803	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803	~
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum	13952.2803 13952.2803 13952.2803 0	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	13952.2803 13952.2803 13952.2803 0 13380	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32	13952.2803 13952.2803 13952.2803 0 13380 13952.2803	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	13952.2803 13952.2803 13952.2803 0 13380 13952.2803 4.97505999	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009	~
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc	13952.2803 13952.2803 0 13380 13952.2803 4.97505999	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	13952.2803 13952.2803 0 13380 13952.2803 4.97505999	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1	•
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	13952.2803 13952.2803 0 13380 13952.2803 4.97505999	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1	•
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.1800000007 ± 0.0009765625	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.1800000007 ± 0.0009765625 1 ± 1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007 1	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007 1 1 3	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.1800000007 ± 0.0009765625 1 ± 1 1 ± 1 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_enum.value	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007 1 1 3	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 3 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007 1 1 3 3	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 3 3 0	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007 1 1 3 3	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 3 3 0 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_DesServEnable_Cnt_lgc.value	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007 1 1 3 3 0 3	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 3 3 0 3 1	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_HwTorqueSV_HwNm_M_IStr.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value	13952.2803 13952.2803 0 13380 13952.2803 4.97505999 1 1 2 3 0.180000007 1 1 3 3 0 3 1	3 13952.2803 ± 1 13952.2803 ± 1 13952.2803 0 13380 ± 1 13952.2803 ± 1 4.97505999 ± 0.000009 1 ± 1 1 ± 1 2 ± 1 3 0.180000007 ± 0.0009765625 1 ± 1 1 ± 1 3 3 0 3 1 ± 1 0.180000007 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.136 (Repeat Count = 1)	Innut Walin
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta 3
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	14437.1104
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	14437.1104
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	14437.1104
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	14362
TrqOvlSta_Haptictime_Sec_M_f32	14437.1104
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.3499999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0108000003
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.70599997
k_APAMaxHwTrq_HwNm_f32	2.24000001
k_APAMaxVehSpd_Kph_f32	28.7999992
k_ESCMaxVehSpd_Kph_f32	167.300003
k_HapticDuration_Sec_f32	0.680000007
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	0.075000003 23.1000004
k_HapticReacttime_Sec_f32	0.448000014
k_LKAMaxVehSpd_Kph_f32	53.2000008
k_LKAMinVehSpd_Kph_f32	49
k_MaxHapticReqTime_Sec_f32	0.584999979
k_SWARLimiter_HwDeg_f32	32.5
k_ShiftLevRevTime_Sec_f32	0.0649999976
k_StandstillThresh_Kph_f32	0.560000002
k_StandstillTime_Sec_f32	0.057
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target TrgOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_MinSecurevenicleSpeed_Rpn_132 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecurevenicleSpeed_kpn_132 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSetvErlable_Glit_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target regeriota i el i Al Alleguest elli Ige. Value	•
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1060.31006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	750		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	448.100006		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	417.200012		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-24.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	-
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	1	1	-
TrqOvlSta_Hapticdur_Cnt_M_u16	14362	14362 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.3499999	5.3499999 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004	11.1000004 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	→
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	11.1000004	11.1000004 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.680000007	0.680000007 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	0 ± 1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.137 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	15891.5996
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	15891.5996
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	15891.5996
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	17308
TrqOvlSta_Haptictime_Sec_M_f32	15891.5996
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	14.3999996





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.739000022
k_APAMaxHwTrq_HwNm_f32	2.48000002 34.2000008
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	19.2000008
k_HapticAmplitude_MtrNm_f32	0.129999995
k_HapticDuration_Sec_f32	0.119999997
k_HapticFreq_Hz_f32	25.2000008
k_HapticReacttime_Sec_f32	0.495999992
k_LKAMaxVehSpd_Kph_f32	58.9000015
k_LKAMinVehSpd_Kph_f32	54.25
k_MaxHapticReqTime_Sec_f32	0.629999995
k_SWARLimiter_HwDeg_f32	33.4000015
k_ShiftLevRevTime_Sec_f32	0.0769999996
k_StandstillThresh_Kph_f32	0.680000007
k_StandstillTime_Sec_f32	0.0689999983
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_linst_Ap_mqovista.mqovista_ren_ArARequest_ciit_igc target_Rte_linst_Ap_mqovista.mqovista_ren_ArARequest_ciit_igc	target TrqOvlSta Per1 APAState State enum
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target TrqOvlSta Per1 LKARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target TrqOvlSta Per1 TrqOscFreq Hz f32
target_Rte_ilist_Ap_11q0vista.11q0vista_Fe11_11q0scFreq_H2_is2 target_Trq0viSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1440.10999
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1007.14001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_Haptickequest_Cnt_lgc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	3.70000005
target_TrqOviSta_Per1_hw10rque_nw10ri_isz.value target_TrqOviSta_Per1_LKAFault_Cnt_lgc.value	3.70000005
target_TrqOvIsta_FeT1_EKAFatit_Cftt_igc.value target_TrqOvIsta_Per1_EKAFatit_Cftt_igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	479
target_freeviola_r err_iviaxoccureveriicicopecu repri loz.value	
target_TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32.value	448.100006
	448.100006 1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	15891.5996	15891.5996 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	17308	17308 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	15891.5996	15891.5996 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.4000001	6.4000001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.119999997	0.119999997 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.119999997	0.119999997 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.129999995	0.129999995 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.2000008	25.2000008 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.138 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16376.4297
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	16376.4297
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	16376.4297
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	18290
TrqOvlSta_Haptictime_Sec_M_f32	16376.4297
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.75
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0196000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	15.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.75
k_APAMaxHwTrq_HwNm_f32	2.55999994
k_APAMaxVehSpd_Kph_f32	36
k_ESCMaxVehSpd_Kph_f32	24.7000008
k_HapticAmplitude_MtrNm_f32	0.150000006
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	25.8999996
k_HapticReacttime_Sec_f32	0.512000024
k_LKAMaxVehSpd_Kph_f32	60.7999992
k_LKAMinVehSpd_Kph_f32	56
k_MaxHapticReqTime_Sec_f32	0.644999981
k_SWARLimiter_HwDeg_f32	33.7000008
k_ShiftLevRevTime_Sec_f32	0.0810000002
k_StandstillThresh_Kph_f32	0.720000029
k_StandstillTime_Sec_f32	0.0729999989
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lo	дс	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>j_</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvIsta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAFault_Cnt_lgc	target TrqOvlSta Per1 LKAFault Cnt Igc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ic	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32	0 - 1		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-360		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1012.08002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 HapticRequest Cnt lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	4.0999999		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKAFatilt_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	489.299988		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	458.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-17.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	*
TrqOvISta_ESCState_State_M_enum	4	4	·
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	~
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	_
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	18290	18290 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	6.75	6.75 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc		0 ± 1	✓
TrqOviota_EtVAI adit_Cit_iv_igc	0	UII	•
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	0 2	0 ± 1 2 ± 1	~

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	15.5	15.5 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	25.8999996	25.8999996 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.139 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16861.2598
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	16861.2598
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	16861.2598
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	19272
TrqOvlSta_Haptictime_Sec_M_f32	16861.2598
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	7.099999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.760999978
k_APAMaxHwTrq_HwNm_f32	2.6400001
k APAMaxVehSpd Kph f32	37.7999992
<pre>c_ESCMaxVehSpd_Kph_f32</pre>	30.2000008
K_HapticAmplitude_MtrNm_f32	0.170000002
K_HapticDuration_Sec_f32	0.150000006
k_HapticFreq_Hz_f32	26.6000004
k_HapticReacttime_Sec_f32	0.527999997
LKAMaxVehSpd_Kph_f32	62.7000008
LKAMinVehSpd_Kph_f32	57.75
k_MaxHapticReqTime_Sec_f32	0.660000026
k SWARLimiter HwDeg f32	34
k_ShiftLevRevTime_Sec_f32	0.0850000009
k_StandstillThresh_Kph_f32	0.75999999
k_StandstillTime_Sec_f32	0.0769999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target TrqOvlSta Per1 CCWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target TrqOvISta Per1 CWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrgOvlSta Per1 ESCState State enum
target Rte Inst Ap Trq0vlSta.Trq0vlSta Per1 GM0SH0scillateState State enur	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt lgc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSH_APAMilgEnable_Cht_lgc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMilgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_BinOSH_ExAmingEnable_Crit_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvIsta_Per1_HwTorque_HwNm_f32
	TURGOT TRANSPORT OF THE FORMACT TWINITE TOP





_				
Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv			
	target_TrqOvISta_Per1_SWARTrgtAngReque			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	igc .		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 1			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	1			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-750			
target TrqOvlSta Per1 CWFound Cnt Igc.value	1			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1017.02002			
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1			
target TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	1			
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.5			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	499.600006			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	468.700012			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	3	3	~	
TrqOvlSta_ESCState_State_M_enum	3	3	~	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16861.2598	16861.2598 ± 1	~	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	16861.2598	16861.2598 ± 1	~	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0		
TrqOvlSta_HapticState_State_M_enum	0	0	¥	
TrqOvISta_Hapticdur_Cnt_M_u16	19272	19272 ± 1	- 4	
TrqOvlSta_Haptictime_Sec_M_f32	16861.2598	16861.2598 ± 1	V	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lqc	7.0433197 1	7.04332018 ± 0.000009 1 ± 1		
TrqOvISta_LKAFatilt_Crit_M_igc TrqOvISta_LKAPermFatilt_Crit_M_igc	1	1±1		
TrqOvlSta_LKAPermFault_Cnt_M_igc TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~	
TrgOvISta LKAState State M enum	3	3		
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.140000001	0.140000001 ± 0.0009765625	~	
TrqOvlSta ReadytoPulse Cnt M lgc	1	1 ± 1		
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	1	1 ± 1	~	
target TrgOvlSta Per1 APAState State enum.value	3	3		
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	-	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~	
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.140000001	0.140000001 ± 0.0009765625	~	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.170000002	0.170000002 ± 0.0009765625	~	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	26.6000004	26.6000004 ± 0.0009765625	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.140 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18460.6309
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	22218
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	18460.6309 8.14999962
TrqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0284000002
TrgOvlSta LKAFault Cnt M lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOviSta_LKAPermFault_Cnt_M_u08	1
TrqOvISta_LKAState_State_M_enum	1
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	19.8999996
TrqOviSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.79400003
k_APAMaxHwTrq_HwNm_f32	2.88000011
k_APAMaxVehSpd_Kph_f32	43.2000008 46.7000008
k_ESCMaxVehSpd_Kph_f32 k_HapticAmplitude_MtrNm_f32	0.230000004
k HapticDuration Sec f32	0.194999993
k_HapticFreq_Hz_f32	28.7000008
k_HapticReacttime_Sec_f32	0.575999975
k_LKAMaxVehSpd_Kph_f32	68.4000015
k_LKAMinVehSpd_Kph_f32	63
k_MaxHapticReqTime_Sec_f32	0.704999983
k_SWARLimiter_HwDeg_f32	34.9000015
k_ShiftLevRevTime_Sec_f32	0.097000029
k_StandstillThresh_Kph_f32	0.87999995
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0890000015 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	V - 1
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	0
0 1=	I.

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1020.40997		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1021.96002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	499.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.69999981		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309	18460.6309 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18460.6309	18460.6309	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	22218	22218 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.08041954	8.08041954 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	19.8999996	19.8999996 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
	4	4	•
target_TrqOvlSta_Per1_LKAState_State_enum.value		0 ± 1	
	0	U±I	-
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0 19.8999996	19.8999996 ± 0.0009765625	•
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value			
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	19.8999996	19.8999996 ± 0.0009765625	~

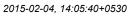
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.141 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21465
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	21465
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	21465
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	23200
TrqOvlSta_Haptictime_Sec_M_f32	21465
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0306000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	21





TROPIESE ARROGATION CONTROL TO 19 A PARAGRADISTRY SEC. 25 A PARAGRADI	_ ' _	
CAPANISATION 1995	Name	Input Value
APANDALIVETATION See. 26 APANDALIVETATION See. 26 APANDALIVETATION SEE. 26 APANDALIVETATION SEE. 26 APANDALIVETATION SEE. 27 APANDAL	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
A APAMarbinity July 1910. - ESCHAROSSE JR. 192 - ESCHAROSSE JR. 193 - ESCHAROSSE JR.	k_APAIncludeHaptic_Cnt_lgc	0
A APAMarbinity July 1910. - ESCHAROSSE JR. 192 - ESCHAROSSE JR. 193 - ESCHAROSSE JR.	k_APAMaxHwTrqTime_Sec_f32	0.805000007
A APAMANOS (pt. 192 A ESTANANOS (pt. 192 A	k APAMaxHwTrg HwNm f32	2.96000004
Lipsofice Tel. Lipsofice Li	k APAMaxVehSpd Kph f32	45
Lipschicharing Sept. 22	k ESCMaxVehSpd Kph f32	52.2000008
Lipsophering Lips		
Lipspicherung Ser, 192 Ser, 193, 193, 193, 193, 193, 193, 193, 193	_ · · ·	
Lapper Resources (1997) Lapper Resources (199		
LAMANY-WEIGH, PM, LEG. A 75		
Moderation Section S		
MastingschopTime_Sec_102		
S. SWARLENDER SEP 12 S. Silmender Times Best 12 S. Silme		
A Sandridi Time, Sec. 122 A Sandridi Time, Sec.		
s. Sandardiffree Sep. 122 singed, Rie, Ind. Ap. Trigo/Sist. [Port JAPAD/virtineventionDetected. Dt. target_Trigo/Sist. [Port JAPAD/virtineventionDetected. Dt. target_Trigo/Sist. [Port JAPAD/virtineventionDetected. Dt. target_Trigo/Sist. [Port JAPAD/virtineventionDetected. Dt. target_Trigo/Sist. [Port JAPAD/virtineventionDetected. Ct. target_Trigo/Sist. [Port JAPAD/Sist. [Port	-	
Opposition of the company of the com		
Issgel, Rie Jan, A. Trigovisia Trigovisia pert APANdrovineoverible auto of the programme of the process of the		
Integer Rev. Intel. Ap. Trockists Trockists. Pert. JANAMORECoveration and programme and process and pr		
issest Rie Ind. Ap. TOO/Sits TOO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. TOO/Sits ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. TOO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Faults Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Crit. (pc inspect Rie) and Ap. ToO/Sits ToO/Sits Pert J. APARecoverable Crit. (pc inspect ToO/Sits Pert J. APARecoverable Crit. (pc inspect ToO/Sits Pert J. APARecoverable Crit. (pc inspect ToO/		
Integer, Rici, Inc. A. D., TOO/Sista TOO/Sista, Pert I. APARGouser, Circl. Ligo supper, Rici, Inc. A. D., TOO/Sista ToO/Sista, Pert I. CAPARGOUSE, See, unum supper, Rici, Inc. A. D., TOO/Sista ToO/Sista, Pert I. CAPARGOUSE, See, unum supper, Rici, Inc. A. D., TOO/Sista ToO/Sista, Pert I. CAPARGOUSE, See, unum supper, Rici, Inc. A. D., TOO/Sista ToO/Sista, Pert I. CAPARGOUSE, See, Inc. A. D., TOO/Sista ToO/Sista, Pert I. CAPARGOUSE, See, Inc. A. D., TOO/Sista ToO/Sista, Pert I. CAPARGOUSE, Pert I. CAPA		
intent. Ris. Incl. Ap. Tro-Orisia Tro-Orisia Pert. LAPASitis State, enum imager. Ris Incl. Ap. Tro-Orisia Tro-Orisia Pert. Community of the pert. Pert. Risk Pert. Community of the pert.		
used: Rijn in A. p. TroJoSta TroJoSta Perl. COVPound. Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. CoVPound. Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. CoVPound. Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. CoVPound. Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. CoVPound. Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. SoStal mode Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. SoStal mode Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. SoStal mode Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. SoStal mode Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. CoMCPS Applications Sostal used: Rijn in A. p. TroJoSta TroJoSta Perl. CoMCPS Applications Sostal used: Rijn in A. p. TroJoSta TroJoSta Perl. CoMCPS Applications Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. CoMCPS Applications Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. CoMCPS Applications Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. In Applications Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. In Applications Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in A. p. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Rijn in Ap. TroJoSta TroJoSta Perl. Loke Cnt. lgs used: Ri		
isseed, Rin, Leaf, Ap. TroOvisia TroOvisia Pert CVProvision, Indice, 12 size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert CVProvision, Indice, 12 size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert CVProvision, Indice, 12 size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert CVProvision, Indice, 12 size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert CVProvision, Indice, 12 size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert Lise Class Cut Lyc size of Rin, Leaf, Ap. TroOvisia TroOvisia Pert L		
uraget Rein La, P. TroOvisia TroOvisia Pert L.Whoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.Whoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 52 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62) uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget Rein Land (TroOvisia TroOvisia Pert L.SCRoston Hadeg 62 uraget TroOvisia Pert L.SCRoston Hadeg 62 uraget TroOvisia Pert L.SCRoston Hadeg 62 uraget TroOvisia Pert L.SCRoston Hadeg 6		
Image Rein Ap TroOvisia TroOvisia Pert CVPosition HavDes 32 Image Rein Ap TroOvisia TroOvisia Pert Section Image Rein Ap TroOvisia TroOvisia Pert Section Image TroOvisia Pert Section Image TroOvisia Pert Section Image TroOvisia Pert TroOvisia Pert Image TroOvisia Pert Image TroOvisia	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	
target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ESCRate City target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ESCRate Size of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ESCRate Size of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ESCRate Size of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ESCRate Size of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL Size of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL Size of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Size of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Size of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Size of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL SIZE OF target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL SIZE OF target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL PERT ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Crity of target, Rie, Inst. Ap. TriqONSIs Pert ISCROBIAL Crity of target, T	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	
tracet, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, SECIsLamied, Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, SECISLamied, Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert CMOSHOScalutoState canus target Rie, Inst. Ap. TriqONSta TroyNSta Pert CMOSHOSCalutoState State canus target Rie, Inst. Ap. TriqONSta TroyNSta Pert CMOSHOSCALUTOSTATE Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert CMOSH ESCMigFinable, Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert LMOSH ESCMigFinable, Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, Table Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, Table Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, Table Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, Table Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, Table Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, Table Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, Table Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, Table Cnt. Igc target, Rie, Inst. Ap. TriqONSta TroyNSta Pert I, MARGE HAMING, TroyNSta Pert I, MARGE HAMING, TriqONSta PERT I, MARGE HAMING	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
tracet, Rie, Inst, Ap, TriqONSta TriqONSta, Pert I, ESCRequest, Cni. [oc target Rie Inst Ap, TriqONSta TriqONSta, Pert I, ESCRequest, Cni. [oc target Rie Inst Ap, TriqONSta TriqONSta, Pert I, CMOSH-APAMIginate State enum target, Rie Inst, Ap, TriqONSta TriqONSta, Pert CMOSH-APAMIginate Cott. [oc target Rie Inst, Ap, TriqONSta, Pert CMOSH-APAMIginate) Cri. [oc target, Rie Inst, Ap, TriqONSta TriqONSta, Pert I, CMOSH-APAMIginate) Cri. [oc target, Rie Inst, Ap, TriqONSta TriqONSta, Pert I, EACH Cott. [oc target, Rie Inst, Ap, TriqONSta, Pert I, EACH Cott. [oc target, Rie Inst, Ap, TriqONSta, Pert I, EACH Cott. [oc target, Rie Inst, Ap, TriqONSta, TriqONSta, Pert I, EACH Cott. [oc target, Rie Inst, Ap, TriqONSta, TriqONSta, Pert I, EACH Cott. [oc target, Rie, Inst, Ap, TriqONSta, TriqONSta, Pert I, EACH Cott. [oc target, Rie, Inst, Ap, TriqONSta, TriqONSta, Pert I, EACH Cott. [oc target, Rie, Inst, Ap, TriqONSta, TriqONSta, Pert I, EACH AND Expert I, EACH Cott. [oc target, Rie, Inst, Ap, TriqONSta, TriqONSta, Pert I, EACH AND Expert I, EACH Cott. [oc target, Rie, Inst, Ap, TriqONSta, TriqONSta, Pert I, EACH AND Expert I, EACH Expert I, EACH CONSTANT I, EACH Expert I, EACH Expe	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target, Rie, Max, Ap. Triqovista, Port J. ESCState, State, enum target, Rie, Inst. Ap. Triqovista, Port J. SOSSis, Perf. GMOSH, APAMIGEnable, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Triqovista, Perf. GMOSH, APAMIGEnable, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Triqovista, Perf. GMOSH, APAMIGEnable, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Triqovista, Perf. GMOSH, APAMIGEnable, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Triqovista, Perf. J. GMOSH, ESCMigenable, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Triqovista, Perf. J. HaptorRequest, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Perf. J. MaxSecureVehicleSpeed, Kph. 32 target, Rie, Inst. Ap. Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Perf. J. KAPARILL, Cnt. Igo target, Rie, Inst. Ap. Triqovista, Perf. J. Kaparill, P	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target, Rie, Inst. Ap. TriqOviSta Prot (SMOSHO-SellaeState State, enum target, Rie, Inst. Ap. TriqOviSta Prot (SMOSH APAMGERoble, Crit. Igc target, Rie, Inst. Ap. TriqOviSta Prot (SMOSH ESCMGERoble, Crit. Igc target, Rie, Inst. Ap. TriqOviSta Prot (SMOSH ESCMGERoble). Crit. Igc target, Rie, Inst. Ap. TriqOviSta Prot (SMOSH ESCMGERoble). Crit. Igc target, Rie, Inst. Ap. TriqOviSta Prot (SMOSH ESCMGERoble). Crit. Igc target, Rie, Inst. Ap. TriqOviSta Prot (SMOSH ESCMGERoble). Crit. Igc target, Rie, Inst. Ap. TriqOviSta Prot (SMOSH Prot (SMOSH Prot (SMOSH ESCMGERoble)). Crit. Igc target, Rie, Inst. Ap. TriqOviSta Prot (SMS) Prot (SMOSH P	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target, Rie, Inst. Ap. TriqOviSta. Prof. (SMOSH, APAMfgEnable, Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (SMOSH, ESCMIgEnable, Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (SMOSH, ESCMIgEnable, Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (SMOSH, ESCMIgEnable, Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (SMOSH, ESCMIgEnable, Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (SMOSH, ESCMIgEnable, Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul, Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul, Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Prof. (LAFFaul), Cnt.) go: target, Rie, Inst. Ap. TriqOviSta. Prof. (LAFFaul), Prof.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. (AMOSH, ESCMI)Enable, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. (AMOSH, ESCMI)Enable, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I philoricequest, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I philoricequest, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I philoricequest, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I LAKAINIBLE, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I LAKAINIBLE, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I LAKAINIBLE, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I LAKAINIBLE, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I LAKAINIBLE, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I LAKAINIBLE, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I Possevicenble Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. Possevicenble, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. Possevicenble, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. Possevicenble, Cnt. Igo target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I SWARTITqANaile, HwDeg. 132 target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I SWARTITqANaile, HwDeg. 132 target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I SWARTITqANaile, HwDeg. 132 target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I SWARTITqANaile, HwDeg. 132 target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I SWARTITqANaile, HwDeg. 132 target, Rie, Inst. Ap. TrqOvilsia. TrqOvilsia. Pert. I SWARTITqANaile, Involled, Inst. Inst	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enunces and the properties of $	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. AphtoRequest, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. AphtoRequest, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. HaptoRequest, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. KAPault, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. KAPault, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. KAPault, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. KAPault, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. KAPault, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. KAPault, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. KAPault, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. KAPault, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. Kapault, Special Kyp. 122 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. PossToviPable, Cnl. Igo target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert J. TrqOviSta Pert J. TrqOviSta Pert J. PossToviPable, PhoBeg, 132 target, Rie, Inst. Ap. TrqOviSta TrqOviSta Pert T. TrqOviSta Pert J. TrqOviS	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. HapticRequest, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. HwTorque, HwNm, 132 target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. LKAFautt, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. LKAFautt, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. LKAFautt, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. LKAFautt, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. LKAFautt, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. LKAState, State, enum target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. LKAState, State, enum target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. MasSecureVehicleSpeed, Kph, 132 target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosServinable, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosServinable, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTapificable, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTapificable, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTapificable, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTapificable, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. PosTapificable, Cnt. Igo target, Rie, Inst, Ap, TrqOvista, TrqOvista, Pert J. T	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target, Rie, Inst, Ap, TrqOv/Sta TrqOv/Sta, Perf, LMATorque, HwNm, 132 target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, LKAFault, Cnt, Igc target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, LKAFault, Cnt, Igc target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, LKARequest, Cnt, Igc target, Rie, Inst, Ap, TrqOv/Sta, Perf, DKARequest, Cnt, Igc target, Rie, Inst, Ap, TrqOv/Sta, Perf, DKASState, State, enum target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, DKASState, State, enum target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, DKASState, State, enum target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, DKASState, State, enum target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, DKASSeure-VerhicleSpeed, Kph, 132 target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, PostrovibANage, HwDeg, 132 target, TrqOv/Sta, Perf, DkasSeverVerhicleSpeed, Kph, 132 target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, PostrovibANage, HwDeg, 132 target, Rie, Inst, Ap, TrqOv/Sta, TrqOv/Sta, Perf, Straffyshange, Reveal, HwDeg, 132 target, TrqOv/Sta, Perf, DsSarVerhange, LmUbeg, 132 target, TrqOv/Sta, Perf, DsWarVerlande, Pubeg, 132 target, TrqOv/Sta, Perf, DsWa	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target, Rie, Inst. Ap. TrqOv/Sta TrqCV/Sta Pert_LKARaul, Cnt.lgc target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_LKARaul, Cnt.lgc target, Rie, Inst. Ap. TrqOv/Sta. Pert_LKARaul, Cnt.lgc target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_LKARaul, Cnt.lgc target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_LKARaul, Ent.lgc target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_LKAState, State, enum target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_MinSecureVehicleSpeed, Kph. 132 target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_MinSecureVehicleSpeed, Kph. 132 target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_Posservéhande, Cnt.lgc target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_Posservéhande, Cnt.lgc target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_Possrovéhande, HuvDeg, 132 target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_Possrovéhande, HuvDeg, 132 target, Rie, Inst. Ap. TrqOv/Sta. TrqCV/Sta. Pert_Possrovéhande, HuvDeg, 132 target, Rie, Inst. Ap. TrqCV/Sta. TrqCV/Sta. Pert_SWARTrgtAngRequest_HwDeg, 132 target, Rie, Inst. Ap. TrqCV/Sta. TrqCV/Sta. Pert_Starlikavarie, HuvDeg, 132 target, Rie, Inst. Ap. TrqCV/Sta. TrqCV/Sta. Pert_TrqCV/Sta. Pert_TrqCV/St	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target, Rie, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKARequest, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf LKARequest, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf LKAState, State, enum target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. JKASstate, State, enum target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. JMinSecureVehicleSpeed, Kph. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. JMinSecureVehicleSpeed, Kph. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. JMinSecureVehicleSpeed, Kph. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. PossevEnable. Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. PossevEnable. Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. PossevEnable. Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. Postariejhable, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. Postariejhable, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. ShiftLeverIsinReverse, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TyoScAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, TrqOviSta. Perf. Lorevalue 1 target, TrqOviSta. Perf. CoVPFound, Cnt. Igo: value 1 target, TrqOviSta. Perf. Lorevision. HwDeg. 132 value 1 target, TrqOviSta. Perf	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target, Rie, Inst. Ap., TrqOviSta. TrqOviSta. Perf LKARequest, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf LKARequest, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf LKAState, State, enum target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. JKASstate, State, enum target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. JMinSecureVehicleSpeed, Kph. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. JMinSecureVehicleSpeed, Kph. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. JMinSecureVehicleSpeed, Kph. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. PossevEnable. Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. PossevEnable. Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. PossevEnable. Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. Postariejhable, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. Postariejhable, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. ShiftLeverIsinReverse, Cnt. Igo: target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TyoScAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, Reinst, Ap., TrqOviSta. TrqOviSta. Perf. TrqOscAmp, Minthm. 132 target, TrqOviSta. Perf. Lorevalue 1 target, TrqOviSta. Perf. CoVPFound, Cnt. Igo: value 1 target, TrqOviSta. Perf. Lorevision. HwDeg. 132 value 1 target, TrqOviSta. Perf	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target TrqOvlSta Per1 LKAFault Cnt lgc
target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert LKARequest_Cnt_lgc target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert LKAState State enum target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert LKAState State enum target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert LKAState State enum target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert MassecurevehricleSpeed_Kph_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert MinssecurevehricleSpeed_Kph_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert PossroviehrideSpeed_Kph_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert Possroviehvangle_HwDeg_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert TrqOscAmp_Mrthin_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert TrqOscAmp_Mrthin_f32 target, Rie, Inst. Ap_TrqOviSta. TrqOviSta. Pert TrqOscAmp_Mrthin_f32 target, TrqOviSta. Pert ApaRecoverableFaults, Crt. Igc. value 1 target, TrqOviSta. Pert ApaRecoverableFaults, Crt. Igc. value 1 target, TrqOviSta. Pert CoWPostion HwDeg_f32 value 1 target, TrqOviSta. Pert GMOSH_APAMIgEnable_Crt. Igc. value 1 target, TrqOviSta. Pert GMOSH_APAMIgEnable_Crt. Igc. value 1 targe		
target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert LKAState_State_enum target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert MaxSecureVehicleSpeed_Kph_f32 target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert MaxSecureVehicleSpeed_Kph_f32 target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert MaxSecureVehicleSpeed_Kph_f32 target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert PosSravEnable_Cnt. Igo: target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert PosSravEnable_Cnt. Igo: target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert PosSravEnable_Cnt. Igo: target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert PosTrajEnable_Cnt. Igo: target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert TrqOsCenable_Cnt. Igo: target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert TrqOsCenable_Cnt. Igo: target, Rel. Inst. Ap_TrqOviSta. TrqOviSta. Pert TrqOsCenable_Cnt. Igo: target, TrqOviSta. Pert APARecoverableFaults_Cnt. Igo: value target, TrqOviSta. Pert APARecoverableFaults_Cnt. Igo: value target, TrqOviSta. Pert CoVPoont, Cnt. Igo: value target, TrqOviSta. Pert CoVPoont, Cnt. Igo: value target, TrqOviSta. Pert ESCPault, Cnt. Igo: value target, TrqOviSta. Pert ESCPault, Cnt. Igo: value target, TrqOviSta. Pert ESCPault, Cnt. Igo: value target, TrqOviSta. Pert GWOSH. PARMigEnable_Cnt. Igo: value target, TrqOviSta. Pert GWOSH. PARMigEnable_Cnt. Igo: value target, TrqOviSta. Pert LKAREDE, Cnt. Igo: value target, TrqOviSta. Pert LKARED, TrqOviSta. Pert LKARED, TrqOviSta. Pert LKARED, TrqOviSta. Pert LKARED, TrqOviSta. P		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_DosSrvehable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvehable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvehable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrvehable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrqJAnqRequest_Hwbog_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrqJAnqRequest_Hwbog_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SvARTrqJAnqRequest_Hwbog_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MinNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscFreq_Hz_f32 target_TrqOviSta_Pert_SvARTrqOviSta_Pert_Svalte_Unit_Dept_TrqOscFreq_Hz_f32 target_TrqOviSta_Pert_ApARnonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_CVWPostton_Hwbog_f32.value target_TrqOviSta_Pert_CVWPostton_Hwbog_f32.value target_TrqOviSta_Pert_CWPostton_Hwbog_f32.value target_TrqOviSta_Pert_ExcFreq_Unit_Cnt_Igc_value target_TrqOviSta_Pert_ExcRequest_Cnt_Igc_value 1 target_TrqOviSta_Pert_ExcRequest_Cnt_Igc_value 1 target_TrqOviSta_Pert_ExcRequest_Cnt_Igc_value 1 target_TrqOviSta_Pert_Hwbrque_Hwbn_f32.value 1 target_TrqOviSta_Pert_Hybrque_Hwbn_f32.value 1 target_TrqOviSta_Pert_Hybrque_Hybrque_Hybrque_Hybrque_Hybrque_Hybrque_Hybrque_Hybrque_Hy		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_MinSecureVehicleSpeed_Kph_G2 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_DosServEnable_Cnt_Igc target_TrqOviSta_Perf_DosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_DosSrovEnable_Cnt_Igc target_TrqOviSta_Perf_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_DosTrajHwAngle_HwDeg_132 target_TrqOviSta_TrqOviSta_Perf_DosTrajHwAngle_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_DosTrajHwAngle_HwDeg_132 target_TrqOviSta_Perf_DosTrajHwAngle_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_ShiffLevertsinReverse_Cnt_Igc target_TrqOviSta_Perf_DosTrajHwAngle_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOsCwTap_MitNm_162 target_TrqOviSta_Perf_ShiffLevertsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOsCwTap_MitNm_162 target_TrqOviSta_Perf_TrqOviSta_Perf_TrqOsCwTap_MitNm_162 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOsCwTap_MitNm_162 target_TrqOviSta_Perf_TrqOviSta_Perf_TrqOsCwTap_MitNm_162 target_TrqOviSta_Perf_Ap_AnonRecoverableFaults_Cnt_Igc_value 1 target_TrqOviSta_Perf_ApARequest_Cnt_Igc_value 1 target_TrqOviSta_Perf_CowPoond_Cnt_Igc_value 1 target_TrqOviSta_Perf_Excis_Inited_Cnt_Igc_value 1 target_TrqOviSta_Perf_GMOSH_ExcMgEnable_Cnt_Igc_value 1 target_TrqOviSta_Perf_Mosh_Revea		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTarjEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTarjEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTarjEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTarjEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgdAngRequest_InvDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgdAngRequest_InvDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SMIfteverIsinReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCanap_MtrNn_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCanap_MtrNn_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCfreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCfreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCfreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc_value target_TrqOvlSta_Per1_APARequest_Cnt_lgc_value target_TrqOvlSta_Per1_APARequest_Cnt_lgc_value target_TrqOvlSta_Per1_CWPound_Cnt_lgc_value target_TrqOvlSta_Per1_CWPound_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRaute_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRaute_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRaute_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRaute_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRaute_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_GMOSH_LEXAMgenable_Cnt_lgc_value target_TrqOvlSta_Per1_ENGROSH_LEXAMgenable_Cnt_lgc_value target_TrqOvlSta_Per1_MARGenable_Cnt_lgc_value target_TrqOvlSta_Per1_HxAFaut_Cnt_lgc_value target_TrqOvlSta_Per1_HxAFaut_Cnt_lgc_value target_TrqOvlSta_Per1_HxAFaut_Cnt_lgc_value target_TrqOvlSta_Per1_HxAFaut_Cnt_lgc_value target_TrqOvlSta_Per1_HxAFaut_Cnt_lgc_value target_TrqOvlSta_Per1_HxAFaut_Cnt_lgc_value target_TrqOvlSta_Per1_HxAFaut_Cnt_lgc_value target_TrqOvlSta_Per1_HxAFaut_Cnt_lgc_value target_TrqOvlSta_Per1_MxAEquest_Cnt_lgc_value target_TrqOvlS		
target_Rie_Inst_Ap_TrqOvlSta_Pert_PosSrvoHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SwiRtTgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ShiftLeverisInReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsAcm_Pinthm_f32 target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsAcm_Pinthm_f32 target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsAcm_Pinthm_f32 target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsAcm_Pinthm_f32 target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsAcmed_Cnt_lgc target_Rie_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsAcmed_Cnt_lgc target_TrqOvlSta_Pert_ApRangecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Pert_ApRangecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Pert_ApRangecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Pert_CwFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_CwFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_Expert_CwFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_Expert_CwFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_Expert_CwFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_Expert_Expert_Cut_lgc.value 1 target_TrqOvlSta_Pert_Expert_CwFound_Cnt_lgc.value 1 target_TrqOvlSta_Pert_Expert_Cmt_lgc.value 1 target_TrqOvlSta_Pert_GMOSH_Expert_Glgc.value 1 target_TrqOvlSta_Pert_GMOSH_Expert_Glgc.value 1 target_TrqOvlSta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Pert_HapticRequest_Cnt_gc.value 1 target_TrqOvlSta_Pert_MisAgenuerverhiceReps_exch_py_f3z.va		
target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscFerq_Hz_f32 target_Rie_Inst_Ap_TrqOviSta_Pert_Ap_NonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_Ap_NonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_Ap_NonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_Ap_NonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_Ap_NonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_CWFound_Cnt_Igc_value target_TrqOviSta_Pert_Ap_NonRecoverableFaults_Cnt_Igc_value target_TrqOviSta_Pert_CWFound_Cnt_Igc_value target_TrqOviSta_Pert_CWFound_Cnt_Igc_value target_TrqOviSta_Pert_CWFound_Cnt_Igc_value target_TrqOviSta_Pert_CWFound_Cnt_Igc_value target_TrqOviSta_Pert_ESCFault_Cnt_Igc_value target_TrqOviSta_Pert_ESCFault_Cnt_Igc_value target_TrqOviSta_Pert_ESCRequest_Cnt_Igc_value target_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_Igc_value target_TrqOviSta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc_value target_TrqOviSta_Pert_HybriRequest_Cnt_Igc_value target_TrqOviSta_Pert_HybriRequest_Cnt_Igc_value target_TrqOviSta_Pert_HybriRequest_Cnt_Igc_value target_TrqOviSta_Pert_HybriRequest_Cnt_Igc_value target_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32_value target_TrqOviSta_Pert_Mixer_ExceuverVehicleSpeed_Kph_f32_value target_TrqOviSta_Pert_Mixer_ExceuverVehicleSpeed_Kph_f32_value target_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc_value target_TrqOviSta_Pert_Mixer_ExceuverVehicleSpeed_Kph_f32_value target_TrqOviSta_Pert_Mixer_ExceuverVehicleSpeed_Kph_f32_value		
target_Rie_Inst_Ap_TrqOviSta_Per1_SVARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_SVARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_SVARTrgtAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_SVIRtEversinReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_SVIRtEversinReverse_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOvSta_Per1_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_TrqOviSta_Per1_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Per1_TrqOviSta_	· · · · · · · · · · · · · · ·	
target_Rie_Inst_Ap_TrqOviSta_Per1_SWARTrgtAngRequest_Hwbeg_f3 target_Rie_Inst_Ap_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtnMm_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtnMm_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtnMm_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtnMm_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOscAmp_MtnMm_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOviSta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOviSta_Per1_HwTorque_Hwbm_f32_value 1 target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kpf_f32_value 1 target_TrqOviSta_Per1_MindoviSta_Per1_MindoviSta_Per1_MindoviSta_Per1_MindoviSta_Per1_		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscEnable_Cnt_Igc target_TrqOvISta_Perl_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_ExcRequest_Cnt_Igc.value target_TrqOvISta_Perl_ExcRequest_Cnt_Igc.value target_TrqOvISta_Perl_ExcRequest_Cnt_Igc.value target_TrqOvISta_Perl_ExcRequest_Cnt_Igc.value target_TrqOvISta_Perl_ExcRequest_Cnt_Igc.value target_TrqOvISta_Perl_GMOSH_ExcMigEnable_Cnt_Igc.value target_TrqOvISta_Perl_GMOSH_ExcMigEnable_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HwTorque_HwNm_f3z.value target_TrqOvISta_Perl_HwTorque_HwNm_f3z.value target_TrqOvISta_Perl_MARequest_Cnt_Igc.value target_TrqOvISta_Perl_PosTrajEnable_Cnt_Igc.value target_TrqOvISta_Perl_PosTrajEnable_Cnt_Igc.value target_TrqO		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_TrqOvISta_Pert_Ap_TrqOvISta_Pert_TrqOscEnable_Cnt_Igc target_TrqOvISta_Pert_Ap_TrqOvISta_Pert_TrqOscEnable_Cnt_Igc.value target_TrqOvISta_Pert_Ap_ArecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_Ap_ArecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_Ap_ArecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_Ap_ArecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_CWPound_Cnt_Igc.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ApAMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ApAMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_ApAMfgEnable_Cnt_Igc.value targe		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANenoRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MisSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value		
target_Rte_Inst_Ap_TrqOvISta_Pert_TrqOscFreq_Hz_f32 target_TrqOvISta_Pert_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Pert_APARequest_Cnt_Igc.value target_TrqOvISta_Pert_APARequest_Cnt_Igc.value target_TrqOvISta_Pert_CCWPound_Cnt_Igc.value target_TrqOvISta_Pert_CCWPosition_HwDeg_f32.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_CWPosition_HwDeg_f32.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCFault_Cnt_Igc.value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc.value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_HapticRequest_Cnt_Igc.value target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value target_TrqOvISta_Pert_LKAFault_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_LKARequest_Cnt_Igc.value target_TrqOvISta_Pert_IminSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Pert_PosTrajEnable_Cnt_Igc.value		
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvISta_Per1_APARecoverableCovInc_value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCR_pault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCR_pault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCR_pault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCR_pault_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1	•	
target_TrqOvISta_Perl_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvISta_Perl_APARequest_Cnt_lgc.value 1 target_TrqOvISta_Perl_CCWPosition_HwDeg_f32.value -980.51001 target_TrqOvISta_Perl_CCWPosition_HwDeg_f32.value -980.51001 target_TrqOvISta_Perl_CWPosition_HwDeg_f32.value 1026.90002 target_TrqOvISta_Perl_CWPosition_HwDeg_f32.value 1026.90002 target_TrqOvISta_Perl_ESCFault_Cnt_lgc.value 1026.90002 target_TrqOvISta_Perl_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Perl_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Perl_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Perl_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Perl_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Perl_HwTorque_HwNm_f32.value 1 target_TrqOvISta_Perl_HwTorque_HwNm_f32.value 1 target_TrqOvISta_Perl_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Perl_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Perl_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Perl_PosTrajEnable_Cnt_lgc.value 1	0	
target_TrqOvISta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value -980.51001 target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value -980.51001 target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value 1026.90002 target_TrqOvISta_Per1_CSCFault_Cnt_lgc.value 1026.90002 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 133.5 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value -980.51001 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 1026.90002 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 6.0999999 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 18.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1026.90002 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 11 target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value 11 target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value 11 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 11 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 11 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 11 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 12 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 11 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 1026.90002 113.5 126.90002 127.9000000000000000000000000000000000000	· - · · -	-980.51001
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1026.90002
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvISta_Per1_HwTorque_HwNm_32.value 6.0999999 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 1arget_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 1arget_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.099999
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 113.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 98.8000031 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
		1





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-7.80000019		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	21465	21465 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	23200	23200 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	21465	21465 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.5	8.5 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.25	0.25 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	29.3999996	29.3999996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.142 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	24470
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	24470
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	24470
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	24182
TrqOvlSta_Haptictime_Sec_M_f32	24470
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	8.85000038
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0328000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.815999985
k_APAMaxHwTrq_HwNm_f32	3.03999996
k_APAMaxVehSpd_Kph_f32	46.7999992
k_ESCMaxVehSpd_Kph_f32	57.7000008
k_HapticAmplitude_MtrNm_f32	0.270000011
k_HapticDuration_Sec_f32	0.224999994
k_HapticFreq_Hz_f32	30.1000004
k_HapticReacttime_Sec_f32	0.60799998
k_LKAMaxVehSpd_Kph_f32	72.1999969
k_LKAMinVehSpd_Kph_f32	66.5
k_MaxHapticReqTime_Sec_f32	0.735000014
k_SWARLimiter_HwDeg_f32	35.5
k_ShiftLevRevTime_Sec_f32	0.104999997
k_StandstillThresh_Kph_f32	0.959999979
k_StandstillTime_Sec_f32	0.0970000029
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_Igc	. 122	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_CWPosition_HwDeg target_TrqOvlSta_Per1_ESCFault_Cnt_lgc]_132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_I	ac	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun	target TrqOvlSta Per1 GMOSHOscillateSta		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	JC	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_eni		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv target_TrqOvlSta_Per1_SWARTrgtAngRequ		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvISta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target TrgOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-940.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1031.83997		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	0 6.5		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvISta Per1 LKAInhibit Cnt Igc.value	0		
target TrgOvlSta Per1 LKARequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	118.400002		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	•
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	24182	24182 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	8.85000038	8.85000038 ± 0.000009	•
		0 ± 1	•
TrqOvISta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 3	3 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0		

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	22.1000004	22.1000004 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.270000011	0.270000011 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	30.1000004	30.1000004 ± 0.0009765625	~

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

est Step 2.143 (Repeat Count = 1)	Innut Value
lame	Input Value
tte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	4
qOvlSta_ESCState_State_M_enum	2
qOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	27475.5293
qOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	27475.5293
qOvlSta_ElapsedStandstillTime_Sec_M_f32	27475.5293
qOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	25164
qOvlSta_Haptictime_Sec_M_f32	27475.5293
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.19999981
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0350000001
rqOvlSta_LKAFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_u08	4
qOvlSta_LKAState_State_M_enum	4
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	23.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
APAMaxHwTrqTime_Sec_f32	0.827000022
APAMaxHwTrq HwNm f32	3.11999989
APAMaxVehSpd Kph f32	48.5999985
ESCMaxVehSpd Kph f32	63.2000008
_LapticAmplitude_MtrNm_f32	0.289999992
HapticDuration Sec f32	0.239999995
HapticFreq Hz f32	30.7999992
HapticReacttime Sec f32	0.624000013
_HapticReactiffle_Sec_132 LKAMaxVehSpd Kph f32	74.0999985
_LKAMinVehSpd_Kph_f32	68.25
_MaxHapticReqTime_Sec_f32	0.75
_SWARLimiter_HwDeg_f32	35.7999992
_ShiftLevRevTime_Sec_f32	0.108999997
_StandstillThresh_Kph_f32	1
_StandstillTime_Sec_f32	0.101000004
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lq	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur	
rget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Nome	Inmut Value		
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-900.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1036.78003		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	6.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvISta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	123.300003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	108.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-4		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	rtoduit
TrqOvISta_ESCState_State_M_enum	3	3	·
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	27475.5293	27475.5293 ± 1	
TrqOvISta ElapsedShiftLevRevTime Sec M f32	27475.5293	27475.5293 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
	0	0	
TrqOvlSta_HapticState_State_M_enum TrqOvlSta Hapticdur Cnt M u16		25164 ± 1	
	25164		-
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293	27475.5293 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.11950016	9.11950016 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	V
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	V
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	•
TrqOvlSta_LKAState_State_M_enum	4	4	V
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.180000007	0.180000007 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	V
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.180000007	0.180000007 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.289999992	0.289999992 ± 0.0009765625	V
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	30.7999992	30.7999992 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V





Test Step 2.144 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	30480
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	30480
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	30480
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	26146
TrqOvlSta_Haptictime_Sec_M_f32	30480
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	9.55000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0372000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.838
k_APAMaxHwTrq_HwNm_f32	3.20000005
k_APAMaxVehSpd_Kph_f32	50.4000015 68.6999969
k_ESCMaxVehSpd_Kph_f32	0.310000002
k_HapticDuration_Sec_f32	0.254999995
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	31.5
k HapticReacttime Sec f32	0.671999991
k LKAMaxVehSpd Kph f32	79.8000031
k_LKAMinVehSpd_Kph_f32	73.5
k_MaxHapticReqTime_Sec_f32	0.764999986
k_SWARLimiter_HwDeg_f32	36.099985
k ShiftLevRevTime Sec f32	0.112999998
k_StandstillThresh_Kph_f32	1.0399996
k_StandstillTime_Sec_f32	0.104999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target TrqOvlSta Per1 LKARequest Cnt lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKAState State enum	
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target Rte Inst Ap TrqOvIsta.TrqOvIsta Per I_miinSecurevenicieSpeed_kpii_isz target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosServEnable Cnt Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cht_Igc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target TrgOvlSta Per1 TrgOscAmp MtrNm f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	, , , , , , , , , , , , , , , , , , , ,
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
	0
$target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value$	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-860.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1041.71997		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	7.30000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	113.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-2.0999999		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	26146	26146 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.55000019	9.55000019 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	24.2999992	24.2999992 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	24.2999992	24.2999992 ± 0.0009765625	•
		0.040000000 . 0.0000705005	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.310000002	0.310000002 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.310000002	0.310000002 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.145 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	33485
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	33485
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	33485
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	27128
TrqOvlSta_Haptictime_Sec_M_f32	33485
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.89999962
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0394000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	25.3999996

2015-02-04, 14:05:40+0530



Name Input Value TrqOvlSta_ReadytoPulse_Cnt_M_lgc 1 k_APAIncludeHaptic_Cnt_lgc 0 k_APAMaxHwTrqTime_Sec_f32 0.848999977	
k_APAIncludeHaptic_Cnt_lgc 0	
k_APAMaxHwTrqTime_Sec_f32 0.848999977	
k_APAMaxHwTrq_HwNm_f32 3.27999997	
k APAMaxVehSpd Kph f32 52.2000008	
k_ESCMaxVehSpd_Kph_f32 74.1999969	
k HapticAmplitude MtrNm f32 0.330000013	
k_HapticDuration_Sec_f32 0.270000011	
k_HapticFreq_Hz_f32 32.2000008	
k_HapticReacttime_Sec_f32 0.688000023	
k_LKAMaxVehSpd_Kph_f32 81.6999969	
k LKAMinVehSpd Kph f32 75.25	
k_MaxHapticReqTime_Sec_f32	
k SWARLimiter HwDeg f32 36.4000015	
k_StandstillThresh_Kph_f32 1.08000004 k_StandstillTime_Sec_f32 0.108999997	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARequest_Cnt_Igc target_TrqOvlSta_Per1_APARequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_APAState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_Igc target_TrqOvlSta_Per1_CWFound_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_state_enum target_TrqOvlSta_Per1_ESCState_state_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_HwTorque_HwNm_f32	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Prq_LKAInhibit_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Prq_UlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Prq_LKAState_State_enum target_TrqOvlSta_Per1_LKAState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvISta_FrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -820.909973	
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 1046.66003	
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 7.69999981	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1	
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5	
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 118.400002	
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.100000001	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-0.200000003		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	33485	33485 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	33485	33485	•
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	27128	27128 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	33485	33485 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	9.89999962	9.89999962 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.100000001	0.100000001 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.100000001	0.100000001 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.330000013	0.330000013 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.2000008	32.2000008 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.146 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	36490.3516
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	36490.3516
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	36490.3516
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	28110
TrqOvlSta_Haptictime_Sec_M_f32	36490.3516
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.10000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0416000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	26.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.860000014
k_APAMaxHwTrq_HwNm_f32	3.3599999
k_APAMaxVehSpd_Kph_f32	54
k_ESCMaxVehSpd_Kph_f32	79.6999969
k_HapticAmplitude_MtrNm_f32	0.34999994
k_HapticDuration_Sec_f32	0.284999996
k_HapticFreq_Hz_f32	32.9000015
k_HapticReacttime_Sec_f32	0.703999996
k_LKAMaxVehSpd_Kph_f32	83.5999985
k_LKAMinVehSpd_Kph_f32	77
k_MaxHapticReqTime_Sec_f32	0.795000017
k_SWARLimiter_HwDeg_f32	36.7000008
k_ShiftLevRevTime_Sec_f32	0.120999999
k_StandstillThresh_Kph_f32	1.12
k_StandstillTime_Sec_f32	0.112999998
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (Ap_TrqOvlSta$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ılts_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg	ус	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEna		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f3	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hw		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_I	igc .	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0		
target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value	0		
target_TrqOvISta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value	0		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-781.01001		
target TrqOvISta Per1 CWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1051.59998		
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target TrqOvISta Per1 ESCRequest Cnt Igc.value	0		
target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlota_renGMOSH_LKAMfgEnable_Cnt_igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvISta Per1 HwTorque HwNm f32.value	8.10000038		
target TrqOvISta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value	0		
target TrqOvISta Per1 LKARequest Cnt Igc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	138		
target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	123.300003		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f32.value	1.70000005		
target TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	1	1	sul
TrqOvISta_AFAState_State_M_enum TrqOvISta_ESCState_State_M_enum	4	4	
TrqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrqOvISta_ElapsedAPAMaxrwTrqTiffle_Sec_M_I32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_I32	0	0 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_LapsedStantstill*Inne_Set_ivi_i32 TrqOvISta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	28110	28110 ± 1	
TrgOvlSta Haptictime Sec M f32	0	0±1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.39120007	1.39119995 ± 0.000009	
goom imiorquoov imitili IVI Ott.OV Old IOE	0	0 ± 1	
		0 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc		0 + 1	
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	2 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0		•

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	26.5	26.5 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.349999994	0.349999994 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.9000015	32.9000015 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
	·
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	39495
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	39495
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	39495
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	29092
TrqOvlSta_Haptictime_Sec_M_f32	39495
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.20000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	27.6000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.870999992
k APAMaxHwTrg HwNm f32	3.44000006
k_APAMaxVehSpd_Kph_f32	55.7999992
k ESCMaxVehSpd Kph f32	85.1999969
k_HapticAmplitude_MtrNm_f32	0.370000005
k_HapticDuration_Sec_f32	0.300000012
k_HapticFreq_Hz_f32	33.599985
	0.720000029
k_HapticReacttime_Sec_f32	85.5
k_LKAMaxVehSpd_Kph_f32	
k_LKAMinVehSpd_Kph_f32	78.75
k_MaxHapticReqTime_Sec_f32	0.810000002
k_SWARLimiter_HwDeg_f32	37
k_ShiftLevRevTime_Sec_f32	0.125
k_StandstillThresh_Kph_f32	1.15999997
k_StandstillTime_Sec_f32	0.116999999
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetecte	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable C	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH ESCMfgEnable C	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_C	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target TrqOvlSta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cit_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
argot_Nto_inot_Ap_11q0viota.11q0viota_Fei1_Hw10ique_HwNiii_132	raigot_11qOviota_r el 1_11w1olque_11wiviii_152

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target TrqOvlSta Per1 APANonRecoverableFaults Cnt lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-741.109985		
target TrgOvlSta Per1 CWFound Cnt lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1056.54004		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.20000005		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.5999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	*
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	39495	39495 ± 1	V
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	Y
TrqOvlSta_HapticState_State_M_enum	1	1	Y
TrqOvISta_Hapticdur_Cnt_M_u16	29092	29092 ± 1	· ·
TrqOvlSta_Haptictime_Sec_M_f32	39495	39495 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.20000005	1.20000005 ± 0.000009 1 ± 1	-
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrgOvlSta LKAPermFault Cnt M u08			
TraOvISta LKAState State M. enum	3	3 ± 1	•
TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	3	3 ± 1 3	· ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3 3 0.119999997	3 ± 1 3 0.119999997 ± 0.0009765625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	3 3 0.119999997 1	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	3 3 0.119999997	3 ± 1 3 0.119999997 ± 0.0009765625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	3 0.119999997 1 0 3	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	3 0.119999997 1 0	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3 3 0.119999997 1 0 3 3	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	3 3 0.119999997 1 0 3 3	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 3 0.119999997 1 0 3 3 1	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3 3 0.119999997 1 0 3 3 1 1 3	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3 3 0.119999997 1 0 3 3 1 1 3 1 0.119999997	3 ± 1 3 0.119999997 ± 0.0009765625 1 ± 1 0 ± 1 3 3 1 1 0.119999997 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.148 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrgOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	42500
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	42500
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	42500
TrqOvlSta_HapticState_State_M_enum	2
TrqOvISta_Haptictime_See M #22	30074 42500
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1,2999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0460000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvISta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1 0.882000029
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	3.5199998
k APAMaxVehSpd Kph f32	57.5999985
k_ESCMaxVehSpd_Kph_f32	90.6999969
k_HapticAmplitude_MtrNm_f32	0.38999986
k_HapticDuration_Sec_f32	0.314999998
k_HapticFreq_Hz_f32	34.2999992
k_HapticReacttime_Sec_f32	0.736000001
k_LKAMaxVehSpd_Kph_f32	87.4000015
k_LKAMinVehSpd_Kph_f32	80.5
k_MaxHapticReqTime_Sec_f32	0.824999988
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	37.2999992 0.128999993
k StandstillThresh Kph f32	1.20000005
k StandstillTime Sec f32	0.120999999
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_enum_results and the property of the propert$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_inst_Ap_irqovista.irqovista_Peri_SwaRirgiangRequest_Hwbeg_rstarget_Rte_inst_Ap_TrqoviSta.TrqoviSta_Peri_ShiftLeverIsInReverse_Cnt_lgc	target_IrqOvlSta_Per1_SwARTrgtAngRequest_Hwbeg_132 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530



TIQONSIA_T ELT			1 - 10 - 10
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-701.210022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1061.47998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	133.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	5.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	✓
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	30074	30074 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.2999995	1.29999995 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008	28.7000008 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value		20 7000000 + 0 0000765625	✓
	28.7000008	28.7000008 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	28.7000008 0.389999986	0.389999986 ± 0.000976562	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc.value			

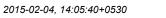
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.149 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	45505
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45505
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	45505
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	31056
TrqOvlSta_Haptictime_Sec_M_f32	45505
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.3999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0482000001
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992

2015-02-04, 14:05:40+0530



TIQOVISIA_PELI	
Name	Input Value
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.893000007
_APAMaxHwTrq_HwNm_f32	3.5999999
_APAMaxVehSpd_Kph_f32	59.4000015
ESCMaxVehSpd_Kph_f32	96.1999969
_HapticAmplitude_MtrNm_f32	0.40999996
_HapticDuration_Sec_f32	0.330000013
_HapticFreq_Hz_f32	35
_HapticReacttime_Sec_f32	0.751999974
_LKAMaxVehSpd_Kph_f32	89.3000031
_LKAMinVehSpd_Kph_f32	82.25
_MaxHapticReqTime_Sec_f32	0.839999974 37.5999985
_SWARLimiter_HwDeg_f32 _ShiftLevRevTime_Sec_f32	0.133000001
StandstillThresh Kph f32	1.24000001
StandstillTime_Sec_f32	0.125
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_ic	
arget_Rte_Inst_Ap_TrqOviota.TrqOviota_Fer1_AFARecoverableFaults_Cnt_lgc	target_TrqOviota_ren_ArAnoninecoverableFaults_Cnt_lgc
riget Rte Inst Ap TrqOviSta.TrqOviSta Per1 APARequest Cnt Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
riget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_State_enum	target TrqOvISta Per1 APAState State enum
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_FeT1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_FeT1_CCWF0sition_FiwDeg_i52 arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_CWF0und_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_FeT1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_FeT_EVYF0sition_TiwDeg_i32	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_lgc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target TrgOvlSta Per1 LKARequest Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-661.309998
arget_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1066.42004
arget_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
rrget_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
rget_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.79999995
arget_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	152.699997
arget_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	138
areat TraCodicta Dard DardTraiEnable Cat Ira value	1
arget_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	7.4000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	45505	45505 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	31056	31056 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	45505	45505 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.41927993	1.41928005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.140000001	0.140000001 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.140000001	0.140000001 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.409999996	0.409999996 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	35	35 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 2.150 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	48510.3398
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	48510.3398
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	48510.3398
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	32038
TrqOvlSta_Haptictime_Sec_M_f32	48510.3398
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0504000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1.
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	30.8999996
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.903999984
k_APAMaxHwTrq_HwNm_f32	3.68000007
k_APAMaxVehSpd_Kph_f32	61.2000008
k_ESCMaxVehSpd_Kph_f32	101.699997
k_HapticAmplitude_MtrNm_f32	0.430000007
k_HapticDuration_Sec_f32	0.344999999
k_HapticFreq_Hz_f32	35.7000008
k_HapticReacttime_Sec_f32	0.768000007
k_LKAMaxVehSpd_Kph_f32	91.1999969
k_LKAMinVehSpd_Kph_f32	84
k_MaxHapticReqTime_Sec_f32	0.855000019
k_SWARLimiter_HwDeg_f32	37.9000015
k_ShiftLevRevTime_Sec_f32	0.136999995
k_StandstillThresh_Kph_f32	1.27999997
k_StandstillTime_Sec_f32	0.128999993
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	;	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH LKAMfgEnable Cnt lgc	target TrqOvISta Per1 GMOSH LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-621.409973		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target TrgOvlSta Per1 CWPosition HwDeg f32.value	1071.35999		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	142.899994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	9.30000019		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0	le	- ·
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	0	0	V
TrqOvlSta_ESCState_State_M_enum	4	4	V
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0 48510.3398	0 ± 1 48510.3398	
TrqOvISta_ElapsedStandstill Time_Sec_M_T32 TrqOvISta_HapticState_State_M_enum	0	0	-
TrqOvISta_Hapticdur_Cnt_M_u16	32038	32038 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_Haptictime_Sec_M_i52 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.5	1.5 ± 0.000009	
TrqOviSta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0±1	-
TrqOvISta_LKAPermFault_Cnt_M_u08	1	1±1	-
TrqOvISta_LKAState_State_M_enum	4	4	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	30.8999996	30.8999996 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~

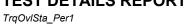
2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	30.8999996	30.8999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.430000007	0.430000007 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	35.7000008	35.7000008 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.151 (Repeat Count = 1)	I WI
lame	Input Value
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	2
rqOvlSta_ESCState_State_M_enum	1
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	51515
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51515
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	51515
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	33020
rqOvlSta_Haptictime_Sec_M_f32	51515
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	32
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic Cnt Igc	0
APAMaxHwTrqTime Sec f32	0.915000021
APAMaxHwTrg HwNm f32	3.75999999
_APAMaxVehSpd_Kph_f32	63
_ESCMaxVehSpd_Kph_f32	0
_Eoomaxveriope_Npri_roz _HapticAmplitude_MtrNm_f32	0.449999988
HapticDuration Sec f32	0.360000014
HapticFreq_Hz_f32	36.4000015
HapticReacttime_Sec_f32	0.783999979
_KAMaxVehSpd_Kph_f32	93.0999985
_LKAMinVehSpd_Kph_f32	85.75
- · - · - · -	0.870000005
_MaxHapticReqTime_Sec_f32 SWAPI imites_HwDog_f32	38.2000008
_SWARLimiter_HwDeg_f32 ShiftlevPayTime_Sec_f32	
_ShiftLevRevTime_Sec_f32	0.141000003
_StandstillThresh_Kph_f32	1.32000005
_StandstillTime_Sec_f32	0.133000001
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target TrqOvlSta Per1 HwTorque HwNm f32





· -			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f	32 target_TrqOvlSta_Per1_MaxSecureVehicle	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3defines and the start of the start $	target_TrqOvlSta_Per1_MinSecureVehicleS	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_l	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_F		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReve		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-581.51001		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1076.30005 0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value target TrqOvlSta_Per1_ESCRequest Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Off_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.4000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrgOvlSta ESCState State M enum	0	0	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51515	51515 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	33020	33020 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	51515	51515 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002	1.60000002 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	*
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.449999988	0.449999988 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	36.4000015	36.4000015 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 ChecknointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.152 (Repeat Count = 1)	la ava
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	54520
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	54520
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	54520
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	34002
TrqOvlSta_Haptictime_Sec_M_f32	54520
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	33.099985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.925999999
k_APAMaxHwTrq_HwNm_f32	3.83999991
k_APAMaxVehSpd_Kph_f32	64.8000031
k_ESCMaxVehSpd_Kph_f32	180
k_HapticDuration_Sec_f32	0.469999999
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	0.375 37,099985
k_HapticReacttime_Sec_f32	0.800000012
k_LKAMaxVehSpd_Kph_f32	95
k_LKAMinVehSpd_Kph_f32	87.5
k_MaxHapticReqTime_Sec_f32	0.88499999
k_SWARLimiter_HwDeg_f32	38.5
k_ShiftLevRevTime_Sec_f32	0.144999996
k_StandstillThresh_Kph_f32	1.36000001
k_StandstillTime_Sec_f32	0.136999995
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target TrqOvISta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_MinSecurevenicleSpeed_Rpn_132 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_lgc	target_trqOvlSta_Per1_MinSecurevenicleSpeed_kpn_132 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	

2015-02-04, 14:05:40+0530



TIQOVISIA_T ELT		(- 10-10
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-541.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1081.23999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.70000005		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	167.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	152.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	13.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrgOvlSta ESCState State M enum	0	0	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	✓
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	-
TrqOvlSta HapticState State M enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	34002	34002 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.75480008	1.75479996 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrgOvlSta LKAPermFault Cnt M lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	33.0999985	33.0999985 ± 0.0009765625	-
TrqOvlSta ReadytoPulse Cnt M Igc	0	0 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	33.0999985	33.0999985 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.469999999	0.469999999 ± 0.0009765625	✓
toward Toward Park Toward Toward Continue			
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.153 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	57525.3203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	57525.3203
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	57525.3203
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	34984
TrqOvlSta_Haptictime_Sec_M_f32	57525.3203
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	34.2000008





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.936999977
k_APAMaxHwTrq_HwNm_f32	3.92000008
k_APAMaxVehSpd_Kph_f32	66.5999985
k_ESCMaxVehSpd_Kph_f32	100.5
k_HapticAmplitude_MtrNm_f32	0.49000001
k_HapticDuration_Sec_f32	0.389999986 37.7999992
k_HapticFreq_Hz_f32 k HapticReacttime Sec f32	0.815999985
k_LKAMaxVehSpd_Kph_f32	96.9000015
k_LKAMinVehSpd_Kph_f32	89.25
k_MaxHapticReqTime_Sec_f32	0.89999976
k_SWARLimiter_HwDeg_f32	38.7999992
k_ShiftLevRevTime_Sec_f32	0.149000004
k_StandstillThresh_Kph_f32	1.3999998
k_StandstillTime_Sec_f32	0.141000003
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARecoverableFaults Cnt Igc	target TrqOvlSta_Per1_APARotiRecoverableFaults_Crit_igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-501.709991
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 4006 40005
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	1086.18005 0
target_IrqOviSta_Peri_ESCFault_Cnt_igc.value target TrqOviSta_Peri_ESCIsLimited Cnt_igc.value	1
target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	172.300003
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.180000007



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	0	0	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	57525.3203	57525.3203 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	34984	34984 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	57525.3203	57525.3203 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.79999995	1.79999995 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.180000007	0.180000007 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.180000007	0.180000007 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.49000001	0.49000001 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	37.7999992	37.7999992 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.154 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	60530
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	60530
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	60530
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	35966
TrqOvlSta_Haptictime_Sec_M_f32	60530
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.89999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0592
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	35.2999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.948000014
k_APAMaxHwTrq_HwNm_f32	4
k_APAMaxVehSpd_Kph_f32	68.4000015
k_ESCMaxVehSpd_Kph_f32	110.199997
k_HapticAmplitude_MtrNm_f32	0.50999999
k_HapticDuration_Sec_f32	0.405000001
k_HapticFreq_Hz_f32	38.5
k_HapticReacttime_Sec_f32	0.832000017
k_LKAMaxVehSpd_Kph_f32	98.8000031
k_LKAMinVehSpd_Kph_f32	91
k_MaxHapticReqTime_Sec_f32	0.915000021
k_SWARLimiter_HwDeg_f32	0
k_ShiftLevRevTime_Sec_f32	0.152999997
k_StandstillThresh_Kph_f32	1.44000006
k_StandstillTime_Sec_f32	0.144999996
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_In$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	. 100	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg	<u>]</u> 132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	lan.	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I target TrqOvlSta Per1 ESCRequest Cnt lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en	•	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enun			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvlSta Per1 GMOSH APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-461.809998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1091.12		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0 0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.2999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	177.199997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.8999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	35966	35966 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.89999998	1.89999998 ± 0.000009	•
Tan O dota LIVA Facility Ont Malana		0 ± 1	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	U±I	
TrqOvIsta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
	0	0 ± 1 0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	0 0 4	0 ± 1 0 ± 1 4	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1 0 ± 1	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	35.2999992	35.2999992 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.50999999	0.50999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	38.5	38.5 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	-
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 2.155 (Repeat Count = 1)	
Name	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrqOvlSta
FrgOvISta_APAState_State_M_enum	1
TrqOvISta_ESCState_State_M_enum	4
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	63535
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	63535
TrgOvlSta ElapsedStandstillTime Sec M f32	63535
FrqOvlSta_HapticState_State_M_enum	0
FrgOvISta Hapticdur Cnt M u16	36948
FrqOvlSta_Haptictime_Sec_M_f32	63535
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	2
FrgOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0614
FrgOvISta LKAFault Cnt M Igc	1
FrqOvISta_LKAPermFault_Cnt_M_lgc	1
FrqOvISta_LKAPermFault_Cnt_M_u08	1
FrqOvISta_LKAState_State_M_enum	1
FrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	36.4000015
FrqOvISta_ReadytoPulse_Cnt_M_lgc	1
<pre><_APAIncludeHaptic_Cnt_lgc</pre>	0
APAMaxHwTrqTime Sec f32	0.958999991
APAMaxHwTrg HwNm f32	4.07999992
APAMaxVehSpd Kph f32	70.1999969
CESCMaxVehSpd Kph f32	115.599998
K_LSGMaxVeriSpu_Kpii_IS2 K HapticAmplitude MtrNm f32	0.529999971
<pre>c_riapideAmplicade_mitrini_ioz</pre> <pre>c_HapticDuration_Sec_f32</pre>	0.419999987
<pre><_napricbutation_occ_joz</pre> <pre><_HapticFreq_Hz_f32</pre>	39.2000008
C HapticReacttime Sec f32	0.84799999
C LKAMaxVehSpd Kph f32	100.699997
<pre><_LKAMinVehSpd_Kph_f32</pre>	92.75
<pre><_CKNIIIIVeIISpu_rpii_i32</pre> <pre><_MaxHapticReqTime_Sec_f32</pre>	0.930000007
SWARLimiter HwDeg f32	60
<pre>C_SWARLITHIEI_FINDEG_IS2</pre> <pre>C_ShiftLevRevTime_Sec_f32</pre>	0.157000005
C_StandstillThresh_Kph_f32	1.48000002
c_StandstillTime_Sec_f32	0.149000004
x_Standstill line_Sec_is2 arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cni	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_reiT_ArAbiviiiiteiveiitioiibetected_Ciii arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_inst_Ap_frqOviSta.frqOviSta_Ferr_APARequest_Citt_igc arget_Rte_inst_Ap_frqOviSta.frqOviSta_Perr_APAState_State_enum	
· · · · ·	target_TrqOvISta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_inst_Ap_rtqOviSta.rtqOviSta_Per1_ESCPault_Cnt_igc arget Rte_Inst_Ap_rtqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
	target TrqOvISta Per1 ESCRequest Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enu	target_TrqOvISta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngletarget_TrqOvlSta_Per1_PosTrajEnable_0		
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvISta_Per1_PosTrajHwAngle		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDe			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igo			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_Mtrl		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_	f32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-421.910004		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1096.06006		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvIsta_Per1_ESCRequest_Cnt_Igc.value	1		
target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.5999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	167.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.100000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt lgc.value	18.7999992		
		Former and Markets	D
Name TracoulCla ADAState State M. cours	Actual Value	Expected Value	Resul
TrqOvISta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrqOvISta_LiapsedAr-Aiwaxi WTrqTillie_Sec_W_i32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	63535	63535 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	63535	63535	
TrgOvlSta HapticState State M enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	36948	36948 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	63535	63535 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.0982399	2.0982399 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_u08	1	1 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.100000001	0.100000001 ± 0.000976562	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
	4	4	
		0	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	2	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3	1 ± 1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3 1 0.100000001	1 ± 1 0.100000001 ± 0.000976562	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3	1 ± 1	25

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	V



Test Step 2.156 (Repeat Count = 1)	<u></u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	66540.6328
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	66540.6328
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	66540.6328
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	37930
TrqOvlSta_Haptictime_Sec_M_f32	66540.6328
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.0999999
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0636000037
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	2
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	37.5
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.970000029
k_APAMaxHwTrq_HwNm_f32	4.15999985
k_APAMaxVehSpd_Kph_f32	72
k_ESCMaxVehSpd_Kph_f32	121
k_HapticAmplitude_MtrNm_f32	0.550000012
k_HapticDuration_Sec_f32	0.435000002
k_HapticFreq_Hz_f32	39.9000015
k_HapticReacttime_Sec_f32	0.864000022
k_LKAMaxVehSpd_Kph_f32	102.599998
k_LKAMinVehSpd_Kph_f32	94.5
k_MaxHapticReqTime_Sec_f32	0.944999993
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	30.2999992 0.160999998
k_StandstillThresh_Kph_f32	1.51999998
k_StandstillTime_Sec_f32	0.152999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCRequest_Crit_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfqEnable Cnt lqc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngie_HwDeg_tsz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_IrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_ts2 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosTrajErlable_Cht_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Crit_igc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target Rie inst Ap TrqOvista.TrqOvista Peri Shinteverse Chi inc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0 0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1101		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.9000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	187		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	172.300003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	20.7000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	-
TrqOvlSta_ESCState_State_M_enum	4	4	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	37930	37930 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.0999999	2.0999999 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	37.5	37.5 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	37.5	37.5 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.550000012	0.550000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	39.9000015	39.9000015 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 2.157 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18460.6309
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18460.6309
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	38912
TrqOvlSta_Haptictime_Sec_M_f32	18460.6309
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0658000037
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0

2015-02-04, 14:05:40+0530



Name		
A. APANAMANIATIN 1948 1955 20	Name	Input Value
APAMAMATIC TIME Sec. 122 APAMAMATIC TIME Sec. 123 APAMAMATIC	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
APANAMONION	k_APAIncludeHaptic_Cnt_lgc	0
	k_APAMaxHwTrqTime_Sec_f32	0.981000006
Scale-control Section	k_APAMaxHwTrq_HwNm_f32	4.23999977
L. Especifychine J. Bes. 122 L. Especifychine J. Bes. 122 L. Especifychine J. Bes. 122 L. Maniferior J. Maniferior J. Maniferior J. Bes. 122 La Maniferior J. Maniferior J. Maniferior J. Bes. 122 La Maniferior J. Maniferior J. Maniferior J. Bes. 122 La Maniferior J. Maniferior	k_APAMaxVehSpd_Kph_f32	193.800003
Name	k_ESCMaxVehSpd_Kph_f32	126.400002
Application	k_HapticAmplitude_MtrNm_f32	0.569999993
A. MANAMONE SEC. No. 10.2	k_HapticDuration_Sec_f32	0.449999988
LMANIAYWINSC Kph 12	k_HapticFreq_Hz_f32	40.5999985
Number Number See 172	k_HapticReacttime_Sec_f32	0.87999995
Manufaction Time, Sec., 12 059999991	k_LKAMaxVehSpd_Kph_f32	104.5
A. S. Shiller, Price	k_LKAMinVehSpd_Kph_f32	96.25
k. Schnidtliffen, Sec., 122 k. Sandhallffren, Sec., 122 k. Sandhallfren, Se	k_MaxHapticReqTime_Sec_f32	0.959999979
LischedulTime, Kgn., 122 Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, College Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, College Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, College Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, SCORE Linger, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, College Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Li	k_SWARLimiter_HwDeg_f32	45.5999985
LischedulTime, Kgn., 122 Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, APADominierventionDetected, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, College Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, College Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, College Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, SCORE Linger, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, College Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst. Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Lindenburg, Crit. Linger, Rie, Inst., Ap., TrigONSis TrigONSis, Perl, Li	k ShiftLevRevTime Sec f32	0.165000007
isange, Rie, Inst. Ap. Trop/Sist Prof. NSA Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sist Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sist Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sist Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sist Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists. Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists. Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists. Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists. Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists. Trop/Sists. Pert. APAPortineterventionDetected. Cnt. Ig- tanger, Rie, Inst. Ap. Trop/Sists. Trop/Sists.	k_StandstillThresh_Kph_f32	1.55999994
tanget, Rib., Ind. Ap. Trody/Sis. TrodySis. Part. JAPANorecoverableFails. Crit. journaget, Rib., Ind. Ap. TrodySis. TrodySis. Part. JAPANorecoverableFails. Crit. journaget, Rib., Ind. Ap. TrodySis. TrodySis. Part. JAPANorecoverableFails. Crit. journaget, Rib., Ind. Ap. TrodySis. TrodySis. Part. JAPANorecover. Crit. journaget, Rib., Ind. Ap. TrodySis. TrodySis. Part. JAPANoreco. State. emm target. Rib., Ind. Ap. TrodySis. TrodySis. Part. JAPANoreco. Crit. journaget, Rib., Ind. Ap. TrodySis. TrodySis. Part. JAPANoreco. Part. JAPANoreco. State. emm target. Rib., Ind. Ap. TrodySis. TrodySis. Part. JAPANoreco. Part. JAPANoreco. State. Common target. Part. JAPANoreco. Part. J	k_StandstillTime_Sec_f32	0.157000005
langet Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Faults, Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Faults, Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Faults, Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. Science Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. Science Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. Science Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst. Ag. TropVisita TropVisita Pert. APARecoverable Crit. Lyc target Rie, Inst.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
langet Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Riss (Land A) Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Trodysias Pert APARcoverable Faults (mt.) get lateget Trodysias Pert APARcoverable Faults (mt.) get lateget Trodysias Trodysias Pert APARcoverable Faults (mt.) get lateget Trodysi		
target Rie Inst. Ap. Trot/Osia Trot/Osia Pert. APARaeuest Ont. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. APARaeuest Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. COVE-ound. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. COVE-ound. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. COVE-ound. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. COVE-ound. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. CovE-ound. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. ScoPal institut. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. ScoPal institut. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. ScoPal institut. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. ScoPal institut. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. ScoPal institut. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. ScoReuset. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. ScoReuset. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. ScoReuset. Cnt. Igo target Ries Inst. Ap. Trot/Osia Trot/Osia Pert. Discored Ries Inst. Ap. Trot/Osia Pert. Josia Trot/Osia Tro	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	
target Rinds Ab Trajovista Trajovista Pert JAPAState State enum target Trajovista Pert JAPAState State enum target Ring Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Pert JAPAState Date (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2009) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2019) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState Pert JAPAState (2019) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2019) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2019) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2019) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2019) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2019) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2019) target Rise Inst. Ap Trajovista Trajovista Pert JAPAState (2019) target R		
taged, Rep. Inst. Ap. TriqONSIa TriqONSIa Pert COWFound, Cnt. Igo taged, Rise Jan. Ap. TriqONSIa TriqONSIa Pert COWFound, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert COWFound, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert COWFound, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert SECSIa Instead, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert SECSIa Instead, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert SECSIa Instead, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert SECSIa Instead, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert SECSIa Instead, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert SECSIa Instead, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert JANASIA Pert SECSIa Instead, Cnt. Igo target, Rise Jan. Ap. TriqONSIa TriqONSIa Pert JANASIA Pert JANASIA Pert SECSIa Instead, Cnt. Igo target, Rise, Inst. Ap. TriqONSIa TriqONSIa Pert JANASIA Pert J		
target Ries Las A D-TroyNista TroyNista Pert (CWPosition Hybrig Rie Just Ap TroyNista TroyNista Pert ESCFault Cnt.) go target Rie Inst. Ap TroyNista TroyNista Pert ESCFault Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert ESCFault Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MOSH North State enum target Rie Inst. Ap TroyNista TroyNista Pert (MOSH North State enum target Rie Inst. Ap TroyNista TroyNista Pert (MOSH North State enum target Rie Inst. Ap TroyNista TroyNista Pert (MOSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MOSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MOSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MOSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MOSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MOSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MOSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) fichial Cnt.] go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) for Inst. go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) for Inst. go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) for Inst. go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) for Inst. go target Rie Inst. Ap TroyNista TroyNista Pert (MoSH ESCM) for Inst. go targe		
target. Rep. Inst. Ap. Trajovista Prof. (Wisund, Ort.) go target. Rep. Inst. Ap. Trajovista Prof. (Wisund, Inst.) Hologo, 132 target. Rep. Inst. Ap. Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC apr. ISSC aut. Ort.) go target. Rep. Inst. Ap. Trajovista Trajovista Prof. ISSC apr. Inst. Ap. Trajovista Trajovista Prof. Inst. Apr. Inst. Ap. Trajovista Trajovista Prof. Inst. Ap. Trajovista Prof. Inst. Apr. Inst. Ap. Trajovista Prof. In		
langer, Rie, Ind.Ap. Trq-Orista, Trq-Orista, Pert LCWP-cation, Hybridge, 122 langer, Riego, Rie, Jon, Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Riego, Ind.Ap. Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit. Joc langer, Trq-Orista, Trq-Orista, Pert LSCR-Clumited, Crit		
target, Rei, Inst., Ap. Trq.Ovista, Pert, ESCFault, Cnt. Ipc sterget, Rei, Inst., Ap. Trq.Ovista, Pert, ESCFault, Cnt. Ipc target, Rei, Inst., Ap. Trq.Ovista, Pert, ESCState, State, enum target, Rei, Inst., Ap. Trq.Ovista, Pert, ESCState, State, enum target, Rei, Inst., Ap. Trq.Ovista, Pert, ESCState, State, enum target, Rei, Inst., Ap. Trq.Ovista, Pert, ESCState, State, enum target, Rei, Inst., Ap. Trq.Ovista, Pert, ESCState, State, enum target, Rei, Inst., Ap. Trq.Ovista, Pert, CMSUS, Pert, LMS, Pert, L		
target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIsLunited, Cnt. Igo target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIsLunited, Cnt. Igo target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIster, State, enum target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIster, State, enum target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIster, State, enum target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIster, State, enum target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIster, PARMIGEnable, Cnt. Igo target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIster, ESSCIster, Isscience, Cnt. Igo target, Rei, Inst. Ap. Tro/Oxista. Prof. ISSCIster, Inst. Ap. Tro/Oxista. Prof. ISSCIster, Inst. Ap. Tro/Oxista. Prof. IASSCIst. Prof. ISSCIster, Inst. Ap. Tro/Oxista. Prof. IASSCIst. Prof. ISSCIster, Inst. Ap. Tro/Oxista. Prof. IASSCIst.	· · · · · · ·	
large R. Inst. Ap. TrqOv/Sta TrqOv/Sta Port LSCRaquest Cnt. Igo target. TrqOv/Sta TrqOv/Sta TrqOv/Sta Port LSCRate, State, enum target. R. Inst. Ap. TrqOv/Sta TrqOv/Sta TrqOv/Sta Port LSCRState, State, enum target. TrqOv/Sta Port LSCRState, State, enum target. R. Inst. Ap. TrqOv/Sta TrqOv/Sta TrqOv/Sta Port LSCRState, State, Enum target. TrqOv/Sta. Port LSCRState, State, enum target. R. Inst. Ap. TrqOv/Sta TrqOv/Sta TrqOv/Sta TrqOv/Sta TrqOv/Sta TrqOv/Sta TrqOv/Sta Port LSCRState, ESCREAGURED, Cnt. Igo target. TrqOv/Sta. Port LSCRState, Cnt. Igo target. R. Inst. Ap. TrqOv/Sta TrqOv/St		
larget, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. GMOSHO Scallast State, enum target, Rie, Inst., Ap. TrqOvista, Pert J. GMOSHO Scallast State, State, enum target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. GMOSH J. APAMIginable Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. GMOSH J. SCAMIginable Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. GMOSH J. SCAMIginable Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. HapticRequest, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. HapticRequest, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. HapticRequest, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. HapticRequest, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. Kafault, Enum target, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. Kafault, Enum target, TrqOvista, Pert J. KAFault, Cnit. go target, Rie, Inst., Ap. TrqOvista, TrqOvista, Pert J. Market, Pert J. State, Pert J. TrqOvista, Pert J. TrqOvista, Pert J. State, Pert J. State, Pert J. TrqOvista, Pert J. T		
target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert GMOSH APAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert GMOSH APAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert GMOSH LXAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert GMOSH LXAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert JAMOSH LXAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert JAMOSH LXAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert JAMOSH LXAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert JAMOSH LXAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert JAMOSH LXAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert JAMOSH LXAM(genable Cnt. lgc target, Ret, Inst. Ap. TrqOv/Sta TrqOv/Sta Pert JAMOSH P		
target. Rie. Inst. Ap. TrqOVSia TrqOVSia. Pert. GMOSH. APAMSEnable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. Pert. GMOSH. ESCMSEnable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. Pert. GMOSH. ESCMSEnable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. GMOSH. LEXAMSEnable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Marker. target. TrqOVSia. Pert. JANAMSE. target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Marker. target. TrqOVSia. Pert. JANAMSE. target. Rie. Inst. Ap. TrqOVSia. Pert. LAVARUIC. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. LAVARUIC. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. LAVARUIC. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. LAVARUIC. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. LAVARUIC. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. LAVARUIC. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Markecure/vehicleSpeed. Kph. 132 target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Markecure/vehicleSpeed. Kph. 132 target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Postrojenable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Postrojenable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Postrojenable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Postrojenable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. Postrojenable. Cnt. Igo target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. SWARTrgMngRequest. HwDeg. 32 target. Rie. Inst. Ap. TrqOVSia. TrqOVSia. Pert. TrqOScAmp. Mrkm. 32 target. TrqOVSia. Pert. Javaricovia. Pert. TrqOScAmp. Mrkm. 32 target. TrqOVSia. Pert. Javaricovia. Pert. TrqOScAmp. Mrkm. 32 target. TrqOVSia. Pert. Javaricovia. Pert. Java		
target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf. GMOSH_LKAMfgEnable_Cnt.l.go target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. GMOSH_LKAMfgEnable_Cnt.l.go target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. HapticRequest_Cnt.l.go target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. HapticRequest_Cnt.l.go target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. HAVToque_HwNm_G32 target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. HAVToque_HwNm_G32 target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. LKAR-Pault_Cnt.l.go target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. MasSecure/velicleSpeed, Kpn_G32 target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. MasSecure/velicleSpeed, Kpn_G32 target, Rie, Inst. Ap. TrqOv/Sia, Perf. Poss-Pos-Pos-Pos-Pos-Pos-Pos-Pos-Pos-Pos		
larget Rie_Inst_Ap_TrqOviSia TrqOviSia_Perf_HaptoRequest_Cnt_Igo target_TrqOviSia_Perf_HaptoRequest_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_Perf_HaptoRequest_Cnt_Igo target_TrqOviSia_Perf_HaptoRequest_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_Perf_LKAFault_Cnt_Igo target_TrqOviSia_Perf_LKAFault_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_Perf_LKAFault_Cnt_Igo target_TrqOviSia_Perf_LKAFault_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_Perf_LKAFault_Cnt_Igo target_TrqOviSia_Perf_LKAFault_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_LKAFault_Cnt_Igo target_TrqOviSia_Perf_LKAFault_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_LKAFault_Cnt_Igo target_TrqOviSia_Perf_LKAFault_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_NascoureVerhioleSpeed_Kph_f32 target_TrqOviSia_Perf_LMAScoureVerhioleSpeed_Kph_f32 larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_PosServEnable_Cnt_Igo target_TrqOviSia_Perf_NascoureVerhioleSpeed_Kph_f32 larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_PosServEnable_Cnt_Igo target_TrqOviSia_Perf_PosServEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_PosTerpEnable_Cnt_Igo target_TrqOviSia_Perf_PosTerpEnable_Cnt_Igo larget_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_SintHaverisinReverse_Cnt_Igo target_TrqOviSia_Perf_SorvEnable_Cnt_Igo larget_TrqOviSia_Perf_Ap_TrqOviSia_Perf_TrqOsSerinq_Ing_Visia_Perf_TrqOviSia_Perf_TrqOviSia_Perf_LEVER_Ing_TrqOviSia_Perf_LEVER_Ing_		
target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, TrqOVSta, TrqOVSta, Perl _HwTorque_HwNm_rg2 target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _HwTorque_HwNm_rg2 target, TrqOVSta, Perl _HwTorque_HwNm_rg2 target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _LKAFautt, Cnt. Igo target, TrqOVSta, Perl _LKAFautt, Cnt. Igo target, TrqOVSta, Perl _LKAFautt, Cnt. Igo target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _LKAState, State_enum target, TrqOVSta, TrqOVSta, Perl _LKAState, State_enum target, TrqOVSta, TrqOVSta, TrqOVSta, Perl _LKAState, State_enum target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _NessecureVerbicleSpeed _Kpf, 132 target, TrqOVSta, Perl _LKAState, State_enum target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _NessecureVerbicleSpeed _Kpf, 132 target, TrqOVSta, Perl _LKAState, State_enum target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _NessecureVerbicleSpeed _Kpf, 132 target, TrqOVSta, Perl _MraScoureVerbicleSpeed _Kpf, 132 target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _PosServEnable, Cnt. Igo target, TrqOVSta, Perl _NessecureNebicleSpeed _Kpf, 132 target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _PosTrajEnable, Cnt. Igo target, TrqOVSta, Perl _PosTrajEnable, Dec. Igo target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _PosTrajEnable, Cnt. Igo target, TrqOVSta, Perl _PosTrajEnable, Dec. Igo target, Rie, Inst, Ap, TrqOVSta, TrqOVSta, Perl _NessecureNeberal _NessecureNeberal _NessecureNeberal _NessecureNeberal _NessecureNeberal _NessecureNeberal _NessecureNeberal _NessecureNeberal		
larget, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. KAFault, Cnt. Igo target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. KAFault, Cnt. Igo target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. KAFault, Cnt. Igo target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. KARequest, Cnt. Igo target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. KARequest, Cnt. Igo target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. KASSaucreVehicleSpeed, Kpf., 132 target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. MaxSecureVehicleSpeed, Kpf., 132 target, TrqOVSIa, Pert J. KASSaucreVehicleSpeed, Kpf., 132 target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. MaxSecureVehicleSpeed, Kpf., 132 target, TrqOVSIa, Pert J. MaxSecureVehicleSpeed, Kpf., 132 target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. MaxSecureVehicleSpeed, Kpf., 132 target, TrqOVSIa, Pert J. MaxSecureVehicleSpeed, Kpf., 132 target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. PasSrvehable, Cnt. Igo target, TrqOVSIa, Pert J. MaxSecureVehicleSpeed, Kpf., 132 target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. PasSrvehable, Cnt. Igo target, TrqOVSIa, Pert J. MaxSecureVehicleSpeed, Kpf., 132 target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. PasSrvehable, Cnt. Igo target, TrqOVSIa, Pert J. PasSrvehable, Cnt. Igo target, Rie, Inst. Ap., TrqOVSIa, TrqOVSIa, Pert J. TrqOScEnable, Cnt. Igo target, TrqOVSIa, Pert J. PasTrgia, TrqOVSIa, Pert		V - 1
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARGeutes_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARGeutes_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARGeutes_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MasSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsServEnable_Rie_Inst_Ap_TrqOviSta_Pert_TrqOsServEnable_Cnt_Igc target_TrqOviSta_Pert_ApARcoverableFaults_Cnt_Igcvalue 1 target_TrqOviSta_Pert_ApARcoverableFaults_Cnt_Igcvalue 1 target_TrqOviSta_Pert_CVFound_Cnt_Igcvalue 1 target_TrqOviSta_Pert_CVFound_Cnt_Igcvalue 1 target_TrqOviSta_Pert_GMOSH_APAMgEnable_Cnt_Igcvalue 1 target_TrqOviSta_Pert_GMOSH_APAMgEnable_Cnt_Igcvalue 1 target_TrqOviSta_Pert_Mosh_Apert_Cnt_Igcvalue 1 target_TrqOviSta_Pert_Mosh_Apert_Cnt_Igcvalue 1 target_TrqOviSta_Pert_Mosh_Apert_Cnt_Igcvalue 1 target_TrqOviSta_Pert_Mosh_Apert_Cnt_Igcvalue 1 target_TrqOviSta_Pert_Mosh		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAInhibit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAInhibit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAIstate_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovhwangle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovhwangle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovhwangle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTraipHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTraipHwAngle_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwARTrgiAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwARTrgiAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwARTrgiAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SwARTrgiAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MinM_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MinM_132 target_TrqOviSta_Pert_SwARTrgiAngRequest_HwDeg_132 target_TrqOviSta_Pert_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MinM_132 target_TrqOviSta_Pert_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MinM_132 target_TrqOviSta_Pert_Ap_TrqOviSta_TrqOviSta_Tert_TrqOscAmp_MinM_132 target_TrqOviSta_Pert_Ap_Request_Cnt_Ig_c_value 10 target_TrqOviSta_Pert_ApARequest_Cnt_Ig_c_value 11 target_TrqOviSta_Pert_CWPound_Cnt_Ig_c_value 12 target_TrqOviSta_Pert_CWPound_Cnt_Ig_c_value 13 target_TrqOviSta_Pert_ESCRequest_Cnt_Ig_c_value 14 target_TrqOviSta_Pert_ESCRequest_Cnt_Ig_c_value 15 target_TrqOviSta_Pert_MinMin_G2_value 16 target_TrqOviSta_Pert_MinMin_G2_value 17 target_TrqOviSta_Pert_MinMin_G2_value 18 target_TrqOviSta_Pert_MinMin_G2_value 19 target_TrqOviSta_Pert_MinMin_G2_value 10 target_TrqOviSta_Per		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IMASSecureVehicleSpeed_Kpt_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosservEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosservEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgMangRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgMangRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOssChable_Cnt_lgc target_TrqOviSta_Pert_SWARTrgMangRequest_HwDeg_132 target_TrqOviSta_Pert_SWARTrgMangRequest_HwDeg_132 target_TrqOviSta_Pert_TrqOssChable_Cnt_lgc target_TrqOviSta_Pert_Trq		
target_Rie_Inst_Ap_TrqOviSta_Per1_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvchwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Per1_PosSrvchwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajFhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajFhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajFhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajFhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajFhable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SWARTrgdAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSramp_MtrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSramp_MtrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSramp_MtrNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSramb_Ent_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsSramble_Cnt_lgc target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOviSta_Per1_APARQuest_Cnt_lgc.value target_TrqOviSta_Per1_APARQuest_Cnt_lgc.value target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value target_TrqOviSta_Per1_CWPosition_HwDeg_f32.value target_TrqOviSta_Per1_ESCRaquest_Cnt_lgc.value target_TrqOviSta_Per1_ESCRaquest_Cnt_lgc.value target_TrqOviSta_Per1_ESCRaquest_Cnt_lgc.value target_TrqOviSta_Per1_ESCRaquest_Cnt_lgc.value target_TrqOviSta_Per1_GMOSH_PAMMgenable_Cnt_lgc.value target_TrqOviSta_Per1_GMOSH_PERSMMgenable_Cnt_lgc.value target_TrqOviSta_Per1_GMOSH_PERSMMgenable_Cnt_lgc.value target_TrqOviSta_Per1_GMOSH_PSCRAMlegnable_Cnt_lgc.value target_TrqOviSta_Per1_LKARfault_Cnt_lgc.value target_TrqOviSta_Per1_LKARfault_Cnt_lgc.value target_TrqOviSta_Per1_LKARfault_Cnt_lgc.value target_TrqOviSta_Per1_LKARfault_Cnt_lgc.value target_TrqOviSta_Per1_LKARfault_Cnt_lgc.value target_TrqOviSta_Per1_LKARfault_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosTarjEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosTarjEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_PosTarjEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTarjEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTarjEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SortarjEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCernap_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCernap_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCernaple_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ApoviSta_Per1_TrqOsCernaple_Cnt_Igc target_TrqOviSta_Per1_ApoviSta_Per1_ApoviSta_Per1_TrqOsCernaple_Cnt_Igc target_TrqOviSta_Per1_ApoviSta_Per1_ApoviSta_Per1_TrqOsCernaple_Cnt_Igc target_TrqOviSta_Per1_Apovi		
larget_Rte_Inst_Ap_TrqOv/Sta_Per1_MinSecureVehicleSpeed_Kph_532 target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosSrovEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOv/Sta_Per1_SWARTrgtApRequest_Wbeg_132 target_Rte_Inst_Ap_TrqOv/Sta_Per1_SWARTrgtApRequest_Wbeg_132 target_Rte_Inst_Ap_TrqOv/Sta_Per1_SWARTrgtApRequest_Wbeg_132 target_Rte_Inst_Ap_TrqOv/Sta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOv/Sta_Per1_TrqOsCAmp_MtrNm_f32 target_TrqOv/Sta_Per1_TrqOsCAmp_MtrNm_f32 target_TrqOv/Sta_Per1_MtrNm_f32 target_TrqOv/Sta_Per1_MtrNm_f32 target_TrqOv/Sta_Per1_MtrNm_f32 target_TrqOv/Sta_Per1_MtrNm_f32 target_TrqOv/Sta_Per1_MtrNm_f32 target_TrqOv/Sta_Per1_MtrNm		
target_Rte_Inst_Ap_TrqOvIsta_Prr1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_Prr1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_Prr1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_SVRATrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_SVMATrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_SVMATrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_Rte_Inst_Ap_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_TrqOsSenable_Cnt_Igc target_TrqOvIsta_Prr1_CWPosition_HwDeg_f32_value target_TrqOvIsta_Prr1_CWPosition_HwDeg_f32_value target_TrqOvIsta_Prr1_CWPosition_HwDeg_f32_value target_TrqOvIsta_Prr1_CWPosition_HwDeg_f32_value target_TrqOvIsta_Prr1_Enst_Ap_TrqOvIsta_Prr1_CWPosition_HwDeg_f32_value target_TrqOvIsta_Prr1_Enst_Ap_TrqOvIsta_Prr1_CWPosition_HwDeg_f32_value target_TrqOvIsta_Prr1_Enst_Ap_TrqOvIsta_Prr1_CWPosition_HwDeg_f32_value target_TrqOvIsta_Prr1_MSADA_HAPMIgFanable_Cnt_Igc_value target_TrqOvIsta_Prr1_MSADA_HAPMIgFanable_Cnt_Igc_value target_TrqOvIsta_Prr1_MSADA_HAPMIgFanable_Cnt_Igc_value target_TrqOvIsta_Prr1_HAPMIgFanable_Cnt_Igc_value target_TrqOvIsta_Prr1_MSADA_HAPMIgFanable_Cnt_Igc_value target_TrqOvIsta_Prr1_LKAFault_Cnt_Igc_value target_TrqOvIsta_Prr1_KAFault_Cnt_Igc_value target_TrqOvI		
larget_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgIAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFostion_HwDeg_f32_value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_LKAlnhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKAlnhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKAlnhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKAlnhibit_Cnt_Igc.value target_TrqOvISta_Per1_LKAlnhibit_Cnt_Igc.value target_TrqOvISta		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_SVARTrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_SWARTrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_SWARTrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_SMIRLeverIsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Pert_TrqOsCAmp_MtrNm_f32 target_TrqOvISta_Pert_ApAnonRecoverableFaults_Cnt_Igc_value target_TrqOvISta_Pert_ApARecoverableFaults_Cnt_Igc_value target_TrqOvISta_Pert_ApARequest_Cnt_Igc_value target_TrqOvISta_Pert_CWFound_Cnt_Igc_value target_TrqOvISta_Pert_CWFound_Cnt_Igc_value target_TrqOvISta_Pert_CWFound_Cnt_Igc_value target_TrqOvISta_Pert_CWFound_Cnt_Igc_value target_TrqOvISta_Pert_ESCFault_Cnt_Igc_value target_TrqOvISta_Pert_ESCFault_Cnt_Igc_value target_TrqOvISta_Pert_ESCFault_Cnt_Igc_value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc_value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc_value target_TrqOvISta_Pert_ESCRequest_Cnt_Igc_value target_TrqOvISta_Pert_GMOSH_APAMfgenable_Cnt_Igc_value target_TrqOvISta_Pert_GMOSH_APAMfgenable_Cnt_Igc_value target_TrqOvISta_Pert_GMOSH_ESCMfgenable_Cnt_Igc_value target_TrqOvISta_Pert_IAphtCrequest_Cnt_Igc_value target_TrqOvISta_Pert_I		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_ExtqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_LestAp_MtrgDenable_Cnt_Igc.value target_TrqOvlSta_Per1_LestAp_MtrgDena		
target_Rte_Inst_Ap_TrqOviSta_Prd_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perd_ShiftLeverIsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perd_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Perd_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Perd_TrqOscAmp_MtrN		
target_Rte_Inst_Ap_TrqOviSta_Perl_ShiftLeverlsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOviSta_Perl_TrqOscFreq_Hz_f32 target_TrqOviSta_Perl_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOviSta_Perl_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Perl_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Perl_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOviSta_Perl_CowFound_Cnt_Igc.value 1 target_TrqOviSta_Perl_CowFound_Cnt_Igc.value 1 target_TrqOviSta_Perl_CowFound_Cnt_Igc.value 1 target_TrqOviSta_Perl_CowFound_Cnt_Igc.value 1 target_TrqOviSta_Perl_ExcFault_Cnt_Igc.value 1 target_TrqOviSta_Perl_ExcFault_Cnt_Igc.value 1 target_TrqOviSta_Perl_ExcFault_Cnt_Igc.value 1 target_TrqOviSta_Perl_ExcFault_Cnt_Igc.value 1 target_TrqOviSta_Perl_ExcRequest_Cnt_Igc.value 1 target_TrqOviSta_Perl_ExcRequest_Cnt_Igc.value 1 target_TrqOviSta_Perl_GmOSH_ExcMigEnable_Cnt_Igc.value 1 target_TrqOviSta_Perl_GmOSH_ExcMigEnable_Cnt_Igc.value 1 target_TrqOviSta_Perl_GmOSH_ExcMigEnable_Cnt_Igc.value 1 target_TrqOviSta_Perl_Mosh_Parl_Mosh_ExcMigEnable_Cnt_Igc.value 1 target_TrqOviSta_Perl_HyDrorque_HyNm_f32_value 1 target_TrqOviSta_Perl_HyDrorque_HyNm_f32_value 1 target_TrqOviSta_Perl_HyDrorque_HyNm_f32_value 1 target_TrqOviSta_Perl_HyDrorque_HyNm_f32_value 1 target_TrqOviSta_Perl_HyDrorque_HyNm_f32_value 1 target_TrqOviSta_Perl_LKAR-pault_Cnt_Igc.value 1 target_TrqOviSta_Perl_LKAR-pault_Cnt_Igc.v		· - · · · · - · · · · · · · · · · ·
target_Rte_inst_Ap_TrqOviSta_Perf_TrqOscAmp_MtrNm_f32 target_Rte_inst_Ap_TrqOviSta_Perf_TrqOscEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOscEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Perf_TrqOscEnable_Cnt_lgc target_TrqOviSta_Perf_APANonRecoverableFaults_Cnt_lgc.value target_TrqOviSta_Perf_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOviSta_Perf_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOviSta_Perf_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOviSta_Perf_CCWFound_Cnt_lgc.value 1 target_TrqOviSta_Perf_CCWFound_Cnt_lgc.value 1 target_TrqOviSta_Perf_CCWFound_Cnt_lgc.value 1 target_TrqOviSta_Perf_CWFound_Cnt_lgc.value 1 target_TrqOviSta_Perf_CWFound_Cnt_lgc.value 1 target_TrqOviSta_Perf_CWFound_Cnt_lgc.value 1 target_TrqOviSta_Perf_ESCFault_Cnt_lgc.value 1 target_TrqOviSta_Perf_ESCFault_Cnt_lgc.value 1 target_TrqOviSta_Perf_ESCFault_Cnt_lgc.value 1 target_TrqOviSta_Perf_ESCRequest_Cnt_lgc.value 1 target_TrqOviSta_Perf_ESCRequest_Cnt_lgc.value 1 target_TrqOviSta_Perf_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Perf_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Perf_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Perf_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Perf_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Perf_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Perf_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Perf_LKARault_Cnt_lgc.value 1 target_TrqOv		
target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APARnonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 tar		
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HwTorque_HwMn_f32.value 4.19999981 target_TrqOvISta_Per1_LKARault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARault_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Ont_Igc.value		
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 461.809998 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 1105.93994 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 1105.93994 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1105.93994 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCISLimited_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCISLimited_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1		
target_TrqOvISta_Per1_APARecoverableFaults_Cnt_igc.value 1 target_TrqOvISta_Per1_APARequest_Cnt_igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_igc.value 461.809998 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 1105.93994 target_TrqOvISta_Per1_CWFound_Cnt_igc.value 1105.93994 target_TrqOvISta_Per1_ESCFault_Cnt_igc.value 1105.93994 target_TrqOvISta_Per1_ESCFault_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCIsLimited_Cnt_igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Per1_HybticRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_HybticRequest_Cnt_igc.value 1 target_TrqOvISta_Per1_HybticRequest_Cnt_igc.value 4.19999981 target_TrqOvISta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_igc.value 1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -461.809998 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1105.93994 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1105.93994 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1105.93994 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1105.93994 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1105.93994 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1105.93994 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1105.93994 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1105.93994 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1105.939994 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1105.93999 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1105.93999 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1105.93999 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 1105.93999 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 1105.93999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1105.93999 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -461.809998 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 1105.93994 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_EXAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 1105.93994 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994	0 = 1 = = = 0=	
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994	· - ·	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 4.19999981 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		•
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 191.899994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 177.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.119999997	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.119999997

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	22.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	18460.6309	18460.6309 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	38912	38912 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.20000005	2.20000005 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0	0 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	0 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.569999993	0.569999993 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	40.5999985	40.5999985 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.158 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	21465
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	21465
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	21465
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	39894
TrqOvlSta_Haptictime_Sec_M_f32	21465
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.2999995
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0680000037
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	90
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.150000006
k_APAMaxHwTrq_HwNm_f32	4.32000017
k_APAMaxVehSpd_Kph_f32	197.600006
k_ESCMaxVehSpd_Kph_f32	131.800003
k_HapticAmplitude_MtrNm_f32	0.58999974
k_HapticDuration_Sec_f32	0.465000004
k_HapticFreq_Hz_f32	41.2999992
k_HapticReacttime_Sec_f32	0.896000028
k_LKAMaxVehSpd_Kph_f32	106.400002
k_LKAMinVehSpd_Kph_f32	98
k_MaxHapticReqTime_Sec_f32	0.975000024
k_SWARLimiter_HwDeg_f32	50.099985
k_ShiftLevRevTime_Sec_f32	0.169
k_StandstillThresh_Kph_f32	1.67999995
k_StandstillTime_Sec_f32	0.169
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnd$	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lo	•	leFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgo		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	eg_r32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg	1 f32	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	<u>,</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_I	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lq		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	num	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enurrenter(State) = 0.0000000000000000000000000000000000$	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_target_TrqOvlSta_Per1_LKAFault_Cnt_lgc	UL.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_Hr target_TrqOvlSta_Per1_SWARTrgtAngRequ	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrgOvISta Per1 ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	-421.910004 0		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	1110.88		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvISta_Per1_LKAInnibit_Cnt_lgc.value target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_EtxNrequest_Ont_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	196.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	182.100006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	24.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlOta_APAState_State_M_enum	4	4	
TrqOvlSta_ESCState_State_M_enum	4	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1 0 ± 1	V
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0 ± 1	
TrqOvISta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	39894	39894 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.44959998	2.44959998 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	•
TrqOvlOta_LKAState_State_M_enum	4	4	•
			Ž
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	90	90 ± 0.0009765625 0 ± 1	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	90	90 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.589999974	0.589999974 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	41.2999992	41.2999992 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	•
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 2.159 (Repeat Count = 1)	Input Value
	target Rte Inst Ap TrqOvlSta
Rte_Inst_Ap_TrqOvISta	2
rqOvISta_APAState_State_M_enum	
rqOvISta_ESCState_State_M_enum	3
rqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	24470
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	24470
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	24470
rqOvISta_HapticState_State_M_enum	
rqOvISta_Hapticdur_Cnt_M_u16	40876
rqOvISta_Haptictime_Sec_M_f32	24470
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.400001
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0702000037
rqOvISta_LKAFault_Cnt_M_lgc	1
rqOvISta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvISta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	50.5
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.159999996
_APAMaxHwTrq_HwNm_f32	4.4000001
_APAMaxVehSpd_Kph_f32	207.399994
_ESCMaxVehSpd_Kph_f32	137.199997
_HapticAmplitude_MtrNm_f32	0.610000014
_HapticDuration_Sec_f32	0.479999989
_HapticFreq_Hz_f32	42
_HapticReacttime_Sec_f32	0.912
_LKAMaxVehSpd_Kph_f32	108.300003
_LKAMinVehSpd_Kph_f32	99.75
_MaxHapticReqTime_Sec_f32	0.109999999
_SWARLimiter_HwDeg_f32	54.5999985
_ShiftLevRevTime_Sec_f32	0.172999993
_StandstillThresh_Kph_f32	1.72000003
_StandstillTime_Sec_f32	0.172999993
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	un target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgo	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



		• "	
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_F		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target TrqOvlSta Per1 APANonRecoverableFaults Cnt lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANotiRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target TrqOvISta Per1 CCWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.01001		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1115.81995		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target TrgOvlSta Per1 ESCRequest Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.80000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	201.699997		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	187		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	24470	24470 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	· ·
TrqOvISta_HapticState_State_M_enum	1	1	
TrqOvISta_Hapticdur_Cnt_M_u16	40876	40876 ± 1 0 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.4000001	2.4000001 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1±1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1±1	
TrgOvlSta LKAPermFault Cnt M u08	0	0 ± 1	_
TrqOvISta_LKAState_State_M_enum	3	3	
TrgOvlSta PosSrvoHwAngle HwDeg M f32	50.5	50.5 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	_
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	_
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	50.5	50.5 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.610000014	0.610000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	42	42 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 2.160 (Repeat Count = 1)	✓
Name	Input Value
Rte Inst Ap TrgOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvISta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlOta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-10
TrqOvISta HwTorqueSV HwNm M Str.K UIs f32	0.00125584798
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	0
k ESCMaxVehSpd Kph f32	0
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	0
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	0
k_LKAMaxVehSpd_Kph_f32	0
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0
k_SWARLimiter_HwDeg_f32 k ShiftLevRevTime Sec f32	0
k StandstillThresh Kph f32	0
k StandstillTime Sec f32	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target TrqOvlSta_Per1_CWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enumerators and the property of the pr$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAInhibit Cnt Igc	target TrqOvISta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_inst_Ap_frqOviSta.frqOviSta_Peri_SniftLeverisinReverse_Cnt_gc target_Rte_inst_Ap_frqOviSta.frqOviSta_Peri_frqOscAmp_MtrNm_f32	target_IrqOviSta_Per1_SnintLeverisinReverse_Cnt_igc target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1440.10999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-10		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-34.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrgOvISta ESCState State M enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	✓
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	~
TrgOvISta HapticState State M enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10	-10 ± 0.00009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	4	4	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0	0 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	✓
		0.4	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	0 ± 1 0 ± 0.0009765625	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0	0 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 2.161 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4.2949673e+009
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	65535
TrqOvlSta_Haptictime_Sec_M_f32	4.2949673e+009
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	4
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	90

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1	MACICAL
lame	Input Value
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	1
_APAMaxHwTrq_HwNm_f32	10
_APAMaxVehSpd_Kph_f32	220
_ESCMaxVehSpd_Kph_f32	180
HapticAmplitude_MtrNm_f32	0.75
_HapticDuration_Sec_f32	2
_HapticFreq_Hz_f32	100
_HapticReacttime_Sec_f32	2
_LKAMaxVehSpd_Kph_f32	220
	220
_LKAMinVehSpd_Kph_f32	
_MaxHapticReqTime_Sec_f32	
_SWARLimiter_HwDeg_f32	60
_ShiftLevRevTime_Sec_f32	0.5
_StandstillThresh_Kph_f32	5
_StandstillTime_Sec_f32	0.5
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lc	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOviSta_TrqOviSta_FeT1_ESCIsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
riget TrqOvlSta Per1 CCWFound Ont Igc.value	1
	-360
arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	
arget_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1440.10999
arget_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
arget TrqOvlSta Per1 HwTorque HwNm f32.value	10
arget_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
arget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	514.799988
arget_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	511
arget_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
arget TrgOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.200000003

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	34.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	2	2	•
TrqOvlSta_Hapticdur_Cnt_M_u16	65535	65535 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	4.2949673e+009	4.2949673e+009 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10	10 ± 0.00009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	100	100 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

2015-02-04, 14:05:40+0530



Test Case 3: Path Test

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cvcles:

TS3.1 874 Cycles TS3.2 1041 Cycles TS3.3 905 Cycles TS3.4 890 Cycles TS3.5 899 Cycles TS3.6 904 Cycles TS3.7 947 Cycles TS3.8 932 Cycles TS3.9 914 Cycles TS3.10 900 Cycles
TS3.11 903 Cycles
TS3.11 903 Cycles
TS3.11 896 Cycles
TS3.12 896 Cycles
TS3.13 880 Cycles
TS3.14 891 Cycles
TS3.16 919 Cycles
TS3.16 919 Cycles
TS3.17 932 Cycles
TS3.18 911 Cycles
TS3.19 952 Cycles
TS3.20 923 Cycles
TS3.21 914 Cycles
TS3.22 900 Cycles
TS3.22 900 Cycles
TS3.22 900 Cycles
TS3.23 875 Cycles
TS3.24 938 Cycles
TS3.25 937 Cycles
TS3.26 931 Cycles
TS3.27 935 Cycles
TS3.27 935 Cycles
TS3.29 910 Cycles
TS3.29 910 Cycles
TS3.39 933 Cycles
TS3.31 913 Cycles
TS3.31 913 Cycles
TS3.31 103 Cycles
TS3.33 1008 Cycles
TS3.34 1022 Cycles
TS3.35 1030 Cycles
TS3.36 1027 Cycles
TS3.37 1019 Cycles
TS3.38 1018 Cycles
TS3.39 1019 Cycles
TS3.34 1010 Cycles
TS3.34 1017 Cycles
TS3.45 1039 Cycles
TS3.47 963 Cycles
TS3.49 966 Cycles
TS3.50 969 Cycles
TS3.50 969 Cycles
TS3.50 969 Cycles
TS3.50 969 Cycles
TS3.50 960 Cycles TS3.51 930 Cycles
TS3.52 936 Cycles
TS3.52 936 Cycles
TS3.55 932 Cycles
TS3.55 923 Cycles
TS3.55 923 Cycles
TS3.56 930 Cycles
TS3.57 937 Cycles
TS3.56 930 Cycles
TS3.56 931 Cycles
TS3.60 932 Cycles
TS3.61 936 Cycles
TS3.62 922 Cycles
TS3.62 922 Cycles
TS3.65 939 Cycles
TS3.66 941 Cycles
TS3.67 909 Cycles
TS3.67 909 Cycles
TS3.68 905 Cycles
TS3.68 905 Cycles
TS3.70 909 Cycles
TS3.71 946 Cycles
TS3.72 949 Cycles
TS3.73 903 Cycles
TS3.74 904 Cycles
TS3.75 953 Cycles
TS3.76 987 Cycles
TS3.77 987 Cycles
TS3.78 908 Cycles
TS3.78 908 Cycles
TS3.80 935 Cycles
TS3.81 933 Cycles
TS3.81 933 Cycles
TS3.83 1037 Cycles
TS3.84 939 Cycles TS3.84 939 Cycles



Description Vector Description:

```
Vector Description:

TS3.1"(LKARequest_Cnt_T_igc == TRUE)=False
(HapticRequest_Cnt_T_igc == TRUE)=False
(ShiftLeverIshIReverse_Cnt_T_igc == TRUE)=False
(MaxSecureVehicleSpeed_Kph_T_ig2 >= k_StandstillThresh_Kph_f32)=Flase
(TrqOvlSta_HapticState_State_M_enum_l= Haptic_Inactive)=True
(Abs_f32_m(HwTrqFilt_Hwhm_T_ig2) >= k_APAMaxHwTrq_Hwhlm_f32)=False
((LKAFauil_Cnt_T_igc == TRUE)_b& (TrqOvlSta_LKAFernFauil_Cnt_M_igc == FALSE) && (TrqOvlSta_LKAFauil_Cnt_M_igc == FALSE))=False
if(CWFound_Cnt_T_igc == TRUE)_b& (CWFound_Cnt_T_igc == TRUE))=True
(GMOSH_APAMigEnable_Cnt_T_igc == TRUE)_ETrue
(TrqOvlSta_Haptictinne_Sec_M_f32 >= k_APAMaxHwTrq_Hwhlm_f32)=False
(ShiftLeverIshIReverse_Cnt_T_igc == TRUE)=True
(TrqOvlSta_Haptictinne_Sec_M_f32 >= k_APAMixHwTrq_Hwhlm_f32)=False
(MaxSecureVehicleSpeed_Kph_T_f32 >= k_APAMixHwTrq_Hwhlm_f32)=False
(MaxSecureVehicleSpeed_Kph_T_f32 >= k_APAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticState_State_M_enum_l= Haptic_Inactive)=False
(Abs_f32_m(HwTrqFilt_Hwhlm_f32) >= k_APAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticState_State_M_enum_l= Haptic_Inactive)=False
(KLKAFauil_Cnt_T_igc == TRUE)=True
(TrqOvlSta_HapticState_MaxHwTrqime_Sec_M_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticState_M_enum_l= Haptic_Inactive)=False
(KLKAFauil_Cnt_T_igc == TRUE)=True
(TrqOvlSta_HapticState_M_enum_l= Sec_M_f32 >= k_KAPAMixHwTrq_Imm_Sec_f32))=False
(KLKAFauil_Cnt_T_igc == TRUE)=True
(TrqOvlSta_HapticInne_Sec_M_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticInne_Sec_M_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticInne_Sec_M_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticInne_Sec_M_f32 >= k_KAPAMixHwTrq_Imm_Sec_f32))=True
(MaxSecureVehicleSpeed_Kph_T_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticInne_Spec_M_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticInne_Spec_M_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticInne_Spec_M_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticInne_Spec_M_f32 >= k_KAPAMixHwTrq_Hwhlm_f32)=True
(TrqOvlSta_HapticInne_Spec_M_f3
     -- FALSE)
TS3.5((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
((HapticRequest_Cnt_T_lgc==TRUE)&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)) ||(LrnEOTFound_Cnt_T_lgc==FALSE) )=True
TS3.6(TrqOvlSta_LKAPermFault_Cnt_M_u08 >= 3U)=True
TS3.7(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=False
TS3.8"((LKAFault_Cnt_T_lgc == TRUE)=True && (TrqOvlSta_LKAPermFault_Cnt_M_lgc == FALSE)=True&& (TrqOvlSta_LKAFault_Cnt_M_lgc == FALSE)=False)
== FALSE)=False)
       case(APA_TempInhibit):True
(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=False"
TS3.9((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32)=False || (MaxSecureVehicleSpeed_Kph_T_f32 >
        k_LKAMaxVehSpd_Kph_f32)=True)
     K_LAWIAXVEISPO_TOIT_102_FTIGE
TS3.10"case(APA_AvailCntrl):True
if(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=True"
TS3.11"(TrqOvlSta_LKAPermFault_Cnt_M_lgc == TRUE)=False
(LKARequest_Cnt_T_lgc == FALSE)=True"
TS3.12"case(APA_TempInhibit):True
    TS3.12"case(APA_TempInhibit):True

(APANonRecoverableFaults_Cnt_T_lgc ==TRUE)=True"

TS3.13(TrqOvlSta_LKAPemFault_Cnt_M_lgc == TRUE)=True

TS3.14(APARequest_Cnt_T_lgc == FALSE)=True

TS3.15"((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
((HapticRequest_Cnt_T_lgc == FALSE) = False"

(LKARequest_Cnt_T_lgc == FALSE) = False"

TS3.16"((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
(HapticRequest_Cnt_T_lgc==TRUE)||(APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
(HapticRequest_Cnt_T_lgc==TRUE)||(APAMaxVehSpd_Kph_f32) ||(APARecoverableFaults_Cnt_T_lgc==TRUE)||
(TrqOvlSta_APAState_State_M_enum == APA_Active) && (PosTrajEnable_Cnt_T_lgc == TRUE))=True"

TS3.17((MaxSecureVehicleSpeed_Kph_T_f32 > k_APAMaxVehSpd_Kph_f32) = False||(APARecoverableFaults_Cnt_T_lgc==TRUE)=false||
((HapticRequest_Cnt_T_lgc==TRUE)=true&(HapticRequest_Cnt_T_lgc==TRUE)) = True||(ImFOTFound_Cnt_T_lgc==TRUE) = True||(ImFOTFound_Cnt_T_lgc==TRUE
        ((HapticRequest_Cnt_T_lgc==TRUE)=true&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)) =True||(LmEOTFound_Cnt_T_lgc == FALSE)|
    ((HapticRequest_Cnt_T_lgc==TRUE)=true&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)) = True||(LrnEOTFound_Cnt_T_lgc == FALSE))  
TS3.18"(k_APAIncludeHaptic_Cnt_lgc == FALSE)=True  
((StandstillTime_Cnt_T_lgc==TRUE)=False && (APARequest_Cnt_T_lgc ==TRUE)=True&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))  
TS3.19((StandstillTime_Cnt_T_lgc==FALSE) = False|| (APARequest_Cnt_T_lgc ==FALSE)=True|| (ShiftLevRevTime_Cnt_T_lgc==FALSE))  
TS3.19((StandstillTime_Cnt_T_lgc==FALSE) = False|| (APARequest_Cnt_T_lgc ==FALSE)=True|| (ShiftLevRevTime_Cnt_T_lgc==FALSE))  
TS3.20((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32))|| (MaxSecureVehicleSpeed_Kph_T_f32 > k_ESCMaxVehSpd_Kph_f32)=True)  
TS3.21((ESCRequest_Cnt_T_lgc == FALSE) || (MinSecureVehicleSpeed_Kph_T_f32 > k_ESCMaxVehSpd_Kph_f32))=True  
TS3.23((CWFound_Cnt_T_lgc == FALSE) || (MinSecureVehicleSpeed_Kph_T_f32 > k_ESCMaxVehSpd_Kph_f32))=True  
TS3.24((MaxSecureVehicleSpeed_Kph_T_f32 < k_APAMaxVehSpd_Kph_f32)  
&& (APARequest_Cnt_T_lgc ==FALSE)  
&& (HapticRequest_Cnt_T_lgc==FALSE)  
&& (APARecoverableFaults_Cnt_T_lgc==FALSE)  
&& (LrnEOTFound_Cnt_T_lgc ==FALSE)  
=True  
&& (CHAPCOVERABLE)  
TRUE)=False)
```



```
((HapticRequest_Cnt_T_lgc==TRUE)&&(HapticReqDurExceeded_Cnt_T_lgc==TRUE)) ||(LrnEOTFound_Cnt_T_lgc == FALSE) )=False
    (LKARequest_Cnt_T_lgc == FALSE)=False"
TS3.39((StandstillTime_Cnt_T_lgc==FALSE)=true || (APARequest_Cnt_T_lgc ==FALSE)|| (ShiftLevRevTime_Cnt_T_lgc==FALSE))
TS3.40((StandstillTime_Cnt_T_lgc==FALSE)=False || (APARequest_Cnt_T_lgc ==FALSE)=True|| (ShiftLevRevTime_Cnt_T_lgc==FALSE))
    TS3.41((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 > k_LKAMinVehSpd_Kph_f32) || (LKARequest_Cnt_T_lgc == FALSE))=True
TS3.42((StandstillTime_Cnt_T_lgc==TRUE) && (APARequest_Cnt_T_lgc ==TRUE)&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))=False
  TS3.42((StandstillTime_Cnt_T_igc==TRUE) && (ĀPĀRequest_Cnt_T_lgc ==TRUE)&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))=False TS3.43((StandstillTime_Cnt_T_lgc==TRUE)=True && (APARequest_Cnt_T_lgc ==TRUE))
TS3.44((StandstillTime_Cnt_T_lgc==TRUE)=True && (APARequest_Cnt_T_lgc==TRUE))
TS3.44((StandstillTime_Cnt_T_lgc==TRUE)=True && (APARequest_Cnt_T_lgc ==TRUE))
TS3.45((StandstillTime_Cnt_T_lgc==TRUE)=False&&(ShiftLevRevTime_Cnt_T_lgc==TRUE))
TS3.45((StandstillTime_Cnt_T_lgc==TRUE)=True && (APARequest_Cnt_T_lgc ==TRUE)=True&&(ShiftLevRevTime_Cnt_T_lgc==TRUE)=False)
TS3.46((StandstillTime_Cnt_T_lgc==FALSE) =False|| (APARequest_Cnt_T_lgc ==FALSE)=False||
(ShiftLevRevTime_Cnt_T_lgc==FALSE)=True)
TS3.47((StandstillTime_Cnt_T_lgc==FALSE)|| (APARequest_Cnt_T_lgc ==FALSE)|| (ShiftLevRevTime_Cnt_T_lgc==FALSE)|| (ShiftLevRevTime_Cnt_T_l
TS3.62((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 >
  TS3.62((MinSecureVehicleSpeed_Kph_T_f32 < k_LKAMinVehSpd_Kph_f32) || (MinSecureVehicleSpeed_Kph_T_f32 > k_LKAMaxVehSpd_Kph_f32) || (LKARequest_Cnt_T_lgc == FALSE))=True
TS3.63((LKAInhibit_Cnt_T_lgc == TRUE) || (LKAFault_Cnt_T_lgc == TRUE) || (TrqOvISta_ESCState_State_M_enum == ESC_Active) || (TrqOvISta_ESCState_State_M_enum == ESC_TemporarilyLimited))=False
TS3.64((LKAInhibit_Cnt_T_lgc == TRUE)=False || (LKAFault_Cnt_T_lgc == TRUE)=False || (TrqOvISta_ESCState_State_M_enum == ESC_Active)=False || (TrqOvISta_ESCState_State_M_enum == ESC_TemporarilyLimited)=True)
TS3.65((LKAInhibit_Cnt_T_lgc == TRUE)=False || (LKAFault_Cnt_T_lgc == TRUE) = False|| (TrqOvISta_ESCState_State_M_enum == ESC_Active)=True || (TrqOvISta_ESCState_State_M_enum == ESC_Active)=True || (TrqOvISta_ESCState_State_M_enum == ESC_Active) = false || (LKAInhibit_Cnt_T_lgc == TRUE) = false || (LKAFault_Cnt_T_lgc == TRUE) = True || (TrqOvISta_ESCState_State_M_enum == ESC_Active) = false || (TrqOvISta_ESCState
   TS3.67(ESCRequest_Cnt_T_lgc == TRUE)=True
TS3.68(ESCRequest_Cnt_T_lgc == TRUE)=False
TS3.69(ESClsLimited_Cnt_T_lgc == TRUE)=False
    TS3.70(ESCRequest_Cnt_T_lgc == FALSE)=True
TS3.71"(ESCFault_Cnt_T_lgc == TRUE)=False
    (ESCIsLimited_Cnt_T_lgc == FALSE)=False"
    TS3.72(ESCIsLimited_Cnt_T_lgc == FALSE)=True
TS3.73(ESCRequest_Cnt_T_lgc == FALSE)=True
TS3.74"((TrqOvISta_APAState_State_M_enum == APA_Active) =True&& (PosTrajEnable_Cnt_T_lgc == TRUE )=False)
    ((TrqOvlSta_APAState_State_M_enum != APA_Active) && (PosTrajEnable_Cnt_T_lgc == TRUE ))=False" TS3.75(LKARequest_Cnt_T_lgc == TRUE)=False TS3.76"switch (TrqOvlSta_APAState_State_M_enum)
    Default:True"
     TS3.77"case (ESC_ManufacturingDisable):True
    case(APA_PermanentlyFailed):=True
     TS3.78"switch (TrqOvlSta_ESCState_State_M_enum)
    default:True
    switch (TrqOvISta HapticState State M enum)
    default:True"
  default: Irue*
TS3.79(TrgOvlSta_Hapticdur_Cnt_M_u16 > (k_HapticDuration_Sec_f32/D_2MS_SEC_F32))=>FALSE
TS3.80((StandstillTime_Cnt_T_lgc==FALSE)F || (APARequest_Cnt_T_lgc ==FALSE)T || (ShiftLevRevTime_Cnt_T_lgc==FALSE)F)=>TRUE
TS3.81((StandstillTime_Cnt_T_lgc==FALSE)F || (APARequest_Cnt_T_lgc ==FALSE)F || (ShiftLevRevTime_Cnt_T_lgc==FALSE)F || (APARequest_Cnt_T_lgc ==FALSE)F || (ShiftLevRevTime_Cnt_T_lgc==FALSE)F || (APARequest_Cnt_T_lgc ==FALSE)F || (ShiftLevRevTime_Cnt_T_lgc==FALSE)F || (ShiftLevRevTime_Cnt_T_lgc==FALS
```

Test Step 3.1 (Repeat Count = 1)		~
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	4	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	
TrqOvlSta_HapticState_State_M_enum	1	





Name	Input Value
TrqOvlSta Hapticdur Cnt M u16	5270
TrqOvlSta Haptictime Sec M f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrgOvlSta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	4
	3
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	1
	0
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	target TrgOvlSta Per1 SWARTrgtAngRequest HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOviota_TrqOviota_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_rer1_frqOscEriable_Crit_igc
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_riqovista_reri_riqoscrieq_nz_isz
	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	200 04004
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-360.01001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360.980011
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

Name				
target_TrqOvISta_Pert_HxForque_HxNm_132.value	Name	Input Value		
target_TrqOvISta_Pert_LKAFault_Cnt_lgc_value 0 target_TrqOvISta_Pert_LKARAnbit_Cnt_lgc_value 0 target_TrqOvISta_Pert_LKARAnbit_Cnt_lgc_value 1 target_TrqOvISta_Pert_MaxSecureVehicleSpeed_Kph_f32_value 0 target_TrqOvISta_Pert_ProStraji-Rable_Cnt_lgc_value 0 target_TrqOvISta_Pert_PosTraji-Rable_Cnt_lgc_value 0 target_TrqOvISta_Pert_PosTraji-Rable_Cnt_lgc_value 0 target_TrqOvISta_Pert_SWARTrgIAngRequest_HWDeg_f32_value 1 target_TrqOvISta_Pert_SWARTrgIAngRequest_HWDeg_f32_value 1 target_TrqOvISta_Pert_Swart_Webg_f32_value 1 target_TrqOvISta_Pert_Swart_Webg_f32_value 1 target_TrqOvISta_Pert_Swart_Webg_f32_value 1 target_TrqOvISta_Pert_Swart_Webg_f32_value 1 target_TrqOvISta_Pert_Swart_Webg_f32_value 1 target_TrqOvISta_Pert_Swart_Webg_f32_value 1 TrqOvISta_Paplate_State_State_Menum 4 4 4 Y TrqOvISta_Paplate_State_State_Menum 1 1 1 Y TrqOvISta_Paplate_State_State_Menum 1 1 1 Y TrqOvISta_Paplate_Threa_State_Menum 1 <td< td=""><td>target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value</td><td>0</td><td></td><td></td></td<>	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
larget TrqOVISta_Pert_LKAInnibit_Cnt_Igc_value	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvIsta_Pert_LkARequest_Cnt_lgc.value 1	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32_value 0	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvISta_Perl_PoSTrajEnable_Drt_Igo.value	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 0	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOviSta_Perl_PosTrajHwAngle_HvDog_f32.value 1 target_TrqOviSta_Perl_SWARTrgtAngRequest_HwDeg_f32.value 1 target_TrqOviSta_Perl_SWARTrgtAngRequest_HwDeg_f32.value 1 target_TrqOviSta_Perl_SHiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Result TrqOviSta_PAPState_State_Menum 4 4 4 4 7 <	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvISta_Perl_SWARTrgtAngRequest_HwDeg_f32.value 1 Agent_TrqOvISta_Perl_ShiftLeverlsInReverse_Cnt_Igc.value Actual Value Expected Value Result TrqOvISta_PAState_State_M_enum 4 4 4 Y Y TrqOvISta_ElapsedARMaxHwTrqTime_Sec_M_f32 0 0 ± 1 Y Y TrqOvISta_ElapsedShiftLeverVirine_Sec_M_f32 0 0.00200000009 0.00200000009 Y TrqOvISta_ElapsedShiftLeverVirine_Sec_M_f32 0 0.00200000009 0 Y TrqOvISta_Elapticutr_Cnt_M_unif 5270 5270 ± 1 Y TrqOvISta_Elapticutr_Cnt_M_unif Y TrqOvISta_Elapticutr_Cnt_M_unif Y TrqOvISta_Elapticutr_Cnt_M_unif Y TrqOvISta_Elapticutr_Cnt_M_unif Y TrqOvISta_Elapticutr_Cnt_M_unif Y TrqOvISta_Elapticutr_Cnt_M_unif Y Y TrqOvISta_Elapticutr_Cnt_M_unif </td <td>target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value</td> <td>0</td> <td></td> <td></td>	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
TrqOvISta_PAState_State_Menum	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
Name Actual Value Expected Value Result TrqOvISta_APAState_State_M_enum 4 4 4 ✓ TrqOvISta_EISpSedAPAMAXHWTqTime_Sec_M_f32 0 0±1 ✓ ✓ ✓ TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0±1 ✓ ✓ ✓ TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0.00200000009 0.0020000009 0.00200000009 ✓ ✓ TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0.00200000009 0.00200000009 ✓ ✓ TrqOvISta_HapticState_State_M_enum 1 1 1 ✓ ✓ TrqOvISta_HapticCtur_Crt_M_u16 5270 5270 ±1 ✓ ✓ TrqOvISta_HapticCtur_Crt_M_u16 5270 5270 ±1 ✓ ✓ TrqOvISta_LKAPCatt_Crt_M_Igc 0 0 ±1 ✓ ✓ ✓ TrqOvISta_LKAPCatt_Crt_M_Igc 0 0 ±1 ✓ ✓ TrqOvISta_LKAPcmFault_Crt_M_u08 4 4 ±1 ✓ ✓ TrqOvISta_LKAPcmFault_Crt_M_u08 4 4 ±1 ✓ ✓ TrqOvISta_LKAPcmFault_Crt_M_u08 4 4 ±1 ✓ ✓ TrqOvISta_LAPCAL_Crt_M_U	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
TrqOvISta_APAState_State_Menum 4 4 4 TrqOvISta_EscState_State_Menum 4 4 4 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0.00200000009 0.0020000009 ✓ TrqOvISta_Hapticstate_State_Menum 1 1 1 ✓ TrqOvISta_Hapticstate_State_Menum 5270 5270 ± 1 ✓ TrqOvISta_Hapticstate_State_Menum 0 0 ± 1 ✓ TrqOvISta_Hapticstate_State_Menum_M_Str.Sv_Uls_f32 5.69000006 5.69000006 ± 0.000009 ✓ TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 0.0009765625 ✓ TrqOvISta_PersTeachWangle_HwDeg_M f32 1 1 ± 0.0009765625 ✓ TrqOvISta_PersLaPes_State_Be_num_value 4 4 4 4 4 4 4 4 <td< td=""><td>target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value</td><td>0</td><td></td><td></td></td<>	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
TrqOvISta_ESCState_State_Menum 4 4 ✓ TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_ElapsedStandslilITime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_BapsedStandslilITime_Sec_M_f32 0.00200000099 0.0020000009 ✓ TrqOvISta_HapticState_State_Menum 1 1 1 ✓ TrqOvISta_HapticGtur_Cnt_M_u16 5270 5270 ± 1 ✓ ✓ ✓ ✓ ✓ 7 TrqOvISta_HapticGtur_Cnt_M_u16 5270 5270 ± 1 ✓ ✓ ✓ ✓ ✓ 7 TrqOvISta_HapticGtur_Cnt_M_u16 ✓ ✓ ✓ ✓ 0 0 ± 1 ✓ ✓ ✓ ✓ ✓ 0 0 ± 1 ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ ✓ 0 0 ± 1 ✓ ✓ ✓ ✓ 0 0 ± 1 ✓ ✓ ✓ ✓ 0 0 ± 1 ✓ ✓ ✓ ✓ ✓	Name	Actual Value	Expected Value	Result
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0.00200000009 0.00200000009 ✓ TrqOvISta_HapticState_State_Menum 1 1 1 TrqOvISta_Hapticdur_Cnt_M_u16 5270 5270 ± 1 ✓ TrqOvISta_Haptictime_Sec_M_f32 0 0 ± 1 ✓ TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIS_f32 5.69000006 5.69000006 ± 0.000009 ✓ TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lgc 4 4 ± 1 ✓ TrqOvISta_LKAState_State_M_enum 3 3 3 ✓ TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 1 1 ± 0.0009765625 ✓ TrqOvISta_Perl_APADrvrinterventionDetected_Cnt_lgc.value 0 0 ± 1 ✓ target_TrqOvISta_Perl_APAState_state_enum.value 4 4 ✓ target_TrqOvISta_Perl_APAState_State_enum.value	TrqOvlSta_APAState_State_M_enum	4	4	~
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_I32 0 0 ± 1 ✓ TrqOvISta_ElapsedStandstillTime_Sec_M_I32 0.00200000009 0.0020000009 ✓ TrqOvISta_HapticState_State_Menum 1 1 ✓ TrqOvISta_Hapticidur_Cnt_M_u16 5270 5270 ± 1 ✓ TrqOvISta_Haptictime_Sec_M_I32 0 0 ± 1 ✓ TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_I32 5.69000006 5.69000006 ± 0.000009 ✓ TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 ✓ TrqOvISta_LKASteut_State_Menum 3 3 3 ✓ TrqOvISta_LKAState_State_Menum 3 3 3 ✓ TrqOvISta_LKAState_State_Menum 3 3 3 ✓ TrqOvISta_PersvoHwAngle_HwDeg_M_I32 1 1 ± 0.0009765625 ✓ TrqOvISta_Perd_APADrivrinterventionDetected_Cnt_lgc.value 0 0 ± 1 ✓ target_TrqOvISta_Perl_APADrivrinterventionDetected_Cnt_lgc.value 4 4 ✓ target_TrqOvISta_Perl_APADriv	TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0.00200000009 0.00200000009 TrqOvISta_HapticState_State_Menum 1 1 TrqOvISta_HapticClur_Cnt_M_u16 5270 5270 ±1 TrqOvISta_Haptictime_Sec_M_f32 0 0 ±1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 5.69000006 5.69000006 ±0.000009 TrqOvISta_LKAFault_Cnt_M_igc 0 0 ±1 TrqOvISta_LKAPermFault_Cnt_M_igc 1 1 ±1 V TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ±1 TrqOvISta_LKASiate_State_M_enum 3 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 1 1 ±0.0009765625 ✓ TrqOvISta_Peri_APADivrinterventionDetected_Cnt_igc.value 0 0 ±1 ✓ target_TrqOvISta_Peri_APAState_State_enum.value 4 4 ✓ target_TrqOvISta_Peri_GMOSHOscillateState_State_enum.value 4 4 ✓ target_TrqOvISta_Peri_GMOSHOscillateState_state_enum.value 1 1 1 4 target_TrqOvISta_Peri_PosSrvoHwAngle_HwDeg_f32.value 0 0 0 ±1 1 target_Tr	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvISta_HapticState_State_Menum 1 1 V TrqOvISta_Hapticdur_Cnt_M_u16 5270 5270 ± 1 V TrqOvISta_Haptictime_Sec_M_f32 0 0 ± 1 V TrqOvISta_HapticUre_M_MStr.SV_UIS_f32 5.69000006 5.69000006 ± 0.000009 V TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 V TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 V TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 V TrqOvISta_LKAState_State_Menum 3 3 3 V TrqOvISta_LKAState_State_Menum 3 3 1 ± 0.0009765625 V TrqOvISta_PosSrvoHwAngle_Hwbeg_M_f32 1 1 ± 0.0009765625 V TrqOvISta_Perdylopluse_Cnt_M_lgc 0 0 ± 1 V target_TrqOvISta_Per1_APAState_State_state_enum.value 4 4 V target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 V target_TrqOvISta_Per1_LKAState_State_enum.value 1 1 1 ± 0.0009765625 V target_TrqOvISta_Per1_PosSrvoHnable_Cnt_lgc.value<	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvISta_Hapticdur_Cnt_M_u16 5270 5270 ± 1 TrqOvISta_Haptictime_Sec_M_f32 0 0 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 5.69000006 5.69000006 ± 0.000009 TrqOvISta_LKAPentl_Cnt_M_lgc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PesSrvoHwAngle_HwDeg_M_f32 1 1 ± 0.0009765625 TrqOvISta_PesdytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_BSCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHoscillateState_State_enum.value 1 1 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32.value 0.282000005	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	~
TrqOvlSta_Haptictime_Sec_M_f32 0 0 ± 1 ✓ TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 5.69000006 5.69000006 ± 0.000009 ✓ TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ± 1 ✓ TrqOvlSta_LKAPermFault_Cnt_M_u08 4 4 ± 1 ✓ TrqOvlSta_LKAState_State_M_enum 3 3 ✓ TrqOvlSta_LKAState_State_M_enum 3 3 ✓ TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 1 1 ± 0.0009765625 ✓ TrqOvlSta_PeadytoPulse_Cnt_M_lgc 0 0 ± 1 ✓ target_TrqOvlSta_Per1_APADstate_State_enum.value 0 0 ± 1 ✓ target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum.value 1 1 1 4 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 3 3 3 ✓ target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 1 ± 0.0009765625 ✓ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0.282000005 0.282000005 ± 0.0009765625 ✓	TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 5.69000006 5.69000006 ± 0.000009 ✓ TrqOvISta_LKAFault_Cnt_M_Igc 0 0 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_Igc 1 1 ± 1 ✓ TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 ✓ TrqOvISta_LKAState_State_M_enum 3 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 1 1 ± 0.0009765625 ✓ TrqOvISta_ReadytoPulse_Cnt_M_Igc 0 0 ± 1 ✓ target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_Igc.value 0 0 ± 1 ✓ target_TrqOvISta_Per1_APAState_State_enum.value 4 4 ✓ target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 4 4 ✓ target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 1 1 1 1 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 ✓ 4 target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value 0 0 ± 1 ✓ 4 target_TrqOvISta_Per1_PosServOHwAngle_HwDeg_f32.value 1 1 ± 0.0009765625 <	TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	•
TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 4 4 ± 1 TrqOvISta_LKAState_State_Menum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 1 1 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 1 1 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.282000005 0.282000005 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 ± 1	TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc 1 1±1 ✓ TrqOvlSta_LKAPermFault_Cnt_M_u08 4 4±1 ✓ TrqOvlSta_LKAState_State_Menum 3 3 3 ✓ TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 1 1±0.0009765625 ✓ TrqOvlSta_ReadytoPulse_Cnt_M_lgc 0 0±1 ✓ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0±1 ✓ target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 1 ✓ target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 ✓ ✓ target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0±1 ✓ <td< td=""><td>TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32</td><td>5.69000006</td><td>5.69000006 ± 0.000009</td><td>•</td></td<>	TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	•
TrqOvISta_LKAPermFault_Cnt_M_u08 4 4±1 TrqOvISta_LKAState_State_Menum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 1 1±0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0±1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0±1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 1 1 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0±1 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0 0±1 target_TrqOvISta_Per1_PosServOHwAngle_HwDeg_f32.value 1 1±0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.282000005 0.282000005 ±0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1	TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAState_State_Menum 3 3 ± 0.0009765625 ✓ TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 1 ± 0.0009765625 ✓ TrqOvlSta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 ✓ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 ✓ target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 1 ✓ target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 ✓ ✓ target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 ✓ ✓ target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 1 1 ± 0.0009765625 ✓ ✓ target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.282000005 0.282000005 ± 0.0009765625 ✓ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 ± 1 ✓	TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 1 1±0.0009765625 ✓ TrqOvlSta_ReadytoPulse_Cnt_M_lgc 0 0±1 ✓ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0±1 ✓ target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 1 ✓ target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 ✓	TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 ✓ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 ✓ target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 ✓ target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 1 ✓ target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 ✓ <t< td=""><td>TrqOvlSta_LKAState_State_M_enum</td><td>3</td><td>3</td><td>•</td></t<>	TrqOvlSta_LKAState_State_M_enum	3	3	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value 0 0 ± 1 target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	✓
target_TrqOvlSta_Per1_APAState_State_enum.value 4 target_TrqOvlSta_Per1_ESCState_State_enum.value 4 target_TrqOvlSta_Per1_ESCState_State_enum.value 4 target_TrqOvlSta_Per1_GMOSHOScillateState_State_enum.value 1 target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 4 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0±1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 1 1±0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.282000005 0.282000005±0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 1 1 ✓ target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 ✓ target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 ✓ target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 1 1 ± 0.0009765625 ✓ target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.282000005 0.282000005 ± 0.0009765625 ✓ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 ± 1 ✓	target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 1 1 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.282000005 0.282000005 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 ± 1	target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 0 0 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 1 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.282000005 0.282000005 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 ± 1	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 1 1 ± 0.0009765625 ✓ target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.282000005 0.282000005 ± 0.0009765625 ✓ target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1 ± 1 ✓	target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 1 1±1	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
aligo_indenota_i ori_indecotalasio_ent_igorialde	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value 3.25	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	<u> </u>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.2 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981





Name	Input Value		
k_HapticReacttime_Sec_f32	1.23199999		
k_LKAMaxVehSpd_Kph_f32	146.300003		
k LKAMinVehSpd Kph f32	100		
k MaxHapticRegTime Sec f32	0.600000024		
k SWARLimiter HwDeg f32			
	0.308999985		
k_ShiftLevRevTime_Sec_f32			
k_StandstillThresh_Kph_f32	0		
k_StandstillTime_Sec_f32	0.308999985	Data da di Oat Ina	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	. 100	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	0	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
0 = 1 = = = = = = = = = = = = = = = = =	0		
<u> </u>	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
0 _ 1	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	~
		2	-
TrqOvlSta_ESCState_State_M_enum	2	2	•
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	





Name	Actual Value	Expected Value	Result
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte Inst Ap TrgOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrgOvISta APAState State M enum	1
TrqOvISta_ESCState_State_M_enum	0
TrgOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0.460000008
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	45
TrgOvlSta ElapsedStandstillTime Sec M f32	0.850000024
TrgOvISta HapticState State M enum	0
TrgOvISta_Hapticdur_Cnt_M_u16	15
TrgOvISta Haptictime Sec M f32	75.6800003
TrgOvlSta HwTorgueSV HwNm M Str.SV UIs f32	-9.85999966
rqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00200000009
rgOviSta LKAFault Cnt M lgc	0
FrgOvISta LKAPermFault Cnt M lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	0
TrgOvISta LKAState State M enum	0
rgOvlSta PosSrvoHwAngle HwDeg M f32	1.20000005
rqOvlSta ReadytoPulse Cnt M lgc	0
APAIncludeHaptic Cnt Igc	1
APAMaxHwTrqTime_Sec_f32	0.109999999
APAMaxHwTrg HwNm f32	0.100000001
APAMaxVehSpd Kph f32	0.20000003
ESCMaxVehSpd_Kph_f32	10.1000004
HapticAmplitude MtrNm f32	0
HapticDuration Sec f32	0.50999999
HapticFreq Hz f32	3
HapticReacttime Sec f32	0.159999996
LKAMaxVehSpd Kph f32	0
: LKAMinVehSpd Kph f32	0.40000006
MaxHapticRegTime Sec f32	0.0149999997
SWARLimiter HwDeg f32	32.2000008
SSWARLIMITEI_HWDeg_132 SShiftLevRevTime_Sec_f32	0.0010000005
StandstillThresh Kph f32	0
s_StandstillTime_Sec_f32	0.00100000005
r_stanustiinnine_sec_isz arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
arget_Rte_inst_Ap_11q0viSta.11q0viSta_Pe11_AFADivfiliterveritionDetected_Cft_ arget Rte Inst Ap Trq0viSta.Trq0viSta Per1 APANonRecoverableFaults Cnt Ig	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_APANonRecoverableFaults_Cnt_Igc arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARonRecoverableFaults_Cnt_lgc
arget_Rte_inst_Ap_rrqOviSta_rrqOviSta_Perr_APARecoverableFaults_Crit_igc arget_Rte_inst_Ap_TrqOviSta_TrqOviSta_Perr_APARequest_Cnt_igc	target TrqOvISta_Per1_APARecoverable=aulis_cnt_igc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_APARequest_Crit_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_State_enum	target TrgOvISta_Per1_APARequest_Cit_gc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_inst_Ap_rrqOviSta_rrqOviSta_Perr_CCWPosition_nwbeg_isz arget_Rte_inst_Ap_rrqOviSta_rrqOviSta_Perr_CCWFound_Cnt_lgc	target TrqOvIsta_Per1_CCWPosition_Hwbeg_152
arget_Rte_inst_Ap_frqOviSta.frqOviSta_Perf_CwPound_Cnt_igc arget_Rte_inst_Ap_frqOviSta.frqOviSta_Perf_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Nte_mat_Ap_11q0viota.11q0viota_Fe11_6vvF0stti011_f1wbey_132	target_riqevista_reri_evvrosition_riwbeg_laz

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc	target TrqOvISta Per1 GMOSH LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvIsta_Per1_HapticRequest_Cnt_	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt target_TrqOvlSta_Per1_PosSrvoHwAngle_H	- ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvISta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-366.450012		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0 361.040009		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target TrqOvIsta_reri_ESCIsLimited Cnt Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.209999993		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0 25.2999992		
target_TrqOvIsta_Per1_MinSecureVehicleSpeed_Kph_f32.value	35.099985		
target TrgOvlSta Per1 PosTrajEnable Cnt Igc.value	0		
	0.100000001		
target TrgOvlSta Per1 PosTraiHwAngle HwDeg f32.value			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrqtAngRequest_HwDeg_f32.value	-32.5		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-32.5 1		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value		Expected Value	Result
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1	Expected Value	Result
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	1 Actual Value	•	•
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	1 Actual Value 4	4	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1 Actual Value 4 4 0.460000008 45	4 4 0.460000008 ± 1 45 ± 1	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1 Actual Value 4 4 0.460000008 45 0	4 4 0.460000008 ± 1 45 ± 1	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	1 Actual Value 4 4 0.460000008 45 0 0	4 4 0.460000008 ± 1 45 ± 1 0	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum	1 Actual Value 4 4 0.460000008 45 0 0 15	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1	•
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Haptictime_Sec_M_f32	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32	1 Actual Value 4 4 0.460000008 45 0 0 0 15 75.6800003 -9.83985996	4 4 0.460000008 \pm 1 45 \pm 1 0 0 0 15 \pm 1 75.6800003 \pm 1 -9.83985996 \pm 0.000009	•
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_Igc	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1 0 ± 1	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0 0	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1 0 ± 1 0 ± 1	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1 Actual Value 4 4 0.460000008 45 0 0 0 15 75.6800003 -9.83985996 0 0 0 0 4	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1 0 ± 1 4	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Hapticdime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0 4 1.20000005	$ 4 \\ 4 \\ 0.460000008 \pm 1 \\ 45 \pm 1 \\ 0 \\ 0 \\ 15 \pm 1 \\ 75.6800003 \pm 1 \\ -9.83985996 \pm 0.000009 \\ 0 \pm 1 \\ 0 \pm 1 \\ 4 \\ 1.20000005 \pm 0.0009765625 $	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0 4 1.20000005 0	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1 0 ± 1 4 $1.20000005 \pm 0.0009765625$ 0 ± 1 1 ± 1 4	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EScState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_HapticOtate_Cnt_M_u16 TrqOvlSta_Hapticotime_Sec_M_f32 TrqOvlSta_HwTorqueSv_HwNm_M_Str.Sv_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0 4 1.20000005 0 1 4 4	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1 0 ± 1 4 1.20000005 ± 0.0009765625 0 ± 1 1 ± 1 4	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EScState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAState_State_M_enum TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_BSCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0 4 1.20000005 0 1 4 4 4	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1 0 ± 1 4 1.20000005 ± 0.0009765625 0 ± 1 1 ± 1 4 4	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EScState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Hkapticime_Sec_M_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0 4 1.20000005 0 1 4 4 4 0 4	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1 0 ± 1 4 1.20000005 ± 0.0009765625 0 ± 1 1 ± 1 4 4 0 4	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_EScState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_Hapticime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0 4 1.20000005 0 1 4 4 0 4 0	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.63985996 ± 0.000009 0 ± 1 0 ± 1 0 ± 1 4 1.20000005 ± 0.0009765625 0 ± 1 1 ± 1 4 4 0 4 0 1.20010000000000000000000000000000000	
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_HapticOtate_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HavtTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	1 Actual Value 4 4 0.460000008 45 0 0 15 75.6800003 -9.83985996 0 0 0 4 1.20000005 0 1 4 4 4 0 4	4 4 0.460000008 ± 1 45 ± 1 0 0 15 ± 1 75.6800003 ± 1 -9.83985996 ± 0.000009 0 ± 1 0 ± 1 4 1.20000005 ± 0.0009765625 0 ± 1 1 ± 1 4 4 0 4	Result

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name Actual Value Expected Value R				
Name	Actual Value	Expected Value	Result	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	✓	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	7.11999989
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	132.839996
	8
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	240
	150
	-9.25
	0.00419999985
	0
	1
	2
	1
	2.29999995
, = _ = _ = _ = _ =	1
	0
	0.119999997
	0.079999982
_ , _ , _	1.79999995
_ , _ , _	12.5
	0.0060000005
	0.0149999997
2 1711 112 21	3.53999996
- 1	0.0160000008
	1.8999998
	1.75
	0.029999993
	33.599985
	0.0049999989 0.039999991
	0.049999989
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvlSta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target_TrqOvISta_Per1_CWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
	target TrqOvlSta Per1 ESCIsLimited Cnt Igc
	target TrqOvlSta Per1 ESCRequest Cnt Igc
	target_TrqOvlSta_Per1_ESCState_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	target_TrqOvISta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target Rie inst Ap Trgovista.Trgovista Peri Posservenable Crit inc	
	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32

© Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDe	eg_f3_target_TrqOvlSta_Per1_SWARTrgtAngRequest_	_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lg	c target_TrqOvlSta_Per1_ShiftLeverIsInReverse_0	Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	2	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target TrgOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target TrqOvlSta Per1 APARequest Cnt lgc.value	0		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-364.670013		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.070007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvISta_Per1_ESCNequest_Crit_gc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvISta_Per1_GMOSH_ESCMigEnable_Cnt_lgc.value	1		
	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	•		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0.560000002		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	30.2000008		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-30.6000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value Ex	cpected Value	Result
TraOulSta ADAStata State M anum			ittesuit
TrqOvlSta_APAState_State_M_enum	3 3		√ V
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	3 3 3		
		± 1	~
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3		V
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3 0 0 1		*
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	t 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	t 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	t 1 0 ± 1 ± 1 25 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 ± 1 ± 1 25 ± 0.000009 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 109999999 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	3 3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	3 3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HawTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	3 3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value	3 3 3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPstate_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 (109999999 ± 0.0009765625 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_EscState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1 £ 1 £ 1 £ 1 £ 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_EscState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	£ 1 0 ± 1 £ 1 25 ± 0.000009 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 £ 1 £ 1 £ 1 109999999 ± 0.0009765625 10000000005 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

3.53999996

Test Step 3.5 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	225.639999	

target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value

3.53999996 ± 0.0009765625

2015-02-04, 14:05:40+0530



Name	Input Value
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	684
TrqOvlSta_Haptictime_Sec_M_f32	1345
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.00860000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	4.5
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.140000001
k_APAMaxHwTrq_HwNm_f32	0.23999995
k_APAMaxVehSpd_Kph_f32	5.4000001
k_ESCMaxVehSpd_Kph_f32	17.2999992
k_HapticAmplitude_MtrNm_f32	0.0179999992
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	4.55999994
k_HapticReacttime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.6999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.0599999987
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	36.4000015 0.0130000003
k StandstillThresh Kph f32	0.119999997
k_StandstillTime_Sec_f32	0.0130000003
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cnt	
target_Rte_Inst_Ap_TrqOviota_TrqOviota_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_state_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target TrqOvlSta Per1_APARequest Cnt_lgc.value	1
	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-362.890015
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-362.690015 1
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target TrqOvlSta Per1 CWPosition HwDeg f32.value	361.100006
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvIsta_Per1_ESCFault_Crit_lgc.value target_TrqOvISta_Per1_ESCIsLimited_Crit_lgc.value	1
target_TrqOvIsta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvIsta_Per1_E3CRequest_Ont_Igc.value target_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
2 _ 12 2 2 7 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	





Name	Input Value		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.25999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	40		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	308.519989 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	684	684 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973	-8.02999973 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0179999992	0.0179999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.55999994	4.55999994 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

T+ 04 0.0 (D+ 0+ -4)	.a
Test Step 3.6 (Repeat Count = 1)	<u> </u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	1050
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.40000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.029999993
k_HapticDuration_Sec_f32	0.075000003

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

TIGONORA_I CIT			1-4-1-10-10
Name	Input Value		
k_HapticFreq_Hz_f32	5.57399988		
k_HapticReacttime_Sec_f32	0.0799999982		
k_LKAMaxVehSpd_Kph_f32	9.5		
k_LKAMinVehSpd_Kph_f32	8.75		
k_MaxHapticReqTime_Sec_f32	0.090000036		
k_SWARLimiter_HwDeg_f32	39.2000008		
k_ShiftLevRevTime_Sec_f32	0.0209999997		
k_StandstillThresh_Kph_f32	0.200000003		
k_StandstillTime_Sec_f32	0.0209999997		
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results and the properties of the $	target_TrqOvlSta_Per1_APADrvrIntervention	Detected_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFar		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_132	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEr	0	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_MinSecureVehicleSptarget_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target TrqOvlSta Per1 TrqOscFreq Hz f32		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target TrqOvlSta Per1 APARequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-361.109985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.130005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.96000004		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Res
TrqOvISta_APAState_State_M_enum	4	4	
TrgOvISta ESCState State M enum	3	3	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	
TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	
TrgOvlSta ElapsedStandstillTime Sec M f32	0	0	

 $TrqOvlSta_ElapsedStandstillTime_Sec_M_f32$

0





Name	Actual Value	Expected Value	Result
TrqOvlSta_HapticState_State_M_enum	2	2	~
TrqOvlSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991	2315.65991 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.029999993	0.0299999993 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvISta APAState State M enum	2
TrqOvISta ESCState State M enum	2
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	2639.65991
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	659.880005
TrqOvISta ElapsedStandstillTime Sec M f32	2670.19995
TrgOvlSta HapticState State M enum	0
TrgOvISta Hapticdur Cnt M u16	6100
TrgOvISta Haptictime Sec M f32	3285.03003
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-5.59000015
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.0174000002
TrqOvISta LKAFault Cnt M lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvISta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
TrgOvlSta ReadytoPulse Cnt M lgc	0
k APAIncludeHaptic Cnt Igc	1
k APAMaxHwTrqTime Sec f32	0.180000007
k APAMaxHwTrq HwNm f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	26.8999996
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReacttime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	42
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cr	nt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	c target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAState State enum	target_TrqOvlSta_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_ lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers	·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-379.329987		
target TrqOvlSta Per1 CWFound Cnt Igc.value	1		
	361.160004		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.599985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value		
TrqOvlSta_APAState_State_M_enum		Expected Value	Result
	0	0	Result
TrqOvlSta_ESCState_State_M_enum	3	•	~
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32		0	~
	3	0 3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	3 2639.65991	0 3 2639.65991 ± 1	* *
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3 2639.65991 659.880005	0 3 2639.65991 ± 1 659.880005 ± 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	3 2639.65991 659.880005 0	0 3 2639.65991 ± 1 659.880005 ± 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	3 2639.65991 659.880005 0	0 3 2639.65991 ± 1 659.880005 ± 1 0	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16	3 2639.65991 659.880005 0 0 6100	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32	3 2639.65991 659.880005 0 0 6100 3285.03003	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 3	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1700000002 ± 0.0009765625	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 3 3 0.170000002	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_Igc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 3 3 0.170000002	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.1700000002 ± 0.0009765625 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 1 3 3 0.170000002 0	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.170000002 ± 0.0009765625 0 ± 1 1 ± 1 0	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 3 3 0.170000002 0 1	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.170000002 ± 0.0009765625 0 ± 1 1 ± 1 0 3	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 3 3 0.170000002 0 1	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.170000002 ± 0.0009765625 0 ± 1 1 ± 1 0 3 0	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 1 3 3 0.170000002 0 1 0 3	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.170000002 ± 0.0009765625 0 ± 1 1 ± 1 0 3 0 3 0	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PesrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 3 3 0.170000002 0 1 0 3	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 3 ± 1 3 0.1700000002 ± 0.0009765625 0 ± 1 1 ± 1 0 3 0 3 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPuIse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	3 2639.65991 659.880005 0 0 6100 3285.03003 -5.44645023 1 1 1 3 3 0.170000002 0 1 0 3	0 3 2639.65991 ± 1 659.880005 ± 1 0 0 6100 ± 1 3285.03003 ± 1 -5.44645023 ± 0.000009 1 ± 1 1 ± 1 3 ± 1 3 0.170000002 ± 0.0009765625 0 ± 1 1 ± 1 0 3 0 3 0	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Took Stan 2.0 (Danash Count - 4)	ي ا
Test Step 3.8 (Repeat Count = 1)	Innut Walia
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvISta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta HwTorqueSV HwNm M Str.K UIs f32	-4.36999989 0.0218000002
TrqOvISta_HW10rqueSV_HW10rl_W_Str.K_Uis_IS2 TrqOvISta_LKAFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvISta_LKAState_State_M_enum	1
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvISta_rossivoriwArigie_riwDeg_ivi_i52 TrqOvISta_ReadytoPulse Cnt M lgc	0
k APAIncludeHaptic Cnt Igc	1
k_APAMaxHwTrqTime_Sec_f32	0.20000003
k APAMaxHwTrg HwNm f32	0.720000029
k APAMaxVehSpd Kph f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k HapticAmplitude MtrNm f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k HapticReacttime Sec f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k LKAMinVehSpd Kph f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k SWARLimiter HwDeg f32	44.7999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_inte_inist_Ap_frqOviota.frqOviota_Peri_PosorvonWArigie_nWDeg_132	laiget_TiqUviota_FeTT_F0501V0FWAIIgle_FIWDeg_132



TrqOvlSta_Per1

target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc			
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f3	2	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_	f3. target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwE	Deg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_I		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-377.549988		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.190002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	1		
target TrqOvlSta Per1 LKARequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value Exped	ted Value	Result
TrqOvlSta_APAState_State_M_enum	0 0		✓ ×
TrqOvISta_ESCState_State_M_enum	4 4		·
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32		7998 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32		9998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0 0	3330 I I	
TrqOvISta_HapticState_State_M_enum	0 0		V
TrqOvISta Hapticdur Cnt M u16	11150	+ 1	
TrqOvlSta_Haptictime_Sec_M_f32	425564 425564		·
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32		18611 ± 0.000009	
TrgOviSta_KAFault_Cnt_M_lgc	1 1±1	0.000003	
TrgOvISta_EK-A adit_Ont_M_gc	0 0 ± 1		
TrqOvISta_LKAPermFault_Cnt_M_u08	2 2 1		
TrqOvISta_LKAFeffiFauit_Cfit_wi_u06 TrqOvISta_LKAState_State_M_enum	0 0		
TrqOvISta_ErAState_State_ivi_entitit TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32		99998 ± 0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0 0 ± 1	99990 I 0.0009703023	
	1 1±1		
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc.value target_TrqOvlSta_Per1_APAState_State_enum.value	0 0		
	4 4		
target_TrqOvlSta_Per1_ESCState_State_enum.value	0 0		
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 0		
target_TrqOvlSta_Per1_LKAState_State_enum.value			Ž
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1 1 ± 1 0.189999998 0.1899	99998 ± 0.0009765625	- 4
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value		99998 ± 0.0009765625 000014 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-

7.63000011

0 ± 1

7.63000011 ± 0.0009765625

0

Test Step 3.9 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	0	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	6198.2998	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999	

 $target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value$

 $target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value$

2015-02-04, 14:05:40+0530



Name	Input Value
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	6149.16016
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	16200
TrqOvISta_Haptictime_Sec_M_f32	5225.33984
TrgOvISta HwTorqueSV HwNm M Str.SV UIs f32	-3.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0262000002
TrqOvISta_LKAFault_Cnt_M_lgc	
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	13.3000002
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.21999999
k_APAMaxHwTrq_HwNm_f32	0.87999995
k_APAMaxVehSpd_Kph_f32	19.7999992
k_ESCMaxVehSpd_Kph_f32	36.5
k_HapticAmplitude_MtrNm_f32	0.0659999996
k_HapticDuration_Sec_f32	0.165000007
k_HapticFreq_Hz_f32	8.65799999
k_HapticReacttime_Sec_f32	0.175999999
k_LKAMaxVehSpd_Kph_f32	20.8999996
k LKAMinVehSpd Kph f32	19.25
k_MaxHapticReqTime_Sec_f32	0.180000007
k_SWARLimiter_HwDeg_f32	47.5999985
k_ShiftLevRevTime_Sec_f32	0.0450000018
k_StandstillThresh_Kph_f32	0.43999998
k_StandstillTime_Sec_f32	0.0450000018
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
· · · · · · ·	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-375.769989
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.220001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1.
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1



Name	Input Value		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.05999994		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	89		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-11.6000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1011.23999	1011.23999 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	16200	16200 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.1500001	-3.1500001 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0659999996	0.0659999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	8.65799999	8.65799999 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.10 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	2	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	11536.2598	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1538.28003	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	11367.5996	
TrqOvlSta_HapticState_State_M_enum	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	31350	
TrqOvlSta_Haptictime_Sec_M_f32	8134.31982	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.50999999	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0394000001	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	19.8999996	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	
k_APAIncludeHaptic_Cnt_lgc	0	
k_APAMaxHwTrqTime_Sec_f32	0.280000001	
k_APAMaxHwTrq_HwNm_f32	1.36000001	
k_APAMaxVehSpd_Kph_f32	30.6000004	
k_ESCMaxVehSpd_Kph_f32	50.9000015	
k_HapticAmplitude_MtrNm_f32	0.101999998	

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

· -			
Name	Input Value		
k_HapticDuration_Sec_f32	0.254999995		
k_HapticFreq_Hz_f32	11.7419996		
k_HapticReacttime_Sec_f32	0.272000015		
k_LKAMaxVehSpd_Kph_f32	32.2999992		
k_LKAMinVehSpd_Kph_f32	29.75		
k_MaxHapticReqTime_Sec_f32	0.270000011		
k_SWARLimiter_HwDeg_f32	56		
k_ShiftLevRevTime_Sec_f32	0.0689999983		
k_StandstillThresh_Kph_f32	0.680000007		
k_StandstillTime_Sec_f32	0.0689999983		
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_RestrictionPerformance (Continuous Continuous Continuo Con$	target_TrqOvlSta_Per1_APADrvrIntervention	Detected_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_end	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_le	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlOta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_ target TrqOvlSta Per1 TrqOscFreq Hz f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	1		
target TrqOvISta Per1 APARecoverableFaults Cnt Igc.value	1		
target TrgOvlSta Per1 APARequest Cnt Igc.value	0		
target TrgOvlSta Per1 CCWFound Cnt Igc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-373.98999		
target TrgOvlSta Per1 CWFound Cnt lgc.value	1		
target TrgOvlSta Per1 CWPosition HwDeq f32.value	361.25		
target_TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value	1		
target TrgOvlSta Per1 HwTorque HwNm f32.value	-3.9000001		
target TrgOvlSta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	108.599998		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	118.400002		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-0.20000003		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrgOvISta ESCState State M enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TraCulCha FlancadChiffl au DayTima Coa M 600	0	0.4	

0

0 ± 1





Name	Actual Value	Expected Value	Result
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	31350	31350 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	8134.31982	8134.31982 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.50999999	0.50999999 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.101999998	0.101999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	11.7419996	11.7419996 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

N	Lucrost Malace
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	13315.5801
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	13107.0801
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	36400
TrqOvlSta_Haptictime_Sec_M_f32	9103.98047
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.73000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0438000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	22.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
<_APAIncludeHaptic_Cnt_lgc	1
<_APAMaxHwTrqTime_Sec_f32	0.300000012
<_APAMaxHwTrq_HwNm_f32	1.51999998
<_APAMaxVehSpd_Kph_f32	34.2000008
k_ESCMaxVehSpd_Kph_f32	55.7000008
<_HapticAmplitude_MtrNm_f32	0.114
K_HapticDuration_Sec_f32	0.284999996
<_HapticFreq_Hz_f32	12.7700005
x_HapticReacttime_Sec_f32	0.30399999
<pre>c_LKAMaxVehSpd_Kph_f32</pre>	36.0999985
<pre>c_LKAMinVehSpd_Kph_f32</pre>	33.25
<pre><_MaxHapticReqTime_Sec_f32</pre>	0.300000012
x_SWARLimiter_HwDeg_f32	58.7999992
<pre>c_ShiftLevRevTime_Sec_f32</pre>	0.0769999996
x_StandstillThresh_Kph_f32	0.75999999
StandstillTime_Sec_f32	0.0769999996
rarget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected (Cnt target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cn	
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igo	
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt Igc	target TrqOvlSta Per1 APARequest Cnt Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWPosition HwDeg f32	target TrgOvlSta Per1 CCWPosition HwDeg f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target TrgOvlSta Per1 CWFound Cnt Igc

2015-02-04, 14:05:40+0530





	I		
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlOta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlOta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-372.209991		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.279999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.80000019		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	118.400002		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	128.199997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.5999999		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1	I	1_
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	V
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1713.95996	1713.95996 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	V
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	36400	36400 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.73000002	1.73000002 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	V
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	V
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~

0

0.189999998

0

1 ± 1

0.189999998 ± 0.0009765625

target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value

 $target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value$

target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.114	0.114 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target TrqOvlSta Per1 TrqOscFreq Hz f32.value	12.7700005	12.7700005 ± 0.0009765625	✓

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.12 (Repeat Count = 1)	
lame	Input Value
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	0
rqOvlSta_ESCState_State_M_enum	3
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	16874.2207
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	16586.0391
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	46500
rqOvlSta_Haptictime_Sec_M_f32	11043.2998
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	26.5
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.340000004
_APAMaxHwTrq_HwNm_f32	1.84000003
_APAMaxVehSpd_Kph_f32	41.4000015
_ESCMaxVehSpd_Kph_f32	65.3000031
_HapticAmplitude_MtrNm_f32	0.137999997
_HapticDuration_Sec_f32	0.344999999
_HapticFreq_Hz_f32	14.8260002
_HapticReacttime_Sec_f32	0.368000001
_LKAMaxVehSpd_Kph_f32	43.7000008
_LKAMinVehSpd_Kph_f32	40.25
_MaxHapticReqTime_Sec_f32	0.360000014
_SWARLimiter_HwDeg_f32	34.4000015
_ShiftLevRevTime_Sec_f32	0.0930000022
_StandstillThresh_Kph_f32	0.920000017
_StandstillTime_Sec_f32	0.0930000022
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target TrgOvlSta Per1 APARequest Cnt Igc
	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target TrgOvlSta Per1 CCWFound Cnt Igc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target TrgOvlSta Per1 CWFound Cnt Igc
	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target TrqOvISta Per1 ESCFault Cnt Igc
	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
	target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Fer1_ErXI auti_Ont_lgc
	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKARequest_Crit_igt target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	targot regoriota i or i manocouro vomoroopooli NDH IOE
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ	uest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	n_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-370.429993		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.309998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-4.4000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	138		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	3	3	•
TrqOvlSta_ESCState_State_M_enum	3	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007	2065.32007 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrgOvISta HanticState State M enum	2	2	

target_TrqOviSta_Peri_ShirtLeverishReverse_Cht_igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2065.32007	2065.32007 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	46500	46500 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	4.17000008	4.17000008 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.137999997	0.137999997 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	14.8260002	14.8260002 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.13 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	0	
TrqOvlSta_ESCState_State_M_enum	0	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	18653.5391	





Name	Input Value
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	2241
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	18325.5195
TrqOvISta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	51550
TrqOvlSta_Haptictime_Sec_M_f32	12012.96
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	5.38999987
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.057
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	
TrqOvISta_LKAPermFault_Cnt_M_u08	1 0
TrqOvlSta_LKAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	28.7000008
TrgOvISta ReadytoPulse Cnt M lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.360000014
k_APAMaxHwTrq_HwNm_f32	2
k_APAMaxVehSpd_Kph_f32	45
k_ESCMaxVehSpd_Kph_f32	70.0999985
k_HapticAmplitude_MtrNm_f32	0.150000006
k_HapticDuration_Sec_f32	0.375
k_HapticFreq_Hz_f32	15.8540001
k_HapticReacttime_Sec_f32	0.400000006
k_LKAMaxVehSpd_Kph_f32	47.5
k_LKAMinVehSpd_Kph_f32	43.75
k_MaxHapticReqTime_Sec_f32	0.38999986
k_SWARLimiter_HwDeg_f32	37.2000008 0.101000004
k_ShiftLevRevTime_Sec_f32 k_StandstillThresh_Kph_f32	1
k StandstillTime Sec f32	0.101000004
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 GMOSHOscillateState State enun	
target Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Peri_PosTrajEnable_Cnt_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Peri_PosTrajHwAngle_HwDeq_f32	target_rrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_SWARTrgtAngRequest_HwDeg_f3	0 = 1 = = 7 0 = 0=
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ShiftLeverIsInReverse Cnt Igc	target TrqOvISta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.649994
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.339996
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	1
LEIGUGE TEU POOLEGE PER POOLEG	·

TrqOvlSta_Per1





Name	Input Value		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-2.29999995		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	157.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	15		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	51550	51550 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.38999987	5.38999987 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	1 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	15.8540001	15.8540001 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.14 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	2	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	31108.7793	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	3470.76001	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	30501.8809	
TrqOvlSta_HapticState_State_M_enum	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	50530	
TrqOvlSta_Haptictime_Sec_M_f32	35000.5391	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	44.099985	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	
k_APAIncludeHaptic_Cnt_lgc	0	
k_APAMaxHwTrqTime_Sec_f32	0.5	
k_APAMaxHwTrq_HwNm_f32	3.11999989	
k_APAMaxVehSpd_Kph_f32	70.1999969	
k_ESCMaxVehSpd_Kph_f32	103.699997	

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

TIGONORA_I CIT		,	00
Name	Input Value		
k_HapticAmplitude_MtrNm_f32	0.233999997		
k_HapticDuration_Sec_f32	0.584999979		
k_HapticFreq_Hz_f32	23.0499992		
k_HapticReacttime_Sec_f32	0.624000013		
k_LKAMaxVehSpd_Kph_f32	74.0999985		
k_LKAMinVehSpd_Kph_f32	68.25		
k_MaxHapticReqTime_Sec_f32	0.600000024		
k_SWARLimiter_HwDeg_f32	56.7999992		
k_ShiftLevRevTime_Sec_f32	0.157000005		
k_StandstillThresh_Kph_f32	1.55999994		
k_StandstillTime_Sec_f32	0.157000005		
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_InstApADrvrIntervention$	target_TrqOvlSta_Per1_APADrvrIntervention	Detected_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	ma	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateStatestatestatestatestatestatestatest		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc	<u></u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-366.869995		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.369995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1		
target_TrqOvISta_Per1_GMOSH_LKAMingEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvISta_Per1_Habitickequest_Ont_igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	0.5		
target_TrqOvISta_Per1_HW10rque_HW10li_i3z.value target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvIsta_ren_EKKI ault_Grit_igc.value target_TrqOvIsta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	216.399994		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	226.199997		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-28.2999992		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Resu
TrqOvlSta_APAState_State_M_enum	1	1	,
TrqOvISta_ESCState_State_M_enum	4	4	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	

0 ± 1

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	50530	50530 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.21000004	1.21000004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.233999997	0.233999997 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	23.0499992	23.0499992 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.15 (Repeat Count = 1) Name	Input Value
	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	220386
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	30810
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	44754
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	20942
TrqOvlSta_Haptictime_Sec_M_f32	51515
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.221000001
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.2999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.620000005
k_APAMaxHwTrq_HwNm_f32	4.71999979
k_APAMaxVehSpd_Kph_f32	106.199997
k_ESCMaxVehSpd_Kph_f32	132.5
C_HapticAmplitude_MtrNm_f32	0.354000002
<_HapticDuration_Sec_f32	0.88499999
k_HapticFreq_Hz_f32	29.2180004
C_HapticReacttime_Sec_f32	0.944000006
LKAMaxVehSpd_Kph_f32	112.099998
C_LKAMinVehSpd_Kph_f32	103.25
<_MaxHapticReqTime_Sec_f32	0.899999976
_SWARLimiter_HwDeg_f32	34
<pre><_ShiftLevRevTime_Sec_f32</pre>	0.237000003
<_StandstillThresh_Kph_f32	2.3599999
<_StandstillTime_Sec_f32	0.237000003
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cr	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target TrqOvISta Per1 CCWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target TrgOvISta Per1 CCWPosition HwDeg f32

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_le	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvISta_Per1_LKARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enu target_TrqOvlSta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvISta_Per1_PosServEnable_Cnt	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosTrajEnable Cnt Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ShiftLeverlsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-365.089996		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.399994		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	4.5		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Fer1_ErxInfilibit_Ont_igc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	275.200012		
target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32.value	285		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-9.10000038		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	_		
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	1
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	30810	0 ± 1 30810 ± 1	~
TrqOvlSta_HapticState_State_M_enum			Y
d	30810	30810 ± 1	
TrqOvlSta_Hapticdur_Cnt_M_u16	30810	30810 ± 1 0	~
	30810 0 0	30810 ± 1 0 0	· · ·
TrqOvlSta_Hapticdur_Cnt_M_u16	30810 0 0 20942	30810 ± 1 0 0 20942 ± 1	· · ·
TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOvlSta_LKAFault_Cnt_M_lgc	30810 0 0 20942 51515 0.994499981	30810 ± 1 0 0 20942 ± 1 51515 ± 1 0.994499981 ± 0.000009 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	30810 0 0 20942 51515 0.994499981 0	30810 ± 1 0 0 20942 ± 1 51515 ± 1 0.994499981 ± 0.0000009 0 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	30810 0 0 20942 51515 0.994499981 0	30810 ± 1 0 0 20942 ± 1 51515 ± 1 0.994499981 ± 0.0000009 0 ± 1 0 ± 1 1 ± 1	· · · · · · · · · · · · · · · · · · ·
TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	30810 0 0 20942 51515 0.994499981 0 0	30810 ± 1 0 0 20942 ± 1 51515 ± 1 0.994499981 ± 0.0000009 0 ± 1 0 ± 1 1 ± 1 2	***************************************
TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999	30810 ± 1 0 0 20942 ± 1 51515 ± 1 0.994499981 ± 0.0000009 0 ± 1 0 ± 1 1 ± 1 2 0.1099999999 ± 0.0009765625	***************************************
TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc	30810 0 0 20942 51515 0.994499981 0 0 1 2 0.1099999999 0	30810 ± 1 0 0 20942 ± 1 51515 ± 1 0.994499981 ± 0.0000009 0 ± 1 1 ± 1 2 0.1099999999 ± 0.0009765625 0 ± 1	***************************************
TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999 0 0	30810 ± 1 0 0 20942 ± 1 51515 ± 1 0.994499981 ± 0.0000009 0 ± 1 1 ± 1 2 0.109999999 ± 0.0009765625 0 ± 1 0 ± 1	
TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	30810 0 0 20942 51515 0.994499981 0 0 1 2 0.1099999999 0 0 3	30810 ± 1 0 0 20942 ± 1 51515 ± 1 $0.994499981 \pm 0.0000009$ 0 ± 1 1 ± 1 2 $0.1099999999 \pm 0.0009765625$ 0 ± 1 0 ± 1 3	
TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	30810 0 0 20942 51515 0.994499981 0 0 1 2 0.109999999 0 0	30810 ± 1 0 0 20942 ± 1 51515 ± 1 0.994499981 ± 0.0000009 0 ± 1 1 ± 1 2 0.109999999 ± 0.0009765625 0 ± 1 0 ± 1	

target_TrqOvlSta_Per1_LKAState_State_enum.value

target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value

2

1 ± 1





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.354000002	0.354000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	29.2180004	29.2180004 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.16 (Repeat Count = 1)	🗸
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	49
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg$	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enun$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_rtte_inst_Ap_11q0viota.11q0viota_1 e11_E1vAotate_otate_enam	
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_C	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cr	nt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg	f3. target_TrqOvlSta_Per1_SWARTrgtAngRed	quest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReve	erse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrN	m f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f:	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-363.309998		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.429993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	· ·
TrqOvISta_ESCState_State_M_enum	3	3	*
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422 0	84736.2422 ± 1 0	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	1	1	-
TrqOvISta_HapticState_State_M_enum	33000	33000 ± 1	
TrqOvlSta_Hapticdur_Cnt_M_u16 TrqOvlSta Haptictime Sec M f32	0	0 ± 1	-
	-4.80000019	-4.80000019 ± 0.000009	
TrqOviSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 TrqOviSta_LKAFault_Cnt_M_lgc	1	1±1	
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	- J
TrqOvISta_LKAState_State_M_enum	0	0	
TrqOvlSta PosSrvoHwAngle HwDeg M f32	38.2000008	38.2000008 ± 0.0009765625	
TrqOvISta ReadytoPulse Cnt M lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	·
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	
target TrgOvISta Per1 ESCState State enum.value	3	3	
target_TrqOvlSta_Per1_GMOSHOscillateState_enum.value	1	1	
target TrqOvISta Per1 LKAState State enum.value	0	0	V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value		38.2000008 ± 0.0009765625	✓
	38.2000008		
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	38.2000008 0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value			· ·
	0.660000026	0.660000026 ± 0.0009765625	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.17 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3

2015-02-04, 14:05:40+0530



Name	Input Value
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2020425
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	86335.6016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	391692
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	31000
TrqOvlSta_Haptictime_Sec_M_f32	210780
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.232999995
k_APAMaxHwTrq_HwNm_f32	8.72000027
k_APAMaxVehSpd_Kph_f32	196.199997
k_ESCMaxVehSpd_Kph_f32	174.5
k_HapticAmplitude_MtrNm_f32	0.671999991
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	55.9459991
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	220
k LKAMinVehSpd Kph f32	196
k MaxHapticReqTime Sec f32	0.135000005
k_SWARLimiter_HwDeg_f32	49.5999985
k_ShiftLevRevTime_Sec_f32	0.0329999998
k StandstillThresh Kph f32	4.36000013
k_StandstillTime_Sec_f32	0.0329999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOviSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.529999
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.459991
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1





Name	Input Value		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	-
TrqOvlSta_ESCState_State_M_enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	-
TrqOvlSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	210780	210780 ± 1	-
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	-
TrqOvlSta_LKAState_State_M_enum	0	0	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.18 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997

2015-02-04, 14:05:40+0530





N	In and Walter		
Name	Input Value		
k_ESCMaxVehSpd_Kph_f32	15.6000004		
k_HapticAmplitude_MtrNm_f32 k HapticDuration Sec f32	0 1.71000004		
k_HapticFreq_Hz_f32	56.973999		
k_HapticReacttime_Sec_f32	1.824		
k_LKAMaxVehSpd_Kph_f32	216.600006		
k_LKAMinVehSpd_Kph_f32	199.5		
k_MaxHapticReqTime_Sec_f32	0.20999993		
k_SWARLimiter_HwDeg_f32	50.2000008		
k_ShiftLevRevTime_Sec_f32	0.0529999994		
k_StandstillThresh_Kph_f32	4.55999994		
k_StandstillTime_Sec_f32	0.0529999994		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APANonRecoverable target_TrqOvlSta_Per1_APARecoverableFau		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target TrqOvISta_Per1_APARequest Cnt Ig		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_end		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEntarget_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f:		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32		· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	•	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 SWARTrgtAngRequest HwDeg f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-389.75		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_132.value	1 361.48999		
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015 1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvIsta_Per1_PosTrajnwArigie_nwDeg_i32.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.10000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	V
			_

 ${\sf TrqOvlSta_ESCState_State_M_enum}$

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte Inst Ap TrgOvlSta	target Rte Inst Ap TrqOvlSta
FrgOvISta APAState State M enum	1
FrgOvISta_Ar Astate_state_M_enum	4
·	2224203
FrgOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 FrgOvlSta ElapsedShiftLevRevTime Sec M f32	91133.6797
FrqOvlSta_ElapsedStandstillTime Sec M f32	430968
	2
rqOvlSta_HapticState_State_M_enum rqOvlSta Hapticdur Cnt M u16	45000
rqOvlSta_Haptictime_Sec_M_f32	228811.703
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.7000005
FrqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.507000029
FrqOvISta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0 2
rqOvlSta_LKAPermFault_Cnt_M_u08	
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime_Sec_f32	0.298999995
_APAMaxHwTrq_HwNm_f32	9.27999973
_APAMaxVehSpd_Kph_f32	208.800003
_ESCMaxVehSpd_Kph_f32	30.399996
_HapticAmplitude_MtrNm_f32	0.46000008
_HapticDuration_Sec_f32	1.7400001
_HapticFreq_Hz_f32	59.0299988
_HapticReacttime_Sec_f32	1.85599995
_LKAMaxVehSpd_Kph_f32	20
_LKAMinVehSpd_Kph_f32	203
_MaxHapticReqTime_Sec_f32	0.239999995
_SWARLimiter_HwDeg_f32	21.3999996
_ShiftLevRevTime_Sec_f32	0.0610000007
_StandstillThresh_Kph_f32	4.63999987
_StandstillTime_Sec_f32	0.0610000007
	tected_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFa	ults Cnt lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
$arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum$	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc

2015-02-04, 14:05:40+0530

Input Value

TrqOvlSta_Per1

Name



name	input value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwI	Deg_f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	:	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target TrqOvISta Per1 ESCState State 6	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_er			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lq			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lg	c target_TrqOvlSta_Per1_GMOSH_ESCMfgl	Enable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lg	c target_TrqOvlSta_Per1_GMOSH_LKAMfgE	Enable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cr	t Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm		
		_102	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_	lgc .	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_e	num	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_C		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cr	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReve		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNi	n_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cn	t_lgc	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3		
	0		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-387.970001		
	1		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.519989		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrgOvlSta Per1 HwTorque HwNm f32.value	-8.60000038		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	103		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrgOvlSta HapticState State M enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	V
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc			
	0	0 ± 1	✓
target TrgOvISta Per1 APADryrInterventionDetected Cnt loc value			
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value			

4

0

3

4

0

3

target_TrqOvlSta_Per1_ESCState_State_enum.value

 $target_TrqOvlSta_Per1_LKAState_State_enum.value$

target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value

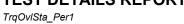
2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.18999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

	Innut Walne
	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
	0
	0
	1884573
	83136.8828
	365508
	0
rqOvlSta_Hapticdur_Cnt_M_u16	29000
rqOvlSta_Haptictime_Sec_M_f32	198760.641
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.5
	0.419
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.36500001
_APAMaxHwTrq_HwNm_f32	8.64000034
_APAMaxVehSpd_Kph_f32	194.399994
_ESCMaxVehSpd_Kph_f32	52.5999985
_HapticAmplitude_MtrNm_f32	0.648000002
_HapticDuration_Sec_f32	1.62
HapticFreq_Hz_f32	3
HapticReacttime Sec f32	1.72800004
LKAMaxVehSpd Kph f32	205.199997
	0
	0.119999997
- · ·	23.2000008
	0.0289999992
	4.32000017
	0.0289999992
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target_TrqOvISta_Per1_APARequest_Cnt_Igc
	target_TrqOvlSta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
•	target_rrqOvlSta_Per1_CvvPosition_nwDeg_is2 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
	target_rqoviSta_Peri_ESCFauit_Cnt_gc target TrqOviSta_Peri_ESCIsLimited Cnt_lgc
	target_irqOvlSta_Peri_ESCIsLimited_Cnt_igc target TrqOvlSta Peri ESCRequest Cnt_lgc
	target_IrqOvlSta_Per1_ESCRequest_Cnt_gc target TrqOvlSta Per1 ESCState State enum
	target_TrqOvlSta_Per1_GMOSH_ABAMfrEpoble_Cot_log
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum





Name	Input Value	need Kab 522			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32					
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cni				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	-			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	arget_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 arget_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3					
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRever				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2			
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value					
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-386.190002				
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.549988				
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.69999981				
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0				
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003				
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994				
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1				
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006				
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004				
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1				
Name	Actual Value	Expected Value	Result		
TrqOvlSta_APAState_State_M_enum	3	3	~		
TrqOvlSta_ESCState_State_M_enum	3	3	~		
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~		
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	~		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~		
TrqOvlSta_HapticState_State_M_enum	0	0	✓		
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	~		
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	~		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	~		
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~		
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~		
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	~		
TrqOvlSta_LKAState_State_M_enum	2	2	~		
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~		
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~		
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	0	0 ± 1	~		
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~		
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~		
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~		
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	~		
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~		
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~		
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	~		
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•		
	3	3 ± 0.0009765625	_		
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	13	3 ± 0.00037 03023			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.21 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2

2015-02-04, 14:05:40+0530



Name	Input Value
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	51515
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51515
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	51515
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	33020
TrqOvlSta_Haptictime_Sec_M_f32	51515
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	1.6000002
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0526000001
TrqOvlSta_LKAFault_Cnt_M_lgc	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1 2
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	2
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	32
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.915000021
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	63
k_ESCMaxVehSpd_Kph_f32	0
k_HapticAmplitude_MtrNm_f32	0.449999988
k_HapticDuration_Sec_f32	0.360000014
k_HapticFreq_Hz_f32	36.4000015
k_HapticReacttime_Sec_f32	0.783999979
k_LKAMaxVehSpd_Kph_f32	93.0999985
k_LKAMinVehSpd_Kph_f32	85.75
k_MaxHapticReqTime_Sec_f32	0.870000005
k_SWARLimiter_HwDeg_f32	38.2000008
k_ShiftLevRevTime_Sec_f32	0.141000003
k_StandstillThresh_Kph_f32	1.32000005
k_StandstillTime_Sec_f32	0.133000001
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOviota_TrqOviota_Per1_CWFound_Cnt_Igc	target TrgOvlSta Per1 CWFound Cnt Igc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CWPosition HwDeg f32	target TrgOvlSta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEriable_Crit_igc
target_Rte_Inst_Ap_IrqOvista_IrqOvista_Peri_IrqOscFreq_Hz_f32 target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	target_irqovista_reri_irqoscrreq_Hz_t32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecuserable=aults_Ont_igc.value	1
target TrgOvlSta Per1 CCWFound Cnt loc value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-384.410004
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-384.410004





Name	Input Value		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.4000001		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	162.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	147.800003		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.159999996		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	11.1999998		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	0	0	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	51515	51515 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	2	2	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	33020	33020 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	51515	51515 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.60000002	1.60000002 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.159999996	0.159999996 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.159999996	0.159999996 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.449999988	0.449999988 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target TrqOvlSta Per1 TrqOscFreq Hz f32.value	36.4000015	36.4000015 ± 0.0009765625	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~	
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	~	

Test Step 3.22 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	54520
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	54520
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	54520
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	34002
TrqOvlSta_Haptictime_Sec_M_f32	54520
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0548
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	33.099985
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.925999999
k_APAMaxHwTrq_HwNm_f32	3.83999991





	
Name	Input Value
k_APAMaxVehSpd_Kph_f32	64.8000031
k_ESCMaxVehSpd_Kph_f32	180
k_HapticAmplitude_MtrNm_f32	0.469999999
k_HapticDuration_Sec_f32	0.375
k_HapticFreq_Hz_f32	37.099985
k_HapticReacttime_Sec_f32	0.80000012
k_LKAMaxVehSpd_Kph_f32	95
k_LKAMinVehSpd_Kph_f32	87.5
k_MaxHapticReqTime_Sec_f32	0.88499999
k_SWARLimiter_HwDeg_f32	38.5
k_ShiftLevRevTime_Sec_f32	0.144999996
k_StandstillThresh_Kph_f32	1.36000001
k_StandstillTime_Sec_f32	0.136999995
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results and the properties of the properti$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target TrgOvlSta Per1 ESCIsLimited Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.630005
arget_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.609985
arget_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
arget TrgOvlSta Per1 ESCIsLimited Cnt Igc.value	0
arget_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0
arget_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
arget_riqOviSta_Ferr_GMOSH_ESCWilgEnable_Crit_igc.value	0
arget_TrqOvlSta_Per1_GivlOSH_EKAWigEriable_Crit_igc.value	0
	2.70000005
arget_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	0
arget_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	
arget_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
rarget_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	167.399994
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	152.699997
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002
torget TraOulSta Port SMADTratAngPoqueet HwDog f22 value	13.1000004
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	
target_TrqOvlSta_Per1_SWAR TrgVangRequest_TwDeg_132.value target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0
	Actual Value Expected Value Res





Name	Actual Value	Expected Value	Result
TrqOvlSta_ESCState_State_M_enum	0	0	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	34002	34002 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	1.75480008	1.75479996 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	33.0999985	33.0999985 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	33.0999985	33.0999985 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.469999999	0.469999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	37.0999985	37.0999985 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.23 (Repeat Count = 1)	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvlSta_APAState_State_M_enum	4
FrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
FrqOvlSta_HapticState_State_M_enum	1
FrqOvlSta_Hapticdur_Cnt_M_u16	5270
FrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
FrqOvlSta_LKAFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_lgc	1
FrqOvISta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	3
FrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
C_APAIncludeHaptic_Cnt_lgc	1
C_APAMaxHwTrqTime_Sec_f32	0.376666665
C_APAMaxHwTrq_HwNm_f32	3.75999999
C_APAMaxVehSpd_Kph_f32	84.5999985
c_ESCMaxVehSpd_Kph_f32	1
_HapticAmplitude_MtrNm_f32	0.282000005
_HapticDuration_Sec_f32	0.704999983
_HapticFreq_Hz_f32	3.25
_HapticReacttime_Sec_f32	0.751999974
_LKAMaxVehSpd_Kph_f32	89.3000031
c_LKAMinVehSpd_Kph_f32	82.25
c_MaxHapticReqTime_Sec_f32	0.720000029
c_SWARLimiter_HwDeg_f32	30
c_ShiftLevRevTime_Sec_f32	0.188999996
 StandstillThresh_Kph_f32	4
 StandstillTime_Sec_f32	0.188999996
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected	Cnt target_TrqOvlSta_Per1_APADrvrInterventionDetected Cnt lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cr	
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrqOvlSta Per1 APAState State enum

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_Hw	/Deg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lc	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwD	Deg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_e			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_I			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target_TrqOvISta_Per1_HapticRequest_C		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lge	_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target TrqOvlSta Per1 LKAInhibit Cnt Ig		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_	enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_	f32 target_TrqOvlSta_Per1_MaxSecureVehicl	eSpeed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_InstAp_TrqOvlSta_Per1_MinSecureVehicleSpeed_K$	f32 target_TrqOvlSta_Per1_MinSecureVehicle	eSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_0	Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosSrvoHwAngle	_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_C		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32$	target_TrqOvlSta_Per1_PosTrajHwAngle_		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRev		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrN	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_C		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_	132	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-380.850006		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.640015		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	0		
		Expected Value	Dog. 14
Name TraCh/Sta ABAState State Michigan	Actual Value	Expected Value 4	Result
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	4	4	
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrqOvISta_ElapsedShiftLevRevTime_Sec_IM_I32	0	0±1 0±1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	
TrqOvlSta HapticState State M enum	1	1	
TrqOvISta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target TraQvISta Per1 APADry/InterventionDetected Cnt Igg value	0	0 ± 1	

0

4

4

0 ± 1

4

4

target_TrqOvlSta_Per1_APAState_State_enum.value

target_TrqOvlSta_Per1_ESCState_State_enum.value

 $target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$

 $target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value$

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Fest Step 3.24 (Repeat Count = 1)	Innut Waling
Name	Input Value
	target_Rte_Inst_Ap_TrqOvlSta
	0
	4
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	11150
rqOvlSta_Haptictime_Sec_M_f32	425564
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.36999989
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
	1
	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
, _ = _ = _ =	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.200000003
_APAMaxHwTrq_HwNm_f32	0.720000029
_APAMaxVehSpd_Kph_f32	70.1999969
_ESCMaxVehSpd_Kph_f32	31.7000008
_HapticAmplitude_MtrNm_f32	0.0540000014
_HapticDuration_Sec_f32	0.135000005
_HapticFreq_Hz_f32	7.63000011
_HapticReacttime_Sec_f32	0.143999994
_LKAMaxVehSpd_Kph_f32	17.1000004
_LKAMinVehSpd_Kph_f32	15.75
_MaxHapticReqTime_Sec_f32	0.150000006
_SWARLimiter_HwDeg_f32	35.7000008
_ShiftLevRevTime_Sec_f32	0.0370000005
_StandstillThresh_Kph_f32	0.360000014
_StandstillTime_Sec_f32	0.0370000005
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APADrvrInterventionDetected Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3	2 target_TrqOvlSta_Per1_MaxSecureVehicles	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_I	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f	3 target_TrqOvlSta_Per1_SWARTrgtAngRequ	uest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn	1_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3:	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-379.070007		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.670013		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	1	1	
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	4418.97998	4418.97998 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	_
TrgOvlSta HapticState State M enum	0	0	✓
TrgOvlSta Hapticdur Cnt M u16	11150	11150 ± 1	
TrgOvlSta Haptictime Sec M f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	_
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrgOvlSta LKAPermFault Cnt M lgc	0	0 ± 1	
TrgOvlSta LKAPermFault Cnt M u08	2	2 ± 1	~
TrgOvlSta LKAState State M enum	0	0	
TrgOvlSta PosSrvoHwAngle HwDeg M f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrgOvlSta ReadytoPulse Cnt M lgc	0	0 ± 1	
target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	1	1±1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.189999998	0.189999998 ± 0.0009765625	
target_TrqOvlSta_Per1_TroScrownWaligle_nwbeg_132.value	0.0540000014	0.0540000014 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.0340000014	0.±1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	
target_riqeviota_reri_riqeserreq_riz_loz.value	7.55500011	7.00000011 ± 0.0000100020	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TroOvISta Per1 CP1 ChecknointReached	1	Rte Call TroOvISta Per1 CP1 CheckpointReached	1	-

Test Step 3.25 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvISta

2015-02-04, 14:05:40+0530



Name	Input Value
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvISta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	425564 -4.3699989
TrqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.0218000002
TrgOvlSta LKAFault Cnt M Igc	1
TrqOvISta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.20000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	0.135000005 7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k LKAMaxVehSpd Kph f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventInst_ApADrvrInt$	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_APAState_State_enum target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target TrqOvlSta Per1 HwTorque HwNm f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_IrqOvlSta_Per1_HW10rque_HWNm_132 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target TrqOvISta Per1 LKAInhibit Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOviSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_inst_Ap_frqOvista.frqOvista_Peri_frqOscEriable_Ont_igc target_Rte_inst_Ap_frqOviSta.frqOviSta_Peri_frqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscEnable_Crit_tigc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
	0
target TrgOvISta Per1 APANonRecoverableFaults Cnt loc value	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lqc.value	0
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0 0 0

2015-02-04, 14:05:40+0530



TrqOviSta_Peri		l Wal	SILAU
Name	Input Value		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TroOvlSta Per1 CP1 ChecknointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	V

Test Step 3.26 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k HapticReacttime Sec f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k LKAMinVehSpd Kph f32	15.75
k MaxHapticRegTime Sec f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k StandstillThresh Kph f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrqOvISta Per1 APAState State enum
· · · · · · · · · · · · · · · · · · ·	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target TrgOvISta Per1 APARecoverableFaults Cnt lgc.value	1
target TrqOvISta Per1 APARequest Cnt Igc.value	0
target_TrqOviota_r et 1_Ar Arcequest_ont_ige.value	0
target_TrqOvIsta_Per1_CCWPosition_HwDeg_f32.value	-375.51001
target_TrqOvIsta_Per1_CWFound_Cnt_Igc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.730011
target_rrqovista_Per1_cwPosition_nwDeg_isz.value target TrqOviSta_Per1_ESCFault_Cnt_lgc.value	1
	1
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value	
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	3.3500000
target_TrqOvlOta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOvlOta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1

© Report created by TESSY V3.1.9, report template V2.1

343

2015-02-04, 14:05:40+0530



Name	A etirel Maline	Funcated Value	Desuit
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.27 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	70.1999969
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApADrvrInterventionDetected_Cnt_Inst_ApADrvrInterventionDetected_Cnt_$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lc	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	0 - 1 1 0 -	0_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target TrqOvlSta Per1 TrqOscEnable Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	•	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvISta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-373.730011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	361.76001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvISta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvISta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvISta Per1 HwTorque HwNm f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target TrgOvISta Per1 LKAInhibit Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969		
target_TrqOvlSta_Per1_MinSecureVerlicleSpeed_Kpr1_isz.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Clit_igc.value target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc.value	1		
		Expected Value	Popult
Name Tracoulsta ADAStata Stata Michael	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	4	4	V
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	V
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	V
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	Y
TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32	425564	425564 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	•
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	V
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	*
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	0 4	4	

4

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
	Autual Falau	Exposiou valuo	rtoouit
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target TrqOvlSta Per1 TrqOscFreq Hz f32.value	7.63000011	7.63000011 ± 0.0009765625	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.28 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
FrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
FrqOvISta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	11150
rqOvlSta_Haptictime_Sec_M_f32	425564
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.36999989
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.200000003
c_APAMaxHwTrq_HwNm_f32	0.720000029
_APAMaxVehSpd_Kph_f32	70.1999969
_ESCMaxVehSpd_Kph_f32	31.7000008
	0.0540000014
	0.135000005
HapticFreq Hz f32	7.63000011
Sec_f32	0.143999994
	17.1000004
LKAMinVehSpd_Kph_f32	15.75
 :_MaxHapticReqTime_Sec_f32	0.150000006
SWARLimiter_HwDeg_f32	35.7000008
s_ShiftLevRevTime_Sec_f32	0.0370000005
StandstillThresh Kph f32	0.360000014
s_standstillTime_Sec_f32	0.0370000005
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target TrqOvlSta Per1 CCWFound Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCFault_Cnt_lgc	target_TrqOviota_ctcty_continue_cg_toz_
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target_TrqOvlota_ren_EscRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlota_ren_EscState_state_enum
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rerT_ESCState_State_enum arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rerr_GMOSH_APAMfgEnable_Cnt_igc	
arget_Rte_inst_Ap_friqOviSta.friqOviSta_PerT_GMOSH_APAMilgEnable_Cnt_igo arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSH_ESCMfgEnable_Cnt_igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc

© Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	Input Value target_TrqOvlSta_Per1_LKAState_State_en	um		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$		um		
target Dte Inst An TraOvISta TraOvISta Darl MinSecureVehicleSpeed Knh f32				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	arget_TrqOvlSta_Per1_PosServEnable_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	y_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32			
	1			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1			
target_TrqOvIsta_Per1_CCWFound_Cnt_Igc.value	0			
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-371.950012			
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1			
target_TrqOvIota_rer1_CWPosition_HwDeg_f32.value	361.790009			
target TrqOvISta Per1 ESCFault Cnt Igc.value	1			
target_TrqOvIota_retr_ESCIsLimited_Cnt_lgc.value	1			
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1			
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1			
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	79.1999969			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1			
target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32.value	0.189999998			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996			
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	0	0	~	
TrqOvlSta_ESCState_State_M_enum	4	4	•	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	•	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	•	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•	
TrqOvlSta_HapticState_State_M_enum	0	0	•	
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~	
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	•	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	•	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~	
TrqOvlSta_LKAState_State_M_enum	0	0	~	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~	
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	1	1 ± 1	~	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~	
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~	
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.18999998	0.189999998 ± 0.0009765625	~	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~	
target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value target_TrqOviSta_Per1_TrqOscEnable_Cnt_Igc.value target_TrqOviSta_Per1_TrqOscFreq_Hz_f32.value	0 7.63000011	0 ± 1 7.63000011 ± 0.0009765625	Ž	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~



Test Step 3.29 (Repeat Count = 1)	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
FrqOvlSta_ESCState_State_M_enum	0
FrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
FrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
FrqOvlSta_HapticState_State_M_enum	1
FrqOvISta_Hapticdur_Cnt_M_u16	41000 216790.344
FrqOvlSta_Haptictime_Sec_M_f32 FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.4000001
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.485000014
FrqOvlSta_LKAFault_Cnt_M_lgc	0
FrqOvISta_LKAPermFault_Cnt_M_lgc	1
FrqOvlSta_LKAPermFault_Cnt_M_u08	3
FrqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	
c_APAIncludeHaptic_Cnt_lgc	0
:_APAMaxHwTrqTime_Sec_f32 : APAMaxHwTrg HwNm f32	0.254999995 9.11999989
C_APAMaxHw1rq_Hwnm_ts2 C_APAMaxVehSpd_Kph_f32	205.199997
ESCMaxVehSpd Kph f32	15.6000004
c_s_napticAmplitude_MtrNm_f32	0
C_HapticDuration_Sec_f32	1.71000004
_HapticFreq_Hz_f32	56.973999
x_HapticReacttime_Sec_f32	1.824
LKAMaxVehSpd_Kph_f32	216.600006
_LKAMinVehSpd_Kph_f32	199.5
_MaxHapticReqTime_Sec_f32	0.20999993
_SWARLimiter_HwDeg_f32 _ShiftLevRevTime_Sec_f32	15.6000004 0.0529999994
StandstillThresh Kph f32	4.5599994
_StandstillTime_Sec_f32	0.0529999994
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target TrqOvlSta Per1 ESCIsLimited Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32 arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAInhibit Cnt Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f3 arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc	a target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SrilliLeverisinReverse_Crit_igc arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_Stritteveristrikeverse_Crit_igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
arget Rte Inst Ap TrqOvIsta.TrqOvIsta Per1 TrqOscEnable Cnt Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
arget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-370.170013		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.820007		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target TrqOvlSta Per1 LKAFault Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	- 100an
TrgOvlSta ESCState State M enum	3	3	~
TrgOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	87934.9609	87934.9609 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrgOvlSta HapticState State M enum	0	0	✓
TrgOvlSta Hapticdur Cnt M u16	41000	41000 ± 1	
TrgOvlSta Haptictime Sec M f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	
TrqOvlSta LKAFault Cnt M lgc	0	0 ± 1	~
TrgOvlSta LKAPermFault Cnt M lgc	1	1±1	
TrqOvlSta LKAPermFault Cnt M u08	3	3 ± 1	~
TrgOvISta LKAState State M enum	3	3	
TrgOvISta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	-
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target TrgOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target TrgOvlSta Per1 ESCState State enum.value	3	3	•
	0	0	
target TrgOvlSta Per1 GMOSHOscillateState State enum.value			✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target TrqOvlSta Per1 LKAState State enum.value	3	3	
target_TrqOvlSta_Per1_LKAState_State_enum.value		1 ± 1	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3	1 ± 1	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3		~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3 1 0.150000006	1 ± 1 0.150000006 ± 0.0009765625	· ·

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.30 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2020425
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	86335.6016
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	391692
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	31000
TrqOvlSta_Haptictime_Sec_M_f32	210780
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.430000007
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	41





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.232999995
k_APAMaxHwTrq_HwNm_f32 k_APAMaxVehSpd_Kph_f32	8.72000027 196.199997
k_ESCMaxVehSpd_Kph_f32	174.5
k_HapticAmplitude_MtrNm_f32	0.671999991
k_HapticDuration_Sec_f32	1.63499999
k_HapticFreq_Hz_f32	55.9459991
k_HapticReacttime_Sec_f32	1.74399996
k_LKAMaxVehSpd_Kph_f32	220
k_LKAMinVehSpd_Kph_f32	196
k_MaxHapticReqTime_Sec_f32	0.135000005
k_SWARLimiter_HwDeg_f32	17.7000008
k_ShiftLevRevTime_Sec_f32	0.032999998
k_StandstillThresh_Kph_f32	4.36000013
k_StandstillTime_Sec_f32	0.032999998
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARequest Cnt lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_linst_Ap_mqovista.mqovista_reri_ArARequest_ont_igc	target TrgOvlSta Per1 APAState State enum
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	a target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvIsta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fei1_TrqOscEnable Cnt lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreq Hz f32	target TrqOvISta Per1 TrqOscFreq Hz f32
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.390015
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.850006
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc.value	1
target_TrqOvISta_Per1_GMOSH_LKAMitgEnable_Cnt_lgc.value target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_Haptickequest_Ont_igc.value target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOvIsta_Per1_LKAFault_Cnt_lgc.value	1
target TrqOvISta Per1 LKAInhibit Cnt Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	61.7999992
	1
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	6.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	✓
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	31000	31000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	210780	210780 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.1500001	-5.1500001 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.671999991	0.671999991 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	55.9459991	55.9459991 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.31 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32	216790.344
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	15.6000004
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnd$	t_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target TrqOvlSta Per1 CCWPosition HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc	.gio2	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_frqOviSta_Peri_GMOSHOscillateSta target TrqOviSta Peri GMOSH APAMfgEr		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_SWARTrgtAngRequitarget TrqOvlSta Per1 ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-366.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.880005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Fe1 _MaxSecureVehicleSpeed_Rpf1_i32.value target TrqOvlSta_Pe11_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1	ı	
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	87934.9609	87934.9609 ± 1	*
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	V
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-3.4000001	-3.4000001 ± 0.000009	V
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0 1	0 ± 1 1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	1.5		
TIYOVISIA LIKAFEITIFAUIL CIIL IVI UUO	3	3 ± 1	V
TrqOvISta_LKAState_State_M_enum	3	3 ± 1 3	~

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Nama	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
FrqOvISta_Hapticdur_Cnt_M_u16	41000
FrqOvISta_Haptictime_Sec_M_f32	216790.344
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
FrqOvlSta_LKAFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_u08	3
FrqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
<pre>c_APAIncludeHaptic_Cnt_lgc</pre>	0
<pre>C_APAMaxHwTrqTime_Sec_f32</pre>	0.254999995
APAMaxHwTrg HwNm f32	9.11999989
<pre><_APAMaxVehSpd_Kph_f32</pre>	205.199997
CSCMaxVehSpd Kph f32	15.6000004
_	0
C_HapticAmplitude_MtrNm_f32	·
<_HapticDuration_Sec_f32	1.71000004
K_HapticFreq_Hz_f32	56.973999
C_HapticReacttime_Sec_f32	1.824
<pre>c_LKAMaxVehSpd_Kph_f32</pre>	216.600006
<_LKAMinVehSpd_Kph_f32	199.5
C_MaxHapticReqTime_Sec_f32	0.20999993
c_SWARLimiter_HwDeg_f32	15.6000004
ShiftLevRevTime_Sec_f32	0.0529999994
c_StandstillThresh_Kph_f32	4.55999994
_StandstillTime_Sec_f32	0.0529999994
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Ci	nt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc	target_TrqOviota_i ori_cown osition_imbog_oz
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCFault_Cnt_Igc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rei1_ESCFauit_Citt_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
arget Rte Inst Ap TrqOvIsta.TrqOvISta Per1 ESCRequest Cnt Igc	target TrgOvISta Per1 ESCRequest Cnt lgc
	0 - 1 - 1 - 2
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_e	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-364.829987		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	361.910004		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCI auti_Crit_igc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target TrqOvlSta Per1 HwTorque HwNm f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	61.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	1	1	~
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	¥ 4
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	Ž
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0.150000006	0.150000006 ± 0.0009765625 1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	-
	1	1	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	-
target TrqOviSta_Per1_LKAState_State_enum.value target TrqOviSta Per1_PosServEnable Cnt lgc.value	1	1 ± 1	
target TrqOvlSta Per1 PosServEhable_Ont_igc.value target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.1500000006 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.150000000	0 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1±1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	-
	1 1111		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.33 (Repeat Count = 1)	<u></u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_equip	0
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	1.3999998
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32 k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH APAMfgEnable Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 HapticRequest Cnt Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-363.049988		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.940002		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	2	2	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvISta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	· ·
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	· ·
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.34 (Repeat Count = 1) ✓		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	
TrqOvlSta_Haptictime_Sec_M_f32	0	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	
TrqOvlSta_LKAState_State_M_enum	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998	





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1.
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k APAMaxHwTrg HwNm f32	0
k APAMaxVehSpd Kph f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k HapticAmplitude MtrNm f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k LKAMinVehSpd Kph f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k SWARLimiter HwDeg f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k StandstillThresh Kph f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.269989
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.970001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target TrgOvlSta Per1 HwTorque HwNm f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.20000003
0 _ 1	· · · · · · · · · · · · · · · · · · ·





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.35 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntorellar (a) and the properties of the prope$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc	:	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enunceters$	target_TrqOvlSta_Per1_GMOSHOscillateSta	ite_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn	able_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_end	um	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-390.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
		0 ± 1	· ·
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1		
	1 0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	1 0 2	0 ± 1 2 ± 1	· ·

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.36 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOvlSta ElapsedStandstillTime Sec M f32	1
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrgOvlSta HwTorqueSV HwNm M Str.SV UIs f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta LKAFault Cnt M lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	1
TrqOvISta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt lgc	1
k APAMaxHwTrqTime Sec f32	0.601666689
k APAMaxHwTrq HwNm f32	0
k APAMaxVehSpd Kph f32	138.600006
k ESCMaxVehSpd Kph f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k LKAMaxVehSpd Kph f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k SWARLimiter HwDeg f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
<pre>k_ctandstiffrine_ess_rez target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_</pre>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_State_enum	target TrqOvISta Per1 APAState State enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_state_endin target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CWFound_Cnt_Igc	target TrqOvISta Per1 CWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target TrqOvISta Per1 ESCFault Cnt Igc
target_Rte_Irist_Ap_mqovista.mqovista_ren_escratii_cni_igc target_Rte_Irist_Ap_mqovista.mqovista_ren_escratii_cni_igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Cnt_Igc	target TrqOvISta Per1 ESCRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCRequest_Crit_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_mqovista.mqovista_ren_escstate_state_enum target Rte Inst Ap Trqovista.Trqovista Per1 GMOSHOscillateState State enum	
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_PeT_GMOSHOScillateState_State_enum target Rte Inst Ap TrqOviSta.TrqOviSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMilgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GwiOSH_ExAwiigEriable_Chi_igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_EXAMIGENABLE_CRIL_igc
target_trie_mat_hp_ridoviota.ridoviota_reri_naptichequest_crit_igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

© Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-388.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 362.029999		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	1		
target TrqOvISta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Clit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	*
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	V
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	V
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	1	*
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0.200000003	1 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.200000003 ± 0.0009765625 0.462000012 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.402000012	1 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	
target_1140VIOta_1 of 1_1140301 foq_fiz_102.Value	OE.0010001	02.00 1000 1 ± 0.00007 00020	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 3.37 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-1.64999998 0.419
TrqOvISta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	129 600006
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	138.600006 12
k_HapticAmplitude_MtrNm_f32	0.462000012
k HapticDuration Sec f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.308999985 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target TrqOvISta Per1 ESCRequest Cnt Igc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ESCState State enum	target TrgOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_LKAState_State_enum target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. \\$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvIsta_Per1_APARecuverablePaulis_Crit_Igc.value target_TrqOvIsta_Per1_APARequest_Cnt_Igc.value	1
target_TrqOvlota_rer1_crWFound_Cnt_lgc.value	1
<u> </u>	1

TrqOvlSta_Per1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-386.450012		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.20000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	✓
TrqOvlSta_ESCState_State_M_enum	2	2	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	*
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.38 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	1
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998

2015-02-04, 14:05:40+0530





A APANdederlong A-Holm 192		
L. APARAdeside Spec. Crt. Lige L. APARAdeside Spec. Rep. 12 1880,0000	Name	Input Value
A APARAMENTATINE Sec. 12 A APARAMENTATION SERVICES A APARAMENTATION SERVICES A APARAMENTATION SERVICES A RESOLVANCHINGS (Sp. 12) Linguistic Fire Linguist	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
A.	k_APAIncludeHaptic_Cnt_lgc	1
A. APAMAWAYES, Chr. 12	k_APAMaxHwTrqTime_Sec_f32	0.601666689
ESCHARVENSE Pai 12	k APAMaxHwTrq HwNm f32	0
A	k APAMaxVehSpd Kph f32	138.600006
A	k ESCMaxVehSpd Kph f32	12
1,1940/07		
1. Inspirite pt. 1/12 12 1399999 14 1300003 1 13000000000000000000000		
1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
K. JAMAWANSPE, KPh. 172		
A LANAMONNESSE (Pp. 132 K. J. WARHSPORTER) See, 132 S. SEMENTER (PP. 132 K. SEMENTER) SEE, 132 K. SEMENTER (PP. 132 K. SEMENTER) SEE, 133 K. SEMENTER (PP. 132 K. SEMENTER) SEE, 134 K. SEMENTER (PP. 132 K. SEMENTER) SEE, 134 K. SEMENTER (PP. 134 K. SEMENTER) SEE		
K. MANTENDER MARCHE SEC. 12 K. SIMELANDER MARCHE SEC. 12 K. SIME		
K. SMILLEN,		
K. Shadeullimer, Sec. 12 k. Shadeullimer, Sec. 13 L. Shadeullimer, Sec.		
k, Sandsdiffmen, Koft, 12 k, Sandsdiffmen, Koft, 12 target, Ric, Jinst, Ap, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. APARovinterventionDetected, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. SECFault, Cnt. Igo target, Ric, Jinst, Ap, Trock/Sia, Trock/Sia, Prof. Jinstock, Prof. Jinstock, Ap, Trock/Sia, Trock/Sia, Prof. Jinstock, Ap, Tr		
A Spandarillime Sec 12 staget, Ripolisa, Proficials, Englosis, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, Cov/Sta, Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip. Inst., Ap. Trock/Sta, Trock/Sta, Port, APADorinterventionDetected, Cri., Igo tutnet, Rip		
sange, Rie, Inst. Ap. Trockista Trockista, Pert. APADortineventionDetected, Cri., Ignore, Rie, Inst. Ap. Trockista Trockista, Pert. APADortineventionDetected, Cri., Ignore, Rie, Inst. Ap. Trockista Trockista, Pert. APARecoverableFaults, Cri., Ignore, Rie, Inst. Ap. Trockista Trockista, Pert. APARecoverableFaults, Cri., Ignore, Rie, Inst. Ap. Trockista Trockista, Pert. APARecoverableFaults, Cri., Ignore, Rie, Inst. Ap. Trockista Trockista, Pert. APARecoverableFaults, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. CVPFound, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. CVPFound, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. CVPFound, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. CVPFound, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. SCPFault, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. SCPFault, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. SCPFault, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. SCPFault, Cri., Ignore, Rie, Inst. Ap. Trockista, Trockista, Pert. GMOSHO-collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Trockista, Pert. GMOSHO-collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSHO-collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSHO-collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. GMOSH-Collabe/state State enum tarope, Rie, Inst. Ap. Trockista, Pert. Jakahine, Cri., Ignore, Rie, Inst. Ap. Trock		
traget, Rei, Inst. Ap. TragOvSta TragOvSta Pert J. APANorRecoverableFaults, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. APARorecoverableFaults, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. APARorecoverableFaults, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. APARorecoverableFaults, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. APARorecoverableFaults, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. CVMPosition J. Hobberg, IS2 target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. CVMPosition J. Hobberg, IS2 target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. SCSIstellined Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. MAROSH Dert J. HaptCRequest, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. InstagOvSta Pert J. MAROSH Dert J. HaptCRequest, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. MAROSH Dert J. HaptCRequest, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. MAROSH Dert J. HaptCRequest, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. MAROSH Dert J. HaptCRequest, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. MAROSH Dert J. HaptCRequest, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. MAROSH Dert J. HaptCRequest, Crit. Igo target, Rie, Inst. Ap. TragOvSta TragOvSta Pert J. MAROS		
traget, Rie, Inst. Ap. Tragovista Tragovista, Pert. J. PARRequest, Cri. Igo target, Tragovista, Pert. J. PARRequest, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. J. PARRequest, Cri. Igo target, Rie, Inst. Ap. Tragovista, Tragovista, Pert. J. COVFound, Cri. Igo target, Rie, Inst. Ap. Tragovista, Tragovista, Pert. J. COVFound, Cri. Igo target, Rie, Inst. Ap. Tragovista, Pert. J. COVFound, Cri. Igo target, Rie, Inst. Ap. Tragovista, Pert. G. COVFound, Cri. Igo target, Rie, Inst. Ap. Tragovista, Pert. G. COVFound, Cri. Igo target, Rie, Inst. Ap. Tragovista, Pert. G. COVFound, Cri. Igo target, Rie, Inst. Ap. Tragovista, Pert. I. SCSC Fault, Cri. Igo target, Rie, Inst. Ap. Tragovista, Pert. I. SCSC Fault, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. SCSC Fault, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. SCSC Fault, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. SCSC Fault, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. SCSC Fault, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. SCSC Fault, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. SCMOSH I. KAMIginable, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. SCMOSH I. KAMIginable, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. SCMOSH I. KAMIginable, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. KAMIginable, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. KAMinibili, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. KAMinibili, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. KAMinibili, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. KAMinibili, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. KAMinibili, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. KAMinibili, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. Prosservienble, Cri. Igo target, Rie, Inst. Ap. Tragovista Tragovista, Pert. I. Pro		
target. Rie. Ma. Ap. Trq-Orista. Prof. APARequest. Cnt. Igo: target. Rie. Ind. Ap. Trq-Orista. Prof. APARequest. Cnt. Igo: target. Rie. Ind. Ap. Trq-Orista. Trq-Orista. Prof. APARequest. Cnt. Igo: target. Rie. Ind. Ap. Trq-Orista. Trq-Orista. Prof. COWFound. Cnt. Igo: target. Rie. Ind. Ap. Trq-Orista. Trq-Orista. Prof. COWFound. Cnt. Igo: target. Rie. Ind. Ap. Trq-Orista. Trq-Orista. Prof. CoWFound. Cnt. Igo: target. Rie. Ind. Ap. Trq-Orista. Trq-Orista. Prof. CoWFound. Cnt. Igo: target. Rie. Ind. Ap. Trq-Orista. Trq-Orista. Prof. CoWFound. Cnt. Igo: target. Rie. Ind. Ap. Trq-Orista. Trq-Orista. Prof. Ind. Company. Ind. Ind. Ind. Ind. Ind. Ind. Ind. Ind		
target, Rie, Inst. Ap. TrqOviSta TrqOviSta. Pert. APAState, State, enum traget, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. CWGround, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. CWGround, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. CWGround, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. CWGround, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. CWGround, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. ESCFault, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. ESCReatt, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. ESCReatt, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. ESCReates, State, enum target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH APANdigficable; Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH APANdigficable; Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH APANdigficable; Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH APANdigficable; Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH. LKANdigficable; Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH. LKANdigficable; Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKARequest, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKARequest, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKARequest, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKARequest, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKARequest, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKARequest, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKARequest, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKARequest, Cnt. Ige target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. InstOviSta. Pert. Ins		
target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. COWFound. Cnt. Lgo target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. COWFound. Cnt. Lgo target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. COWFound. Cnt. Lgo target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. COWFound. Cnt. Lgo target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. COWFound. Cnt. Lgo target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. ESCISLINITED. Cnt. Lgo target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. ESCISLINITED. Cnt. Lgo target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. ESCISLINITED. Cnt. Lgo target. Rie. Inst. Ap. TrqOvista. TrqOvista. Pert. ESCISLINITED. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. ESCISLINITED. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. GMOSHOcillausctatus. State. enum target. Rie. Linst. Ap. TrqOvista. Pert. GMOSHOcillausctatus. State. enum target. Rie. Linst. Ap. TrqOvista. Pert. GMOSH. ESCIMBjenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LAMInfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LaMinfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LaMinfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LaMinfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista. Pert. LaMinfenable. Cnt. Lgo target. Rie. Linst. Ap. TrqOvista. TrqOvista		
target Rie Inst. Ap. Tro/Oxista Tro/Oxista Pert. CCWPosition. HwDeg. 32 target. Tro/Oxista Tro/Oxista Pert. CCWPosition. HwDeg. 32 target. Tro/Oxista Pert. CCWPosition. HwDeg. 32 target. Tro/Oxista Pert. CCWPosition. HwDeg. 32 target. Tro/Oxista Pert. CWPosition. HwDeg. 32 target. Tro/Oxista. Pert. CWPosition. PwDeg. 32 target. Tro/Oxista. Pert. LWD. PwDe	·	0 _ 1
tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl CWPound, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl CWPound Ent. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiStamited, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiStamited, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiStamited, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiState State enum tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiState State enum tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiState State, enum tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiState State, enum tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiState State, enum tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiState State, enum tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiState State, enum tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl ESCiState, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl State, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl LKAFault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl InstAfault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl InstAfault, Cnt. Ige tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl Star Perl Minisceure/eniceSpeed, Kph. 122 tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl SynArtrytAngeneuvel HwDeg. 132 tanget, Rei, Inst. Ap. TrqOviSta TrqOviSta Perl SynArtrytAngeneuvel HwDeg. 132 tanget, Rei, Inst. Ap. Trq		
tanget Ries Inst. Ap. TrqOvista TrqOvista Pert CWPosition HwDeg. [32] tanget Ries Inst. Ap. TrqOvista TrqOvista Pert ESCFault Cnt. Igc tanget Ries Inst. Ap. TrqOvista TrqOvista Pert ESCRaquest Cnt. Igc tanget Ries Inst. Ap. TrqOvista TrqOvista Pert ESCRaquest Cnt. Igc tanget Ries Inst. Ap. TrqOvista TrqOvista Pert ESCRaquest Cnt. Igc tanget Ries Inst. Ap. TrqOvista TrqOvista Pert ISCRaquest Cnt. Igc tanget Ries Inst. Ap. TrqOvista TrqOvista Pert ISCRAquest Cnt. Igc tanget Ries Inst. Ap. TrqOvista Pert ISCRAQuest Cnt. Igc tanget Ries Inst. Ap. TrqOvista Pert ISCRAQuest Estate enum tanget Rie Inst. Ap. TrqOvista Pert ISCRAQUEST PERT	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	
target, Rie, Inst. Ap_TrqOviSta TrqOviSta. Perf_ESCRust_Cnt_Igo target_TrqOviSta. Perf_ESCRust_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta. Perf_ESCRust_Cnt_Igo target_TrqOviSta. Perf_ESCRust_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta. Perf_ESCRust_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta. Perf_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta. Perf_GSCState_State_enum target_Rie_Inst_Ap_TrqOviSta. Perf_GSCSt_LXMRienable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta. Perf_GSCSt_LXMRienable_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta. Perf_HaptiCRequest_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta. Perf_LXKRault_Cnt_Igo target_Rie_Inst_Ap_TrqOviSta. Perf_DScoreNavAgle_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_DScoreNavAgle_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_DScoreNavAgle_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_DScoreNavAgle_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_PscoreNavAgle_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_PscoreNavAgle_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_PscoreNavAgle_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_PscoreNavAgle_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_TrqOviSta. Perf_TrqOscRep_HivDeg_Rie_Inst_Ap_TrqOviSta. Perf_TrqOviSta. Perf_TrqOscRep_HivDeg_Rie_Inst_Ap_TrqOviSta. Per		
target, Riel, Inst. Ap. TrqOVISIa TrqOVISIa Perf_ESCRequest_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_ESCRequest_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_ESCRequest_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_ESCRequest_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_ESCRequest_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_GMOSH-DAMIGEnable_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_GMOSH-DAMIGEnable_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_GMOSH-LAMIGEnable_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_GMOSH-LAMIGEnable_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.Perf_HaptcRequest_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.Perf_HaptcRequest_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.Perf_HaptcRequest_Cnt_lgc target_Rel_Inst.Ap. TrqOVISIa.Perf_LAMIGE_CRL_Igc target_Rel_Inst.Ap. TrqOVISIa.Perf_MassecureVelvicleSpeed_Kph_RS2 target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_MassecureVelvicleSpeed_Kph_RS2 target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_PosservEnable_Cnt_Igc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_Schem_IntringLamide_Crl_Igc target_Rel_Inst.Ap. TrqOVISIa.TrqOVISIa.Perf_Schem_IntringLamide_Crl_Igc target_Rel_Inst.Ap. TrqOVISIa.Perf_Schem_Intri	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _ESCRequest_Cnt_lgc target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _ESCState_State_enum target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _GMOSH-OscillateState_State_enum target, Rie, Inst. Ap., TrqOviSta, TrqOviSta, Perl _GMOSH-OscillateState_State_enum target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _GMOSH-ASMIGEnable_Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _GMOSH-ASMIGEnable_Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _GMOSH LKAMigEnable_Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _HapticRequest, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _LKAFaut, Cnt_lgc target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _MKAFaut, Perl, Perl, MarkecureverbicleSpeed_Kpt, Inst. target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _MKAFaut, Perl, MarkecureverbicleSpeed_Kpt, Inst. target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _PosTrajhtwAngle_Hwbeg132 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Perl _ToposCena_MtriMtriMtriAg target, TrqOviSta, Perl _Ap. TrqOviSta, Perl _ToposCena_MtriMtriAg target, Tr	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ERCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMIgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARAMfgEnable_Crt_Igo target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Pers_Inst_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Sol_TrqOviSta_Pert_Tr	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH-DSOILaleState, State, enum target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH-ApAMIgEnable Cnt. I.go target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH-LESCMIgEnable Cnt. I.go target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH, LESCMIgEnable Cnt. I.go target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. GMOSH, LESCMIgEnable, Cnt. I.go target, Rie, Inst. Ap. TrqOviSta. TrqOvSta. Pert. JAMDISta. Pert. JAMDIST	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target. Rite. Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH LAPAMigEnable Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH LESKIRgEnable Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. GMOSH LESKIRgEnable Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. HapticRequest. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. HapticRequest. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. HapticRequest. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKAFault. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKAFault. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKAFault. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKAFault. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKAFault. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKAFault. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKAFault. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LKAFault. End. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. JKAFault. End. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. MinsceureVerhicleSpeed Kph. f32 target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosSevEnable. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosSevEnable. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosTrajiehable. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosTrajiehable. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosTrajiehable. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosTrajiehable. Inst. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. TrqOviSta. Pert. PosTrajiehable. Cnt. Igo target. Rive. Inst. Ap. TrqOviSta. TrqOviSta. Pert. Lego. Trq	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rie_Inst_Ap_TrqOviSta_Prer1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_HaptiCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_HaptiCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_HaptiCRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_MasSecureVehicleSpeed_Kph_GS2 target_Rie_Inst_Ap_TrqOviSta_Per1_PasServehArable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Per1_PasServehArable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSroviAvafapl_HwDeg_1S2 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCafapl_HwDeg_1S2 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCafapl_HwDeg_1S2 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCafapl_HwDeg_1S2 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCafapl_HwDeg_1S2 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCafapl_HwDeg_1S2 target_Rie_Inst_Ap_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Per1_CwFound_Cnt_lgc_value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Per1_GSCReples_Cnt_lgc_value 1 target_TrqOviSta_Per1_GSCReples_Cnt_lgc_value 1 target_TrqOviSta_P	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enun$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_NimSccureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrorhwangle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrorhwangle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Shift_everisInReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Shift_everisInReverse_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchaple_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchaple_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsSchaple_Cnt_Igc target_TrqOviSta_Pert_Swartgrager_Cnt_Igc target_TrqOviSta_Pert_ApARomRecoverableFaults_Cnt_Igc-value 0 target_TrqOviSta_Pert_ApARomRecoverableFaults_Cnt_Igc-value 1 target_TrqOviSta_Pert_ApARomRecoverableFaults_Cnt_Igc-value 1 target_TrqOviSta_Pert_CWPostion_HwDeg_f32.value 10 target_TrqOviSta_Pert_CWPostion_HwDeg_f32.value 10 target_TrqOviSta_Pert_SCRequest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCRequest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCRequest_Cnt_Igc-value 1 target_TrqOviSta_Pert_SCRequest_Cnt_Igc-value 1 target_TrqOviSta_Pert_GMOSH_APAMgEnable_Cnt_Igc-value 1 target_TrqO	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HarpticRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HwTorque_HwNn_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARstae_Unt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARstae_Unt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARstae_Unt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StriJthwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StriJthwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StriJthwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_StriJthwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_TrqOviSta_Pert_TrqOsCate_Mpt_Mthm_f32 target_TrqOviSta_Pert	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rie_Inst_Ap_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAState_State_enum target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecurevehicleSpeed_Kph_gr32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SorTifInable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SorTifInable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SorTifInable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SorTifInable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_Mithm_gr32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_Mithm_gr32 target_Rie_Inst_Ap_TrqOviSta_Pert_Aproxect_able_Cnt_lgc target_TrqOviSta_Pert_Aproxect_Aprox	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ShriftLeverIsinReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_TrqOsCenable_Cnt_lgc target_TrqOviSta_Per1_CoviPosition_HwDeg_f32_value 1 target_TrqOviSta_Per1_CoviPosition_HwDeg_f32_value 2 target_TrqOviSta_Per1_CoviPosition_HwDeg_f32_value 3 target_TrqOviSta_Per1_CoviPosition_HwDeg_f32_value 1 target_TrqOviSta_Per1_EnsCenable_Cnt_lgc_value 1 target_TrqOviSta_Per1_EnsCenable_Cnt_lgc_value 1 target_TrqOvi	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKASRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_LKASRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovHavAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwNorHvAngle_HvDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CvtFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_EsCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_	target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt Igc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_KARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_RtqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSevnehable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrovhWangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TosSrovhWangle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCFreq_Hz_f32 target_TrqOvISta_Per1_APANorRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRault_Cnt_Igc.value 1 target_TrqOvISta_Per1		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta.Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_SVMARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_SVMARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_SVMARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_SVMARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_TrqOsCenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta.Per1_TrqOsCenable_Cnt_Igc target_TrqOviSta.Per1_APARcoverableFaults_Cnt_Igc.value target_TrqOviSta.Per1_APARcoverableFaults_Cnt_Igc.value 1 target_TrqOviSta.Per1_APARcoverableFaults_Cnt_Igc.value 1 target_TrqOviSta.Per1_CWPound_Cnt_Igc.value 1 target_TrqOviSta.Per1_CWPound_Cnt_Igc.value 1 target_TrqOviSta.Per1_CWPosition_HwDeg_f32_value 362_089996 target_TrqOviSta.Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOviSta.Per1_GMOSH_APAMfigenable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwiRtTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_APARevoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 10 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 11 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 12 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 13 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 13 target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value 14 target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value 15 target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value 16 target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value 17 target_TrqOvlSta_Per1_MDeg_f32_tarlov 17 targo		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvbndwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOsCamp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOsCamp_MtrNm_f32 target_TrqOvlSta_Per1_ApAnonRecoverableFaults_Cnt_lgc_value target_TrqOvlSta_Per1_ApANonRecoverableFaults_Cnt_lgc_value target_TrqOvlSta_Per1_ApANonRecoverableFaults_Cnt_lgc_value target_TrqOvlSta_Per1_ApARequest_Cnt_lgc_value target_TrqOvlSta_Per1_ApARequest_Cnt_lgc_value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32_value target_TrqOvlSta_Per1_CWPound_Cnt_lgc_value target_TrqOvlSta_Per1_CWPound_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRaut_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc_value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc_value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc_value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc_value		
target_Rte_Inst_Ap_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvISta_Per1_APARCeoverableFaults_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARCeoverableFaults_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvISta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc_value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc_value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc_value 1		
target_Rte_Inst_Ap_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANnonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_PANMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_PANMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value		
target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_inst_Ap_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_inst_Ap_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_inst_Ap_TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_f33 target_Rte_inst_Ap_TrqOviSta_Per1_SHiftLeverIsInReverse_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_ShiftLeverIsinReverse_Cnt_lgc target_Rte_inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_ShiftLeverIsinReverse_Cnt_lgc target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_CovFound_Cnt_lgc.value 1 target_TrqOviSta_Per1_CovFound_Cnt_lgc.value 1 target_TrqOviSta_Per1_CovFound_Cnt_lgc.value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOviSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_ShiftLeverIsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta.Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_ApraceoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARequest_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARcoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARcoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value		
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_igc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_igc.value target_TrqOvlSta_Per1_APARequest_Cnt_igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_igc.value target_TrqOvlSta_Per1_CCWFound_Cnt_igc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target_TrqOvlSta_Per1_ESCFault_Cnt_igc.value target_TrqOvlSta_Per1_ESCFault_Cnt_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_igc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_igc.value 1	0 = = = 1 = 1 = = = = = = = = = = = = =	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_SCMfgEnable_Cnt_lgc.value 1	0 = = = 1	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	· - ·	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value -384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_SECMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -384.670013 target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value 1 target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_Igc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.089996 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.089996
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
1 T 0 191 B 4 0 10 0 11 1 1 1 1 1 1 1 1 1 1 1 1 1 1	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_IrqOviSta_Per1_GMOSH_LKAMigEnable_Cnt_lgc.value	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1	· - ·	
	target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1	· - ,	
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	
target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 1		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.200000003		

© Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	0	1 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	<u> </u>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.39 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.6499998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc$	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target TrqOvlSta Per1 CCWPosition HwDe		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_lgc	.gio2	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target TrqOvISta Per1 GMOSH APAMfgEr		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvISta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAState State enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lg target_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	-	
<pre>target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc</pre>	target_TrqOvlSta_Per1_SWARTrgtAngRequitarget_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-382.890015		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.119995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	V
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	2 0.00200000009 ± 1	V
TrgOvISta_EtapsedAPAMaxHwTrqTime_Sec_Iw_i52 TrgOvISta_EtapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1 0.002000000009 ± 1	-
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	1 ± 1 0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_gc TrqOvISta_LKAPermFault_Cnt_M_u08	2	2±1	-
TrqOvlSta_LKAState_State_M_enum	0	0	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	✓

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.20000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	•
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 3.40 (Repeat Count = 1)	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
ГгgOvlSta APAState State M enum	2
rqOvISta_ESCState_State_M_enum	1
rqOvISta_ESCState_state_M_enum rqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
rqOvISta_ElapsedArAwaxnwTrqTime_sec_M_f32	84736.2422
	378600
rqOvlSta_ElapsedStandstillTime_Sec_M_f32 rqOvlSta_HapticState_State_M_enum	1
rqOvISta_Hapticdur_Cnt_M_u16	33000
rqOvISta Haptictime Sec M f32	204770
	-4.8000019
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.441000015
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32 rqOvlSta_LKAFault_Cnt_M_lgc	
	1 0
rqOvISta_LKAPermFault_Cnt_M_lgc	3
rqOvISta_LKAPermFault_Cnt_M_u08	
rqOvISta_LKAState_State_M_enum	0
qOvISta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
rqOvlSta_ReadytoPulse_Cnt_M_lgc	
APAMovHwTraTime See #33	0.210999995
_APAMaxHwTrqTime_Sec_f32 APAMaxHwTrq HwNm f32	8.80000019
_APAMaxVehSpd_Kph_f32	198
ESCMaxVehSpd_Kph_f32	164.300003 0.660000026
HapticAmplitude_MtrNm_f32	
HapticDuration_Sec_f32	1.6499998 54.9179993
HapticFreq_Hz_f32	
HapticReacttime_Sec_f32	1.75999999
_LKAMaxVehSpd_Kph_f32	209
_LKAMinVehSpd_Kph_f32	120.220001
_MaxHapticReqTime_Sec_f32	0.150000006
SWARLimiter_HwDeg_f32	58.2999992
ShiftLevRevTime_Sec_f32	0.0370000005 4.400001
StandstillThresh_Kph_f32	0.0370000005
StandstillTime_Sec_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_t			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_F		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_ShiftLeverIsInReve	_ *	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Ont_igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3		
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0	2	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 CCWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.529999		
target TrqOvlSta Per1 CWFound Cnt Igc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.480011		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value Name	Actual Value	Expected Value	Result
Name TrqOvlSta_APAState_State_M_enum	Actual Value	0	~
Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	Actual Value 0 3	0	~
Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	Actual Value 0 3 0	0 3 0±1	, , , , , , , , , , , , , , , , , , ,
Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	Actual Value 0 3 0 84736.2422	0 3 0±1 84736.2422±1	, , , , , , , , , , , , , , , , , , ,
Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	Actual Value 0 3 0 84736.2422 0	0 3 0±1 84736.2422±1 0	, , , , , , , , , , , , , , , , , , ,
Name TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	Actual Value 0 3 0 84736.2422 0 1	0 3 0±1 84736.2422±1 0	, , , , , , , , , , , , , , , , , , ,
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16	Actual Value 0 3 0 84736.2422 0 1 33000	0 3 0±1 84736.2422±1 0 1 33000±1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32	Actual Value 0 3 0 84736.2422 0 1 33000 0	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1	· · · · · · · · · · · · · · · · · · ·
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1 0 ± 1	
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1 0 ± 1 3 ± 1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 0.109999999	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0 0.109999999 ± 0.0009765625	
Name TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 0.1099999999 1	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0 0.109999999 ± 0.0009765625 1 ± 1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 0.109999999	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0 0.109999999 ± 0.0009765625	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 0.109999999 1	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0 0.109999999 ± 0.0009765625 1 ± 1 0 ± 1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 0.109999999 1 0 0	0 3 0 ± 1 84736.2422 ± 1 0 1 33000 ± 1 0 ± 1 -4.80000019 ± 0.000009 1 ± 1 0 ± 1 3 ± 1 0 0 0.109999999 ± 0.0009765625 1 ± 1 0 ±	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticotlor_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_ESCState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 0.109999999 1 0 0 3	0 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 0±1 3±1 0 0.109999999±0.0009765625 1±1 0±1 0 3	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticdur_Cnt_M_u16 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 3 0 0.109999999 1 0 0 3 1	0 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 0.1099999999±0.0009765625 1±1 0±1 0 3 1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_EIapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_EIapsedShifftLevRevTime_Sec_M_f32 TrqOvISta_EIapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_HapticotIne_Sec_M_f32 TrqOvISta_HaptictIme_Sec_M_f32 TrqOvISta_HaptictIme_Sec_M_f32 TrqOvISta_HaptictIme_Sec_M_f32 TrqOvISta_HaptictIme_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_gc TrqOvISta_LKAPermFault_Cnt_M_gc TrqOvISta_LKAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 0 3 0 0.109999999 1 0 0 3 1 0 0	0 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 0.1099999999±0.0009765625 1±1 0±1 0 3 1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_EIapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_EIapsedShifftLevRevTime_Sec_M_f32 TrqOvISta_EIapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticotime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_Igc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_PcsSrvoHwAngle_HwDeg_M_f32 TrqOvISta_PcsSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 0 0.109999999 1 0 0 1 1 0 1	0 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 0.1099999999±0.0009765625 1±1 0±1 0 3 1 0 1±1	
TrqOvISta_APAState_State_M_enum TrqOvISta_ESCState_State_M_enum TrqOvISta_EIapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvISta_EIapsedShiftLevRevTime_Sec_M_f32 TrqOvISta_EIapsedStandstillTime_Sec_M_f32 TrqOvISta_HapticState_State_M_enum TrqOvISta_Hapticitime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_Haptictime_Sec_M_f32 TrqOvISta_HavTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_LKAPermFault_Cnt_M_u08 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvISta_ReadytoPulse_Cnt_M_lgc target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_Igc.value target_TrqOvISta_Per1_APAState_State_enum.value target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_LKAState_State_enum.value target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value	Actual Value 0 3 0 84736.2422 0 1 33000 0 -4.80000019 1 0 0 3 0 0.109999999 1 0 0 1 0 1 0 1 0 1 0.109999999	0 3 0±1 84736.2422±1 0 1 33000±1 0±1 -4.80000019±0.000009 1±1 0±1 3±1 0 0.109999999±0.0009765625 1±1 0±1 0 3 1 0 1±1 0 1±1 0 1.009999999±0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V



Test Step 3.41 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	38.2000008
TrqOvISta_ReadytoPulse_Cnt_M_lgc	1
k APAIncludeHaptic Cnt Igc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	59.2999992 0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k StandstillTime Sec f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOviota_TrqOviota_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_state_enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target Rte Inst Ap TrqOvIsta.TrqOvIsta Per I_viiinSecurevenicieSpeed_kpn_isz target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosServEnable Cnt Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-389.75		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.51001		
target TrgOvlSta Per1 ESCFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	-
TrqOvlSta_ESCState_State_M_enum	3	3	·
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499	1952499 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	·
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	-
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	-
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	-

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.42 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k APAMaxVehSpd Kph f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k HapticAmplitude MtrNm f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k LKAMinVehSpd Kph f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k SWARLimiter HwDeg f32	59.2999992
	0.0370000005
k_ShiftLevRevTime_Sec_f32	
k_StandstillThresh_Kph_f32	4.400001 0.037000005
k_StandstillTime_Sec_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle HwDeg f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 SWARTrgtAngRequest HwDeg f3.	V = 1
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ShiftLeverIsInReverse Cnt Igc	
	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-387.970001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.540009
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999
5 _ 1 =	· · · · · · · · · · · · · · · · · · ·





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	204770	204770 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	-
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.43 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	59.2999992
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ılts_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDe	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWFound_Cnt_lgc target_TrqOvlSta_Per1_CWPosition_HwDeg	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvISta_Per1_ESCFault_Cnt_lgc	_102	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lg	ac	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrqOvlSta Per1 ESCRequest Cnt Ig		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_end	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateSta	te_State_enum	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEn	able_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKARequest Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	•	
target Rte Inst Ap TrqOvISta.TrqOvISta Per I LKAState State enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lgraget_TrqOvlSta_Per1_LKAState_State_enu		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvISta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvISta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvlSta_Per1_SWARTrgtAngReque	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target TrgOvlSta Per1 CCWPosition HwDeg f32.value	-386.190002		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.570007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	-8.10000038 1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ErxIntequest_Ont_igc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	-
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422 0	84736.2422 ± 1 0	Ĭ
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 TrqOvlSta_HapticState_State_M_enum	1	1	
TrqOvISta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019	-4.80000019 ± 0.000009	
TrqOvISta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvISta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.109999999	0.109999999 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.109999999	0.109999999 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Name	Input Value
	•
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
FrqOvlSta_LKAFault_Cnt_M_lgc	1
FrqOvlSta_LKAPermFault_Cnt_M_lgc	0
FrqOvlSta_LKAPermFault_Cnt_M_u08	0
FrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
<_APAIncludeHaptic_Cnt_lgc	0
C_APAMaxHwTrqTime_Sec_f32	0.36500001
C_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
c_ESCMaxVehSpd_Kph_f32	52.5999985
_HapticAmplitude_MtrNm_f32	0.648000002
_HapticDuration_Sec_f32	1.62
<_HapticFreq_Hz_f32	3
K_HapticReacttime_Sec_f32	1.72800004
k LKAMaxVehSpd Kph f32	205.199997
c_LKAMinVehSpd_Kph_f32	0
MaxHapticRegTime Sec f32	0.119999997
<_SWARLimiter_HwDeg_f32	0.119999997
<pre>ShiftLevRevTime_Sec_f32</pre>	0.0289999992
 <_StandstillThresh_Kph_f32	4.32000017
StandstillTime Sec f32	0.0289999992
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Ci	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target_TrqOvIsta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Fet 1_CCWFosition_Hwbeg_is2 arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CWFound_Cnt_Igc	target_TrqOvISta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_PerT_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOvIsta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target TrqOvlSta Per1 ESCRequest Cnt lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	0 = 1 = 2 = 2
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_c	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Ig	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_C		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg		·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRev		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrN		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f	32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-384.410004		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.600006		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994		
target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1	1=	1
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvISta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	*
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.449800014	0.449800014 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	2	2	V
T 0 101 D 0 11 A 1 11 T 11 T 11 T 11 T 11 T 11 T		0.150000006 ± 0.0009765625	→
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006		
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1 0	1 ± 1 0 ± 1	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	1 0 3	1 ± 1 0 ± 1 3	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value	1 0 3 3	1±1 0±1 3 3	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1 0 3 3 0	1±1 0±1 3 3 0	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	1 0 3 3 3 0 2 2	1±1 0±1 3 3 0	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1 0 3 3 0 2	1±1 0±1 3 3 0 2 1±1	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 0 3 3 0 2 1 0.150000006	1±1 0±1 3 3 0 2 1±1 0.150000006±0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	1 0 3 3 0 2 1 0.150000006 0.648000002	1±1 0±1 3 3 0 2 1±1 0.150000006±0.0009765625 0.648000002±0.0009765625	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value target_TrqOvlSta_Per1_ESCState_State_enum.value target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1 0 3 3 0 2 1 0.150000006	1±1 0±1 3 3 0 2 1±1 0.150000006±0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.45 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	-5.5 0.419
TrqOvISta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	194.399994 52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k HapticDuration Sec f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	0.119999997
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017 0.0289999992
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target TrqOvlSta Per1 ESCRequest Cnt lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 ESCState State enum	target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	V - 1
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_inst_Ap_irqOviSta.irqOviSta_Peri_LKAState_state_enum target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Peri_MaxSecureVehicleSpeed_Kph_f32	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MinSecureVehicleSpeed Kph f32	target TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3. \\$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlota_rer1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.630005		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.630005		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.69999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	3	3	_
TrgOvlSta ESCState State M enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	_
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	83136.8828	83136.8828 ± 1	•
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	_
TrqOvlSta HapticState State M enum	0	0	_
TrqOvISta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	_
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	0.449800014	0.449800014 ± 0.0000009	
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	_
TrqOvISta LKAPermFault Cnt M Igc	0	0 ± 1	
TrqOvISta_LKAPermFault_Cnt_M_u08	0	0 ± 1	-
TrqOvISta_LKAState_State_M_enum	2	2	
TrqOvISta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	-
TrqOviSta ReadytoPulse Cnt M lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	
target TrqOvISta Per1 ESCState State enum.value	3	3	-
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target TrqOvlSta Per1 LKAState State enum.value	2	2	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.648000002	0.648000002 ± 0.0009765625	
target_TrqOviSta_Per1_TrqOscEnable_Cnt_lgc.value	0.646000002	0.646000002 ± 0.0009765625 0 ± 1	
	3	3 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	٥	3 I U.UUU3/00020	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.46 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k APAMaxHwTrg HwNm f32	8.64000034
k APAMaxVehSpd Kph f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k HapticAmplitude MtrNm f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k LKAMinVehSpd Kph f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k SWARLimiter HwDeg f32	52.5999985
k_ShiftLevRevTime_Sec_f32	0.0289999992
k StandstillThresh Kph f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-380.850006
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.660004
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.69999981
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	•
TrqOvlSta_LKAState_State_M_enum	1	1	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.47 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	29000
TrqOvlSta_Haptictime_Sec_M_f32	198760.641
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	1.
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	0
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.36500001
k_APAMaxHwTrq_HwNm_f32	8.64000034
k_APAMaxVehSpd_Kph_f32	194.399994
k_ESCMaxVehSpd_Kph_f32	52.5999985
k_HapticAmplitude_MtrNm_f32	0.648000002
k_HapticDuration_Sec_f32	1.62
k_HapticFreq_Hz_f32	3
k_HapticReacttime_Sec_f32	1.72800004
k_LKAMaxVehSpd_Kph_f32	205.199997
k_LKAMinVehSpd_Kph_f32	0
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	53.5999985
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	4.32000017
k_StandstillTime_Sec_f32	0.0289999992
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (Ap_TrqOvlSta$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Singel, Rev. Mar. CrossRes Track State CrossRes	Name	Input Value		
toggs, Rev. Let. 2, Trotoks Trotoks per J. APARagemes, Cnt. 19: taggs, Rev. Let. 2, Trotoks Trotoks per J. APARagemes, Cnt. 19: taggs, Rev. Let. 2, Trotoks Trotoks per J. APARagemes, Cnt. 19: taggs, Rev. Let. 2, Trotoks Trotoks per J. Common Judge, 22: target, Rev. Let. 2, Trotoks Trotoks per J. Common Judge, 22: target, Rev. Let. 2, Trotoks Trotoks per J. Common Judge, 22: target, Rev. Let. 2, Trotoks Trotoks per J. Common Judge, 22: target, Rev. Let. 2, Trotoks Trotoks per J. Common Judge, 22: target, Rev. Let. 2, Trotoks Trotoks per J. Common Judge, 22: target, Rev. Let. 2, Trotoks Trotoks per J. ESCReparaget, Cnt. 19: target, Rev. Let. 2, Trotoks Trotoks per J. ESCReparaget, Cnt. 19: target, Rev. Let. 2, Trotoks Trotoks per J. Common J. Comm	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg$	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
Imaged Rept Rept April Profession Troky Sep Pert April	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFau	ılts_Cnt_lgc	
stager, Rev. (1-pt-05/98a TripO5/98a print (2007-pt-05/98a) and (2007-pt-05/98a) arranger, Rev. (1-pt-05/98a) arranger, Rev. (1-pt-0				
singer, Re, Jink J. P. (Foldbillar Spork) Bay Pert J. COMPosition, 1946, 194 singer, Re, Jink J. P. (Foldbillar Spork) Bay Pert J. COMPosition, 1946, 194 singer, Re, Jink J. P. (Foldbillar Spork) Bay Pert J. COMPosition, 1946, 194 singer, Rep. Jink J. P. (Foldbillar Spork) Bay Pert J. COMPosition, 1946, 194 singer, Rep. Jink J. P. (Foldbillar Spork) Bay Pert J. Schollar, or L. (p. 194) singer, Rep. Jink J. P. (Foldbillar Spork) Bay Pert J. Schollar, or L. (p. 194) singer, Rep. Jink J. P. (Foldbillar Spork) Bay Pert J. Schollar, or L. (p. 194) singer, Rep. Jink J. P. (Foldbillar Spork) Bay Pert J. Schollar, or L. (p. 194) singer, Rep. J. (p. 1946) Bay Pert J. Schollar, or L. (p. 1944) singer, Rep. J. (p. 1946) Bay Pert J. Schollar, or L. (p. 1944) singer, Rep. J. (p. 1946) Bay Pert J. Schollar, or L. (p. 1944) singer, Rep. J. (p. 1946) Bay Pert J. Schollar, or L. (p. 1944) singer, Rep. J. (p. 1946) Bay Pert J. (p. 1946) Bay Pe				
langer, Rim, Jan. J. Procisional Procisional Part Conference Circles straget, Trajoutine David Conference				
Singel, Rev. Ap. TropONSE TOOKSE PET. SEPTIAL TOURS SEPTIAL TOOKSE PET. SECTIAL CHILD			g_t32	
Larged, Rev. Mar. Apt. Trigothias Trigothias Pert ESCRIANIC CHI go			422	
Singet, Rev. May Trop/Nets Trop/Nets Perf. Schilamed Ont. 190			_132	
targer, Rev. Aut. Troch/State Troch/State Pert Section State came targer, Rev. Int. Aut. Troch/State Troch/State Pert Section State came targer, Rev. Int. Aut. Troch/State Troch/Stat			70	
Langer, Rep. Inst. Ap. Trop/Ness Trop/Ness Perf. SSOSies Sate, enum Insign Rep. Inst. Ap. Trop/Ness Trop				
larger, Rev. May. Track/State Track State Part State S				
Imaged Rie, Beat, Apt TripONSIs TripONSIs Pert (SIGNOSI EARMIGNEDIC, CH. Up:				
Langer, Ree, Inst. Ap., Troch/Sist. Troch/Sist. Perf. MORSH. ESCM/genable, Cnl. go. Langer, Troch/Sist. Troch/Sist. Perf. Morsh. Perf. Morsh. Esc. Mignation. Cnl. go. Langer, Troch/Sist. Troch/Sist. Troch/Sist. Perf. Morsh. Perf. Morsh. Perf. Morsh. Esc. Mignation. Cnl. go. Langer, Troch/Sist. Troch/Sist. Perf. Morsh. Perf. Perf				
larget Ring May 1 TopO/Sist Prof Notice Port posting Reging May 1 TopO/Sist Port posting May 1 To				
targer, Rev. Ind. Ap. TropOsits. TropOsits. Pert I - Harpitch Name. 12				
targer, Reinst, Ap. TropOsita TropOsita, Pert J. LAFA aut. Cort. [go targer, Rein, Ind. Ap. TropOsita TropOsita, Pert J. LAFA aut. Cort. [go targer, Rein, Ind. Ap. TropOsita, Pert J. LAFA aut. Cort. [go targer, Rein, Ind. Ap. TropOsita, Pert J. LAFA aut. Cort. [go targer, Rein, Ind. Ap. TropOsita, Pert J. LAFA aut. Cort. [go targer, Rein, Ind. Ap. TropOsita, Pert J. LAFA aut. Cort. [go targer, Rein, Ind. Ap. TropOsita, Pert J. LAFA aut. Cort. [go targer, Rein, Ind. Ap. TropOsita, Pert J. Manifeccure/whickSpeed, Krp. 1.22 targer, Rein, Ind. Ap. TropOsita, Pert J. Manifeccure/whickSpeed (Krp. 1.22 targer, Rein, Ind. Ap. TropOsita, Pert J. Manifeccure/whickSpeed (Krp. 1.22 targer, Rein, Ind. Ap. TropOsita, Pert J. Manifeccure/whickSpeed (Krp. 1.22 targer, Rein, Ind. Ap. TropOsita, Pert J. Manifeccure/whickSpeed (Krp. 1.22 targer, Rein, Ind. Ap. TropOsita, Pert J. Parasivor-thiving), J. Wholeg, I.22 targer, Rein, Ind. Ap. TropOsita, Pert J. Parasivor-thiving, J. Wholeg, I.22 targer, Rein, Ind. Ap. TropOsita TropOsita, Pert J. Parasivor-thiving, J. Wholeg, I.22 targer, Rein, Ind. Ap. TropOsita TropOsita, Pert J. ShittLeverlainFlowerse, Crit. Lyc. targer, Rein, Ind. Ap. TropOsita TropOsita, Pert J. ShittLeverlainFlowerse, Crit. Lyc. targer, Rein, Ind. Ap. TropOsita TropOsita, Pert J. TropOsita, Pert J. ShittLeverlainFlowerse, Crit. Lyc. targer, TropOsita, Pert J. Shittleverlain TropOsita, Pert J. Shitt	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	<u>lgc</u>	
Imager Rev Inst. Ap. TropOvisia Prof. LiAshnbit Cnt. go Imager Rev Inst. Ap. TropOvisia Prof. LiAshnbit Cnt. go Imager Rev Inst. Ap. TropOvisia Prof. LiAshnbit Cnt. go Imager Rev Inst. Ap. TropOvisia Prof. LiAshnbit State, crum Imager Rev. Inst. Ap. TropOvisia Prof. LiAshnbit State, crum Imager Rev. Inst. Ap. TropOvisia Prof. LiAshnbit State, crum Imager Rev. Inst. Ap. TropOvisia Prof. LiAshnbit State, crum Imager Rev. Inst. Ap. TropOvisia Prof. LiAshnbit State, crum Imager Rev. Inst. Ap. TropOvisia Prof. LiAshnbit State, crum Imager Rev. Inst. Ap. TropOvisia Prof. LiAshnbit State, crum Imager Rev. Inst. Ap. TropOvisia Prof.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f	32	
target, Pieck, Ap. Tro/Orisia, Pert J. KARRequest, Ch. 1, go target, Pieck, Ap. Tro/Orisia, Pert J. KARRequest, Ch. 1, go target, Pieck, Ap. Tro/Orisia, Pert J. MacSecure/vehicleSpeed Kph, 122 target, Pieck, Ap. Tro/Orisia, Pert J. MacSecure/vehicleSpeed Kph, 122 target, Pieck, Ap. Tro/Orisia, Pert J. MacSecure/vehicleSpeed Kph, 122 target, Pieck, Ap. Tro/Orisia, Pert J. MacSecure/vehicleSpeed Kph, 122 target, Pieck, Ap. Tro/Orisia, Pert J. MacSecure/vehicleSpeed Kph, 122 target, Pieck, Ap. Tro/Orisia, Pert J. PoisSvorbinable, Ch. 1, go target, Pieck, Pieck, Tro/Orisia, Pert J. PoisSvorbinable, Ch. 1, go target, Pieck, Pieck, Tro/Orisia, Pert J. PoisSvorbinable, Ch. 1, go target, Pieck,	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
larged_File_inst_Ap_TrqONSta_TrdONSta_Pert_LKASalas_State_enum target_Rel_inst_Ap_TrqONSta_Pert_MassecwevheloisSpeed_Kph_122 target_Rel_inst_Ap_TrqONSta_Pert_MassecwevheloisSpeed_Kph_122 target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_PossevEnable_Cni_Upc target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_TrqOnSta_Pert_Ap_Arrows. target_Rel_inst_Ap_TrqONSta_TrdONSta_Pert_TrqOnSca_Pert_Cni_Upc target_TrqONSta_Pert_PossevEnable_Cni_Upc target_TrqONSta_Pert_Ap_Arrows.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
larger, Rep. Incl. Ap. Tro,OrSia, Pert MassecureVehicleSpeed, Kpl. 72 larger, Rep. Incl. Ap. Tro,OrSia, Pert MassecureVehicleSpeed, Kpl. 72 larger, Rep. Incl. Ap. Tro,OrSia, Pert PossarvEnable Cnl. ground larger, Rep. Incl. Ap. Tro,OrSia, Pert PossarvEnable Cnl. ground larger, Rep. Incl. Ap. Tro,OrSia, Pert PossarvEnable Cnl. ground larger, Rep. Incl. Ap. Tro,OrSia, Tro,OrSia, Pert PossarvEnable Cnl. ground larger, Rep. Incl. Ap. Tro,OrSia, Tro,OrSia, Pert PossarvEnable Cnl. ground larger, Rep. Incl. Ap. Tro,OrSia, Tro,OrSia, Pert PossarvEnable Cnl. ground larger, Rep. Incl. Ap. Tro,OrSia, Tro,OrSia, Pert PossarvEnable Cnl. ground larger, Rep. Incl. Ap. Tro,OrSia, Tro,OrSia, Pert ShiftLeversianReverse, Cnl. ground larger, Rep. Incl. Ap. Tro,OrSia, Tro,OrSia, Pert	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
Integroup Inte	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target. Rie. Inst. Ap. TrqOviSta. Pert. PosServEnable, Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. Pert. PosSrevEnable, Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosTrapHonable, Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosTrapHonable, Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosTrapHonable, Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Pert. PosTrapHonable, Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Pert. Shrift-Indoverse. Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Pert. TsqCoviSta. Pert. Shrift-Indoverse. Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Pert. TrqOviSta. Pert. TrqOviSta. Pert. TrqOviSta. Pert. PosTrapHonable, Cnt. Igc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Pert. Sciencia. Indoverse. TrqOviSta. Pert. Sciencia. Indoverse. TrqOviSta. Pert. Sciencia. Indoverse. TrqOviSta. Pert. Sciencia. Indoverse. Pert. Sciencia. Indoverse. Pert. Sciencia. Indoverse. Pert. Sciencia. Pert. Pert	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_PostSochwAngle_HwDeg_I32 Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_PostTrijEnable Cnt_Igo Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_PostTrijHwAngle_HwDeg_I32 Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_PostTrijHwAngle_HwDeg_I32 Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_TSWARTrijAnngRequest_HwDeg_I32 Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_TSWARTrijAnngRequest_HwDeg_I32 Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_TsWartrijAnngRequest_HwDeg_I32 Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_TrijOvišta Pert_TrijOvišta Pert_TrijOvišta Pert_TrijOvišta Pert_TsWartrijAnngRequest_HwDeg_I32 Israget_Re_Inst_AD_TrijOvišta TrijOvišta Pert_TrijOvišta Per	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	eed_Kph_f32	
target, Rie, Inst. Ap., TrqOVSta FrqOVSta Pert PosTrajEnable Cnt. Igo: target, Rie, Inst. Ap., TrqOVSta TrqOVSta, Pert PosTrajEnable Cnt. Igo: target, Rie, Inst. Ap., TrqOVSta, TrqOVSta, Pert SWARTrgtAngRequest, HwDeg, 132 target, Rie, Inst. Ap., TrqOVSta, TrqOVSta, Pert SWARTrgtAngRequest, HwDeg, 132 target, Rie, Inst. Ap., TrqOVSta, TrqOVSta, Pert SWARTrgtAngRequest, HwDeg, 132 target, Rie, Inst. Ap., TrqOVSta, TrqOVSta, Pert TrqOvSta, Pert Lysia, Pert Lys				
larget Ric. Inst. Ap. TrqOviSta TrqOviSta Perf. PostTaj+WAngle_HvDog_132 target_Re_Inst. Ap. TrqOviSta_Perf_SWARTrgtAngRequest_HvDog_132 target_Re_Inst. Ap. TrqOviSta_Perf_TrqOviSta_Perf_Ingtockmap.Wrlm. 132 target_Re_Inst. Ap. TrqOviSta_Perf_TrqOviSta_Perf_Ingtockmap.Wrlm. 132 target_TrqOviSta_Perf_TrqOviSta_Perf_SWARTrgtAngRequest_HvDog_132 target_TrqOviSta_Perf_TrqOscAng.Wrlm. 132 target_TrqOviSta_Perf_DrqOscAng.Wrlm. 132 target_TrqOviSta_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
Integred Ric Inst Ap, Tracy Visita Tracy Visita Perf Shift S				
larget_Ric_Inst_Ap_TrqOviSia_TrqOviSia_Pert_ShiftLevertsinReverse_Cnt_lgc target_Ric_Inst_Ap_TrqOviSia_Pert_TrqOscAmp_MnNm_f32 target_Ric_Inst_Ap_TrqOviSia_Pert_TrqOscAmp_MnNm_f32 target_Ric_Inst_Ap_TrqOviSia_TrqOviSia_Pert_TrqOscEntable_Cnt_lgc target_TrqOviSia_Pert_TrqOscAmp_MnNm_f32 target_TrqOviSia_Pert_TrqOviSia_Pert_TrqOscEntable_Cnt_lgc target_TrqOviSia_Pert	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Ris_Inst_Ap_TrqOvisia_TrqOvisia_Perl_TrqOscAmp_Minhm_f32 target_Ris_Inst_Ap_TrqOvisia_Perl_TrqOscEnable_Crt_igc target_Ris_Inst_Ap_TrqOvisia_Perl_TrqOscEnable_Crt_igc target_TrqOvisia_Perl_ApAnonRecoverableFaults_Cnt_igc value 1 target_TrqOvisia_Perl_CoViPound_Cnt_igc value 1 target_TrqOvisia_Perl_CoViPound_Cnt_igc value 3 target_TrqOvisia_Perl_CoViPound_Cnt_igc value 3 target_TrqOvisia_Perl_CoViPound_Cnt_igc value 1 target_TrqOvisia_Perl_EsCRequest_Cnt_igc value 1 target_TrqOvisia_Perl_MosH_LKAMigfanable_Cnt_igc value 1 target_TrqOvisia_Perl_MosH_LKAMigfanable_Cnt_igc value 1 target_TrqOvisia_Perl_Havioraue_Havin_igc value 1 target_TrqOvisia_Perl_Havioraue_Havin_igc value 0 target_TrqOvisia_Perl_Havioraue_Havin_igc value 0 target_TrqOvisia_Perl_Havioraue_Havin_igc value 0 target_TrqOvisia_Perl_Havioraue_Havin_igc value 0 target_TrqOvisia_Perl_MarkecurevehicleSpeed_Kpi_igc value 1 target_TrqOvisia_Perl_MarkecurevehicleSpeed_Kpi_igc value 1 target_TrqOvisia_Perl_MarkecurevehicleSpeed_Kpi_igc value 1 target_TrqOvisia_Perl_MarkecurevehicleSpeed_Kpi_igc value 1 target_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisia_Perl_Spi_TrqOvisi				
Integret Rel. Inst. Ap. TrqOVISta TrqOVISta Perl TrqOscEnable Cerl. Ugc target Rel. Inst. Ap. TrqOVISta Perl TrqOscEnable Cerl. Ugc target TrqOVISta Perl Ap. TrqOVISta Perl TrqOscEnable Cerl. Ugc Value Larget TrqOVISta Perl ApARocoverableFaults Cerl. Ugc value			,	
target_TrqOv/Sta_Pert_APATrqOv/Sta_TrqOv/Sta_Pert_TrqOscFreq_Hz_G2 target_TrqOv/Sta_Pert_APARonecoverableFaults_Ort_lgovalue 1 target_TrqOv/Sta_Pert_APARonecoverableFaults_Ort_lgovalue 1 target_TrqOv/Sta_Pert_APARonecoverableFaults_Ort_lgovalue 1 target_TrqOv/Sta_Pert_APARonecoverableFaults_Ort_lgovalue 1 target_TrqOv/Sta_Pert_CCWPoverableFaults_Ort_lgovalue 1 target_TrqOv/Sta_Pert_CCWPoverableFaults_Ort_lgovalue 1 target_TrqOv/Sta_Pert_CCWPoverableFaults_Ort_lgovalue 1 target_TrqOv/Sta_Pert_CCWPoverableFaults_Ort_lgovalue 3 3 3 3 3 3 3 3 3				
Isrget_TrqOVISIa_Pert_APANonRecoverableFaults_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_APARecoverableFaults_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_APARecoverableFaults_Cnt_Igc_value 0 Isrget_TrqOVISIa_Pert_CCWFound_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_CCWFound_Cnt_Igc_value 379.070007 Isrget_TrqOVISIa_Pert_CCWFound_Cnt_Igc_value 379.070007 Isrget_TrqOVISIa_Pert_CCWFound_Cnt_Igc_value 362.690002 Isrget_TrqOVISIa_Pert_ESCREQUEST_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 0 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 0 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 0 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 0 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 0 Isrget_TrqOVISIa_Pert_ASCREQUEST_Cnt_Igc_value 1 Isrget_TrqOVISIa_Pert_ASCREQUES			•	
target_TrqOvISta_Perr _APARecoverableFaults_Cnt_lgc.value				
target_TrqOvISta_Pert_APARequest_Cnt_Igc.value 1 target_TrqOvISta_Pert_CCWFound_Cnt_Igc.value 1 target_TrqOvISta_Pert_CCWFound_Cnt_Igc.value 379.070007 target_TrqOvISta_Pert_CWFound_Cnt_Igc.value 1 target_TrqOvISta_Pert_EXFOREU_Cnt_Igc.value 1 target_TrqOvISta_Pert_EXFOREU_Cnt_Igc.value 1 target_TrqOvISta_Pert_EXFOREU_Cnt_Igc.value 1 target_TrqOvISta_Pert_EXFOREU_EX_Cnt_Igc.value 1 target_TrqOvISta_Pert_GMOSH_APAMIgEnable_Cnt_Igc.value 1 target_TrqOvISta_Pert_GMOSH_EXMIGEnable_Cnt_Igc.value 1 target_TrqOvISta_Pert_HotICRequest_Cnt_Igc.value 1 target_TrqOvISta_Pert_LAFAraut_Cnt_Igc.value 1 target_TrqOvISta_Pert_HotICRequest_Cnt_Igc.value 1 target_TrqOvISta_Pert_LAFAraut_Cnt_Igc.value 0 target_TrqOvISta_Pert_LKAFaut_Cnt_Igc.value 0 target_TrqOvISta_Pert_MaxSecureVehicleSpeed_Kph_f32.value 164.80003 target_TrqOvISta_Pert_MaxSecureVehicleSpeed_Kph_f32.value 185.399994 target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Pert_PostrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Pert_PostrajEnable				
target_TrqOvIsta_Pert_CCWFound_Cnt_lgc.value				
target_TrqOvISta_Perl_CCWPosition_HwDeg_f32.value				
target_TrqOvIsta_Pert_CWFound_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_ESCFaul_Cnt_Ig.cvalue 362.690002 target_TrqOvIsta_Pert_ESCFaul_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_ESCR_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_ESCRequest_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_ESCRequest_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_GMOSH_ESCMIgEnable_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_MOSH_ESCMIgEnable_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_HapticRequest_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_HapticRequest_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_LKAInhibit_Cnt_Ig.cvalue 0 target_TrqOvIsta_Pert_LKARequest_Cnt_Ig.cvalue 0 target_TrqOvIsta_Pert_LKARequest_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_LKARequest_Cnt_Ig.cvalue 1 target_TrqOvIsta_Pert_MinSecureVehicleSpeed_Kph_132.value 185.39994 target_TrqOvIsta_Pert_Size_Pe				
target_TrqOvISta_Per1_ESCFault_Cnt_Igc_value 1 target_TrqOvISta_Per1_ESCISLIMITEd_Cnt_Igc_value 1 target_TrqOvISta_Per1_ESCISLIMITEd_Cnt_Igc_value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_GMOSH_APMIGEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_GMOSH_ESCMIGEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_LKARFault_Cnt_Igc_value 0 target_TrqOvISta_Per1_LKARFault_Cnt_Igc_value 0 target_TrqOvISta_Per1_LKARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32_value 164.800003 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc_value 1 target_TrqOvISta_Per1_SiviARTgtqnRequest_HwDeg_f32_value 26.1000004 target_				
target_TrqOvIsta_Per1_ESCFault_Cnt_lgc.value				
target_TrqOvISta_Per1_ESCIsLimited_Ont_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Ont_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LSCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Ont_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Ont_lgc.value 1 target_TrqOvISta_Per1_HwTorque_Hwnn_St2.value 8.69999981 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARphiblic_Ont_lgc.value 1 target_TrqOvISta_Per1_LKARpulest_Ont_lgc.value 164.800003 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 185.399994 target_TrqOvISta_Per1_PostrajHwAngle_HwDeg_f32.value 1 target_TrqOvISta_Per1_PostrajHwAngle_HwDeg_f32.value 1 target_TrqOvISta_Per1_PostrajHwAngle_HwDeg_f32.value 26.1000004 target_TrqOvISta_Per1_ShiftLeverIsInRequest_HwDeg_f32.value 26.1000004 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Ont_lgc.value 1 target_TrqOvISta_Per1_ShiftLeve				
target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value				
target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 8.69999981 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_LKAInibit_Cnt_lgc.value 0 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MASsecureVehicleSpeed_Kph_f32.value 164.800003 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 185.399994 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_SwiftLeverIsInReverse_Cnt_lgc.value 1 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value TrqOvISta_APAState_State_Menum 3 3 TrqOvISta_ElapsedAPAMaxHvTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 0 TrqOvISta_ElapsedStandstillTime				
target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc.value				
target_TrqOv/Sta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value				
target_TrqOv/Sta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOv/Sta_Per1_HwTorque_HwNm_f32_value 8.69999981 target_TrqOv/Sta_Per1_LKAFault_Cnt_Igc.value 0 target_TrqOv/Sta_Per1_LKARequest_Cnt_Igc.value 0 target_TrqOv/Sta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOv/Sta_Per1_MaxSecureVehicleSpeed_Kph_f32_value 164.800003 target_TrqOv/Sta_Per1_MinSecureVehicleSpeed_Kph_f32_value 185.399994 target_TrqOv/Sta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOv/Sta_Per1_PosTrajHwAngle_HwDeg_f32_value 0.150000006 target_TrqOv/Sta_Per1_SWARTrgtAngRequest_HwDeg_f32_value 26.1000004 target_TrqOv/Sta_Per1_SwARTrgtAngRequest_HwDeg_f32_value 1 Name Actual Value Expected Value TrqOv/Sta_Per1_ShifftLeverIsInReverse_Cnt_Igc.value 1 Name Actual Value Expected Value TrqOv/Sta_PAAIsate_State_M_enum 3 3 TrqOv/Sta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOv/Sta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOv/Sta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOv/Sta_Hapticctate_State_M_en				
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 164.800003 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 164.800003 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 185.399994 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 26.1000006 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 26.1000004 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value I TrqOvISta_PAState_State_M_enum 3 3 3 3 TrqOvISta_ESCState_State_M_enum 3 3 3 3 TrqOvISta_ESCState_State_M_enum 3 3 3 3 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 0 0 0 TrqOvISta_HapticState_State_M_enum 0 0 0 0 TrqOvISta_HapticCur_Cnt_M_u16 29000 29000 ± 1 TrqOvISta_HapticCur_Cnt_M_u16 198760.641 198760.641 198760.641 1 TrqOvISta_HapticCur_Cnt_M_u16 0 0 0 ± 1 TrqOvISta_HapticCur_Cnt_M_u16 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0				
target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 164.800003 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 164.800003 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 185.399994 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 26.1000006 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 26.1000004 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value I TrqOvISta_PAState_State_M_enum 3 3 3 3 TrqOvISta_ESCState_State_M_enum 3 3 3 3 TrqOvISta_ESCState_State_M_enum 3 3 3 3 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 0 0 0 TrqOvISta_HapticState_State_M_enum 0 0 0 0 TrqOvISta_HapticCur_Cnt_M_u16 29000 29000 ± 1 TrqOvISta_HapticCur_Cnt_M_u16 198760.641 198760.641 198760.641 1 TrqOvISta_HapticCur_Cnt_M_u16 0 0 0 ± 1 TrqOvISta_HapticCur_Cnt_M_u16 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		8.69999981		
target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 164.800003 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 185.399994 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 26.1000004 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value TrqOvISta_APAState_State_M_enum 3 3 TrqOvISta_ESCState_State_M_enum 3 3 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_M_enum 0 0 TrqOvISta_HapticState_State_M_enum 0 0 TrqOvISta_HapticCime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvISta_HapticCime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvISta_LK		0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 164.800003 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 185.399994 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 26.1000004 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value TrqOvlSta_PAState_State_M_enum 3 3 TrqOvlSta_EScState_State_M_enum 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvlSta_HapticState_State_M_enum 0 0 TrqOvlSta_HapticGtur_Cnt_M_u16 29000 29000 ± 1 TrqOvlSta_HapticCtime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 0		0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 185.399994 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 26.1000004 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 1 Name Actual Value Expected Value I TrqOvlSta_APAState_State_Menum 3 3 3 3 TrqOvlSta_EscState_State_Menum 3 3 3 3 3 1 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 0 1 1 TrqOvlSta_ElapsedShintLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 <		1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 26.1000004 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value I TrqOvlSta_APAState_State_Menum 3 3 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 0 0 0 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0<	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	164.800003		
target_TrqOvlSta_Per1_posTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 26.1000004 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value I TrqOvlSta_APAState_State_M_enum 3 3 3 TrqOvlSta_ESCState_State_M_enum 3 3 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 0 0 0 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0<	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 26.1000004 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value I TrqOvlSta_APAState_State_M_enum 3 3 3 TrqOvlSta_ESCState_State_M_enum 3 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 0 TrqOvlSta_HapticState_State_M_enum 0 0 0 TrqOvlSta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 0 TrqOvlSta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 198760.641 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 0 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 0 ± 1	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 1 Name Actual Value Expected Value TrqOvlSta_APAState_State_Menum 3 3 TrqOvlSta_ESCState_State_Menum 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvlSta_HapticState_State_Menum 0 0 TrqOvlSta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvlSta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ± 1	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
Name Actual Value Expected Value TrqOvISta_APAState_State_Menum 3 3 TrqOvISta_ESCState_State_Menum 3 3 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_Menum 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvISta_LKAFault_Cnt_Mlgc 0 0 ± 1	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
TrqOvISta_APAState_State_Menum 3 3 TrqOvISta_ESCState_State_Menum 3 3 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_Menum 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
TrqOvISta_ESCState_State_Menum 3 3 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_Menum 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1	Name	Actual Value	Expected Value	Result
TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_M_enum 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1	TrqOvlSta_APAState_State_M_enum			•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 83136.8828 83136.8828 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvlSta_HapticState_State_M_enum 0 0 TrqOvlSta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvlSta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ± 1			3	•
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 0 0 TrqOvISta_HapticState_State_M_enum 0 0 TrqOvISta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvISta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvISta_LKAFault_Cnt_M_lgc 0 0 ± 1				•
TrqOvlSta_HapticState_State_M_enum 0 0 TrqOvlSta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvlSta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ± 1				•
TrqOvlSta_Hapticdur_Cnt_M_u16 29000 29000 ± 1 TrqOvlSta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ± 1				•
TrqOvlSta_Haptictime_Sec_M_f32 198760.641 198760.641 ± 1 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ± 1				•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 0.449800014 0.449800014 ± 0.0000009 TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ± 1				•
TrqOvlSta_LKAFault_Cnt_M_lgc 0 0 ± 1				•
				•
rrquvista_tkaPermFauit_Unt_M_igc 0 0 ± 1				•
				•
TrqOvlSta_LKAPermFault_Cnt_M_u08 0 0 ± 1 TrpOvlSta_LKAStata_Stata_M_apurp 2				
TrqOvlSta_LKAState_State_M_enum 2 2 TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625				- V
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvlSta_ReadytoPulse_Cnt_M_lgc 1 1 ± 1				

2015-02-04, 14:05:40+0530



TrqOvlSta_Per1

Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	•
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.48 (Repeat Count = 1) Name	Input Value
	· ·
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
FrqOvISta_APAState_State_M_enum	0
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1884573
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	365508
TrqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	29000
rqOvlSta_Haptictime_Sec_M_f32	198760.641
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.5
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
rqOvlSta_LKAFault_Cnt_M_lgc	1
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	0
rqOvlSta_LKAState_State_M_enum	0
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	57.7999992
rqOvlSta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.36500001
_APAMaxHwTrq_HwNm_f32	8.64000034
_APAMaxVehSpd_Kph_f32	194.399994
_ESCMaxVehSpd_Kph_f32	52.5999985
_HapticAmplitude_MtrNm_f32	0.648000002
_HapticDuration_Sec_f32	1.62
	3
sf32	1.72800004
 _LKAMaxVehSpd_Kph_f32	205.199997
 _LKAMinVehSpd_Kph_f32	0
 MaxHapticReqTime_Sec_f32	0.119999997
SWARLimiter HwDeg f32	54.5999985
ShiftLevRevTime_Sec_f32	0.0289999992
standstillThresh_Kph_f32	4.32000017
_StandstillTime_Sec_f32	0.0289999992
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
	target TrqOvISta Per1 APARecoverable Faults Cnt Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
	target_TrqOvlSta_Per1_HwTorque_HwNm_f32





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	jc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3:	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32000000000000000000000000000000000000$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1 -377.290009		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1 362.720001		
	1		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	8.6999981		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	164.800003		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	185.399994		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	26.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	83136.8828	83136.8828 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	29000	29000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	198760.641	198760.641 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0.449800014	0.449800014 ± 0.0000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	0	0 ± 1	V
TrqOvlSta_LKAState_State_M_enum	2	2	V
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1±1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	-
target_TrqOvlSta_Per1_LKAState_State_enum.value target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1±1	
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.648000002	0.648000002 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3	3 ± 0.0009765625	
0	1:		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-





Test Step 3.49 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.3699989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvISta_LKAPermFault_Cnt_M_lgc	2
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	1
TrqOvISta PosSrvoHwAngle HwDeg M f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.20000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.099985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32	35.7000008 0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOvISta Per1 GMOSH APAMfgEnable Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target TrgOvlSta Per1 GMOSH LKAMfgEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscAmp MtrNm f32	target TrqOvISta Per1 TrqOscAmp MtrNm f32
J	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscEnable Cnt Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-375.51001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.75		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	- -
TrqOvlSta_HapticState_State_M_enum	0	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	- •
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	- •
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.50 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.20000003
k_APAMaxHwTrq_HwNm_f32	0.720000029 16.2000008
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0370000005
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSHOscillateState_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosSrvonwArigle_HwDeg_isz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrgOvlSta Per1 PosTrajHwAngle HwDeg f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	V = 1
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvISta_Per1_CCWPosition_HwDeg_f32.value	-373.730011 4
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	1 362.779999
target_TrqOvISta_Per1_CWPosition_HwDeg_t3z.value target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvIsta_rer1_ESCrault_Clit_igc.value target TrqOvIsta Per1 ESCIsLimited Cnt Igc.value	1
target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16 1
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	
arget_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	2	2	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.51 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	17.1000004
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Immust Value		
Name	Input Value	F. W. O. I.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u> _</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_I	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateSta	ate_State_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr	nable Cnt lgc	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc	target TrqOvlSta Per1 GMOSH LKAMfgEr	nable Cnt lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_t		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAInhibit Cnt Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ır	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAState State enum	target TrqOvISta Per1 LKAState State en		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LRAState_State_Entirin target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MiaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleS		
target_Rte_inst_Ap_irqovista.irqovista_Peri_minSecurevenicieSpeed_Rpn_132 target_Rte_inst_Ap_TrqoviSta.TrqoviSta_Peri_minSecurevenicieSpeed_Rpn_132			
	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	- ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-371.950012		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.809998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	1		
target_TrqOvISta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	14		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	0	0	
TrqOvlSta_ESCState_State_M_enum	4	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	835.559998	835.559998 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	425564	425564 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	2 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	0 2 0	2 ± 1 0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	2 ± 1	•

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Namo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
FrqOvlSta_Hapticdur_Cnt_M_u16	11150
FrqOvlSta_Haptictime_Sec_M_f32	425564
FrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
FrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
FrqOvlSta_LKAPermFault_Cnt_M_lgc	0
FrqOvISta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
<pre>c_APAIncludeHaptic_Cnt_lgc</pre>	1
<pre><_APAMaxHwTrqTime_Sec_f32</pre>	0.200000003
APAMaxHwTrq HwNm f32	0.720000029
<_APAMaxVehSpd_Kph_f32	16.2000008
CSCMaxVehSpd Kph f32	31.700008
_	0.0540000014
(_HapticAmplitude_MtrNm_f32 (_HapticPuration_Co632	
< HapticDuration_Sec_f32 < HapticFrom Units (32)	0.135000005
K_HapticFreq_Hz_f32	7.63000011
C_HapticReacttime_Sec_f32	0.143999994
K_LKAMaxVehSpd_Kph_f32	77.099985
k_LKAMinVehSpd_Kph_f32	15.75
<_MaxHapticReqTime_Sec_f32	0.150000006
C_SWARLimiter_HwDeg_f32	35.7000008
C_ShiftLevRevTime_Sec_f32	0.0370000005
x_StandstillThresh_Kph_f32	0.360000014
c_StandstillTime_Sec_f32	0.0370000005
$arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Classification and the properties of the proper$	nt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCIsLimited Cnt Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
arget Rte Inst Ap TrqOvlota.TrqOvlSta Per1 ESCRequest Cnt Igc	target TrgOvlSta Per1 ESCRequest Cnt lgc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_inst_Ap_1rqOviSta.1rqOviSta_Per1_ESCState_state_enum arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_en	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Ig	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Ign	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	0 - 1 - 0 0 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-370.170013		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 362.839996		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	1		
target_TrqOvlSta_Fer1_ESCFault_Crit_tgc.value target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_E3CRequest_Crit_igc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	1	1	V
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.18999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	V
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	V
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	-
target_TrqOvlSta_Per1_LKAState_State_enum.value	1		V
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0.189999998	1 ± 1	-
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.189999998 ± 0.0009765625 0.0540000014 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0.0340000014 ± 0.0009763623	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	
	1.00000011		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.53 (Repeat Count = 1)	▼
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrgOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	425564 -4.3699989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrgOvlSta LKAFault Cnt M Igc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.20000003
k_APAMaxWahSad_Wah_632	0.720000029
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	16.2000008 31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k HapticDuration Sec f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	32.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0370000005 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
	1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.390015		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.869995		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	3.3599999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.18999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	✓
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvISta_Haptictime_Sec_M_f32	425564	425564 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	2	2	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.54 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004





Input Value	
k, APAlncludeHaptic, CnL Igc 1 k, APAMasht-Yrtq Time, Sec, 132 0.20000003 k, APAMasht-Yrtq Hwlm, 132 0.720000029 k, APAMasht-Yrtq Hwlm, 132 16.2000008 k, APAMasht-Yrtq Hwlm, 132 0.0540000014 k, ESCMaxVenSpd, Kph, 132 0.0540000014 k, HapticPoult, MrNm, 132 0.0139000005 k, LANAIW-RSDd, Kph, 132 7.00000000000 k, LLANAIW-RSDd, Kph, 132 15.76 k, LANAIW-RSDd, Kph, 132 15.76 k, MaxHapticReq Time, Sec, 132 0.150000006 k, Standstill Time, Sec, 132 0.0370000005 k, Standstill Streen, Kph, 132 0.0370000005 k, Standstill Time, Sec, 132 0.0370000005 k, Standstill Streen, Kph, 132 0.0370000005 k, Standstill Time, Sec, 132 0.0370000005 <tr< td=""><td></td></tr<>	
K_APAInachderHaptinc_Rep_132 0.200000039	
k_APAMaxHwTrq Time, Sec, f32 k_APAMaxHwTrq Time, Sec, f32 k_APAMaxHwTrq Time, Sec, f32 k_ESCMaxVehSpd_Kpt, f32 k_ESCMaxVehSpd_Kpt, f32 k_Esptichate, Mrhwm, f32 k_Esptichat	
k, APAMaxHwTrq, HwNm, f32 0,7200000028 k, APAMaxHwSpd, Kph, f32 16,2000008 k, ESCMaxVehSpd, Kph, f32 31,7000008 k, HapticAmplitude, Mrthm, f32 0,0540000014 k, HapticPreq, Hz, f32 0,33000005 k, HapticPreq, Hz, f32 7,63000011 k, HapticPreq, Hz, f32 0,14399994 k, LKAMaxVehSpd, Kph, f32 0,14399994 k, LKAMaxVehSpd, Kph, f32 15,75 k, LKAMaxVehSpd, Kph, f32 15,75 k, LKAMaxVehSpd, Kph, f32 0,15000006 k, LKAMaxVehSpd, Kph, f32 0,15000006 k, SwARLIMIERH HwBeg, f32 3,37000008 k, ShandstillThresh, Kph, f32 0,3370000005 k, StandstillThresh, Kph, f32 0,300000014 k, StandstillThresh, Ap, TrqOvISta, Perl, APANoRRecoverableFaults, Cnl. lgc target, Rel, nst, Ap, TrqOvISta, TrqOvISta, Perl, APARceoverableFaults, Cnl. lgc target, Rel, nst, Ap, TrqOvISta, TrqOvISta, Perl, APARceoverableFaults, Cnl. lgc target, TrqOvISta, Perl, APARceoverableFaults, Cnl. lgc target, Rel, nst, Ap, TrqOvISta, TrqOvISta, Perl, CWFound, Cnl. lgc target, TrqOvISta, Perl, ApARceoverableFaults, Cnl. lgc target, Rel, nst, Ap, TrqOvISta, TrqOvISta, Perl, ESCFault, Cnl. lgc target, Tr	
k_BSCMavNehSpd_Kph_f32 k_BSCMavNehSpd_Kph_f32 k_BapticDuration_Sec_f32 k_HapticDuration_Sec_f32 k_HapticDuration_Sec_f32 k_HapticDuration_Sec_f32 k_HapticDuration_Sec_f32 k_HapticPreq_Hz_f32 k_HapticPreq_Hz_f32 k_HapticPreq_Hz_f32 d_f48pspeak_f48	
k_ESCMaxVehSpd_Kph_r32 31,7000008 k_HapticAmplitude_Mrhim_r52 0.0540000014 k_HapticFace_Hz_r632 0.135000005 k_HapticFreq_Hz_r632 7.63000011 k_HapticFreq_Hz_r632 0.143999994 k_LKAMAxVehSpd_Kph_r632 77.0999995 k_LKAMAxVehSpd_Kph_r632 15.75 k_MaxHapticReqTime_Sec_f32 0.150000006 k_SWARLImiter_HwDeg_r622 33.70000008 k_StandstillTries_Mcpf_r62 0.0370000005 k_StandstillTries_Sec_f32 0.0370000005 k_StandstillTries_Sec_f32 0.0370000005 k_StandstillTries_Sec_f32 0.0370000005 target_Re_Inst_Ap_TrqOVISta_Per1_APADrivInterventionDetected_Cnt_target_Ric_Inst_Ap_TrqOVISta_Per1_APARceoverableFaults_Cnt_lgc target_Re_Inst_Ap_TrqOVISta_Per1_APARceoverableFaults_Cnt_lgc target_Re_Inst_Ap_TrqOVISta_TrqOVISta_Per1_APARceoverableFaults_Cnt_lgc target_TrqOVISta_Per1_APARceoverableFaults_Cnt_lgc target_Re_Inst_Ap_TrqOVISta_TrqOVISta_Per1_CCWPound_Cnt_lgc target_TrqOVISta_Per1_CCWPound_Cnt_lgc target_Re_Inst_Ap_TrqOVISta_TrqOVISta_Per1_CWPosition_HwDeg_r32 target_TrqOVISta_Per1_CCWPosition_HwDeg_r32 target_Re_Inst_Ap_TrqOVISta_TrqOVISta_Per1_ESCReut_Cnt_lgc target_TrqOVISta_Per1_ESCRelumet_Cnt_lgc	
k. HapticDuration. Sec 132 0.135000005 k. HapticPactorn. Sec 132 0.135000005 k. HapticPacttime. Sec 132 7.3000011 k. LANAMAVeNSpd_Kpt, 132 77.0999995 k. LLANAMAVENSpd_Kpt, 132 15.75 k. MaxHapticReqTime. Sec, 132 0.150000006 k. SWARLImiter. HwDeg, 132 33.7000008 k. SwARLImiter. HwDeg, 132 0.37000005 k. StandstillTimes. Mcph, 132 0.037000005 target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl . APARcequest.	
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32 k_HapticFreq_Hz_f32 k_LKAMaxVehSpd_Kph_f32 k_LKAMaxVehSpd_Kph_f32 k_LKAMaxVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_SwarLimiter_HwDeg_f32 33.7000006 k_SwarLimiter_HwDeg_f32 33.7000006 k_SwarLimiter_HwDeg_f32 33.7000006 k_SwarLimiter_HwDeg_f32 33.7000006 k_StandstillTime_Sec_f32 k_StandstillTime_Sec_f32 0.0377000005 k_StandstillTime_Sec_f32 k_StandstillTime_Sec_f32 0.0377000005 larget_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_APADrvinterventionDetected_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_APARecoverableFaults_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_APARet_und_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_APAState_state_enum target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_CowPosition_HwDeg_f32 target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_CowPosition_HwDeg_f32 target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcFault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_TrqOwSta_Per1_ExcRault_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_Per1_GMOSH_ExcMigEnable_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_Per1_GMOSH_ExcMigEnable_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_Per1_GMOSH_ExcMigEnable_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_Per1_MapticRequest_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_Per1_MapticRequest_Cnt_lgc target_Rie_inst_Ap_TrqOwSta_Per1_HwTorque_HwNim_f32 target_Rie_inst_Ap_TrqOwSta_Per1_HwTorque_HwNim_f32 target_Rie_inst_Ap_TrqOwSta_	
K_HapticFreq_Hz_32 7.63000011 K_HapticReacttime_Sec_f32 0.143999994 K_LKAMAWAYENGA (Kph_132 77.099985 K_LKAMAWAYENGA (Kph_132 15.75 K_MAKHAPICREQTIME_Sec_f32 0.150000006 K_SWARLIMITER_HWDeg_f32 33.7000008 K_SHIFLEVERVTIME_Sec_f32 0.0370000005 K_StandstillTimes Ap_F192 0.360000014 K_StandstillTime_Sec_f32 0.370000005 Larget_Re_Inst_Ap_TrqOvIsta.TrqOvIsta_Perl_APARecoverableFaults_Cnt_lgc target_Re_Inst_Ap_TrqOvIsta_Perl_APARecoverableFaults_Cnt_lgc Larget_Re_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_APARecoverableFaults_Cnt_lgc target_TrqOvIsta_Perl_APARecoverableFaults_Cnt_lgc Larget_Re_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_APARecoverableFaults_Cnt_lgc target_TrqOvIsta_Perl_APARecoverableFaults_Cnt_lgc Larget_Re_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_CowFound_Cnt_lgc target_TrqOvIsta_Perl_APARequest_Cnt_lgc Larget_Re_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_CowFound_Cnt_lgc target_TrqOvIsta_Perl_CowFound_Cnt_lgc Larget_Re_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_CowFound_Cnt_lgc target_TrqOvIsta_Perl_CowFound_Cnt_lgc Larget_Re_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_ESCFaul_Cnt_lgc target_TrqOvIsta_Perl_CowFound_Cnt_lgc Larget_Re_Inst_Ap_TrqOvIsta_TrqOvIsta_Perl_ESCFaul_Cnt_lgc targ	
k_HapticReactitime_Sec_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_Hubeg_f32 k_ShiffLevRevTime_Sec_f32 d_0.037000006 k_ShiffLevRevTime_Sec_f32 d_0.037000005 k_StandstillTimes_kph_f32 d_0.037000005 k_StandstillTime_Sec_f32 d_0.037000005 k_StandstillTime_Sec_f32 d_0.037000005 d_0.03700005 d_0.037000005 d_0.037000005 d_0.03700005	
k_LKAMarVehSpd_Kph_f32 k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32 k_MaxHapticReqTime_Sec_f32 k_SWARLimiter_HwDeg_f32 s3.7000008 k_ShiffLevRevTime_Sec_f32 k_StandstillTimes_Rec_f32 k_StandstillTime_Sec_f32 d_0.307000005 k_StandstillTime_Sec_f32 d_0.307000005 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARonRecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequet_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequet_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequet_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequet_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMIgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rt	
k_KMAHapticReqTime_Sec_f32	
k_MaxHapticReqTime_Sec_f32 k_SMRALlimiter_HwDeg_f32 k_ShiftLevRevTime_Sec_f32 k_ShiftLevRevTime_Sec_f32 k_StandstillTimesh_Kph_f32 0.380000014 0.0370000005 k_StandstillTimesh_Kph_f32 0.380000014 0.0370000005 k_StandstillTimes_Sec_f32 k_StandstillTime_Sec_f32 k_StandstillTime_Sec_f32 clarget_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APADrvInterventionDetected_Cnt_target_TrqOvIsta_Per1_APADrvInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CWFositlc_flugc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_EMOSH_APAMfgenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_GMOSH_APAMfgenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_GMOSH_EXMfgenable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvIst	
k_SMARLimiter_HwDeg_132 k_StinftLevRevTime_Sec_132 0.3370000005 k_StandstillThresh_Kph_132 k_StandstillThresh_Kph_132 0.3370000005 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPound_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPosition_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPosition_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CWPosition_HwDeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HwTorque_HwMm_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HwTorque_HwMm_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rt	
k_ShiftLevRevTime_Sec_f32 k_StandstillTimes_Kpth_f32 0.380000014 k_StandstillTimes_Sec_f32 target_Rel_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APADrvrinterventionDetected_Cnt_target_Rel_Inst_Ap_TrqOvIsta_Per1_APADrvrinterventionDetected_Cnt_target_Rel_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APANonRecoverableFaults_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APARecoverableFaults_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APARecoverableFaults_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APARetoverableFaults_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APARetoverableFaults_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_APAState_state_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CCWFound_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CCWFound_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_CCWFosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCFault_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCFault_Cnt_gc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_GMOSH_OSCIBlateState_State_enum target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_MTOrque_HwNm_f32 target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAPAIT_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAPAIT_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAPAIT_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAPAIT_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_LKAPAIT_Cnt_lgc target_Rte_Inst_Ap_TrqOvIsta_TrqOvIsta_P	
k_StandstillThresh_Kph_132	
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_APANecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_APARecquest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPound_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosltion_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_BMOSH_APAMfgEnable_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lg target_Rte_Inst_Ap_TrqOvlSta_Per1_LKA	
target_Rte_Inst_Ap_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_state_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_SECMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_SECMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HNDrque_HNNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HNDrque_HNNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HNDrque_HNNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_lgc target_TrqOvISta_Per1_LKAFault_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_lgc target_	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APARetate_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_Igc target_TrqOvlSta_Per1_LKARequest_Cnt_Igc target_TrqOvlSta_Per1_LKARequest_Cnt_Igc target_TrqOvlSta_Per1_LKARequest_Cnt_Igc target_TrqOvlSta_Per1_LKARequest_Cnt_Igc target_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_TrqOvlSta_Per1_LKARequest_Cnt	
target_Rte_Inst_Ap_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_CVPOsition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_CVPOsition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_CVPOsition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCS_Inimited_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCS_Inimited_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCS_Inimited_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCS_Inimited_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_ESCS_Inimited_State_Enum target_Rte_Inst_Ap_TrqOviSta_Per1_ESCS_Inimited_State_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_OSHOSHOSHIBATEST_ESC_Int_Igc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_TrqOviSta_Per1_LKARequest_Cnt_Igc target_TrqOviSta_Per1_LKARequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_APARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CCWPound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_APAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCStalt_insted_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CWFound_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_state_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvISta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvISta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOScillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_PrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_HavTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_Igc target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_TrqOvlSta_Per1_LKARequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_enum target_TrqOvlSta_Per1_LKAState_enum target_TrqOvlSta_Per1_LKAState_enum	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value -366.609985	
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 362.899994	
target TrqOvlSta Per1 ESCFault Cnt Igc.value 1	
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_gc.value	
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	
3.2 4 1 - April 14 20 - 20 - 11.	
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0	
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 69.4000015	
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 16	
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.189999998	

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	2	2	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.55 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	0
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	4409.68018
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	11150
TrqOvlSta_Haptictime_Sec_M_f32	425564
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.36999989
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0218000002
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	11.1000004
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.200000003
k_APAMaxHwTrq_HwNm_f32	0.720000029
k_APAMaxVehSpd_Kph_f32	16.2000008
k_ESCMaxVehSpd_Kph_f32	31.7000008
k_HapticAmplitude_MtrNm_f32	0.0540000014
k_HapticDuration_Sec_f32	0.135000005
k_HapticFreq_Hz_f32	7.63000011
k_HapticReacttime_Sec_f32	0.143999994
k_LKAMaxVehSpd_Kph_f32	77.0999985
k_LKAMinVehSpd_Kph_f32	15.75
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	35.7000008
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	0.360000014
k_StandstillTime_Sec_f32	0.0370000005
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>j_</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_APAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_t		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt Igc	02	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	IC	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
$target_TrqOvlSta_Per1_APAN on Recoverable Faults_Cnt_lgc.value$	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-364.829987		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.929993		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	3.3599999		
target TrgOvlSta Per1 LKAFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target TrgOvlSta Per1 LKARequest Cnt lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	16		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-15.3999996		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrgOvlSta APAState State M enum	0	0	~
TrqOvISta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	4418.97998	4418.97998 ± 1	_
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	835.559998	835.559998 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	11150	11150 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	425564	425564 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.20148611	-4.20148611 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc		0 ± 1	✓
	1	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
	0 2	0 ± 1 2 ± 1	*
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	0 2 2	0 ± 1 2 ± 1 2	· · · · ·
TrqOvlSta_LKAPermFault_Cnt_M_lgc TrqOvlSta_LKAPermFault_Cnt_M_u08	0 2	0 ± 1 2 ± 1	*

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.18999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0540000014	0.0540000014 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	7.63000011	7.63000011 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

est Step 3.56 (Repeat Count = 1)	Innut Value
ame	Input Value
te_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	4
rqOvlSta_ESCState_State_M_enum	0
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
rqOvlSta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	1050
rqOvlSta_Haptictime_Sec_M_f32	2315.65991
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	3
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.6999981
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	0
_APAMaxHwTrqTime_Sec_f32	0.159999996
APAMaxHwTrg HwNm f32	0.40000006
APAMaxVehSpd Kph f32	9
ESCMaxVehSpd Kph f32	22.1000004
_HapticAmplitude_MtrNm_f32	0.029999993
HapticDuration_Sec_f32	0.075000003
_HapticFreq_Hz_f32	5.57399988
_HapticReacttime_Sec_f32	0.079999982
_naplicreactiline_sec_isz LKAMaxVehSpd Kph f32	9.5
	8.75
_LKAMinVehSpd_Kph_f32 MayHantisPegTime_Sec_f32	
MaxHapticReqTime_Sec_f32	0.090000036
SWARLimiter_HwDeg_f32	22.1000004
_ShiftLevRevTime_Sec_f32	0.020999997
_StandstillThresh_Kph_f32	0.200000003
_StandstillTime_Sec_f32	0.0209999997
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cr	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
$rget_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSHOscillateState_State_error and all the properties of $	enun target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_	lgc target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_	lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_l	gc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

© Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530

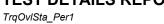


TrqOvIsta_APAState_State_Menum			• "	
Sugge Rg. Bird. Ap. TropOvSta TropOvSta Epide Part LOAPinouse Cont. Jo	Name	Input Value		
Images Res M.A. TopOriSta TopOriSta TopOst See TopOst TopOst See TopOst Se	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Pres_Pres_Pres_Processes_Pres_Pres_Pres_Pres_Pres_Pres_Pres	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target, Rev. May. Profosition Track/site. Pert. Missonarvivalentispeed. (pd.) 12 larget, Rev. (pd.) profosition Track/site. Pert. Missonarvivalentispeed (pd.) 12 larget, Rev. (pd.) profosition Track/site. Pert. Missonarvivalentispeed (pd.) 12 larget, Rev. (pd.) profosition Track/site. Pert. Track/site. Pert. Profosition Track/site. Pert. Profosition Track/site. Pert. Track/site. Pert. Track/site. Pert. Profosition Track/site. Pert. Track/site. Pert. Track/site. Pert. Profosition Track/site. Pert. Profosition Track/site. Pert. Track/site. Pert. Track/site. Pert. Pe	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_le	gc	
Image: Rev. Ap. TQ-ONSES TQ-ONSES TQ-ONSES TEP T-PASS-PERMISS DELT (1) por larger Rev. Per TQ-ONSES	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum			
Larget Tro/OSE Perf PoSS-on-Challe Cnt go	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3			
singst_Rest_Not_NotOsis_Toron (Size Perf Posis Toron (Size Coll.)		0 - 1		
Images Part A. P., Trick/Sis Trick/Sis Pert Part				
Image Tend Ap Tend T				
bigge_RR_bind_Ap_TriQUSSIs_TQUSSIs_PerT_SMIRLeverIsRReverse_Cnt_go: bigge_RR_bind_Ap_TriQUSSIs_TQUSSIs_PerT_TqQuER_bind_Ap_Ent_TquQuER_bind_Ap_Ent_TquQuER_bind_Ap_Ent_TquQuER_bind_Ap_Ent_TquQuER_bind_Ap_Ent_TquQuER_bind_Ap_Ent_TquQuER_bind_Ap_Ent_TquER_bind_Ap_Ent_TquQuER_bind_Ap_Ent_TquQuER_bind_Ap_Ent_TquER_bind_Ap_Ent_Ent_Ap_Ent_TquER_bind_Ap_Ent_Ent_Ap_Ent_TquER_bind_Ap_Ent_Ent_Ap_Ent_TquER_bind_Ap_Ent_Ent_Ap_E				
larger, Ten, Inst. Ap. Top-Osis TropOsis Pert. TropCocking. Mrivin 152 targer, Ten, Osis TropOsis TropOsis Pert. TropCocking Cnt. (bc. starger, Ten, Inst. Ap. TropOsis TropOsis Pert. TropCocking Cnt. (bc. starger, Ten, Inst. Ap. TropOsis TropOsis Pert. TropCocking Cnt. (bc. starger, Ten, Osis Pert. ApaRhametersate Cnt. (bc. state targer, TropOsis Pert. Osis Pert. (bc. state targer, TropOsis Pert. CoWPosito, Cnt. (bc. state targer, TropOsis Pert. COWPosition, Harbeig, 122-value targer, TropOsis Pert. COWPosition, Harbeig, 122-value targer, TropOsis Pert. (bc. State State Pert. (bc. state State Pert. CoWPosition, Harbeig, 122-value targer, TropOsis Pert. (bc. State State Pert. (
larger, Riv. Ma. A. Trigo/Sista Trigo/Sista Pert. Trigo/Sista Pert. Trigo/Sista Pert. Trigo/Sista Pert. Trigo/Sista Pert. APPAROVEMENT Pert. Per		· - ·	-	
Image Fire Inst An Trajovista Prof. Trajovista Prof. Trajovista Prof. Trajovista Prof. Trajovista Prof. APARAmeroverable Faulty C. IT. Speake Image Trajovista Prof. Common Tondon Speake Image Trajovista Prof. Common Tondon Speake Speake Image Trajovista Prof. Common Tondon Speake S				
Imaged_TroCoVista_Pert_APARometerorealDefaults_Cnt_govalue 1				
Ingred_Tro/Orista_Pert_APARcoverents/Fariats_Cnt_Igs-value 1			2	
Image TroOvisia Perl APRicagnest Cnt Igovalue 1				
Images Trq:O/Sta Pert COMPosition Pert				
Singel_TroONSis_Perl_CWPosition_HwDeg_R2xvalue				
ImageL_TrQONSIs_PerL_CVPCount_Cnt_[pc:value 1 1 1 1 1 1 1 1 1				
larget_TroOvSis_Pert_ESCFaul_Cnt_lgc.value 1				
larget_TroOvSta_Pert_ESCRequest_Cnt_tgc.value 1				
		1		
larget_TrqOvIsta_Pert_ApriCRequest_Cnt_Igc.value		1		
target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
larget_TrqOvISla_Pert_LKAFault_Cnt_lgc_value	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Perl_LKAFault_Cnt_Igo.value	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
larget_TrqOviSta_Pert_LKAInhibit_Cnt_Igc.value 1 1 1 1 1 1 1 1 1	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.96000004		
target_TrqOvSta_Per1_KARequest_Cnt_lgc_value 0 target_TrqOvSta_Per1_MaxSecureVehicleSpeed_Kph_132_value 55.599995 target_TrqOvSta_Per1_MaxSecureVehicleSpeed_Kph_132_value 55.5999985 target_TrqOvSta_Per1_PosTrajEnable_Cnt_lgc_value 1 target_TrqOvSta_Per1_PosTrajEnable_Cnt_lgc_value 0.150000006 target_TrqOvSta_Per1_PosTrajEnable_Cnt_lgc_value 23 target_TrqOvSta_Per1_ShiffLeverIshReverse_Cnt_lgc_value 23 target_TrqOvSta_Per1_ShiffLeverIshReverse_Cnt_lgc_value 0 target_TrqOvSta_Per1_ShiffLeverIshReverse_Cnt_lgc_value 0 target_TrqOvSta_Per1_ShiffLeverIshReverse_Cnt_lgc_value 0 target_TrqOvSta_Per1_ShiffLeverIshReverse_Cnt_lgc_value 0 target_TrqOvSta_Per1_ShiffLeverIshReverse_Cnt_lgc_value 0 target_TrqOvSta_PaState_State_M_enum 1 target_TrqOvSta_PaState_State_M_enum 1 target_TrqOvSta_ElapsedShiffLevRevTime_Sec_M_132 0 0 target_TrqOvSta_ElapsedShiffLevRevTime_Sec_M_132 0 target_TrqOvSta_ElapsedShiffLevRevTime_Sec_M_132 0 target_TrqOvSta_ElapsedShiffLevRevTime_Sec_M_132 target_TrqOvSta_Elaps	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32_value 49,7999992	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
Target_TrqOvIsta_Pert_NinSecureVehicleSpeed_Kph_f32.value 1	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 2.23 target_TrqOvISta_Per1_ShifftLeverIshinReverse_Cnt_lgc.value 2.3 Name	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value			
target_TrqOviSta_Pert_SWARTrgtAngRequest_HwDeg_f32.value 23				
Name				
Name				
TrqOvISta_PAState_State_Menum		· ·	1	
TrqOvISta_ESCState_State_Menum			•	Result
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32				~
TrqOviSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ±1 TrqOviSta_HapticState_State_Menum 2 2 TrqOviSta_HapticState_State_Menum 2 2 TrqOviSta_HapticCtime_Sec_M_f32 1050 1050 ±1 TrqOviSta_Hapticime_Sec_M_f32 2315.65991 2315.65991 ±1 TrqOviSta_HwTorqueSV_HwMm_M_Str.SV_UIs_f32 -6.80999994 -6.80999994 ±0.000009 TrqOviSta_LKAFault_Cnt_M_lgc 1 1 ±1 TrqOviSta_LKAPermFault_Cnt_M_lgc 1 1 ±1 TrqOviSta_LKAPermFault_Cnt_M_u08 3 3 ±1 TrqOviSta_LKAState_State_M_enum 3 3 TrqOviSta_LKAState_State_Menum 3 3 TrqOviSta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ±0.0009765625 TrqOviSta_ReadytoPulse_Cnt_M_lgc 0 0 ±1 target_TrqOviSta_Per1_APADtrinterventionDetected_Cnt_lgc.value 0 0 ±1 target_TrqOviSta_Per1_APADtrinterventionDetected_Cnt_lgc.value 4 4 target_TrqOviSta_Per1_ESCState_State_enum.value 1 1 target_TrqOviSta_Per1_ESCState_State_enum.value 2 2				~
TrqOvISta_ElapsedStandstillTime_Sec_M_f32		-		~
TrqOvISta_HapticState_State_M_enum 2 2 TrqOvISta_Hapticdur_Cnt_M_u16 1050 1050 ± 1 TrqOvISta_Haptictime_Sec_M_f32 2315.65991 2315.65991 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -6.80999994 -6.80999994 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_Igc 1 1±1 TrqOvISta_LKAPermFault_Cnt_M_Igc 1 1±1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3±1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_PeadytoPulse_Cnt_M_Igc 0 0 ± 1 target_TrqOvISta_Per1_APADrivrinerventionDetected_Cnt_Igc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value 2 2 target_TrqOvISta_Per1_DesServEnable_Cnt_Igc.value 1 1 ± 1 target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value 0.15000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOsCAmp_Mt				*
TrqOvISta_Hapticdur_Cnt_M_u16 1050 1050 ± 1 TrqOvISta_Haptictime_Sec_M_132 2315.65991 2315.65991 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -6.80999994 -6.80999994 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_Menum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrivrinterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_EACState_State_state_enum.value 2 2 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOsCEnable_Cnt_lgc.value 0 0 ± 1				V
TrqOvISta_Haptictime_Sec_M_f32 2315.65991 2315.65991 ± 1 TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -6.80999994 -6.80999994 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_Igc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_Igc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_Igc 0 0 ± 1 target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_Igc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOScillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value 1 1 ± 1 target_TrqOvISta_Per1_PosServOHwAngle_HwDeg_f32.value 0.150000006 0.15000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc.value				
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32 -6.80999994 -6.80999994 ± 0.000009 TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_GMOSHO_ScillateState_State_enum.value 1 1 target_TrqOvISta_Per1_LKAState_State_enum.value 2 2 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				-
TrqOvISta_LKAFault_Cnt_M_lgc 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrvInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				
TrqOvISta_LKAPermFault_Cnt_M_u08 1 1 ± 1 TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrivInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				
TrqOvISta_LKAPermFault_Cnt_M_u08 3 3 ± 1 TrqOvISta_LKAState_State_M_enum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				
TrqOvISta_LKAState_State_Menum 3 3 TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrivInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				_
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32 0.150000006 0.150000006 ± 0.0009765625 TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				
TrqOvISta_ReadytoPulse_Cnt_M_lgc 0 0 ± 1 target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_lgc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				_
target_TrqOvISta_Per1_APADrvrInterventionDetected_Cnt_Igc.value 0 0 ± 1 target_TrqOvISta_Per1_APAState_State_enum.value 4 4 target_TrqOvISta_Per1_ESCState_State_enum.value 1 1 target_TrqOvISta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvISta_Per1_LKAState_State_enum.value 3 3 target_TrqOvISta_Per1_PosServEnable_Cnt_Igc.value 1 1 ± 1 target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc.value 0 0 ± 1				_
target_TrqOvlSta_Per1_APAState_State_enum.value 4 4 target_TrqOvlSta_Per1_ESCState_State_enum.value 1 1 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				V
target_TrqOvlSta_Per1_ESCState_State_enum.value 1 1 target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				_
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value 2 2 target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				~
target_TrqOvlSta_Per1_LKAState_State_enum.value 3 3 target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value 1 1 ± 1 target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value 0.150000006 0.150000006 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value 0.0299999993 0.0299999993 ± 0.0009765625 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1				~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value 0 0 ± 1		0.150000006	0.150000006 ± 0.0009765625	~
	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.029999993	0.029999993 ± 0.0009765625	~
target TrgOylSta Per1 TrgOscFreg Hz f32.value 5.57399988 5.5739998 5.5739998 5.5739998 5.5739998 5.5739998 5.5739998 5.5739998 5.5739998 5.5739998 5.573999 5.57399 5.57399 5.57399 5.57399 5.57399 5.5739 5.	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
0.01000000 ± 0.00001000±0	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	5.57399988	5.57399988 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.57 (Repeat Count = 1)	la constant de la con
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	4 0
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	860.340027
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	484.200012
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	930.719971
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	1050
TrqOvlSta_Haptictime_Sec_M_f32	2315.65991
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0130000003
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0 2
TrqOvlSta_LKAPermFault_Cnt_M_u08 TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	6.69999981
TrqOvISta ReadytoPulse Cnt M Igc	0
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.159999996
k_APAMaxHwTrq_HwNm_f32	0.40000006
k_APAMaxVehSpd_Kph_f32	9
k_ESCMaxVehSpd_Kph_f32	22.1000004
k_HapticAmplitude_MtrNm_f32	0.029999993
k_HapticDuration_Sec_f32	0.075000003
k_HapticFreq_Hz_f32 k_HapticReacttime_Sec_f32	5.57399988 0.0799999982
k_LKAMaxVehSpd_Kph_f32	9.5
k_LKAMinVehSpd_Kph_f32	8.75
k_MaxHapticReqTime_Sec_f32	0.090000036
k_SWARLimiter_HwDeg_f32	22.1000004
k_ShiftLevRevTime_Sec_f32	0.0209999997
k_StandstillThresh_Kph_f32	0.200000003
k_StandstillTime_Sec_f32	0.0209999997
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Fe11_GMOSH_AFAMilgEnable_Cnt_lgc	target_TrqOvIsta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 HapticRequest Cnt Igc	target TrqOvlSta Per1 HapticRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvonwAngle_Hwbeg_isz target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1



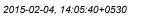


Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-361.269989		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	362.98999		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.96000004		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	49.7999992		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-23		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Resul
TrqOvlSta_APAState_State_M_enum	4	4	
TrgOvISta ESCState State M enum	0	0	
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	,
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	,
TrgOvlSta HapticState State M enum	2	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	1050	1050 ± 1	
TrgOvlSta Haptictime Sec M f32	2315.65991	2315.65991 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-6.80999994	-6.80999994 ± 0.000009	•
TrgOvlSta LKAFault Cnt M lgc	1	1 ± 1	
TrgOvISta LKAPermFault Cnt M Igc	1	1 ± 1	,
TrgOvlSta LKAPermFault Cnt M u08	3	3 ± 1	
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrgOvlSta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	
TrgOvlSta ReadytoPulse Cnt M lgc	0	0 ± 1	•
target TrgOvlSta Per1 APADrvrInterventionDetected Cnt lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	
	_	2	,
target TrqOvlSta Per1 GMOSHOscillateState State enum.value	2		
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target TrqOvlSta Per1 LKAState State enum.value	3	3	•
target_TrqOvlSta_Per1_LKAState_State_enum.value		3 1±1	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3	·	•
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3	1 ± 1	
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	3 1 0.150000006	1 ± 1 0.150000006 ± 0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.58 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	1	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	
TrqOvlSta_HapticState_State_M_enum	0	
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	
TrqOvlSta_Haptictime_Sec_M_f32	0	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	1	
TrqOvlSta_LKAState_State_M_enum	0	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998	

TrqOvlSta_Per1





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1.
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k APAMaxHwTrg HwNm f32	0
k APAMaxVehSpd Kph f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k HapticAmplitude MtrNm f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k LKAMinVehSpd Kph f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k SWARLimiter HwDeg f32	13
k_ShiftLevRevTime_Sec_f32	0.308999985
k StandstillThresh Kph f32	0
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOviSta_Per1_ESCState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enuncer.$	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Iqc	target TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-390.01001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.019989
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003

© Report created by TESSY V3.1.9, report template V2.1



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	1	1	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

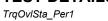
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.59 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	2
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	6100
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	1
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.180000007
k_APAMaxHwTrq_HwNm_f32	0.560000002
k_APAMaxVehSpd_Kph_f32	12.6000004
k_ESCMaxVehSpd_Kph_f32	26.8999996
k_HapticAmplitude_MtrNm_f32	0.0419999994
k_HapticDuration_Sec_f32	0.104999997
k_HapticFreq_Hz_f32	6.60200024
k_HapticReacttime_Sec_f32	0.112000003
k_LKAMaxVehSpd_Kph_f32	13.3000002
k_LKAMinVehSpd_Kph_f32	12.25
k_MaxHapticReqTime_Sec_f32	0.119999997
k_SWARLimiter_HwDeg_f32	28.8999996
k_ShiftLevRevTime_Sec_f32	0.0289999992
k_StandstillThresh_Kph_f32	0.280000001
k_StandstillTime_Sec_f32	0.0289999992
$target_Rte_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_In$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverabl	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFar	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgd		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u> _</u> f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_I		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lo		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	*	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvISta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_ target_TrqOvlSta_Per1_HwTorque_HwNm_1		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	ıc	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_en		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MaxSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleS		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	!	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-388.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.049988		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target TrqOvlSta_Per1_MaxSecureVehicleSpeed Kph f32.value	1 59.5999985		
target_TrqOvISta_Per1_MaxSecureVenicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvISta_Per1_NilliSecureverlicleSpeed_kpi1_isz.value target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta APAState State M enum	0	0	ixesuit
TrqOvISta_ESCState_State_M_enum	0	0	-
TrqOvISta_ESCState_state_M_enum TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	
TrqOvISta_ElapsedAhiftLevRevTime_Sec_IM_I32 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	-
TrqOvISta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvISta_HapticState_State_M_enum	0	0	-
TrqOvlSta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	·
TrqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-5.44645023	-5.44645023 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	_
		1±1	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	' - '	
TrqOvISta_LKAPermFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	·
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.170000002	0.170000002 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Fest Step 3.60 (Repeat Count = 1)	Innut Value
Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	2
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	6100
rqOvlSta_Haptictime_Sec_M_f32	3285.03003
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.180000007
APAMaxHwTrq HwNm f32	0.560000002
APAMaxVehSpd Kph f32	12.6000004
	12.6000004 26.8999996
_ESCMaxVehSpd_Kph_f32	
_HapticAmplitude_MtrNm_f32	0.041999994
_HapticDuration_Sec_f32	0.104999997
_HapticFreq_Hz_f32	6.60200024
_HapticReacttime_Sec_f32	0.112000003
_LKAMaxVehSpd_Kph_f32	13.3000002
_LKAMinVehSpd_Kph_f32	12.25
_MaxHapticReqTime_Sec_f32	0.119999997
_SWARLimiter_HwDeg_f32	28.8999996
_ShiftLevRevTime_Sec_f32	0.0289999992
_StandstillThresh_Kph_f32	0.280000001
_StandstillTime_Sec_f32	0.0289999992
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	g target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
urget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWFound Cnt Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCFault Cnt Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
rget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
rget_Rte_inst_Ap_rrqOviSta_rrqOviSta_Perr_ESCState_State_enum rget_Rte_inst_Ap_rrqOviSta_TrqOviSta_Perr_ESCState_State_enum	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lc	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3	2 target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f3222222222222222222222222222222222222$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_h	HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value target TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-386.450012		
· - ·	1		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.079987		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrgOvlSta Per1 HwTorque HwNm f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005	659.880005 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	
TrqOvlSta_HapticState_State_M_enum	0	0	V
TrqOvlSta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	· ·
TrqOvISta_LKAFault_Cnt_M_lgc TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1±1 1±1	
TrgOvISta_LKAPermFault_Cnt_M_gc TrgOvISta_LKAPermFault_Cnt_M_u08	3	3±1	
TrgOvISta LKAState State M enum	3	3	
TrqOvlSta_ExAState_State_M_enum TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.170000002	0.170000002 ± 0.0009765625	-
TrqOvISta ReadytoPulse Cnt M Igc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvISta_Per1_ESCState_State_enum.value	2	2	·
target_TrqOvlSta_Per1_GMOSHOscillateState_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	
target TrqOvlSta Per1 PosServEnable Cnt Igc.value	1	1±1	
target TrqOvISta Per1 PosSrvoHwAngle HwDeg f32.value	0.170000002	0.170000002 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0419999994	0.0419999994 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	6.60200024	6.60200024 ± 0.0009765625	•
	•		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	V





est Step 3.61 (Repeat Count = 1)	Input Value
tte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvISta_APAState_State_M_enum	2
rqOvlSta_ESCState_State_M_enum	2
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	659.880005
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	2670.19995
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	6100
rqOvlSta_Haptictime_Sec_M_f32	3285.03003
rqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.59000015
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0174000002
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	1
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	8.89999962
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.180000007
_APAMaxHwTrq_HwNm_f32	0.560000002
_APAMaxVehSpd_Kph_f32	12.6000004
ESCMaxVehSpd_Kph_f32	26.8999996
_HapticAmplitude_MtrNm_f32 _HapticDuration_Sec_f32	0.0419999994 0.104999997
HapticFreq_Hz_f32	6.60200024
HapticReacttime_Sec_f32	0.112000003
_LKAMaxVehSpd_Kph_f32	13.3000002
LKAMinVehSpd_Kph_f32	12.25
MaxHapticReqTime_Sec_f32	0.119999997
SWARLimiter_HwDeg_f32	28.8999996
ShiftLevRevTime_Sec_f32	0.0289999992
StandstillThresh Kph f32	0.280000001
StandstillTime_Sec_f32	0.0289999992
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui	n target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvISta_Per1_TrqOscEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
riget_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
arget_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
rget_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	

2015-02-04, 14:05:40+0530



TIQOVISIA_T ELT		(- 10-10
Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-384.670013		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.109985		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.66000009		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	59.5999985		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	69.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.170000002		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-19.2000008		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	110000
TrgOvlSta ESCState State M enum	1	1	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2639.65991	2639.65991 ± 1	
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	659.880005	659.880005 ± 1	•
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	
TrgOvISta HapticState State M enum	0	0	
TrqOvISta_Hapticdur_Cnt_M_u16	6100	6100 ± 1	
TrqOvISta_Haptictime_Sec_M_f32	3285.03003	3285.03003 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.44645023	-5.44645023 ± 0.000009	
TrqOviSta_LKAFault_Cnt_M_lgc	1	1 ± 1	
TrqOvISta LKAPermFault Cnt M Igc	1	1±1	
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	
TrqOvISta_LKAState_State_M_enum	3	3	
TrqOvISta PosSrvoHwAngle HwDeg M f32	0.170000002	0.170000002 ± 0.0009765625	
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	1	1±1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target TrqOvISta Per1 LKAState State enum.value	3	3	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.170000002	0.170000002 ± 0.0009765625	
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.0419999994	0.0419999994 ± 0.0009765625	
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.0419999994	0.0418999994 1 0.0009703023	•
target_TrqOvlSta_Per1_TrqOscEriable_Cit_gc.value	6.60200024	6.60200024 ± 0.0009765625	
target_riqOviota_reri_riqOsorreq_Fiz_isz.value	0.00200024	0.00200024 I 0.0009703023	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.62 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	3
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	175.460007
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	225.639999
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	684
TrqOvlSta_Haptictime_Sec_M_f32	1345
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00860000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	4.5





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.140000001
k_APAMaxHwTrq_HwNm_f32 k_APAMaxVehSpd_Kph_f32	0.239999995 5.4000001
k_ESCMaxVehSpd_Kph_f32	17.2999992
k_HapticAmplitude_MtrNm_f32	0.0179999992
k_HapticDuration_Sec_f32	0.0450000018
k_HapticFreq_Hz_f32	4.55999994
k_HapticReacttime_Sec_f32	0.0480000004
k_LKAMaxVehSpd_Kph_f32	5.69999981
k_LKAMinVehSpd_Kph_f32	5.25
k_MaxHapticReqTime_Sec_f32	0.0599999987
k_SWARLimiter_HwDeg_f32	20.2999992
k_ShiftLevRevTime_Sec_f32	0.0130000003
k_StandstillThresh_Kph_f32	0.119999997
k_StandstillTime_Sec_f32	0.0130000003
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3222222222222222222222222222222222222$	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg_f3	
target_rite_inst_Ap_riqovista.riqovista_reri_swartrigiangrequest_riwbeg_starget Rte Inst Ap Trq0vlSta.Trq0vlSta Per1 ShiftLeverIsInReverse Cnt Igc	target TrgOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target TrqOvlSta Per1 TrqOscAmp MtrNm f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-382.890015
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.140015
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOviota_r cr_GMOSH_LKAMfgEnable_Crit_igc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	1.25999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	40
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	49.7999992
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.129999995

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-26.7999992		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	0	0	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	308.519989	308.519989 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	684	684 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.02999973	-8.02999973 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.129999995	0.129999995 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.129999995	0.129999995 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0179999992	0.0179999992 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	4.55999994	4.55999994 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.63 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	2
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	33000
TrqOvlSta_Haptictime_Sec_M_f32	204770
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
TrqOvlSta_LKAFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.210999995
k_APAMaxHwTrq_HwNm_f32	8.80000019
k_APAMaxVehSpd_Kph_f32	198
k_ESCMaxVehSpd_Kph_f32	164.300003
k_HapticAmplitude_MtrNm_f32	0.660000026
k_HapticDuration_Sec_f32	1.64999998
k_HapticFreq_Hz_f32	54.9179993
k_HapticReacttime_Sec_f32	1.75999999
k_LKAMaxVehSpd_Kph_f32	209
k_LKAMinVehSpd_Kph_f32	120.220001
k_MaxHapticReqTime_Sec_f32	0.150000006
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.0370000005
k_StandstillThresh_Kph_f32	4.4000001
k_StandstillTime_Sec_f32	0.0370000005
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_APADrvrIntervent$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	g target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_l			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_er			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwD	eg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	~ 400		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	<u>g_</u> 132		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	lao		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_ target_TrqOvlSta_Per1_ESCRequest_Cnt_I	-		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_er			
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 GMOSHOscillateState State enur				
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	V - '			
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l	gc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	um		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	Speed_Kph_f32		
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleS	peed_Kph_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn	t_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt	_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	3 target_TrqOvlSta_Per1_SWARTrgtAngRequ	uest_HwDeg_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	1_f32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt	_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-381.109985			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.170013			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1			
target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1			
target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value	1			
target TrgOvlSta Per1 HapticRequest Cnt Iqc.value	0			
target TrqOvlSta Per1 HwTorque HwNm f32.value	-8.10000038			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0			
target TrqOvlSta Per1 LKARequest Cnt Igc.value	1			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	41.2000008			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.109999999			
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.70000005			
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc.value	1			
Name	Actual Value	Expected Value	Result	
TrqOvlSta_APAState_State_M_enum	2	2	-	
TrqOvISta_ESCState_State_M_enum	3	3	•	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	•	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422	84736.2422 ± 1	•	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•	
TrqOvlSta_HapticState_State_M_enum	1	1	•	
TrqOvlSta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	•	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019	-4.80000019 ± 0.000009	•	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•	
TrqOvlSta_LKAState_State_M_enum	0	0	•	
TrqOviota_Etv>otate_ivi_etiatii	U	0		
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	3.70000005	3.70000005 ± 0.0009765625	•	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	3.70000005	3.70000005 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	54.9179993	54.9179993 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

ame	Input Value
	·
e_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOvlSta
qOvISta_APAState_State_M_enum	2
qOvISta_ESCState_State_M_enum	1
qOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1952499
qOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	84736.2422
qOvlSta_ElapsedStandstillTime_Sec_M_f32	378600
qOvlSta_HapticState_State_M_enum	1
qOvlSta_Hapticdur_Cnt_M_u16	33000
QOVISta_Haptictime_Sec_M_f32	204770
qOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-4.80000019
qOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.441000015
qOvlSta_LKAFault_Cnt_M_lgc	1
qOvlSta_LKAPermFault_Cnt_M_lgc	0
qOvlSta_LKAPermFault_Cnt_M_u08	3
qOvlSta_LKAState_State_M_enum	0
qOvlSta_PosSrvoHwAngle_HwDeg_M_f32	38.2000008
qOvlSta_ReadytoPulse_Cnt_M_lgc	1
APAIncludeHaptic_Cnt_lgc	0
APAMaxHwTrqTime_Sec_f32	0.210999995
APAMaxHwTrq_HwNm_f32	8.80000019
APAMaxVehSpd_Kph_f32	198
ESCMaxVehSpd_Kph_f32	164.300003
HapticAmplitude_MtrNm_f32	0.660000026
HapticDuration_Sec_f32	1.6499998
HapticFreq_Hz_f32	54.9179993
HapticReacttime_Sec_f32	1.75999999
LKAMaxVehSpd_Kph_f32	209
LKAMinVehSpd_Kph_f32	120.220001
MaxHapticReqTime_Sec_f32	0.150000006
SWARLimiter_HwDeg_f32	60
ShiftLevRevTime_Sec_f32	0.0370000005
StandstillThresh_Kph_f32	4.400001
StandstillTime_Sec_f32	0.0370000005
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected	Cnt_target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cr	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lg	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
get Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target_TrqOvlSta_Per1_APAState_State_enum
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target TrqOvlSta Per1 CWFound Cnt Igc
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvISta_Per1_CWPosition_HwDeg_f32
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOviota_rer1_ESCFault_Cnt_lgc
get Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCIsLimited Cnt Igc	target TrqOvISta Per1 ESCIsLimited Cnt Igc
get Rte Inst Ap TrgOvISta.TrgOvISta Per1 ESCRequest Cnt Igc	target TrqOvIsta Per1 ESCRequest Cnt Igc
get Rte Inst Ap TrgOvISta.TrgOvISta Per1 ESCState State enum	target TrqOvISta Per1 ESCState State enum
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_E3c3tate_5tate_entini get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_e	
get_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GiviOSHOSciliateState_State_6 get_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_	
get_Rte_inst_Ap_frqOviSta.frqOviSta_Per1_GMOSH_APAMilgEnable_Crit_ get_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Crit_	
get_Rte_inst_Ap_frqOvista.frqOvista_Per1_GiMOSH_ESCINIgEnable_Crit_ iget_Rte_inst_Ap_frqOviSta.frqOviSta_Per1_GMOSH_LKAMfgEnable_Crit_	
get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc get_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc target TrqOvlSta_Per1_HwTorque_HwNm_f32

2015-02-04, 14:05:40+0530

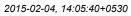


· -				
Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3;				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	- ·		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_PosTrajHwAngle_Hraget_TrqOvlSta_Per1_SWARTrgtAngRequ			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscAmp MtrNm f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32			
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0			
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-379.329987			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.200012			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.10000038			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	20.6000004			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	121			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0 0.109999999			
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	3.7000005			
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1			
Name	Actual Value	Expected Value	Result	
	2	2	Result	
TrqOvlSta_APAState_State_M_enum TrqOvlSta_ESCState_State_M_enum	3	3	-	
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1		
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	84736.2422	84736.2422 ± 1	-	
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	-	
TrqOvISta_HapticState_State_M_enum	1	1	~	
TrqOvISta_Hapticdur_Cnt_M_u16	33000	33000 ± 1	_	
TrqOvISta_Haptictime_Sec_M_f32	0	0 ± 1	✓	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-4.80000019	-4.80000019 ± 0.000009	-	
TrgOvlSta LKAFault Cnt M lgc	1	1±1	~	
TrqOviSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	-	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•	
TrqOvlSta_LKAState_State_M_enum	0	0	~	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	3.70000005	3.70000005 ± 0.0009765625	~	
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~	
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	~	
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~	
target_TrqOvlSta_Per1_LKAState_State_enum.value	•		✓	
	0	0	•	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	1±1	~	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 1 3.70000005	1 ± 1 3.70000005 ± 0.0009765625	~	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0 1 3.70000005 0.660000026	1 ± 1 3.70000005 ± 0.0009765625 0.660000026 ± 0.0009765625	, ,	
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0 1 3.70000005	1 ± 1 3.70000005 ± 0.0009765625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.65 (Repeat Count = 1)	√
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	5
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450 0
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
TrqOvISta HwTorqueSV HwNm M Str.K UIs f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvISta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0.601666689
k_APAMaxHwTrqTime_Sec_f32 k_APAMaxHwTrq_HwNm_f32	0.0010000009
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003 100
k_LKAMinVehSpd_Kph_f32 k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	100.599998
k ShiftLevRevTime Sec f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0.308999985
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Inst_Ap_TrqOvlSta_Per1_ApadrvrInterventionDetected_Cnt_Inst_ApadrvrInterventionDetected_Cnt_I$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOviSta_Per1_APARequest_Cnt_lgc target_TrqOviSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target TrgOvlSta Per1 GMOSH ESCMfgEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKARequest_Cnt_lgc target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_Fer1_FrqOscEriable_Crit_gc
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-377.549988		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.230011		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	5	5	~
TrqOvlSta_ESCState_State_M_enum	2	2	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	5	5	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	Y
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.66 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	3
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	5270
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k APAMaxVehSpd Kph f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k HapticAmplitude MtrNm f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k LKAMaxVehSpd Kph f32	89.3000031
k LKAMinVehSpd Kph f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k SWARLimiter HwDeg f32	0
k_ShiftLevRevTime_Sec_f32	0.188999996
k StandstillThresh Kph f32	4
k_StandstillTime_Sec_f32	0.188999996
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target TrgOvlSta Per1 APADrvrInterventionDetected Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APAState State enum	target TrgOvlSta Per1 APAState State enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CCWFound Cnt Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvISta_Per1_CWFound_Cnt_Igc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target TrgOvlSta Per1 ESCFault Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 LKAState State enum	target_TrqOvISta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 SWARTrgtAngRequest HwDeg f3.	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ShiftLeverlsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 TrgOscFreg Hz f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-375.769989
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	363.26001
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	0
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	2.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	3	3	~
TrqOvlSta_ESCState_State_M_enum	4	4	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	~
TrqOvlSta_HapticState_State_M_enum	1	1	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.67 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta ESCState State M enum	5
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	34667.4219
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	5450
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	3
TrqOvlSta_Hapticdur_Cnt_M_u16	5270
TrqOvlSta_Haptictime_Sec_M_f32	27475.5293
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0890000015
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	4
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.376666665
k_APAMaxHwTrq_HwNm_f32	3.75999999
k_APAMaxVehSpd_Kph_f32	84.5999985
k_ESCMaxVehSpd_Kph_f32	1
k_HapticAmplitude_MtrNm_f32	0.282000005
k_HapticDuration_Sec_f32	0.704999983
k_HapticFreq_Hz_f32	3.25
k_HapticReacttime_Sec_f32	0.751999974
k_LKAMaxVehSpd_Kph_f32	89.3000031
k_LKAMinVehSpd_Kph_f32	82.25
k_MaxHapticReqTime_Sec_f32	0.720000029
k_SWARLimiter_HwDeg_f32	60
k_ShiftLevRevTime_Sec_f32	0.188999996
k_StandstillThresh_Kph_f32	4
k_StandstillTime_Sec_f32	0.188999996
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_APADrvrInterventionDetected_Cntorellar (a) and the properties of the prope$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	·g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lg		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	0 _ 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEr	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEntarget_TrqOvlSta_Per1_HapticRequest_Cnt_	,	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvISta Per1 LKAFault Cnt Igc	0 <u>2</u>	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvISta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_lgc	target_TrqOvISta_Per1_LKARequest_Cnt_lg	c	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target TrqOvlSta Per1 LKAState State end		
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 MaxSecureVehicleSpeed Kph f32			
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MinSecureVehicleSpeed Kph f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 PosServEnable Cnt Igc	target TrqOvlSta Per1 PosServEnable Cnt	· -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_	.lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	vDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	est_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-373.98999		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	360.200012		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target TrqOvISta Per1 HwTorque HwNm f32.value	2.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	1		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	5	5	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0.00200000009	0.00200000009	~
TrqOvlSta_HapticState_State_M_enum	3	3	~
TrqOvlSta_Hapticdur_Cnt_M_u16	5270	5270 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.69000006	5.69000006 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	4	4 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	V
TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	1	1 ± 0.0009765625	V
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	5	5	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	1	1 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.282000005	0.282000005 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	3.25	3.25 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
	1
TrqOvISta_APAState_State_M_enum	4
FrgOvISta_ESCState_State_M_enum	
FrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	8867.28027 1274.76001
FrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32	8758.37988
FrqOvISta_ElapsedStandstillTime_Sec_M_f32	1
FrqOvISta_HapticState_State_M_enum	23775
FrqOvISta_Hapticdur_Cnt_M_u16	
FrqOvISta_Haptictime_Sec_M_f32	6679.83008
FrgOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.32000005 0.0328000002
FrqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	
FrqOvISta_LKAFault_Cnt_M_lgc	1
FrqOvISta_LKAPermFault_Cnt_M_lgc	1 2
FrqOvISta_LKAPermFault_Cnt_M_u08	2
FrgOvISta_LKAState_State_M_enum	
FrgOvISta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004
FrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
x_APAIncludeHaptic_Cnt_lgc	
<_APAMaxHwTrqTime_Sec_f32 <_APAMaxHwTrq_HwNm_f32	0.25
(_APAMaxHwTrq_HwNm_f32	1.12
<_APAMaxVehSpd_Kph_f32	25.2000008
C_ESCMaxVehSpd_Kph_f32	43.700008
(_HapticAmplitude_MtrNm_f32 (_HapticPuration_Soc_f32	0.083999989
(_HapticDuration_Sec_f32	0.20999993
K_HapticFreq_Hz_f32	10.199998
C_HapticReacttime_Sec_f32 HAMS: \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \text{AMS: } \(\text{AMS: } \(\text{AMS: } \t	0.224000007
(_LKAMaxVehSpd_Kph_f32	26.6000004
C_LKAMinVehSpd_Kph_f32	24.5
<_MaxHapticReqTime_Sec_f32	0.224999994
C_SWARLimiter_HwDeg_f32	21.7999992
C_ShiftLevRevTime_Sec_f32	0.057
x_StandstillThresh_Kph_f32	0.560000002
StandstillTime_Sec_f32 StandstillTime_Sec_f33 StandstillTim	0.057
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igo	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc





_			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
	target_TrqOvlSta_Per1_MaxSecureVehicleS		
	target_TrqOvlSta_Per1_MinSecureVehicleSp	- · -	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- ·	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvISta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
	target_TrqOvISta_Per1_SWARTrgtAngReque		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-998.710022		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	429.160004		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	0		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	0		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	5.11000013		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	103.699997		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.140000001		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		l
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	4	4	~
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	Ž
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	1274.76001	1274.76001 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	8758.37988	8758.37988	-
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrgOvlSta Hapticdur Cnt M u16	23775	23775 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	6679.83008	6679.83008 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.32000005	-1.32000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	4	4	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	16.6000004	16.6000004 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	4	4	
target_TrqOvlSta_Per1_ESCState_State_enum.value		4	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	4	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	4 0 ± 1	-
target_rrqOvlSta_Per1_PosServErlable_Cnt_lgc.value target_rrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	16.6000004	16.6000004 ± 0.0009765625	~
target_TrqOvlSta_Per1_F0sStvOnwArigie_Hwbeg_lsz.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.0839999989	0.0839999989 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	10.1999998	10.1999998 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.69 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target Rte Inst Ap TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	41000
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	216790.344 -3.4000001
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014
TrqOvlSta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1
TrqOvlSta_LKAPermFault_Cnt_M_u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997 15.6000004
k_ESCMaxVehSpd_Kph_f32 k_HapticAmplitude_MtrNm_f32	15.000004
k HapticDuration Sec f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.209999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	0.0529999994 target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc target TrqOvlSta Per1 ESCState State enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	V - 1
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosServEnable_Cnt_lgc	target TrqOvlSta Per1 PosServEnable Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3$	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32
$target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc$	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value	1
	1

2015-02-04, 14:05:40+0530



Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	849.059998		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrgOvlSta ESCState State M enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	87934.9609	87934.9609 ± 1	•
TrgOvlSta ElapsedStandstillTime Sec M f32	404784	404784	-
TrqOvlSta HapticState State M enum	1	1	-
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	-
TrqOvISta LKAPermFault Cnt M Igc	1	1 ± 1	-
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	-
TrqOvlSta PosSrvoHwAngle HwDeg M f32	0.150000006	0.150000006 ± 0.0009765625	-
TrqOvlSta ReadytoPulse Cnt M lgc	1	1 ± 1	•
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	-
target TrqOvlSta Per1 ESCState State enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	-
target TrqOvlSta Per1 LKAState State enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1±1	-
target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0	0 ± 0.0009765625	-
○ - · 1 1 1 1 1 1 - 1 1 1 1 1 1 1 1 1	1.5		
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.70 (Repeat Count = 1)		V
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	0	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2088351	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784	
TrqOvlSta_HapticState_State_M_enum	1	
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	
TrqOvlSta_Haptictime_Sec_M_f32	216790.344	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.485000014	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992	





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k StandstillThresh Kph f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc
target Rte Inst Ap TrqOviSta.TrqOviSta Per1 APANonRecoverableFaults Cnt Ig	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target Rte Inst Ap TrgOvISta.TrgOvISta Per1 APAState State enum	target TrgOvISta Per1 APAState State enum
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 CCWFound Cnt Igc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 CWPosition HwDeg f32	target TrqOvISta_Per1_CWPosition_HwDeg f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-928.01001
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	849.059998
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKAInhibit_Cnt_Igc.value	1.
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.600000024
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	82.4000015
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006
5 _ 1y	<u> </u>





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	10.1000004		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	87934.9609	87934.9609 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	404784	404784	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	41000	41000 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.4000001	-3.4000001 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992	43.7999992 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	43.7999992	43.7999992 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Name	Input Value
Rte Inst Ap TrqOvlSta	target Rte Inst Ap TrgOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	2088351
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	87934.9609
TrqOvlSta ElapsedStandstillTime Sec M f32	404784
TrgOvlSta HapticState State M enum	1
TrgOvISta Hapticdur Cnt M u16	41000
TrgOvlSta Haptictime Sec M f32	216790.344
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-3.4000001
TrgOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.485000014
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrgOvISta LKAPermFault Cnt M lgc	1
TrgOvISta LKAPermFault Cnt M u08	3
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	43.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	0
k_APAMaxHwTrqTime_Sec_f32	0.254999995
k_APAMaxHwTrq_HwNm_f32	9.11999989
k_APAMaxVehSpd_Kph_f32	205.199997
k_ESCMaxVehSpd_Kph_f32	15.6000004
k_HapticAmplitude_MtrNm_f32	0
k_HapticDuration_Sec_f32	1.71000004
k_HapticFreq_Hz_f32	56.973999
k_HapticReacttime_Sec_f32	1.824
k_LKAMaxVehSpd_Kph_f32	216.600006
k_LKAMinVehSpd_Kph_f32	199.5
k_MaxHapticReqTime_Sec_f32	0.20999993
k_SWARLimiter_HwDeg_f32	20.2000008
k_ShiftLevRevTime_Sec_f32	0.0529999994
k_StandstillThresh_Kph_f32	4.55999994
k_StandstillTime_Sec_f32	0.0529999994

2015-02-04, 14:05:40+0530

TrqOvlSta_Per1



toget, Rev. Ju. (ProcNess Teprich Sep. 1 APARteromental Seate, Cell 1961 target, Rie land A pt Too Ness Teprich Seate APARteromental Seate, Cell 1961 target, Rie land A pt Too Ness Teprich Seate APARteromental Seate, Cell 1961 target, Rie land A pt Too Ness Teprich Seate APARteromental Seate, Cell 1961 target, Rie land A pt Too Ness Teprich Seate APARteromental Seate, Cell 1961 target, Rie land A pt Too Ness Teprich Seate APARteromental SEATE APA	Name	Input Value		
	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverable	eFaults_Cnt_lgc	
Singet, Past, Pat ChroNosta Tracholists. Pert Cornivation City to being Resident Aproxists Tracholists. Pert Cornivation City to being Resident Aproxists. Tracholists Tracholists. Pert Cornivation City to being Resident Aproxists. Tracholists Tracholists. Pert Cornivation City to being Resident Aproxists. Tracholists. Tracholists. Tracholists. Pert Cornivation City to being Resident Aproxists. Tracholists. Pert Cornivation City to being Resident Aproxists. Tracholists. Pert ESCREPATION CITY. By tracholists. Tracholists. Pert ESCREPATION CITY. By tracholists. Pert Cornivation City. By tracholists. Tracholists. Pert ESCREPATION CITY. By tracholists. Pert Cornivation City.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFau	ılts_Cnt_lgc	
target_Re_Inst_A_D_TO_ONES TROVSES PAT_COMPOSITED_TOURSES	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lg	С	
singer, Rev. May, Trophysian (1704) Start (1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enu	um	
	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	$target_TrqOvlSta_Per1_CCWFound_Cnt_lgc$		
Singer, Rev. Mar. Trop/Sista Trop/Sista Perf. Scribball (Ch. 1)ge	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	$target_TrqOvlSta_Per1_CCWPosition_HwDe$	g_f32	
tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Cnt. Jose tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Cnt. Jos tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Cnt. Jos tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Cnt. Jos tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert ESChaill. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita TropVisita Pert Ind. Sels. e. nam tager, Rev. Ind. Ap. TropVisita				
			_f32	
target. Part Inst. Ap. Trock/sis. Prof. SSC Part Part SSC Part SSC Part SSC Part SSC Part Part SSC Part SSC Part Part SSC Part SSC Part Part SSC Part Part SSC Part Part Part Part Part Part Part Part				
Image Res Ind. Ap. TopOSIs TopOSIs Pert ESCState State enum Image Res Ind. Ap. TopOSIs TopOSIs Pert SCSState State enum Image Res Ind. Ap. TopOSIs TopOSIs Pert SCSState State Pert Image Res Ind. Ap. TopOSIs TopOSIs Pert SCSState State Pert Image Res Ind. Ap. TopOSIs TopOSIs Pert SCSState State Image I				
Image Re, Inst. Ap. TopO/SSI Prof. (MOSH-SeculateState, State, enum Image Re, Inst. Ap. TopO/SSI Prof. (MOSH-SECMISTER) Image Re, Inst. Ap. TopO/SSI Prof. (MOSH-SECMISTER) Image Re, Inst. Ap. TopO/SSI TOPO/SSI Prof. (MOSH-SECMISTER) Image Re, Inst. Ap. TopO/SSI TOPO/SSI Prof. (MOSH SECMISTER) Image Re, Inst. Ap. TopO/SSI TOPO/SSI Prof. (MOSH SECMISTER) Image Re, Inst. Ap. TopO/SSI TOPO/SSI Prof. (MOSH SECMISTER) Image Re, Inst. Ap. TopO/SSI TOPO/SSI Prof. (LAPA Inst. Ap. TopO/SSI TOPO/SSI Prof. LAPA TOPO/SSI TOPO/SSI Prof. LAPA Inst. Ap. TopO/SSI TOPO/SSI Prof. LAPA TOPO/SSI TOPO/SSI Prof. LAPA Inst. Ap. TopO/SSI TOPO/SSI Prof. LAPA Inst. Ap. TopO/SSI Prof. LAPA				
langer, Ries, Mar, Tracovisia, Perd, GMOSH, APAMIginahue, Crit. Joc. langer, Ries, Mar, Tracovisia, Tracovisia, Perd, GMOSH, ESCAMIG-pate, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, MICHAN, Escamie, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Hardrager, Jewhen, 122 langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Hardrager, Jewhen, 122 langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Hardrager, Jewhen, 122 langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Hardrager, Jewhen, 122 langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Hardrager, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Hardrager, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Mardrager, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Mardrager, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Mardrager, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Mardrager, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Mardrager, Crit. Joc. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Ind. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Tracovisia, Perd, Ind. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Tracovisia, Perd, Ind. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, Tracovisia, Perd, Ind. langer, Ries, Ind. Ap. Tracovisia, Tracovisia, Perd, T		· - ·		
Integrow Tells Intell. Apt Trod/Stail Trod/Stail Prof. (SMSAIL Prof. (SMSAIL Prof.) Haptic Request Cell 1965				
Images Test Inst. Ap. TroCVSIsta TroCVSIs Pert ApaConsequent Cont. program TroCVSIs TroCVSIs Pert		· - · ·		
target_Re Inst_AD_TO(NSIA Tra(NSIA Pert I) Hadroffequeed_(Indiget_Re Inst_AD_TO(NSIA Tra(NSIA Pert I) Hadroffequeed_(Indiget_Re Inst_AD_TO(NSIA Tra(NSIA Pert I) LAWFault_Crt_lgc target_Re Inst_AD_TO(NSIA Tra(NSIA Pert I_LAWFault_Crt_lgc target_Re Inst_AD_TO(NSIA Tra(NSIA Pert I_LAWFault_Crt_lgc target_Re Inst_AD_TO(NSIA Tra(NSIA Tra(NSIA Pert I_NSIA Pert I_LAWFault_Crt_lgc target_Re Inst_AD_TO(NSIA Tra(NSIA Tra(NSIA Pert I_NSIA Pert I_LAWFault_Crt_lgc target_Re Inst_AD_TO(NSIA Tra(NSIA Tra(NSIA Pert I_NSIA Pert I_NSIA Pert I_LAWFault_Crt_lgc target_Re Inst_AD_TO(NSIA Tra(NSIA Tra(NSIA Pert I_NSIA Pert I_NS				
Image File Incl. Ap. TrigO/Sita Pert File TrigO/Sita Pert Tri		· - · ·		
Image Re, Inst. Ap. TrqOVSIs TrqOVSIs Pert LKAFaul , Cnt, go Image Re, Inst. Ap. TrqOVSIs Pert LKAFaul , Cnt, go Image Re, Inst. Ap. TrqOVSIs TrqOVSIs Pert LKAFaul , Cnt, go Image Re, Inst. Ap. TrqOVSIs TrqOVSIs Pert LKAFaul , Cnt, go Image Re, Inst. Ap. TrqOVSIs TrqOVSIs Pert LKAFaul , Cnt, go Image Re, Inst. Ap. TrqOVSIs TrqOVSIs Pert LKAFaul , Cnt, go Image Re, Inst. Ap. TrqOVSIs TrqOVSIs Pert Miniscence/whickSpeed Kpt. S2 Image Re, Inst. Ap. TrqOVSIs TrqOVSIs Pert Aps. TrqOVSIs TrqOVSIs Pert Aps. TrqOVSIs Pert Aps. TrqOVSIs TrqOVSIs Pert Aps. TrqOVSIs Tr				
Integror Ris Inst. Ap. TropOvisia TropOvisia Pert LKAInhibit Cnt. pc				
sarget Rie nst Ap, TrqOv/Sta Pert LKASiate, Slate, enum sarget TrqOv/Sta Pert MasSecureVehicleSpeed Kph 122				
target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Perl MinSecureVehicleSpeed, Kip. 122 target, Rie, Inst. Ap. TrqOVISIa. Perl MinSecureVehicleSpeed Kip. 132 target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Perl PosservErable, Crit. 190 target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Perl PosservErable, Crit. 190 target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Perl PosservErable, Crit. 190 target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Perl PosservErable, Crit. 190 target, Rie, Inst. Ap. TrqOVISIa. TrqOVISIa. Perl Possarity Institute Visia. Perl Possarity Visia. Perl Possarity Visia. Perl Possarity Visia. Perl Possarity Visia. Perl TrqOvisia. Perl Possarity Visia. Perl TrqOvisia. Perl Visia. Perl TrqOvisia. Perl Visia.	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgd	С	
larget_Ric_Inst_Ap_TrqOv/Sta_Per1_Posser/Enable_Cnt_Igc target_Ric_Inst_Ap_TrqOv/Sta_Per1_Posser/Enable_Cnt_Igc target_Ric_Inst_Ap_TrqOv/Sta_Per1_Posser/Enable_Cnt_Igc target_Ric_Inst_Ap_TrqOv/Sta_Per1_Posser/Enable_Cnt_Igc target_Ric_Inst_Ap_TrqOv/Sta_Per1_Posser/Enable_Cnt_Igc target_Ric_Inst_Ap_TrqOv/Sta_Per1_Posser/Enable_Cnt_Igc target_Ric_Inst_Ap_TrqOv/Sta_Per1_Posser/Enable_Cnt_Igc target_Ric_Inst_Ap_TrqOv/Sta_Per1_SVARTrgtAnglequest_HvDeg_32 target_Ric_Inst_Ap_TrqOv/Sta_Per1_SVARTrgtAnglequest_HvDeg_32 target_Ric_Inst_Ap_TrqOv/Sta_Per1_SVARTrgtAnglequest_HvDeg_32 target_Ric_Inst_Ap_TrqOv/Sta_Per1_SVARTrgtAnglequest_HvDeg_32 target_Ric_Inst_Ap_TrqOv/Sta_Per1_SVARTrgtAnglequest_HvDeg_32 target_Ric_Inst_Ap_TrqOv/Sta_Per1_TrqOs-Rich_P Mintm_R2 target_TrqOv/Sta_Per1_TrqOs-Rich_P Mintm_R2 target_TrqOv/Sta_Per1_Mintm_R2		target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target Re. Inst. Ap. TrqOVSta TrqOVSta Port PosServEnable Cit. Cit	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleSp	peed_Kph_f32	
target_Rre_Inst_Ap_TrqOvSta_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_HwDeg_132 target_Rre_Inst_Ap_TrqOvSta_Pert_PostRorbAwage_Ert_PostRor	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	$target_TrqOvlSta_Per1_MinSecureVehicleSp$	eed_Kph_f32	
target, Re_Inst, Ap_TrqOviSta_TrqOviSta_Pert_PosTrajehable_Cnt_lgc target, Re_Inst, Ap_TrqOviSta_Pert_PosTrajehable_Cnt_lgc target, Re_Inst, Ap_TrqOviSta_Pert_PosTrajehable_Cnt_lgc target, Re_Inst, Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgiAngRequest_HwDeg_1S2 target, Re_Inst, Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgiAngRequest_HwDeg_1S2 target, Re_Inst, Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgiAngRequest_HwDeg_1S2 target, Re_Inst, Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgiAngRequest_HwDeg_1S2 target_Re_Inst, Ap_TrqOviSta_TrqOviSta_Pert_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_SWARTrgiAngRequest_HwDeg_1S2 target_Re_Inst, Ap_TrqOviSta_TrqOviSta_Pert_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm_1S2 target_TrqOviSta_Pert_Ap_TrqOsAch_Mithm	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	$target_TrqOvlSta_Per1_PosServEnable_Cnt_$	_lgc	
target_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_SWARTigMngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSia_Perf_SWARTigMngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSia_Perf_SWARTigMngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_TrqOscEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSia_TrqOviSia_Perf_TrqOscEnable_Cnt_Igc target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_TrqOscEnable_Cnt_Igc target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_TrqOscEnable_Cnt_Igc target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_TrqOscEnable_Cnt_Igc_value 1 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_TrqOscEnable_Cnt_Igc_value 1 target_TrqOviSia_Perf_SMRETrgMngRequest_HwDeg_132 target_TrqOviSia_Perf_SMRETrgMngReques	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	$target_TrqOvlSta_Per1_PosSrvoHwAngle_Hv$	wDeg_f32	
target Re mst Ap TrqOviSla TrqOviSla Pert SWARTrgAngRequest Hwbeg 15 target Rie mst Ap TrqOviSla TrqOviSla Pert Shifft evertsin Reverse Cnt go target Rie mst Ap TrqOviSla TrqOviSla Pert TrqOscAmp Mrthm 132 target Rie mst Ap TrqOviSla TrqOviSla Pert TrqOscAmp Mrthm 132 target Rie mst Ap TrqOviSla TrqOviSla Pert TrqOscAmp Mrthm 132 target Rie mst Ap TrqOviSla TrqOviSla Pert TrqOscAmp Mrthm 132 target TrqOviSla Pert APAREcoverableFaults Cnt go: value 0 target TrqOviSla Pert APAREcoverableFaults Cnt go: value 0 target TrqOviSla Pert APARecoverableFaults Cnt go: value 1 target TrqOviSla Pert CWPosition Hwbeg f32 value 849 059998 target TrqOviSla Pert CWPosition Hwbeg f32 value 1 target TrqOviSla Pert ESCRequest Cnt go: value 1 target TrqOviSla Pert ESCRequest Cnt go: value 1 target TrqOviSla Pert Scrieculest Cnt go: value 1 target TrqOviSla Pert ApAMigEnable Cnt go: value 1 target TrqOviSla Pert ApaMigEnable Cnt go: value 1 target TrqOviSla Pert Morote Hwbm f32 value 6 target TrqOviSla Pert Dostra Hwbeg f32 value 6 target TrqOviSla Pert Morote	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	$target_TrqOvlSta_Per1_PosTrajEnable_Cnt_$	lgc	
target_Rie_Inst_Ap_TrqOviSta_Pert_ShiftLeverIsInReverse_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_Pert_ShiftLeverIsInReverse_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscEnable_Cnt_lgc target_TrqOviSta_Pert_Ap_TrqOviSta_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hw	vDeg_f32	
target_Re_Inst_Ap_TropOvSta_TropOvSta_Pert_TropScAmp_MthVm_32 target_Re_Inst_Ap_TropOvSta_TropOvSta_Pert_TropScAmp_MthVm_32 target_Re_Inst_Ap_TropOvSta_TropOvSta_Pert_TropScAmp_MthVm_32 target_Re_Inst_Ap_TropOvSta_TropOvSta_Pert_TropScAmple_Core_Insc_target_TropOvSta_Pert_TropScAmp_MthVm_32 target_TropOvSta_Pert_APARonecoverableFaults_Cnt_Igc_value 1				
target_Re_inst_Ap_TrqOviSta_Prr1_TrqOscEnable_Cnt_lgc target_TrqOviSta_Prr1_TrqOscEnable_Cnt_lgc target_Re_inst_Ap_TrqOviSta_Prr1_TrqOscFreq_Hz_f32 target_TrqOviSta_Prr1_TrqOscFreq_Hz_f32 target_TrqOviSta_Prr1_APARecoverableFaults_Cnt_lgc_value 0 target_TrqOviSta_Prr1_APARecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Prr1_CVFound_Cnt_lgc_value 1 target_TrqOviSta_Prr1_CVFound_Cnt_lgc_value 1 target_TrqOviSta_Prr1_CVFound_Cnt_lgc_value 1 target_TrqOviSta_Prr1_CVFound_Cnt_lgc_value 498.01001 target_TrqOviSta_Prr1_ESCFault_Cnt_lgc_value 1 target_TrqOviSta_Prr1_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Prr1_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Prr1_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Prr1_MSCRequest_Cnt_lgc_value 1 target_TrqOviSta_Prr1_EXCRequest_Cnt_lgc_value 1 target_TrqOviSta_Prr1_EXCRequest_Cnt_lgc_value 1 target_TrqOviSta_Prr1_EXCRequest_Cnt_lgc_value 1 target_TrqOviSta_Prr1_EXCRequest_Cnt_lgc_value 1 target_TrqOviSta_Prr1_EXCRequest_Cnt_lgc_value 0 target_TrqOviSta_Prr1_EXCRequest_Cnt_lgc_value 0		· - ·		
target_Re_Inst_Ap_TrqOviSta_Pert_TrqOscFreq_Hz_f32 target_TrqOviSta_Pert_APANonRecoverableFaults_Cnt_Igc.value 1				
larget_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0			gc	
target_TrqOvIsta_Pert_APARequest_Cnt_Igc.value 1 1 1 1 1 1 1 1 1				
target_TrqOvISta_Per1_APARequest_Cnt_Igc.value	· - ·			
target_TrqOvISta_Pert_CCWFound_Cnt_Igc.value				
target_TrqOvISta_Per1_CWPosition_HwDeg_f32.value	* = 1 = = -			
target_TrqOvISta_Per1_CWFound_Cnt_Igc.value	3-2 1			
target_TrqOv/Sta_Per1_CWPosition_HwDeg_f32.value				
target_TrqOv/Sta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_ESCReturited_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_ESCReturited_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_GMOSH_APAM(gEnable_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_Haptorequest_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_Haptorequest_Cnt_lgc.value 0 target_TrqOv/Sta_Per1_Haptorequest_Cnt_lgc.value 0 target_TrqOv/Sta_Per1_Haptorequest_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOv/Sta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOv/Sta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOv/Sta_Per1_MinSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOv/Sta_Per1_PosTrajHavAngle_HuDeg_f32.value 1 target_TrqOv/Sta_Per1_PosTrajHavAngle_HuDeg_f32.value 10.1000006 target_TrqOv/Sta_Per1_SwARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOv/Sta_Per1_SwiftLeverIsinReverse_Cnt_lgc.value 0 Name				
target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMIgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMIgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 0 target_TrqOvISta_Per1_HxFraut_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFaut_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKAFaut_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32_value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32_value 1 target_TrqOvISta_Per1_More_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajImable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajImable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajImaple_Cnt_Igc.value 1 target_TrqOvISta_Per1_SwARTrgtAngRequest_HwDeg_f32_value 10.1000004 target_TrqOvISta_Per1_ShiffLeverIsInReverse_Cnt_Igc.value 0 Name				
target_TrqOviSta_Per1_ESCRequest_Cnt_igc.value 1 target_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value 0 target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value 0 target_TrqOviSta_Per1_HapticRequest_Cnt_igc.value 0 target_TrqOviSta_Per1_HwTorque_HwNm_f32.value 1 target_TrqOviSta_Per1_LKAFault_Cnt_igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_igc.value 1 target_TrqOviSta_Per1_LKAInhibit_Cnt_igc.value 1 target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOviSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 1 target_TrqOviSta_Per1_PosTrajEnable_Cnt_igc.value 1 target_TrqOviSta_Per1_PosTrajEnable_Cnt_igc.value 1 target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32.value 1 target_TrqOviSta_Per1_SwARTrgtAngRequest_HwDeg_f32.value 10.15000006 target_TrqOviSta_Per1_ShiftLeverIsInReverse_Cnt_igc.value 0 Name	312 11 112 1 2 11 11 21 231 111			
target_TrqOvISta_Pert_GMOSH_APAMfgEnable_Cnt_lgc.value	0 = 1 = = = = = = = = = = = = = = = = =			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 1		1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32_value -6.0999999 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32_value 0.600000024 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32_value 82.4000015 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32_value 0.150000006 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32_value 10.1000004 target_TrqOvlSta_Per1_ShiffLeverIsInReverse_Cnt_lgc.value 0 Name Actual Value Expected Value Rest TrqOvlSta_APAState_State_M_enum 1 1 1 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 ± 1 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 0 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784 404784	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 0 target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.600000024 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Residence TrqOvISta_APAState_State_M_enum 1	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvISta_Per1_LKAInhibit_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.600000024 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Rest TrqOvISta_APAState_State_M_enum 1	target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-6.0999999		
target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.600000024 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Residence TrqOvISta_APAState_State_M_enum 1 1 1 1 TrqOvISta_ESCState_State_M_enum 3 3 3 3 TrqOvISta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 0 1 TrqOvISta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 0 1 0 0 0 1 0 0 0 0 0 1 0	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.600000024 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value 0 Name Actual Value Expected Value Rest TrqOvlSta_APAState_State_M_enum 1 1 1 TrqOvlSta_ESCState_State_M_enum 3 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 82.4000015 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Residence TrqOvlSta_APAState_State_M_enum 1 1 1 TrqOvlSta_ESCState_State_M_enum 3 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedShiffLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_FlapsedStandstillTime_Sec_M_f32	0 = 1 = = 1 = = 0			
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value 0 Name Actual Value Expected Value Result TrqOvlSta_APAState_State_Menum 1 1 1 TrqOvlSta_ESCState_State_Menum 3 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784				
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value 0 Name Actual Value Expected Value Rest TrqOvlSta_APAState_State_M_enum 1 1 1 TrqOvlSta_ESCState_State_M_enum 3 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784				
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value 10.1000004 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value 0 Name Actual Value Expected Value Rest TrqOvlSta_APAState_State_Menum 1 1 1 1 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 0 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 0 1 1 0<				
Name Actual Value Expected Value Rest TrqOvISta_APAState_State_M_enum 1				
Name Actual Value Expected Value Residence TrqOvlSta_APAState_State_M_enum 1 1 1 TrqOvlSta_ESCState_State_M_enum 3 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 0 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784 404784				
TrqOvlSta_APAState_State_M_enum 1 1 TrqOvlSta_ESCState_State_M_enum 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784	3.2 4 2. 2 2 2 2 2 2 2 2 2 2 2 2 2 2			
TrqOvlSta_ESCState_State_M_enum 3 3 TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784			•	Result
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784				· ·
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32 0 0 ± 1 TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784				
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32 404784 404784				*
				Ž
inquinotal_naphrociato_otato_m_crium				~
TrgOvlSta_Hapticdur_Cnt_M_u16 41000 41000 41000 ± 1				
TrqOvlSta_Haptictime_Sec_M_f32				~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32 -3.4000001 -3.4000001 -3.4000009				-
TrgOvlSta LKAFault Cnt M lgc 0 0 ± 1				~
TrgOvlSta LKAPermFault Cnt M lgc 1 1 1±1	, – – – –			
TrgOvISta LKAPermFault Cnt M u08 3 3±1				•
TrqOvlSta_LKAState_State_M_enum 3 3	,			•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32		0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc 1 1±1		4	4.4	

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	•
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0	0 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	56.973999	56.973999 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Test Step 3.72 (Repeat Count = 1)	· ·
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOviSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta ElapsedStandstillTime Sec M f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvISta LKAFault Cnt M Igc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k APAMaxVehSpd Kph f32	138.600006
k ESCMaxVehSpd Kph f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k HapticReacttime Sec f32	1.23199999
k LKAMaxVehSpd Kph f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k SWARLimiter HwDeg f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target TrqOvISta Per1 APAState State enum
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_reri_ArAState_State_entime target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_CCWPosition_HwDeg_f32	target TrqOvISta Per1 CCWPosition HwDeg f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target TrqOvISta Per1 CWFound Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 ESCFault Cnt Igc	target TrqOvISta Per1 ESCFault Cnt Igc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target TrqOvIsta Per1 ESCRequest Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvISta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_mqoviota.mqoviota_ren_sostate_state_state enur	
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc	target_TrqOvlota_ren_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvIsta_Per1_HapticRequest_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
g	





			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_ target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
	target_TrqOvISta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Crit_igc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		l
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1 2	1 2	-
TrqOvlSta_ESCState_State_M_enum TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.002000000009 ± 1	Ž
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta ElapsedStandstillTime Sec M f32	0	0	-
TrqOvISta_HapticState_State_M_enum	0	0	~
TrgOvlSta Hapticdur Cnt M u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	V
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	- 4
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	
target_TrqOvlSta_Per1_LKAState_State_enum.value	0		*
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value target TrqOvlSta Per1 PosSrvoHwAngle HwDeg f32.value	0.20000003	1 ± 1 0.200000003 ± 0.0009765625	~
target_TrqOvlSta_Per1_FosSivonwArigie_Hwbeg_isz.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	-
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	-
0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1			

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Name	Test Step 3.73 (Repeat Count = 1)	▼
No. Impure Impu	Name	Input Value
Tropolities Section		•
TropOstes Bispeed Name Interface Set. M. 192 0 TropOstes Bispeed Name Interface Set. M. 192 0 TropOstes Bispeed Name Interface Set. M. 192 0 TropOstes Industrian Conf. M. 192 0 TropOst	TrqOvlSta_APAState_State_M_enum	
TROMOSE Bispectabilities See, M. 20 TropOSE See, See, C. M. 20 TropOSE See, C. M. 20 TropOSE See, See, C. M. 20 TropOSE See, See, C. M. 20	TrqOvlSta_ESCState_State_M_enum	1
Topockas Supposed Service Service Supposed Service Servi	TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
Topicida Sparticus State March	TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TROUBLE SPRINGER CH. M. 151 TROUBLE SPRINGER SE M. 152 TROUBLE SPRINGER SE	TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	
TROMBIS Informacy Hole Min SISY US 102 TROMBIS INFORMACY Hole Min SISY US 102 TROMBIS INFORMACY Hole Min SISY US 10 TROMBIS INFORMACY HOLE MIN SIS		i e
TEROMES INFORMED/ LINKING MS 05 Mg 102 TEROMOS INFORMED/ LINKING MS 105 Mg 102 TEROMOS INFORMED (CPI Mg 10) TEROMOS INFORME		
TROWSE JAKSHER MATERIAL CH M. 196		·
TROWING JAKPent LOT M. 190 TROWING JAKPent JC M. 190 TROWING		
TECOMOSIS LIA/Particul Cott M		
TODO/Sile J NOSHINE SINE M, M you A PARAMENHORIS COLLING A PARAMENHORIS COLL	TrqOvlSta_LKAPermFault_Cnt_M_lgc	
1,000/058 Perside Crit My De	TrqOvlSta_LKAPermFault_Cnt_M_u08	1
Track/instance Trac	TrqOvlSta_LKAState_State_M_enum	0
A.P.A.MacHort True, Dec. 12	TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	
A. PAMAMAN Trailing Sec., 152 0.00160088	TrqOvlSta_ReadytoPulse_Cnt_M_lgc	
A.PAMAWAYISID KN. 122		
K. JOANAW-NISPEL (Apr.) 12 138.000006		
Language Main 12		
k. Hapscharden, 1924 k. Hapschren, 192, 192 k. Hapschren, 192, 192 k. Hapschren, 192, 192 k. Hapschren, 192, 192 k. Hapschren, 192, 193 k. Hapschren, 192, 193 k. Hapschren, 192, 193 k. LAMANIVERS, 193 k. Sandatillines, 193 k. Sandatill	_ :=:=	
k, Hapsticheactime, Sec. 132 k, Hapsticheactime, Sec. 132 k, Hapsticheactime, Sec. 132 k, LARAMARVSB, Kyh. 132 k, Simitar, Hapsticheactime, Sec. 132 k, Si	k_HapticAmplitude_MtrNm_f32	
k, Lykandwinkpis Mp. 122 k, Likarkamyten Spr. Kph. 122 k, Likarkamyten Spr. Kph. 122 k, Lykarkamyten Spr. Kph. 122 k, Shiffuer New Time, Sec. 122 target, Rein New Timovista Trayovista, Pert J. APANoriterovention-Detected, Cirt. Ligo target, Rein New Timovista, Pert J. APANoriterovention-Detected, Cirt. Ligo target, Rein New Timovista, Pert J. APANoriterovention-Detected, Cirt. Ligo target, Rein New Timovista, Pert J. APANoriterovention-Detected, Cirt. Ligo target, Rein New Timovista, Pert J. APANoriterovention-Detected, Cirt. Ligo target, Rein New Timovista, Pert J. APANoriterovention-Detected, Cirt. Ligo target, Rein New Timovista, Pert J. APANoriterovention-Detected, Cirt. Ligo target, Rein New Timovista, Pert J. COWPostion HwDeg, 122 target, Rein New Timovista, Pert J. CoWPostion HwDeg, 122 target, Rein New Timovista, Pert L. CowPostion HwDeg, 122 target, Rein New Timovista, Pert L. Gwostia, Pert L. Scotlain-Bert L. Cirt. Ligo target, Rein New Timovista, Pert L. Gwostia, Pert L. Scotlain-Bert L. Cirt. Ligo target, Rein New Timovista, Pert L. Gwostia, Pert L. Scotlain-Bert L. Cirt. Ligo target, Rein New Timovista, Pert L. Gwostia, Pert L. Scotlain-Bert L. Cirt. Ligo target, Rein New Timovista, Pert L. Gwostia, Pert L. Monosita, Pe	k_HapticDuration_Sec_f32	
L KAMAINVerSigd Koft, 132 L KAMAINVerSigd Koft, 132 L KAMAINVerSigd Koft, 132 L L KAMAINVERSigd Koft, 132 L SIMARAILIMITE, HACRES, 132 S O 0.800000024 L SIMARAILIMITE, HACRES, 132 O 0.8000000024 L SIMARAILIMITE, HACRES, 132 O 0.8000000024 L SIMARAILIMITE, HACRES, 132 O 0.800000000000000000000000000000000000	k_HapticFreq_Hz_f32	32.3019981
k, LMAMPHONEOR (pt.) 12 k. Marthoplic Real Time, Sec. (132 k. Marthoplic Real Time, Sec. (132 k. Shifted Windows (pt.) 2 k. Shift	k_HapticReacttime_Sec_f32	1.23199999
k, Marklamiter I-Mong, 152 k, Shintlamer I-M	k_LKAMaxVehSpd_Kph_f32	146.300003
k, SindstellThesh, Kpn, 132 k, SlandstellThesh, Kpn, 132 logo Relationsh, TrqOviSta, Perl APADrivinterventionDetected, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APADrivinterventionDetected, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APADrivinterventionDetected, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARomecoverableFaults, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARomecoverableFaults, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARoguest, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARoguest, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. APARoguest, Cnt. Ignore, Rite, Inst. Ap. TrqOviSta, Prof. CoWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CWPosition, Hwbog, 322 target, Rie, Inst. Ap. TrqOviSta, TrqOviSta, Prof. CMSOrd, Prof. Inst. Ap. TrqOviSta, Prof. CMSOrd, Prof. Inst. Ap. TrqOviSta, Prof. CMSOrd, Prof. Mosition, Prof. Inst. Ap. TrqOviSta, Prof. OMSOrd, Prof. Mosition, Prof.	k_LKAMinVehSpd_Kph_f32	
k, Sindhardiffresh, Kyn, 132 k, Slandstiffresh, Kyn, 132 k, Slandstiffresh, Kyn, 132 c, Slandstiffresh, 132 c, Slandstiffre		
SandstillTries_Sec_f32 Inspect Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APADrvinterventionDetected_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARcquest_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_APARcquest_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CCWFound_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOSSD_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOSSD_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOSSD_Per1_CMOSSD_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOSSD_Per1_CMOSSD_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_CMOSSD_PER1_CMOSSD_Per1_ECSCBart_Cnt_target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_CMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_LMOSSD_PER1_EVAD_TrqOviSta_TrqOviSta_Per1_TMOSSD_PER1_EV		
k Standställtime, Sec. 132 target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APADrvinterventionDetected Cnt. target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APANonRecoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Inst. Ap. TrqOviSta TrqOviSta Pert J.APARocoverableFaults, Cnt. Igo target, Rie Ins		
target, Rie, Inst. Ap. TrqOv/Sia TrqOv/Sia, Perf. APANorn/encevorerableFaults, Cnt. Igo target, Rie, Inst. Ap. TrqOv/Sia Perf. APANorn/encevorerableFaults, Cnt. Igo target, Rie, Inst. Ap. TrqOv/Sia Perf. APANorn/encevorerableFaults, Cnt. Igo target, Rie, Inst. Ap. TrqOv/Sia Perf. APANorn/encevorerableFaults, Cnt. Igo target, Rie, Inst. Ap. TrqOv/Sia Perf. APANorn/Sia, Perf. APANorn/encevorerableFaults, Cnt. Igo target, Rie, Inst. Ap. TrqOv/Sia Perf. APANorn/Sia, Pe		
target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_APARecoverableFaults, Cnt. Igs target, Rite, Inst. Ap. TrqOv/Sia Perf_APASiate, State_enum target, Rite, Inst. Ap. TrqOv/Sia Perf_APASiate, State_enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_CVFound_Cnt. Igs target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_CVFound_Cnt. Igs target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_CVFound_Cnt. Igs target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_ESCFault_Cnt. Igs target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_ESCSiate, State_enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_ESCSiate, State_enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-Six Six Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-Six Six Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-Six Six Six Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target, Rite, Inst. Ap. TrqOv/Sia TrqOv/Sia Perf_GSVSH-ESCS/Sia Six enum target,		•
target, Rie, Inst, Ap, TrqOviSta TrqOviSta, Perl, APARecoverableFaults, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta TrqOviSta, Perl, APARecoverableFaults, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta TrqOviSta, Perl, APARecoverableFaults, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, Perl, CWFound, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, Perl, ESCFault, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, Perl, GMOSH, APAMeghanible, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, GMOSH, APAMeghanible, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, GMOSH, APAMeghanible, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, GMOSH, APAMeghanible, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, GMOSH, APAMeghanible, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, LKAFault, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, LKAFault, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, LKAFault, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, LKAFault, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, LKAFault, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, LKAFault, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, LKAFault, Cnt.l.gc target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, MarsecurevehicleSpeed, Kph, 132 target, Rie, Inst, Ap, TrqOviSta, TrqOviSta, Perl, PosSrovhAwnaje, HwD		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CXMPooting_HvDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CXWPooting_HvDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CXWPooting_HvDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CXWPooting_HvDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CXWPooting_HvDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CXWPooting_HvDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CXWPooting_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SSCFault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SSCFault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SSCFault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SSCFault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SSCFault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SSCFault_State_enun target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_SSCMgEnable_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_LKAMfgEnable_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HubroRequest_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HubroRequest_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKARault_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MusceurevehicleSpeed_Kph_732 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MusceurevehicleSpeed_Kph_732 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_MusceurevehicleSpeed_Kph_732 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DosSrovEnable_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_PosSrovEnable_Cnt_Ige target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Perl_	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CCWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCIStall_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCIStall_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCIStall_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCIStall_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCIStall_Cont_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCIStall_CSCIStall_Cont_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCIStall_CSCIStall_Cont_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DScallateState_Enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DScallateState_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMIgenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_ESCMIgenable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_RAScauceveholicSpeed_Kph_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_RAScauceveholicSpeed_Kph_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DsSrontwAngle_Hwbeg_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Sortarips_Angle_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Sortarips_Angle_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Sortarips_Angle_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Sortarips_Angle_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_Sortarips_Angle_Rte_Inst_Ap_TrqOviSta_Tr	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CCWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_CWFound_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCFault_Ent_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCReques_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCReques_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DAVISta_DaviSta_Perl_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DAVISta_DaviSta_Perl_ESCState_State_Enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_GMOSH_DAVISta_DaviSta_DaviSta_Perl_GMOSH_DAVISta_DaviSta_DaviSta_Perl_GMOSH_DAVISta_Perl_GMOSH_DAVISta_Perl_GMOSH_DAVISta_Perl_GMOSH_DAVISta_Perl_GMOSH_DAVISta_Perl_GMOSH_DAVISta_Perl_GMOSH_DAVISta_Perl_GMOSH_LKAMIGEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HavIoRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HavIoRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPABUS_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPABUS_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPABUS_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_LKAPABUS_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_HavIoRepus_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DAVISTA_PASSenveNatioRepead_Kph_132 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Perl_DAVISTA_Perl_DAVISTA_Perl_BAVISTA_PERL_PMOSING_PAVISTA_PAVISTA_PERL_PMOSING_PAVISTA_PAVISTA_PERL_PMOSING_PAVISTA_PAVIST	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_CWFound_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCRequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCSIate_Inst_Ap_TrqOviSta_Pert_ESCMIgfcnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HaptiCrequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HaptiCrequest_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IAKAPatl_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IAKAPatl_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IAKAPatl_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IAKAPatl_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IAKAPatl_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IAKAPatl_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_IAKAPatl_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraipHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraipHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraipHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraipEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostraipEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Sore_TrqOviSta_Pert_PostraipEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOssCrap_HitNmf_f32 t	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEault_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_ESCEAULT_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_APAMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_GMOSH_ESCMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MoSH_ESCMigEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_HapitcRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Describable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_Describable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PostTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqO	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	
target_Rte_Inst_Ap_TrqOv/Sta_Perf_ESCFault_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_Perf_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCState_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCSIstate_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCSIstate_State_enum target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_ESCMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_ESCMgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_EXCAMGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_EXCAMGEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EMOSH_EXCAMGENable_Cnt_lgc target_Rte_Inst_Ap_TrqOv/Sta_TrqOv/Sta_Perf_EXCAMDENTACAMDEN		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Ont_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCRequest_Ont_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_ESCState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSHOscillateState_State_enum target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Ont_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Ont_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_MasceureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Possrvehandpe_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_Possrvehandpe_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PossrrqHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwARTrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwARTrgIAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqO	· · · · · · · · · · · · · · · · · · ·	
target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOScillateState_State_enum target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Per1_LKARAIIt_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARaiit_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_LKARsecureVehicleSpeed_Kpf_32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_MaxSecureVehicleSpeed_Kpf_32 target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosServEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_PosTrajEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable_Cnt_lgc target_Rie_Inst_Ap_TrqOviSta.TrqOviSta_Per1_SortIngEnable		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_ESCState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_APMifgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_APMifgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMifgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_GMOSH_ESCMifgEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_State_enum target_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_LKAFatult_State_enum target_TrqOvlSta_Pert_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosSrovHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SortingEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_SortingEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Pert_TrqOsCEnable_Cnt_Igc target_TrqOvlSta_Pert_TrqOsCEnable_Cnt_Igc target_TrqOvlSta_Pert_APARecoverableFaults_Cnt_Igc.value 1 target_TrqOvlSta_Pert_APARecov		
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSHOscillateState_State_enun target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GMOSH_APAMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_APAMIgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_GMOSH_ESCMigEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOviSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSevEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosSrvipHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SwARTgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_SiffLeverIsinReverse_Cnt_lgc target_TrqOviSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOvScan_DwITNTM_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Per1_TrqOvScan_DwITNTM_f32 target_TrqOviSta_Per1_ShiffLeverIsinReverse_Cnt_gc target_TrqOviSta_Per1_TrqOvScan_DwITNTM_f32 target_TrqOviSta_Per1_APARequest_Cnt_Igc_value 0 target_TrqOviSta_Per1_APARequest_Cnt_Igc_value		
target_Rte_Inst_Ap_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HapticRequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_MasSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_Perl_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_SwartrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_TrqOsCamp_MtrNm_f32 target_TrqOvISta_Perl_TrqOsCamp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Perl_TrqOsCamp_MtrNm_f32 target_TrqOvISta_Perl_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Perl_A	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enum	target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_Hwhm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASInhibit_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASInte_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKASite_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SorAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOsCEnable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1	$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvISta_Per1_HapticRequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_HwTorque_HwNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_DosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc.value 1	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKARequest_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_state_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc_value 0 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1 target_TrqOvISta_Per1_APARequest_Cnt_Igc_value 1	$target_Rte_Inst_Ap_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_LKAFault_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Perl_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta.Perl_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Perl_SvARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_SvARTrgtAngRequest_HwDeg_f33 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Perl_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Perl_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Perl_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Perl_APARequest_Cnt_lgc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKARequest_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_DosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_DosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_DosTrajHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_LKARequest_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsinReverse_Cnt_igc target_TrqOvlSta_Per1_SwARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_igc.value target_TrqOvlSta_Per1_APARequest_Cnt_igc.value target_TrqOvlSta_Per1_APARequest_Cnt_igc.value target_TrqOvlSta_Per1_APARequest_Cnt_igc.value target_TrqOvlSta_Per1_APARequest_Cnt_igc.value target_TrqOvlSta_Per1_APARequest_Cnt_igc.value		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_LKAState_State_enum target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShifftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1 1		
target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_PosServEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 11	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3 target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_SwaRTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscErable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_Rte_Inst_Ap_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_Igc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value 1	target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value 1	target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	✓
TrqOvlSta_ESCState_State_M_enum	2	2	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	•
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	•
TrqOvlSta_HapticState_State_M_enum	0	0	•
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
TrqOvlSta_LKAState_State_M_enum	0	0	•
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.20000003	0.200000003 ± 0.0009765625	•
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	~
	0.462000012	0.462000012 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.402000012	0	
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0.462000012	0 ± 1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.74 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32 k_HapticFreq_Hz_f32	1.15499997 32.3019981
k HapticReacttime Sec f32	1.23199999
k LKAMaxVehSpd Kph f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0 target TraCviSta Per1 APADayInterventionDetected Cet Inc.
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_ target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 APARecoverableFaults Cnt Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target TrqOvlSta Per1 APARequest Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_Igc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 MaxSecureVehicleSpeed Kph f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3.	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_Igc target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 TrqOscEnable Cnt Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1 261 01001
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value target TrqOvlSta Per1 ESCFault Cnt lgc.value	361.01001 0
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.20000003

© Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	✓
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	0	0	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.75 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	1
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0
TrqOvlSta_HapticState_State_M_enum	0
TrqOvlSta_Hapticdur_Cnt_M_u16	44450
TrqOvlSta_Haptictime_Sec_M_f32	0
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	1
TrqOvlSta_LKAState_State_M_enum	0
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	1.39999998
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.601666689
k_APAMaxHwTrq_HwNm_f32	0
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	12
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.15499997
k_HapticFreq_Hz_f32	32.3019981
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	100
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	30
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	0
k_StandstillTime_Sec_f32	0
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Results (See Section 1) and the properties of the properties$	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_Igc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_l	target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lq	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_en	um	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	g_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_l	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enui			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEi		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE target_TrqOvlSta_Per1_GMOSH_LKAMfgEr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvIsta_Per1_HapticRequest_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvIsta_Per1_HwTorque_HwNm_		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 LKAFault Cnt Igc	target TrqOvlSta Per1 LKAFault Cnt lgc	102	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKARequest_Cnt_Igc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	ac	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 MaxSecureVehicleSpeed Kph f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3	target_TrqOvlSta_Per1_SWARTrgtAngRequ	iest_HwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	se_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-368.230011		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	361.01001		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	8.10000038		
target TrqOvlSta Per1 LKAFault Cnt lgc.value	1		
target TrqOvlSta Per1 LKAInhibit Cnt Igc.value	0		
target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	-
TrqOvlSta_ESCState_State_M_enum	2	2	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	1	1 ± 1	•
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	-
TrqOvlSta_HapticState_State_M_enum	1	1	•
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	•
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvlSta_LKAState_State_M_enum	0	0	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.200000003	0.200000003 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	0 ± 1	

2015-02-04, 14:05:40+0530





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Namo	Input Value
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	25475
TrqOvlSta_Haptictime_Sec_M_f32	186740.953
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
FrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k APAMaxHwTrq HwNm f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k ESCMaxVehSpd Kph f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.462000012
<_HapticDuration_Sec_f32	1.55999994
	51.8339996
k_HapticFreq_Hz_f32	
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.60000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
<_StandstillTime_Sec_f32	0.308999985
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetect	ted_Cnt_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults	s_Cnt_lg target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cn	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ESCState State enum	target_TrqOvlSta_Per1_ESCState_State_enum
rarget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_Sta	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable C	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_0	
:arget_Nte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_GMOSH_LKAMfgEnable_0	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_GiviOSH_ERAiviligEnable_C target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HapticRequest_Cnt_lgc	target TrqOvlSta_Per1_GinOSh_LRAMilgEnable_Cht_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32

@ Report created by TESSY V3.1.9, report template V2.1

2015-02-04, 14:05:40+0530



		• "	
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_l	gc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_er		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f3:	target_TrqOvlSta_Per1_MaxSecureVehicles	Speed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_h		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_ft			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNn	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f3	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target TrqOvISta Per1 CCWFound Cnt Igc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt Igc.value	1		
target TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	3	3	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	· ·
TrqOvISta_HapticState_State_M_enum	2	2	
TrqOvISta_Hapticdur_Cnt_M_u16	0	0 ± 1	
TrqOvlSta_Haptictime_Sec_M_f32 TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	0 ± 1 -1.64999998 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	-1.64999998	-1.64999998 ± 0.000009	~
TrgOvISta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrgOvISta LKAPermFault Cnt M u08	2	2±1	_
TrqOvlSta_LKAState_State_M_enum	3	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrgOvlSta ReadytoPulse Cnt M lgc	1	1 ± 1	_
target TrqOvlSta Per1 APADrvrInterventionDetected Cnt Igc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	-
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.77 (Repeat Count = 1)	▼ v
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
	1
	25475
	186740.953
	-1.64999998
	0.419
	0
	0
	3
	29.7999992
1 = 0 = 0= =	1
1 = 7 = = = = = = = = = = = = = = = = =	1
_ ' _ = = 0	0.144999996
	6.15999985
	138.600006
	133.699997
	0.462000012
- '	1.55999994
	51.8339996
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
	3.07999992
	0.308999985
	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
	target_TrqOvlSta_Per1_APAState_State_enum
	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
	target_TrqOviSta_Per1_ESCFault_Cnt_lgc
	target TrqOvISta Per1 ESCIsLimited Cnt Igc
	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
	target_TrqOvlSta_Per1_ESCState_State_enum
	target_TrqOvlSta_Per1_GMOSHOscillateState_state_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
	target_TrqOvlSta_Per1_LKAState_State_enum
	target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc
	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
	target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32 target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_IrqOviSta_Peri_SniftLeverisinReverse_Cht_igc target_TrqOviSta_Peri_SniftLeverisinReverse_Cht_igc
target Rte Inst An TrgOv/Sta TrgOv/Sta Perf TrgOscAmp MtrNm f22	
	target Irg()viSta Per1 Irg()scEnable Cnt lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_TrgOvlSta_Per1_TrgOscEreg_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32





Name	Input Value		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	3	3	•
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	-
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvISta_LKAPermFault_Cnt_M_u08	2	2 ± 1	~
TrqOvISta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	~
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target TrqOvlSta Per1 TrqOscAmp MtrNm f32.value	0.462000012	0.462000012 ± 0.0009765625	~
3-2 1 1 12			
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.78 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	25475
TrqOvlSta_Haptictime_Sec_M_f32	186740.953
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992

2015-02-04, 14:05:40+0530



Tapolisis Recognish Coll Inject	Name	Input Value
LPAPANISH Top 1, 1965. 1, 1969. 1,		·
A APAMaterior Service Service 12		
	. –	0.144999996
ESDIANON NINE 15 15 15 15 15 15 15 1	k_APAMaxHwTrq_HwNm_f32	6.15999985
Lipspichparing See 122 15099994 Lipspichparing See 122 15099994 Lipspichparing See 122 15099994 Lipspichparing See 122 15099994 Lipspichparing See 122 15099999 Lipspichparing See 122 150999999 Lipspichparing See 122 15099999 Lipspichparing See 122 15099999 Lipspichparing See 122 150999999999999999999999999999999999999	k_APAMaxVehSpd_Kph_f32	138.600006
Lipsperhalmer, Sec. 122		
Light Ligh		
Light Resident Form Sec. 202 123/20000		
L. MAMAWAYSE, Mr. 122 L. Manickellother, Ser., 127 L. Manickellother, Ser., 127 L. Manickellother, Ser., 127 L. Salther, Micro-Time, Ser., 127 L. Salther, Ser., 127 L. Salther, Micro-Ser., 127 L. Salthe		
S. JAMAN S. JAMAN JAMAN S. JAMAN S		
E. SWARE Interfect Proc. 12		
N. Samusett Tree, Sec. 122 N. Barnestett Tree, Sec. 122 N. Barnestett Tree, Man. 123 O. 309999992 N. Barnestett Tree, Man. 127 (1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 124 Datagel, Rie, Ind. Ap. 1700-1851 a Trock) Sec. 125 Da	k_MaxHapticReqTime_Sec_f32	0.60000024
A Sandastillimen (sp. 102 A Sandastillimen (sp. 17g0/SSB Trof) ASA Divinine-vertical Detailed (bit (sp. 17g0/SSB Perl (ASA) Detailed (bit	k_SWARLimiter_HwDeg_f32	17.2000008
A Subsolutione See, 192 storage, Ris, Innol. Ap. (1905)Sta (1905)Sta (1905) Pert. APADorintencentionicidetated, Cot. Up larget, Ris, Innol. Ap. (1905)Sta (
sangel, Rein Ma, Porgo-Stati Prock Stati P		
stage, Re, Inst. Ap. TrigONists ToPONISts Pert LAPANomecoventational Crit. Igo sapet TrigONists ToPONIST ToPONIST ToPONIST Pert LAPANocoversite and Crit. Igo stage, Re, Inst. Ap. TrigONists ToPONIST Pert LAPANocoversite and Crit. Igo sapet TrigONists ToPONIST ToPONIST ToPONIST Pert LAPANocoversite and Crit. Igo stage, Re, Inst. Ap. TrigONists ToPONIST Pert LAPANocoversite and Crit. Igo sapet TrigONists ToPONIST ToPONIST Pert LAPANOST Sept. COMPosition Printing Sept. Inst. Ap. TrigONists ToPONIST Pert LAPANOST Confidence of the Crit. Igo sapet TrigONists ToPONIST Pert LAPANOST Sept. Inst. Ap. TrigONists ToPONIST Pert LAPANOST Confidence of the Crit. Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Crit. Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Crit. Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Crit. Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Crit. Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Igo sapet TrigONists ToPONIST Pert LAPANOST Confidence of the Igo sapet TrigONists ToPONIST Pert LAPANOST Sept. Inst. In		
staget, Rin, Int. Ap. Trick/Wista Trick/Wist Pert L. APARecoverable auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. APARecoverable auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. APARecoverable auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. APARecoverable auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Composition I vehicle, ISI. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Composition I vehicle, ISI. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Composition I vehicle, ISI. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Scotal vehicle, Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Scotal vehicle, Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Scotal vehicle, Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Scotal vehicle, Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Mistage auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Mistage auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Mistage auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Mistage auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Mistage auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Trick/Wista Pert L. Mistage auth. Crit. go. staget, Rin, Int. Ap. Trick/Wista Tric		
saget, Ris, Inst. Ap. Trochista Trochista Pert LANARequest, Cnt. Igo saget, Ris. Inst. Ap. Trochista Trochista Pert LANARequest, Cnt. Igo saget, Ris. Inst. Ap. Trochista Trochista Pert LONARequest, Cnt. Igo saget, Ris. Inst. Ap. Trochista Trochista Pert Londard Land Land Land Land Land Land Land Lan		
singet, Rin, Inst. Ap. TropOvisia TropOvisia Pert LAPASias, Sales, enum sarget, Rin Inst. Ap. TropOvisia TropOvisia Pert LOWDration of the part TropOsia and Pert Low Sales and Pert Low		
staget, Rise, Land, A.D. TGO/Sian TGO/SIAD, Pert. COVPosition, Holder, 122 staget, Rise, Land, A.D. TGO/Sian TGO/SIAD, Pert. COVPosition, Holder, 122 staget, Rise, Land, A.D. TGO/Sian TGO/SIAD, Pert. COVPosition, Holder, 122 staget, Rise, Land, A.D. TGO/Sian TGO/SIAD, Pert. SCORAUL COT. Up staget, Rise, Land, A.D. TGO/Sian TGO/SIAD, Pert. SCORAUL COT. Up staget, Rise, Land, A.D. TGO/SIAS TGO/SIAD, Pert. SCORAUL COT. Up staget, Rise, Land, A.D. TGO/SIAS TGO/SIAD, Pert. SCORAUL COT. Up staget, Rise, Land, A.D. TGO/SIAS TGO/SIAD, Pert. SCORAGUES, State, exturn staget, Rise, Land, A.D. TGO/SIAS TGO/SIAD, Pert. SCORAGUES, State, exturn staget, Rise, Land, A.D. TGO/SIAS TGO/SIAD, Pert. SCORAGUES, State, exturn staget, Rise, Land, A.D. TGO/SIAS TGO/SIAD, Pert. SCORAGUES, State, exturn staget, Rise, Land, A.D. TGO/SIAS TGO/SIAD, Pert. SCORAGUES, Cot. Up staget, Rise, Land, A.D. TGO/SIAS TGO/SIAD, Pert. JAGG/SIAD,		target_TrqOvlSta_Per1_APAState_State_enum
tampet, Re, Inst. Ap. Troch/Sita Troph/Sita Pert. (Divisions Holes) Ed. target, Troch/Sita Pert. (Divisions Holes) Ed. tampet, Rice, Inst. Ap. Troch/Sita Troph/Sita Pert. (SECS auth. Cert. Jgs.) target, Troch/Sita Pert. (SECS auth. Cert. Jgs.) tampet, Rice, Inst. Ap. Troch/Sita Troph/Sita Pert. ESCRIbutioned, Crt. Jgs. target, Troch/Sita Pert. ESCRIbutioned, Crt. Jgs. tampet, Rice, Inst. Ap. Troch/Sita Troph/Sita. Pert. ESCRIbutioned, Crt. Jgs. target, Troch/Sita. Pert. ESCRIbutioned, Crt. Jgs. tampet, Rice, Inst. Ap. Troch/Sita Troph/Sita. Pert. ESCRIbutioned, Crt. Jgs. target, Troch/Sita. Pert. ESCRIbutioned, Crt. Jgs. tampet, Rice, Inst. Ap. Troch/Sita. Troch/Sita. Pert. ESCRIbutioned, Crt. Jgs. target, Troch/Sita. Pert. ESCRIbutioned, Crt. Jgs. tampet, Rice, Inst. Ap. Troch/Sita. Troch/Sita. Pert. BARAMISTRANCE Crt. Igs. target, Troch/Sita. Pert. ESCRIbutioned, Crt. Igs. tampet, Rice, Inst. Ap. Troch/Sita. Troch/Si	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
singet, Rie, Inst. Ap. Trock/Sis. Trop/Sis. Pert. SCP and LOT Ing singet, Trock/Sis. Pert. SCP and LOT Ing singer, Rie, Inst. Ap. Trock/Sis. Trop/Sis. Pert. SCSIs. Inst. Chr. Ing singet, Trock/Sis. Pert. SCSIs. Inst. Chr. Ing singer, Rie, Inst. Ap. Trock/Sis. Trop/Sis. Pert. SCSIs. Sist. evenum singet, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. SCSIs. Sist. evenum singer, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. GMOSH APAMIginable Cnit. Igo singet, Trock/Sis. Pert. SCSIs. Sist. evenum singer, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. GMOSH APAMIginable Cnit. Igo singet, Trock/Sis. Pert. SCSIs. Sist. evenum singer, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. GMOSH APAMIginable Cnit. Igo singet, Trock/Sis. Pert. GMOSH APAMIginable Cnit. Igo singer, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. MARIANIE Pert. Inst. Sist. Evenum singet, Trock/Sis. Pert. GMOSH APAMIginable Cnit. Igo singer, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. MARIANIE Cnit. Igo singet, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. MARIANIE Cnit. Igo singer, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. MARIANIE Cnit. Igo singet, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. MARIANIE Cnit. Igo singer, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. Marianie Cnit. Igo singet, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. Sis. Rie, evenum singer, Rie, Inst. Ap. Trock/Sis. Trock/Sis. Pert. Sis. Rie, Pert. Sis. R		
tanger, Reit, Inst. Ap. TropOsitian Profits perf. ESCFault, Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian Profits perf. ESCFault (no.l.go tanger, Reit, Inst. Ap. TropOsitian Profits perf. ESCRequest, Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. CMOSPI APAMIgEnable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian Profits perf. CMOSPI APAMIgEnable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian Profits perf. CMOSPI APAMIgEnable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian Profits perf. CMOSPI APAMIgEnable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. CMOSPI APAMIgEnable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian Profits perf. LAMANIgenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANIgenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Perf. Lamanigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Perf. Lamanigenable Perf. Lamanigenable Cnt.l.go tanger, Reit, Inst. Ap. TropOsitian TropOsitian Perf. LAMANigenable Perf		
tanget Rein Land, An TropONSIa Prof LSCIstamined Cert Lipc target Rein Land, An TropONSIa TropONSIa Prof LSCISCANSE PART LSCROSCANSE, Perf LSCISCANSE, Perf LSC		
target. Rei. Jind. Ap. TriqOVISIa. Prof. JSCRequest. Cni. (pc barget. Rei. Jind. Ap. TriqOVISIa. Prof. JSCRequest. Cni. (pc barget. Rei. Jind. Ap. TriqOVISIa. Prof. SSCStele. State cenum target. Rei. Jind. Ap. TriqOVISIa. Prof. CMOSH. APAMIGEnable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. CMOSH. APAMIGEnable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. CMOSH. APAMIGEnable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. LAMOSH. LAMAMIGEnable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. LAMOSH. LAMAMIGEnable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. LAMOSH. LAMAMIGEnable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. LAMOSH. LAMAMIGEnable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. LAMAMIGENable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. LAMAMIGENable. Cni. (pc target. Rei. Jind. Ap. TriqOVISIa. Prof. LAMAMIGEN.		
target. Ries. May. Trq:OviSta. Prof. IESCState. State. enum target. Ries. May. Trq:OviSta. Trq:OviSta. Prof. ISCSTate. State. enum target. Ries. May. Trq:OviSta. Trq:OviSta. Prof. ISCSTate. State. enum target. Ries. May. Trq:OviSta. Trq:OviSta. Prof. ISCSTate. State. enum target. Ries. May. Trq:OviSta. Trq:OviSta. Prof. ISCSTate. State. enum target. Ries. May. Trq:OviSta. Trq:OviSta. Prof. ISCSTate. State. enum target. Trq:OviSta. Prof. ISCSTate. Prof. ISCSTate. enum target. Trq:OviSta. Pr		
target. Rel. Inst. Ap. TrqOviSta. Prof. (SMOSHO-sellateState. State. enum breget. Rel. Inst. Ap. TrqOviSta. Prof. (SMOSH. Pert. (GMOSH. APAMgEnable, Cnt.) go target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. (GMOSH. ESCMigEnable). Cnt. Igo target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. (GMOSH. LEAMigEnable. Cnt.) go target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. (GMOSH. LEAMigEnable. Cnt.) go target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. (GMOSH. LEAMigEnable. Cnt.) go target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LEARING. Cnt. Igo target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LEARING. Cnt. Igo target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LEARING. Cnt. Igo target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LEARING. Cnt. Igo target. Rel. Inst. Ap. TrqOviSta. TrqOviSta. Pert. LEARING. Pert. LE		
target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. (SMOSH. APAMfenable, Cnt.) go: target, Rie, Inst. Ap. TrqOviSta. Pert. (SMOSH. ESCM/genable, Cnt.) go: target, Rie, Inst. Ap. TrqOviSta. Pert. (SMOSH. ESCM/genable, Cnt.) go: target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. HapticRequest. Cnt. I go: target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. HapticRequest. Cnt. I go: target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. HapticRequest. Cnt. I go: target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LiAFault, Cnt. I go: target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LiAFault, Cnt. I go: target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LiAFault, Cnt. I go: target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LiAFault, Cnt. I go: target, Rie, Inst. Ap. TrqOviSta. TrqOviSta. Pert. LiAFault, Cnt. I go: target, Trq		
target, Rie, Inst, Ap. TrqOv/Sta, TrqOv/Sta, Pert J. GMOSH, L/KM/Grable, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. HapticRequest, Crt. Jgc target, TrqOv/Sta. Pert J. HapticRequest, Crt. Jgc target, TrqOv/Sta. Pert J. Hw7 Group, Hw7 Mm. f22 target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. L/KAFault, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. L/KAFault, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. KARequest, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. KARequest, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. KARequest, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. KARequest, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. KAREQUEST, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. KAREQUEST, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. KAREQUEST, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. Post-Svort-Mable, Crt. Jgc target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. Post-Svort-Mable, Crt. Jgc target, TrqOv/Sta. Pert J. MaxSecure/VehicleSpeed, Kpf. Jg2 target, Rel. Inst, Ap. TrqOv/Sta. TrqOv/Sta. Pert J. Post-Svort-Mable, Crt. Jgc target, TrqOv/Sta. Pert J. Post-Svort-Mable, Crt. Jgc target, TrqOv/Sta. Pert J. Post-Tayle-Maria, Pert J. Svart-TrqOv/Sta. Pert J. Svart-TrqOv/Sta. Pert J. TrqOv/Sta. Pert J.		
target, Reta, Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl HaptorRequest, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, Perl HwTorque, HwMm, f32 target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAFautt, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAFautt, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAFautt, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAFautt, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAState, State, enum target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAState, State, enum target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAState, State, enum target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAState, State, enum target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAState, State, enum target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl LKAState, State, enum target, Ret., Inst., Ap. TrqOv/Sta, Perl Lyer, Post State, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl Post Trgi-Inable, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl Post Trgi-Inable, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl Post Trgi-Inable, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl Post Trgi-Inable, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, Perl T., Shift, Evertisin/Reverse, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl T., Shift, Evertisin/Reverse, Cnt. Igo target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl T., Tqo, SecFeq., Itz., 23 target, Ret., Inst., Ap. TrqOv/Sta, TrqOv/Sta, Perl T., Tqo, SecFeq., Itz., 23 target, Ret., Inst., Ap. TrqOv/Sta, Tqo, VSta, Perl T., Tqo, SecFeq., Itz., 23 target, Ret., Inst., Ap. TrqOv/Sta, Tqo, VSta, Perl T., Tqo, SecFeq., Itz., 23 target, TrqOv/Sta, Perl S., Tqo, VSta, Perl T., Tqo, SecFeq., Itz., 23 target, TrqOv/Sta, Perl S., Perl	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target, Ret, Inst. Ap. TrqOviSta TrqOviSta. Perf _ LivATorque_ Hwithin_132 target_ Ret_ Inst. Ap. TrqOviSta TrqOviSta. Perf _ LivAFault_Cnt go target_ TrqOviSta. Perf _ LivAFault_Cnt go target_ Ret_ Inst. Ap. TrqOviSta TrqOviSta. Perf _ LivAFault_Cnt go target_ TrqOviSta. Perf _ LivAFault_Cnt go target_ Ret_ Inst. Ap. TrqOviSta TrqOviSta. Perf _ LivARequest_Cnt go target_ TrqOviSta. Perf _ LivARequest_Cnt go target_ Ret_ Inst. Ap. TrqOviSta. TrqOviSta. Perf _ Massecure VehicleSpeed_Kph_132 target_ TrqOviSta. Perf _ LivARequest_Cnt go target_ Ret_ Inst. Ap. TrqOviSta. TrqOviSta. Perf_ Server_Nable_Cnt go target_ TrqOviSta. Perf_ Posservenable_Cnt go target_ Ret_ Inst. Ap. TrqOviSta. TrqOviSta. Perf_ Posservenable_Cnt go target_ TrqOviSta. Perf_ Posservenable_Cnt go target_ Ret_ Inst. Ap. TrqOviSta. TrqOviSta. Perf_ Postrajethwangle_HwDeg_132 target_ TrqOviSta. Perf_ Posservenable_Cnt go target_ Ret_ Inst. Ap. TrqOviSta. TrqOviSta. Perf_ Postrajethwangle_HwDeg_132 target_ TrqOviSta. Perf_ Postrajethwangle_HwDeg_132 target_ Ret_ Inst. Ap. TrqOviSta. TrqOviSta. Perf_ TrqOseAmp_Minm_132 target_ TrqOviSta. Perf_ SwiKantrigAngRequest_ HwDeg_162 target_ Ret_ Inst. Ap. TrqOviSta. TrqOviSta. Perf_ TrqOseAmp_Minm_132 target_ TrqOviSta. Perf_ TrqOseAmp_Minm_182 target_ TrqOviSta. Perf_ ApAnonRecoverableFaults_ Cnt go: value 0 target_ TrqOviSta. Perf_ LivARequest_ Cnt go:	$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target. Rite. Inst. Ap. TrqOviSta TrqOviSta. Perf_LKAFault. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. TrqOviSta. Perf_LKAFault. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. TrqOviSta. Perf_LKAFault. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. MarsecureVerhicleSpeed. Kph. 122 target. Rite. Inst. Ap. TrqOviSta. Perf. AssecureVerhicleSpeed. Kph. 122 target. Rite. Inst. Ap. TrqOviSta. Perf. AssecureVerhicleSpeed. Kph. 122 target. Rite. Inst. Ap. TrqOviSta. Perf. Sester. PassecureVerhicleSpeed. Kph. 122 target. Rite. Inst. Ap. TrqOviSta. Perf. PostrajiEnable. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. PostrajiEnable. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. PostrajiEnable. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. PostrajiEnable. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. PostrajiEnable. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. PostrajiEnable. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. Sester. Sester. Passecure Ing. target. Rite. Inst. Ap. TrqOviSta. Perf. PostrajiEnable. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. Sester. Sester. Sester. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. Sester. Sester. Sester. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. Sester. Sester. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. Sester. Sester. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. Sester. Ing. Sester. Ing. Sester. Ing. Ing. TrqOviSta. Perf. TrqOseEnable. Cnt. Igo target. Rite. Inst. Ap. TrqOviSta. Perf. Sester. Ing. Sester.		
larget_Rie_Inst_Ap_TrqOvSta.TrqOvSta_Pert_LKARques_Cnt_Igc target_TrqOvSta_Pert_LKARques_Cnt_Igc target_TrqOvSta_Pert_NthinsceureVehicleSpeed_Kph_132 tar		
target Rite_Inst_Ap_TrqOviSta_TrqOviSta_Pert_LKARequest_Cnt_lgc target Rite_Inst_Ap_TrqOviSta_Pert_LKAState_State_enum target_TrqOviSta_Pert_LKAState_State_enum target_Rite_Inst_Ap_TrqOviSta_Pert_MoviSte_Pert_Movi		
target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _LKAState_State_enum target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _MinSecureVehicleSpeed_Kph_f32 target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _MinSecureVehicleSpeed_Kph_f32 target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosServEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosServEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosServEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _PosTrajEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _TosTrajEnable_Cnt. [gc target. Rie. Inst. Ap. TrqOviSta. TrqOviSta. Perl _TosCert. Rie. Rie. Inst. Ap. TrqOviSta. Perl _TosCert. Rie. Rie. Rie. Inst. Ap. TrqOviSta. Perl _TosCert. Rie. Rie. Rie. Rie. Rie. Rie. Rie. Rie		
target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MaxSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_DosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosSrovEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgIAngRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MinVm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MinVm_f32 target_TrqOviSta_Pert_SWARTrgIAngRequest_HwDeg_f32 target_TrqOviSta_Pert_ApAnoRecoverableFaults_Cnt_Igc_value 1 target_TrqOviSta_Pert_APARRecoverableFaults_Cnt_Igc_value 10 target_TrqOviSta_Pert_APARRecoverableFaults_Cnt_Igc_value 10 target_TrqOviSta_Pert_ApARRecoverableFaults_Cnt_Igc_value 11 target_TrqOviSta_Pert_CWPosition_HwDeg_f32_value 12 target_TrqOviSta_Pert_CWPosition_HwDeg_f32_value 12 target_TrqOviSta_Pert_CWPosition_HwDeg_f32_value 13 target_TrqOviSta_Pert_ESCFaul_Cnt_Igc_value 14 target_TrqOviSta_Pert_ESCFaul_Cnt_Igc_value 14 target_TrqOviSta_Pert_ESCRequest_Cnt_Igc_value 14 target_TrqOviSta_Pert_ESCRequest_Cnt_Igc_value 14 target_TrqOviSta_Pert_HwTqque_HwNn_f32_value 15 target_TrqOviSta_Pert_HwTqque_HwNn_f32_value 16 target_TrqOviSta_Pert_HwTqque_HwNn_f32_value 16 target_TrqOviSta_Pert_HwTqque_HwNn_f32_value 17 target_TrqOviSta_Pert_HwTqque_HwNn_f32_value 18 target_TrqOviSta_Pert_MinSecureVehicleSpeed_Kph_f32_value 19 target_TrqOviSta_Pert_MinSecureVehicleSpeed_Kp		
target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_PosServEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_PosTerlEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_PosTerlEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_SVMARTghangRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_SVMARTghangRequest_HwDeg_f32 target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOVISta_TrqOVISta_Pert_TrqOscAmp_MirNm_f32 target_TrqOVISta_Pert_Ap_TrqOscAmp_MirNm_f32 target_TrqOVISta_Pert_Ap_TrqOscAmp_MirNm_f32 target_TrqOVISta_Pert_Ap_TrqOscAmp_MirNm_f32 target_TrqOVISta_Pert_TrqOscAmp_MirNm_f32 target_TrqOVISta_Pert_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_Ap_		
target_Re_Inst_Ap_TrqOviSta_Pert_PosSrvoHwAngle_HwDeg_f32 target_Re_Inst_Ap_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_TrqOviSta_Pert_PosTrajEnable_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_Pert_SorTajEnable_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_Pert_ShiftLeverisInReverse_Cnt_lgc target_Re_Inst_Ap_TrqOviSta_Pert_TrqOsCarp_MriMn_f32 target_Re_Inst_Ap_TrqOviSta_Pert_TrqOsCarp_MriMn_f32 target_Re_Inst_Ap_TrqOviSta_Pert_TrqOsCarp_MriMn_f32 target_Re_Inst_Ap_TrqOviSta_Pert_TrqOsCarp_MriMn_f32 target_TrqOviSta_Pert_APANnecoverableFaults_Cnt_lgc_value target_TrqOviSta_Pert_APANnecoverableFaults_Cnt_lgc_value 0 target_TrqOviSta_Pert_APANnecoverableFaults_Cnt_lgc_value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc_value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc_value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc_value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCFault_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCFault_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCFault_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCFault_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Pert_ESCRequest_Cnt_lgc_value 1 target_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_lgc_value 1 target_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_lgc_value 1 target_TrqOviSta_Pert_Mrorque_HwNm_f32_value 1 target_TrqOviSta_Pert_HwTorque_HwNm_f32_value 1 target_TrqOviSta_Pert_Mrorque_HwNm_f32_value 1 target_TrqOviSta_Pert_Mrorque	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_Pert_PosTrajHwAngle_HwDeg_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgtAngRequest_HwDeg_f33 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_SWARTrgtAngRequest_HwDeg_f33 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_Rie_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_TrqOviSta_Pert_CrqOscAmp_MirNm_f32 target_TrqOviSta_Pert_TrqOscAmp_MirNm_f32 target_TrqOviSta_Pert_CrqOscAmp_MirNm_f32 target_TrqOviSta_Pert_MirNm_f32 target_TrqOviS	target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rie_Inst_Ap_TrqOvIsta_Per1_SVMARTigtAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOvIsta_Per1_SVMARTigtAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_SVMARTigtAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_SVMARTigtAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_SVMARTigtAngRequest_HwDeg_132 target_Rie_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOvIsta_TrqOvIsta_Per1_TrqOscEnable_Cnt_Igc target_Rie_Inst_Ap_TrqOvIsta_Per1_SVMARTigtAngRequest_HwDeg_132 target_TrqOvIsta_Per1_ApAnonRecoverableFaults_Cnt_Igc_value target_TrqOvIsta_Per1_ApARecoverableFaults_Cnt_Igc_value 0 target_TrqOvIsta_Per1_ApARecoverableFaults_Cnt_Igc_value 1 target_TrqOvIsta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvIsta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvIsta_Per1_CWFound_Cnt_Igc_value 1 target_TrqOvIsta_Per1_ExCEption_HwDeg_132_value 1 target_TrqOvIsta_Per1_ESCFault_Cnt_Igc_value 1 target_TrqOvIsta_Per1_ESCFault_Cnt_Igc_value 1 target_TrqOvIsta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvIsta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvIsta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvIsta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvIsta_Per1_ESCRequest_Cnt_Igc_value 1 target_TrqOvIsta_Per1_ENGNSH_APAMgEnable_Cnt_Igc_value 1 target_TrqOvIsta_Per1_MNSH_ENGMgEnable_Cnt_Igc_value 1 target_TrqOvIsta_Per1_HwTorque_HwNm_132_value 1 target_TrqOvIsta_Per1_HwTorque_HwNm_132_value 1 target_TrqOvIsta_Per1_HwTorque_HwNm_132_value 1 target_TrqOvIsta_Per1_HwTorque_HwNm_132_value 1 target_TrqOvIsta_Per1_HwTorque_HwNm_132_value 1 target_TrqOvIsta_Per1_HxAequest_Cnt_Igc_value 1 target_TrqOvIsta_Per1_HxAequest_Cnt_Igc_valu		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_SWARTrgIAngRequest_HwDeg_i32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtNm_i32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtNm_i32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscAmp_MtNm_i32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscCAmp_MtnNm_i32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscCAmp_MtnNm_i32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscCAmp_MtnNm_i32 target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Perl_TrqOscCAmp_MtnNm_i32 target_TrqOvISta_Perl_ApANorRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_ApARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_ApARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_ApARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_CWFound_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_ESCFault_Cnt_Igc.value target_TrqOvISta_Perl_GNOSH_ApANtigEnable_Cnt_Igc.value target_TrqOvISta_Perl_GNOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Perl_GNOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Perl_GNOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HapticRequest_Cnt_Igc.value target_TrqOvISta_Perl_HAVIN_fige.value target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value target_TrqOvISta_Perl_LKAFault_Cnt_Igc.value target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Perl_PaxTen_Apa_TrqOvIsta_Perl_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvIsta_Perl		
target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_ShiftLeverIsinReverse_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_HwTorques_HapticRequest_Cnt_Igc.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value target_TrqOvISta_Per1_Misc_Url_Url_Url_Url_Url_Url_Url_Url_Url_Url		
target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscFrable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_TrqOviSta_Pert_TrqOscFrable_Cnt_lgc target_Rte_Inst_Ap_TrqOviSta_Pert_TrqOscFrae_Hz_f32 target_TrqOviSta_Pert_Ap_TrqOviSta_Pert_TrqOscFrae_Hz_f32 target_TrqOviSta_Pert_ApARocoverableFaults_Cnt_lgc.value target_TrqOviSta_Pert_ApARocoverableFaults_Cnt_lgc.value 0 target_TrqOviSta_Pert_ApARocoverableFaults_Cnt_lgc.value 1 target_TrqOviSta_Pert_ApARocoverableFaults_Cnt_lgc.value 1 target_TrqOviSta_Pert_CCWFound_Cnt_lgc.value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc.value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc.value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc.value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc.value 1 target_TrqOviSta_Pert_CWFound_Cnt_lgc.value 1 target_TrqOviSta_Pert_ExcFault_Cnt_lgc.value 1 target_TrqOviSta_Pert_ExcFault_Cnt_lgc.value 1 target_TrqOviSta_Pert_ExcRequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_ExcRequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Pert_GMOSH_ExcMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Pert_GMOSH_ExcMfgEnable_Cnt_lgc.value 1 target_TrqOviSta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_HapticRequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 1 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 2 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 3 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 4 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 5 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 5 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 5 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 5 target_TrqOviSta_Pert_LKARequest_Cnt_lgc.value 5 target_TrqOviSta_Per		
target_Rte_Inst_Ap_TrqOvISta_PrqOvISta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvISta_TrqOvISta_Per1_TrqOscFreq_Hz_f32 target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value target_TrqOvISta_Per1_APARequest_Cnt_Igc.value target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_CWFound_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_APAMIgEnable_Cnt_Igc.value target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value target_TrqOvISta_Per1_HybricRequest_Cnt_Igc.value target_TrqOvISta_Per1_HybricRequest_Cnt_Igc.value target_TrqOvISta_Per1_HybricRequest_Cnt_Igc.value target_TrqOvISta_Per1_HybricRequest_Cnt_Igc.value target_TrqOvISta_Per1_HybricRequest_Cnt_Igc.value target_TrqOvISta_Per1_HybricRequest_Cnt_Igc.value target_TrqOvISta_Per1_HybricRequest_Cnt_Igc.value target_TrqOvISta_Per1_Hybroque_Hybry_Ts2.value target_TrqOvISta_Per1_Hybroque_Hybry_Ts2.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value target_TrqOvISta_Per1_MinSecureVerbicleSpeed_Kph_Ts2.value 500.600006 target_TrqOvISta_Per1_PostrajEnable_Cnt_Igc.value 1 1		
target_TrqOvISta_Per1_APANonRecoverableFaults_Cnt_lgc.value target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Per1_APARecoverableFaults_Cnt_lgc.value 0 target_TrqOvISta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFostifon_HwDeg_f32.value 1 target_TrqOvISta_Per1_CWFostifon_HwDeg_f32.value 1 target_TrqOvISta_Per1_CWFostifon_HwDeg_f32.value 1 target_TrqOvISta_Per1_CWFostifon_HwDeg_f32.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GSCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 2 500.600006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 1		
target_TrqOvISta_Perl_APARecoverableFaults_Cnt_igc.value 0 target_TrqOvISta_Perl_APARequest_Cnt_igc.value 1 target_TrqOvISta_Perl_CCWFound_Cnt_igc.value 1 target_TrqOvISta_Perl_CCWFound_Cnt_igc.value 1 target_TrqOvISta_Perl_CWFound_Cnt_igc.value 1 target_TrqOvISta_Perl_CWFound_Cnt_igc.value 1 target_TrqOvISta_Perl_CWFound_Cnt_igc.value 799.659973 target_TrqOvISta_Perl_ESCFault_Cnt_igc.value 1 target_TrqOvISta_Perl_ESCFault_Cnt_igc.value 1 target_TrqOvISta_Perl_ESCIsLimited_Cnt_igc.value 1 target_TrqOvISta_Perl_ESCIsLimited_Cnt_igc.value 1 target_TrqOvISta_Perl_ESCRequest_Cnt_igc.value 1 target_TrqOvISta_Perl_GMOSH_APAMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Perl_GMOSH_ESCMfgEnable_Cnt_igc.value 1 target_TrqOvISta_Perl_HapticRequest_Cnt_igc.value 1 target_TrqOvISta_Perl_HapticRequest_Cnt_igc.value 1 target_TrqOvISta_Perl_HapticRequest_Cnt_igc.value 2 target_TrqOvISta_Perl_HwTorque_HwNm_f32.value 3 target_TrqOvISta_Perl_LKAFault_Cnt_igc.value 3 target_TrqOvISta_Perl_LKAFault_Cnt_igc.value 3 target_TrqOvISta_Perl_LKARequest_Cnt_igc.value 4 target_TrqOvISta_Perl_LKARequest_Cnt_igc.value 5 target_TrqOvISta_Perl_LKARequest_Cnt_igc.value 5 target_TrqOvISta_Perl_LKARequest_Cnt_igc.value 5 target_TrqOvISta_Perl_LKARequest_Cnt_igc.value 5 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 5 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 5 target_TrqOvISta_Perl_MaxSecureVehicleSpeed_Kph_f32.value 5 target_TrqOvISta_Perl_PosTrajEnable_Cnt_igc.value 5 target_TrqOvISta_Perl_PosTrajEnable_Cnt_igc.value 6 target_TrqOvISta_Perl_PosTrajEnable_Cnt_igc.value 7 target_TrqOvISta_Perl_PosTrajEnable_Cn		
target_TrqOvISta_Per1_APARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_CCWFound_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1-1029.01001 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1-1029.01000000000000000000000000000000000	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvISta_Per1_CCWFound_Cnt_Igc.value 1029.01001 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 1029.01001 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 11 target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 129.659973 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 11 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 11 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 11 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 11 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 11 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 11 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 11 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 12 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 13 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 14 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 14 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 14 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 14 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 14 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 15 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 15 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 15 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 15 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 15 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 16	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	
target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 109.01001 target_TrqOvISta_Per1_CWFound_Cnt_lgc.value 799.659973 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 11 target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 11 target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc.value 11 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 11 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 11 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 11 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 11 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 11 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 11 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 10 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 10 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 10 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 10 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 10 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 10 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 11 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 11 target_TrqOvISta_Per1_LMinhibit_Cnt_lgc.value 11 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 10.5 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 10.6 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 11		
target_TrqOvISta_Per1_CWFound_Cnt_Igc.value 799.659973 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCFault_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCIsLimited_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_Igc.value 8.1000038 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 8.1000038 target_TrqOvISta_Per1_LKAFault_Cnt_Igc.value 0 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvISta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value 799.659973 target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 8.10000038 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.10000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvISta_Per1_ESCFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvISta_Per1_HwTorque_HwNm_f32.value 8.10000038 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvISta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvISta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvISta_Per1_PosTrajEnable_Cnt_lgc.value 1 1		
target_TrqOvlSta_Per1_ESCIsLimited_Ont_Igc.value 1 target_TrqOvlSta_Per1_ESCRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_Igc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_Igc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.10000038 target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_Igc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1	·	
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.1000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.1000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 1 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	· - ·	
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value 1 target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.1000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value 0 target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.10000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value 8.10000038 target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value 0 target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value 1 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value 0.5 target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value 1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value 500.600006 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value 1		
		500.600006
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value 0.150000006	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	~
TrqOvlSta_ESCState_State_M_enum	3	3	✓
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563	79938.1563 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324	339324	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	✓
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.150000006	0.150000006 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1	1 ± 1	✓
$target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value$	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	•
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	•
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	51.8339996	51.8339996 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.79 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1.
TrqOvlSta_ESCState_State_M_enum	0
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	1748721
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	79938.1563
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	339324
TrqOvlSta_HapticState_State_M_enum	1
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	186740.953
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.419
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	3
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	29.7999992
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	1
k_APAIncludeHaptic_Cnt_lgc	1.
k_APAMaxHwTrqTime_Sec_f32	0.144999996
k_APAMaxHwTrq_HwNm_f32	6.15999985
k_APAMaxVehSpd_Kph_f32	138.600006
k_ESCMaxVehSpd_Kph_f32	133.699997
k_HapticAmplitude_MtrNm_f32	0.462000012
k_HapticDuration_Sec_f32	1.5599994
k_HapticFreq_Hz_f32	51.8339996
k_HapticReacttime_Sec_f32	1.23199999
k_LKAMaxVehSpd_Kph_f32	146.300003
k_LKAMinVehSpd_Kph_f32	134.75
k_MaxHapticReqTime_Sec_f32	0.600000024
k_SWARLimiter_HwDeg_f32	17.2000008
k_ShiftLevRevTime_Sec_f32	0.308999985
k_StandstillThresh_Kph_f32	3.07999992
k_StandstillTime_Sec_f32	0.308999985
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cn	t_ target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	ults_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgr		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDe	eg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc	~ 622	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_CWPosition_HwDeq target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	g_132	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCIsLimited Cnt Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_	lac	
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvIsta_Per1_ESCRequest_Cnt_l		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_er		
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSHOscillateState State enur			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgE	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_ESCMfgE	nable_Cnt_lgc	
$target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc$	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr	nable_Cnt_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_	f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	•	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvISta_Per1_LKAState_State_en		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	0 - 1		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosServEnable_Cn target_TrqOvlSta_Per1_PosSrvoHwAngle_F		
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosSrvonwArigie_nwDeg_is2	target_TrqOvISta_Per1_PosStvoHwAfigle_F		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target TrqOvlSta Per1 PosTrajHwAngle H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3		·-	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target TrqOvlSta Per1 ShiftLeverlsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-1029.01001		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	799.659973		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value target TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOviSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target TrqOvlSta Per1 HapticRequest Cnt Igc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0.5		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	500.600006		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.150000006		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	-5.9000001		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvISta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	3	3	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32 TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0 79938.1563	0 ± 1 79938.1563 ± 1	
TrqOvISta_ElapsedStandstillTime_Sec_M_f32 TrqOvISta_ElapsedStandstillTime_Sec_M_f32	79938.1563 339324	79938.1563 ± 1 339324	
TrqOvISta_HapticState_State_M_enum	1	1	
TrqOvISta_Hapticdur_Cnt_M_u16	1	1 ± 1	
TrqOvISta Haptictime Sec M f32	0	0 ± 1	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-1.64999998	-1.64999998 ± 0.000009	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	•
Targordota IVA Otata Otata Maranas			
TrqOvlSta_LKAState_State_M_enum	3	3	•
TrqOvISta_ExAState_State_M_enum TrqOvISta_PosSrvoHwAngle_HwDeg_M_f32	3 0.150000006	3 0.150000006 ± 0.0009765625	•

2015-02-04, 14:05:40+0530



Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.150000006	0.150000006 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	1	1 ± 1	✓
target TrqOvlSta Per1 TrqOscFreq Hz f32.value	51.8339996	51.8339996 ± 0.0009765625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	•

Test Step 3.80 (Repeat Count = 1) Name	Input Value
Rte_Inst_Ap_TrqOvISta	target_Rte_Inst_Ap_TrqOviSta
TrgOvISta_APAState_State_M_enum	4
rqOvISta_ESCState_State_M_enum	
rqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
rqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
rqOvISta_HapticState_State_M_enum	2
rqOvlSta_Hapticdur_Cnt_M_u16	45000
rqOvlSta_Haptictime_Sec_M_f32	228811.703
rqOvISta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.70000005
rqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
rqOvlSta_LKAFault_Cnt_M_lgc	0
rqOvlSta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	2
rqOvlSta_LKAState_State_M_enum	2
rqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
rqOvlSta_ReadytoPulse_Cnt_M_lgc	0
_APAIncludeHaptic_Cnt_lgc	1
_APAMaxHwTrqTime_Sec_f32	0.29899995
_APAMaxHwTrq_HwNm_f32	9.27999973
_APAMaxVehSpd_Kph_f32	208.800003
_ESCMaxVehSpd_Kph_f32	30.3999996
_HapticAmplitude_MtrNm_f32	0.460000008
_HapticDuration_Sec_f32	1.74000001
_HapticFreq_Hz_f32	59.0299988
_HapticReacttime_Sec_f32	1.85599995
_LKAMaxVehSpd_Kph_f32	20
_LKAMinVehSpd_Kph_f32	203
_MaxHapticReqTime_Sec_f32	0.239999995
_SWARLimiter_HwDeg_f32	21.3999996
_ShiftLevRevTime_Sec_f32	0.0610000007
_StandstillThresh_Kph_f32	4.63999987
_StandstillTime_Sec_f32	0
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
rrget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
irget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
riget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
rget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_Igc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
urget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enur	• - ·
arget Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrqOvISta Per1 GMOSH APAMfgEnable Cnt Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_GMOSH_ESCMfgEnable_Cnt_Igc	
arget Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH LKAMfgEnable Cnt Igc	target_TrqOviSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_HapticRequest_Cnt_Igc	target TrqOvISta Per1 HapticRequest Cnt Igc
arget_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_HwTorque_HwNm_f32	target_TrqOvISta_Per1_HwTorque_HwNm_f32
.goco_no_np_nqonota.nqonota_non_nwnotque_nwmin_toz	goqoou or in rolquo_ in ini_ioz





_			
Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	С	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enu	ım	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	- *	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv		
	target_TrqOvISta_Per1_SWARTrgtAngReque		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRevers target TrqOvlSta Per1 TrqOscAmp MtrNm		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_	_	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 TrqOscFreq Hz f32	target TrqOvISta_rer1_TrqOscFreq Hz f32		
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0 123.599998		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	✓
TrgOvlSta ESCState State M enum	4	4	~
TrgOvlSta ElapsedAPAMaxHwTrgTime Sec M f32	0	0 ± 1	-
TrgOvlSta ElapsedShiftLevRevTime Sec M f32	0	0 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	~
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1±1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	~
TrqOvlSta_LKAState_State_M_enum	3	3	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	~
TrqOvISta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	- 4
target_TrqOvlSta_Per1_APADtvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	V
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	
target_TrqOvlSta_Per1_ESCState_State_enum.value		4 0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0 3	3	~
target_TrqOvlSta_Per1_LKAState_State_enum.value target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	-
target_TrqOvlSta_Per1_PosSetVErlable_Crlt_gc.value target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_FdsStvOnwArigie_Hwbeg_ls2.value target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	•
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-



Test Step 3.81 (Repeat Count = 1)	<u></u>
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvISta_APAState_State_M_enum	1
TrqOvlSta_ESCState_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	45000
TrqOvlSta_Haptictime_Sec_M_f32	228811.703
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvISta_LKAPermFault_Cnt_M_lgc	0 2
TrqOvISta_LKAState_State_M_equip	2
TrqOvlSta_LKAState_State_M_enum TrqOvlSta PosSrvoHwAngle HwDeg M f32	49.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32 k ShiftLevRevTime Sec f32	21.3999996 0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	·
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_ESCRequest_Crit_igc target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH APAMfgEnable Cnt Igc	target TrgOviSta Per1 GMOSH APAMfqEnable Cnt lgc
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 GMOSH ESCMfgEnable Cnt Igc	target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_Igc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32 target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target_rte_inst_Ap_TrqOviSta.TrqOviSta_rer1_F0sTrajrtwAngre_rtwDeg_i32 target_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_SWARTrgtAngRequest_HwDeg_i3	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_Igc
	target_TrqOvISta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscAmp_MtiNm_isz target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc	target_rideviola_reri_rideseEnable_ent_ige
	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc	
target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOviSta.TrqOviSta_Per1_TrqOscFreq_Hz_f32 target_TrqOviSta_Per1_APANonRecoverableFaults_Cnt_Igc.value target_TrqOviSta_Per1_APARecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0 0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32 target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Igc.value	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32 0

2015-02-04, 14:05:40+0530



TrqOviSta_Peri		Ida	CILAB
Name	Input Value		
target TrqOvlSta Per1 CCWPosition HwDeg f32.value	-887.609985		
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007		
target TrqOvlSta Per1 ESCFault Cnt Igc.value	1		
target TrqOvlSta Per1 ESCIsLimited Cnt Igc.value	1		
target TrqOvlSta Per1 ESCRequest Cnt lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverlsInReverse_Cnt_lgc.value	0		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	1	1	•
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_UIs_f32	-2.70000005	-2.70000005 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•
TrqOvlSta_LKAState_State_M_enum	3	3	~
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	0.189999998	0.189999998 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	~
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	~
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	~
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	~
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	✓

Test Step 3.82 (Repeat Count = 1)		
Name	Input Value	
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta	
TrqOvlSta_APAState_State_M_enum	1	
TrqOvlSta_ESCState_State_M_enum	4	
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203	
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797	
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	
TrqOvlSta_HapticState_State_M_enum	2	
TrqOvlSta_Hapticdur_Cnt_M_u16	45000	
TrqOvlSta_Haptictime_Sec_M_f32	228811.703	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029	
TrqOvlSta_LKAFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	
TrqOvlSta_LKAState_State_M_enum	2	
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015	

TrqOvlSta_Per1

2015-02-04, 14:05:40+0530





Name	Input Value
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973 208.800003
k_APAMaxVehSpd_Kph_f32 k_ESCMaxVehSpd_Kph_f32	30,399996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.239999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0 torrest TraCulCto Port ADADau(InterventionDetected Cot Inc.
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_Ig	
target_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_APARecoverableFaults_Cnt_Igc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 APARequest Cnt Igc	target_TrqOvlSta_Per1_APARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvISta_Per1_APAState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_lgc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_lgc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_lgc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESClsLimited_Cnt_lgc	target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCRequest_Cnt_lgc	target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_enum
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSHOscillateState_State_enun	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvIsta.TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvIsta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_Igc	target_TrqOvISta_Per1_HapticRequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32$	2 target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 SWARTrgtAngRequest HwDeg f3	
target Rte Inst Ap TrqOvlSta.TrqOvlSta Per1 ShiftLeverlsInReverse Cnt Igc	target TrqOvlSta Per1 ShiftLeverIsInReverse Cnt Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target TrqOvlSta Per1 ESCIsLimited Cnt lgc.value	1
target_TrqOvISta_Per1_ESCISLIMIted_Cnt_igc.value target_TrqOvISta_Per1_ESCRequest_Cnt_lgc.value	1
target_TrqOvIsta_reT_ESCRequest_Crit_igc.value target_TrqOvIsta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	123.599998
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1 0.18999998
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	

© Report created by TESSY V3.1.9, report template V2.1





Name	Input Value		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	2	2	~
TrqOvlSta_ESCState_State_M_enum	4	4	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0	0 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797	91133.6797 ± 1	✓
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	430968	✓
TrqOvlSta_HapticState_State_M_enum	0	0	✓
TrqOvlSta_Hapticdur_Cnt_M_u16	0	0 ± 1	✓
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	•
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	~
TrqOvlSta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•
TrqOvlSta_LKAPermFault_Cnt_M_u08	3	3 ± 1	✓
TrqOvlSta_LKAState_State_M_enum	3	3	✓
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015	49.4000015 ± 0.0009765625	✓
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0	0 ± 1	✓
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	~
target_TrqOvlSta_Per1_APAState_State_enum.value	2	2	•
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	~
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	0	0	•
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	49.4000015	49.4000015 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	~
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrqOvlSta Per1 CP1 CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	✓

Test Step 3.83 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
TrqOvlSta_APAState_State_M_enum	1
TrqOvlSta_ESCState_State_M_enum	4
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	2224203
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	91133.6797
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968
TrqOvlSta_HapticState_State_M_enum	2
TrqOvlSta_Hapticdur_Cnt_M_u16	0
TrqOvlSta_Haptictime_Sec_M_f32	228811.703
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.507000029
TrqOvlSta_LKAFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0
TrqOvlSta_LKAPermFault_Cnt_M_u08	2
TrqOvlSta_LKAState_State_M_enum	2
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32	49.4000015
TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0
k_APAIncludeHaptic_Cnt_lgc	1
k_APAMaxHwTrqTime_Sec_f32	0.298999995
k_APAMaxHwTrq_HwNm_f32	9.27999973
k_APAMaxVehSpd_Kph_f32	208.800003
k_ESCMaxVehSpd_Kph_f32	30.3999996
k_HapticAmplitude_MtrNm_f32	0.460000008
k_HapticDuration_Sec_f32	1.74000001
k_HapticFreq_Hz_f32	59.0299988
k_HapticReacttime_Sec_f32	1.85599995
k_LKAMaxVehSpd_Kph_f32	20
k_LKAMinVehSpd_Kph_f32	203
k_MaxHapticReqTime_Sec_f32	0.23999995
k_SWARLimiter_HwDeg_f32	21.3999996
k_ShiftLevRevTime_Sec_f32	0.0610000007
k_StandstillThresh_Kph_f32	4.63999987
k_StandstillTime_Sec_f32	0
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_	target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc

2015-02-04, 14:05:40+0530



Name	Input Value			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lg	target_TrqOvlSta_Per1_APANonRecoverab	leFaults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFa	aults_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_lgc		target_TrqOvlSta_Per1_APARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APAState_State_enum	target_TrqOvlSta_Per1_APAState_State_enum			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWFound_Cnt_Igc	target_TrqOvlSta_Per1_CCWFound_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CCWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWFound_Cnt_Igc	target_TrqOvlSta_Per1_CWFound_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_CWPosition_HwDeg_f32	target_TrqOvlSta_Per1_CWPosition_HwDeg	g_132		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCFault_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvlSta_Per1_ESCFault_Cnt_lgc	lan		
target Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCRequest Cnt Igc	target_TrqOvISta_Per1_ESCRequest_Cnt_I	target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ESCState_State_enum	target_TrqOvlSta_Per1_ESCState_State_er			
target Rte Inst Ap TrgOvlSta.TrgOvlSta Per1 GMOSHOscillateState State enur				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc	V - 1			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc	target_TrqOvlSta_Per1_GMOSH_LKAMfgEr			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HapticRequest_Cnt_lgc	target_TrqOvlSta_Per1_HapticRequest_Cnt	:_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	target_TrqOvlSta_Per1_HwTorque_HwNm_	<u>f</u> 32		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lg	gc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_en	num		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32				
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	0 - 1			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cn			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_Igc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_H	-		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3				
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc	target_TrqOvlSta_Per1_ShiftLeverIsInRever	*		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm target TrqOvlSta Per1 TrqOscEnable Cnt			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_Igc target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32			
target TrqOvlSta Per1 APANonRecoverableFaults Cnt Igc.value	0	2		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value	-887.609985			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	868.820007			
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESClsLimited_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_ESCRequest_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_HwTorque_HwNm_f32.value	-8.60000038			
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1			
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	0			
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	0			
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	123.599998			
target_TrqOvISta_Per1_PosTrajEnable_Cnt_igc.value target_TrqOvISta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.189999998			
target_TrqOvISta_Per1_PosTrajnwArigie_nwbeg_132.value target_TrqOvISta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	16.5			
target_TrqOvISta_Per1_ShiftLeverIsInReverse_Cnt_lgc.value	1			
Name	Actual Value	Expected Value	Result	
TrqOvISta_APAState_State_M_enum	1	1	1.0301	
TrgOvISta ESCState State M enum	4	4		
TrqOvISta ElapsedAPAMaxHwTrqTime Sec M f32	0	0 ± 1		
TrqOvlSta ElapsedShiftLevRevTime Sec M f32	91133.6797	91133.6797 ± 1		
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	430968	430968		
TrqOvlSta_HapticState_State_M_enum	2	2	•	
TrqOvlSta_Hapticdur_Cnt_M_u16	1	1 ± 1	•	
TrqOvlSta_Haptictime_Sec_M_f32	0	0 ± 1	•	
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-2.70000005	-2.70000005 ± 0.000009	•	
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1 ± 1	•	
TrqOvISta_LKAPermFault_Cnt_M_lgc	1	1 ± 1	•	
TrqOvISta_LKAPermFault_Cnt_M_u08	3	3 ± 1	•	
TrqOvlSta_LKAState_State_M_enum		3		
	3			
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	3 0.189999998 0	0.189999998 ± 0.0009765625 0 ± 1		





Name	Actual Value	Expected Value	Result
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_APAState_State_enum.value	1	1	✓
target_TrqOvlSta_Per1_ESCState_State_enum.value	4	4	✓
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value	2	2	~
target_TrqOvlSta_Per1_LKAState_State_enum.value	3	3	✓
target_TrqOvlSta_Per1_PosServEnable_Cnt_lgc.value	1	1 ± 1	✓
target_TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.189999998	0.189999998 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.460000008	0.460000008 ± 0.0009765625	✓
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	✓
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	59.0299988	59.0299988 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP1_CheckpointReached	1	~

Fest Step 3.84 (Repeat Count = 1) lame	Innuit Value
	Input Value
Rte_Inst_Ap_TrqOvlSta	target_Rte_Inst_Ap_TrqOvlSta
rqOvlSta_APAState_State_M_enum	1
rqOvlSta_ESCState_State_M_enum	1
rgOvlSta ElapsedAPAMaxHwTrqTime Sec M f32	0
rqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0
rgOvlSta ElapsedStandstillTime Sec M f32	0
rqOvlSta_HapticState_State_M_enum	0
rqOvlSta_Hapticdur_Cnt_M_u16	44450
rqOvlSta_Haptictime_Sec_M_f32	0
rgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	-1.64999998
rqOvISta_HwTorqueSV_HwNm_M_Str.K_UIs_f32	0.419
rgOvISta LKAFault Cnt M lgc	0
rqOvISta_LKAPermFault_Cnt_M_lgc	0
rqOvISta_LKAPermFault_Cnt_M_u08	1
rqOvISta_LKAState_State_M_enum	0
rqOvISta_PosSrvoHwAngle_HwDeg_M_f32	1.3999998
rqOvISta_ReadytoPulse_Cnt_M_lgc	1
_APAIncludeHaptic_Cnt_lgc	1
APAMaxHwTrqTime Sec f32	0.601666689
_APAMaxHwTrq_HwNm_f32	0
APAMaxVehSpd Kph f32	138.600006
ESCMaxVehSpd Kph f32	12
LapticAmplitude_MtrNm_f32	0.462000012
_HapticDuration_Sec_f32	1.15499997
_HapticFreq_Hz_f32	32.3019981
HapticReacttime Sec f32	1.23199999
LKAMaxVehSpd Kph f32	146.300003
_LKAMinVehSpd_Kph_f32	100
MaxHapticReqTime_Sec_f32	0.600000024
SWARLimiter HwDeg f32	30
_ShiftLevRevTime_Sec_f32	0.308999985
StandstillThresh_Kph_f32	0
_StandstillTime_Sec_f32	0.308999985
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APADrvrInterventionDetected_Cni	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_I	
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc	target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_APARequest_Cnt_Igc	target_TrqOvISta_Per1_APARequest_Cnt_Igc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_APARequest_Citt_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_State_enum	target TrqOvISta Per1 APAState State enum
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_APAState_State_entitin arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_CCWFound_Cnt_lgc	target_TrqOvISta_Per1_CCWFound_Cnt_lgc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_CCWPound_Crit_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_CCWPosition_HwDeg_f32	target_TrqOvISta_Per1_CCWPosition_HwDeg_f32
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWFound_Cnt_Igc	target TrqOvISta Per1 CWFound Cnt Igc
arget_Rte_Inst_Ap_TrqOvISta.TrqOvISta_Per1_CWPosition_HwDeg_f32	target TrqOvISta Per1 CWPosition HwDeg f32
arget Rte Inst Ap TrqOvISta.TrqOvISta Per1 ESCFault Cnt Igc	target TrqOvISta Per1 ESCFault Cnt Igc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rerr_ESCFault_Citt_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCIsLimited_Cnt_Igc	target_TrqOvISta_Per1_ESCIsLimited_Cnt_lgc
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_rerT_ESCISE.fillited_Crit_igc arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_Per1_ESCRequest_Crit_igc	target_TrqOvISta_Per1_ESCRequest_Cnt_Igc
arget_Rte_inst_Ap_riqOviSta.frqOviSta_reri_ESCRequest_Cnt_igc	target_TrqOvISta_Per1_ESCState_State_enum
arget_Rte_inst_Ap_11q0viSta.11q0viSta_rei1_E3c3tate_5tate_enum arget_Rte_inst_Ap_11q0viSta.11q0viSta_Per1_GMOSHOscillateState_State_enu	
arget_Rte_inst_Ap_11q0viSta.11q0viSta_Per1_GiviOSHOsciliateState_state_eriu arget Rte Inst Ap Trq0viSta.Trq0viSta Per1 GMOSH APAMfgEnable Cnt Igc	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSH_APAMilgEnable_Cnt_igo arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSH_ESCMfgEnable_Cnt_igo	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSH_ESCMigEnable_Crit_igo arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GMOSH_LKAMfgEnable_Crit_igo	
arget_Rte_inst_Ap_TrqOviSta.TrqOviSta_PerT_GiviOSH_ERAWilgErlable_Crit_igc arget_Rte_inst_Ap_TrqOviSta.TrqOvISta_PerT_HapticRequest_Cnt_igc	target TrqOvISta_Peri_GMOSH_EKAMigenable_Cht_igc
	target_TrqOvISta_Per1_HwTorque_HwNm_f32
arget_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_HwTorque_HwNm_f32	





Name	Input Value		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAFault_Cnt_lgc	target_TrqOvlSta_Per1_LKAFault_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAInhibit_Cnt_lgc	target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKARequest_Cnt_lgc	target_TrqOvlSta_Per1_LKARequest_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_LKAState_State_enum	target_TrqOvlSta_Per1_LKAState_State_enum		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32	target_TrqOvlSta_Per1_MaxSecureVehicleS	speed_Kph_f32	
$target_Rte_Inst_Ap_TrqOvlSta_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32$	target_TrqOvlSta_Per1_MinSecureVehicleSp	peed_Kph_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosServEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosServEnable_Cnt	t_lgc	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosSrvoHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosSrvoHwAngle_H	lwDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc	target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32	target_TrqOvlSta_Per1_PosTrajHwAngle_Hv	wDeg_f32	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f3			
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_lgc	target_TrqOvlSta_Per1_ShiftLeverIsInRever		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32	target_TrqOvlSta_Per1_TrqOscAmp_MtrNm	_	
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc	target_TrqOvlSta_Per1_TrqOscEnable_Cnt_		
target_Rte_Inst_Ap_TrqOvlSta.TrqOvlSta_Per1_TrqOscFreq_Hz_f32	target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32	2	
target_TrqOvlSta_Per1_APANonRecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARecoverableFaults_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_APARequest_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_CCWFound_Cnt_lgc.value	-368.230011		
target_TrqOvlSta_Per1_CCWPosition_HwDeg_f32.value			
target_TrqOvlSta_Per1_CWFound_Cnt_lgc.value	361.01001		
target_TrqOvlSta_Per1_CWPosition_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ESCFault_Cnt_lgc.value target_TrqOvlSta_Per1_ESCIsLimited_Cnt_lgc.value	1		
target TrqOvISta_rer1_ESCISEIIIIIted_Off_igc.value target TrqOvISta Per1 ESCRequest Cnt Igc.value	1		
target_TrqOvlSta_Per1_ESCRequest_Crit_lgc.value target_TrqOvlSta_Per1_GMOSH_APAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_ESCMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_GMOSH_LKAMfgEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_HapticRequest_Cnt_lgc.value	1		
target TrqOvlSta Per1 HwTorque HwNm f32.value	8.10000038		
target_TrqOvlSta_Per1_LKAFault_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_LKAInhibit_Cnt_lgc.value	0		
target_TrqOvlSta_Per1_LKARequest_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_MaxSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_MinSecureVehicleSpeed_Kph_f32.value	1		
target_TrqOvlSta_Per1_PosTrajEnable_Cnt_lgc.value	1		
target_TrqOvlSta_Per1_PosTrajHwAngle_HwDeg_f32.value	0.200000003		
target_TrqOvlSta_Per1_SWARTrgtAngRequest_HwDeg_f32.value	0		
target_TrqOvlSta_Per1_ShiftLeverIsInReverse_Cnt_Igc.value	1		
Name	Actual Value	Expected Value	Result
TrqOvlSta_APAState_State_M_enum	0	0	~
TrqOvlSta_ESCState_State_M_enum	2	2	~
TrqOvlSta_ElapsedAPAMaxHwTrqTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedShiftLevRevTime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	~
TrqOvlSta_ElapsedStandstillTime_Sec_M_f32	0	0	~
TrqOvlSta_HapticState_State_M_enum	0	0	~
TrqOvlSta_Hapticdur_Cnt_M_u16	44450	44450 ± 1	~
TrqOvlSta_Haptictime_Sec_M_f32	0.00200000009	0.00200000009 ± 1	V
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	2.43524981	2.43525004 ± 0.000009	V
TrqOvlSta_LKAFault_Cnt_M_lgc	1	1±1	V
TrqOvlSta_LKAPermFault_Cnt_M_lgc	0	0 ± 1	
TrqOvlSta_LKAPermFault_Cnt_M_u08	2	2 ± 1	¥
TrqOvISta_LKAState_State_M_enum	0	0	*
TrqOvlSta_PosSrvoHwAngle_HwDeg_M_f32 TrqOvlSta_ReadytoPulse_Cnt_M_lgc	0.200000003	0.200000003 ± 0.0009765625	
	0	1 ± 1 0 ± 1	-
target_TrqOvlSta_Per1_APADrvrInterventionDetected_Cnt_lgc.value target_TrqOvlSta_Per1_APAState_State_enum.value	0	0	
target_TrqOvlSta_Per1_ESCState_State_enum.value	2	2	
	0	0	
target_TrqOvlSta_Per1_GMOSHOscillateState_State_enum.value target_TrqOvlSta_Per1_LKAState_State_enum.value	0	0	-
target_rqovista_Peri_LRAState_State_enum.value target_TrqOviSta_Peri_LRAState_State_enum.value	1	1 ± 1	- 40
target_IrqOviSta_Per1_PosServEnable_Cnt_igc.value target_TrqOviSta_Per1_PosSrvoHwAngle_HwDeg_f32.value	0.200000003	0.200000003 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscAmp_MtrNm_f32.value	0.462000012	0.462000012 ± 0.0009765625	-
target_TrqOvlSta_Per1_TrqOscEnable_Cnt_lgc.value	0	0 ± 1	
target_TrqOvlSta_Per1_TrqOscFreq_Hz_f32.value	32.3019981	32.3019981 ± 0.0009765625	-
0	1		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	Rte_Call_TrqOvlSta_Per1_CP0_CheckpointReached	1	~
Rte Call TrgOvlSta Per1 CP1 CheckpointReached	1	Rte Call TrgOvISta Per1 CP1 CheckpointReached	1	-

2015-02-04, 14:05:40+0530



2015-02-04, 13:58:15+0530



TrqOvlSta_Init

 Project
 CBD_UnitTest

 Module
 TrqOvlSta

 Test Object
 TrqOvlSta_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\TrqOvlSta_CBD
Configuration File	D:\Synergy_Work_Area\TrqOvlSta_CBD\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\TrqOvlSta\src\Ap_TrqOvlSta.c
Compiler Options	-DSKIP_MAGIC_NUMBER= -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\TrqOvlsta\utp\contract -I\$(PROJECTROOT)\TrqOvlsta \utp\contract\Ap_TrqOvlSta -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments \ccsv4\tools\compiler\tms470_4.9.5\include

Comments/Descript	ion/Specification
Name	Text
Name Module 'TrqOvlSta'	Name of Tester:Imran desai Code File(s) Under Test:Ap_TrqOvISta.c Code File(s) Version:8 Module Design Document:TrqOvISta_MDD.docx Module Design Document Version:7 Data Dictionary Version:6 Unit Test Plan Version:5 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):2128 Total RAM Used (Bytes):65
	Special Test Requirements: Test Date:2/4/2015 Comments:"NOTE 1:- Inline functions defined in ""GlobalMacro.h"" are not unit tested. NOTE 2:- ""CBD_Sandbox_dbg.map"" map file is embedded for reference. NOTE 3:- In function ""TrqOvlSta_Per1"", for 100% path coverage, Variable ""TrqOvlSta_APAState_State_M_enum"" is given out of range value at Vector Number ""76" in Path sheet, Hence at this test vector output variable ""TrqOvlSta_APAState_State_M_enum"" and""APAState State enum"" is going Out of Range.
	NOTE 4:-In function "TrqOvlSta_Per1"", for 100% path coverage Variable ""TrqOvlSta_HapticState_State_M_enum"" and ""TrqOvlSta_ESCState_State_M_enum"" is given out of range value at Vector Number ""78"" in Path sheet, Hence at this test vector, output variable ""TrqOvlSta_ESCState_State_M_enum"",""TrqOvlSta_HapticState_State_M_enum"",""ESCState_State_enum""and""GMOSHOscillateState_State is going Out of Range. NOTE 5:-In function ""TrqOvlSta_Per1"",100% MCDC coverage is not achieved because (TrqOvlSta_APAState_State_M_enum == APA_Active)=>True and (PosTrajEnable_Cnt_T_lgc == FALSE) => False cannot be covered as control goes to if loop. NOTE 6:- When value of 'TrqOvlSta_LKAPermFault_Cnt_M_uo8' is greater than three, then the value of 'TrqOvlSta_LKAPermFault_Cnt_M_lgc' is taken as 'TRUE' as 'INPUT' to avoid overflow issue.
	NOTE 7:-In function ""TrqOvISta_Per1"", (SWARTrgtAngRequest_HwDeg_T_f32<=(CCWPosition_HwDeg_T_f32 + k_SWARLimiter_HwDeg_f32)) = True and (SWARTrgtAngRequest_HwDeg_T_f32 >=(CWPosition_HwDeg_T_f32 - k_SWARLimiter_HwDeg_f32)) = True cannot be covered."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5

2015-02-04, 13:58:15+0530

TrqOvlSta_Init



Attributes	
Name	Value
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enable	false
Timer Prescale	0
Timer Resolution	1.0
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\Trq0vlSta_CBD\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 114.00 Cycles TS1.2 116.00 Cycles TS1.3 115.00 Cycles TS1.3 115.00 Cycles TS1.4 113.00 Cycles TS1.5 113.00 Cycles TS1.6 113.00 Cycles TS1.7 113.00 Cycles TS1.8 115.00 Cycles TS1.9 115.00 Cycles TS1.10 115.00 Cycles TS1.11 115.00 Cycles TS1.12 113.00 Cycles

Description

Vector Description:

TS1.1k_APAHwTrqLPFKn_Hz_f32 = MIN
TS1.2k_APAHwTrqLPFKn_Hz_f32 = MAX
TS1.3k_APAHwTrqLPFKn_Hz_f32 = POS
TS1.4TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV= MIN
TS1.5TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV= MAX
TS1.6TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV= ZERO
TS1.7TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV= POS
TS1.8TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV= NEG
TS1.9TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = MIN
TS1.10TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = MAX
TS1.11TrqOvlSta_HwTorqueSV_HwNm_M_Str.K = ZERO
TS1.12ALL MIN

TS1.12ALL MIN TS1.13ALL MAX

TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32

TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-8.53999996		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.023		
k_APAHwTrqLPFKn_Hz_f32	0.10000001		
Name	Actual Value	Expected Value	Result
TrgOvlSta HwTorqueSV HwNm M Str.SV Uls f32	0	0 ± 0.000009	✓

0.00125586987

0.00125584798 ± 0.00000009

0.715390444 ± 0.00000009

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.32000017		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0869999975		
k_APAHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~

0.715390444

Test Step 1.3 (Repeat Count = 1)			V
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-3.38000011		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.150999993		
k_APAHwTrqLPFKn_Hz_f32	50.6500015		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	-
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470851779	0.470851779 ± 0.00000009	•

Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.215000004		
k_APAHwTrqLPFKn_Hz_f32	5.40999985		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.0657246709	0.0657246411 ± 0.00000009	✓



TrqOvlSta_Init

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.279000014		
k_APAHwTrqLPFKn_Hz_f32	11.4200001		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.133686125	0.133686095 ± 0.00000009	✓

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.342999995		
k_APAHwTrqLPFKn_Hz_f32	15.8500004		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.18059516	0.18059513 ± 0.00000009	✓

Test Step 1.7 (Repeat Count = 1)			
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-5.5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.407000005		
k_APAHwTrqLPFKn_Hz_f32	20		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.222232342	0.222232327 ± 0.00000009	~

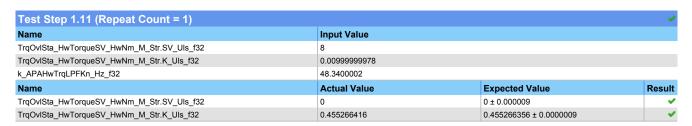
Test Step 1.8 (Repeat Count = 1)			
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5.5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.470999986		
k_APAHwTrqLPFKn_Hz_f32	28.6100006		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	✓
TrqOvlSta HwTorqueSV HwNm M Str.K Uls f32	0.301991463	0.301991403 ± 0.0000009	✓

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	3.56999993		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798		
k_APAHwTrqLPFKn_Hz_f32	36.8600006		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.370730996	0.370730966 ± 0.0000009	~

Test Step 1.10 (Repeat Count = 1)			
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	5		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444		
k_APAHwTrqLPFKn_Hz_f32	42		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.4100914	0.410091341 ± 0.0000009	✓



TrqOvISta_Init



Test Step 1.12 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	-10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125584798		
k_APAHwTrqLPFKn_Hz_f32	0.100000001		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	•
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.00125586987	0.00125584798 ± 0.00000009	•

Test Step 1.13 (Repeat Count = 1)			✓
Name	Input Value		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	10		
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444		
k_APAHwTrqLPFKn_Hz_f32	100		
Name	Actual Value	Expected Value	Result
TrqOvlSta_HwTorqueSV_HwNm_M_Str.SV_Uls_f32	0	0 ± 0.000009	~
TrqOvlSta_HwTorqueSV_HwNm_M_Str.K_Uls_f32	0.715390444	0.715390444 ± 0.00000009	•